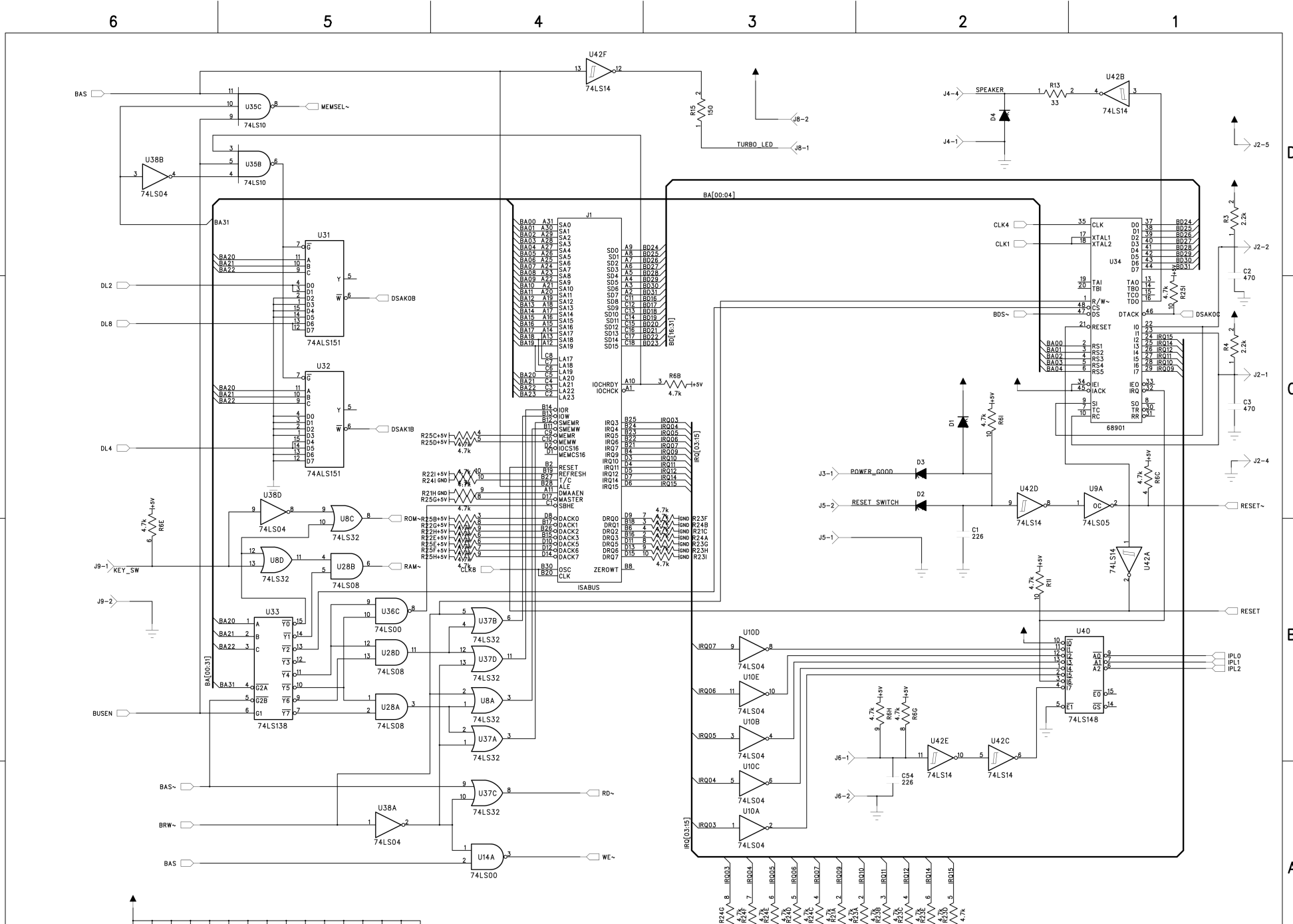


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CPU & Mem

DRAWN: hess	DATED: 5/26/93	TITLE:
CHECKED:	DATED:	PROJECT: 68030 WorkStation
		SHEET: 1 OF 3 REV:



C34	C35	C36	C37	C38	C39	C40	C41	C42	C43	C44	C45	C46	C47	C48	C49
103	103	103	103	103	103	103	103	103	103	103	103	103	103	103	103

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DRAWN:	DATED:	TITLE:
hess	5/26/93	ISA Bus & interrupts
CHECKED:	DATED:	PROJECT:
		68030 WorkStation
		SHEET: 2 OF 3 REV:

6

5

4

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2

1

D

C

B

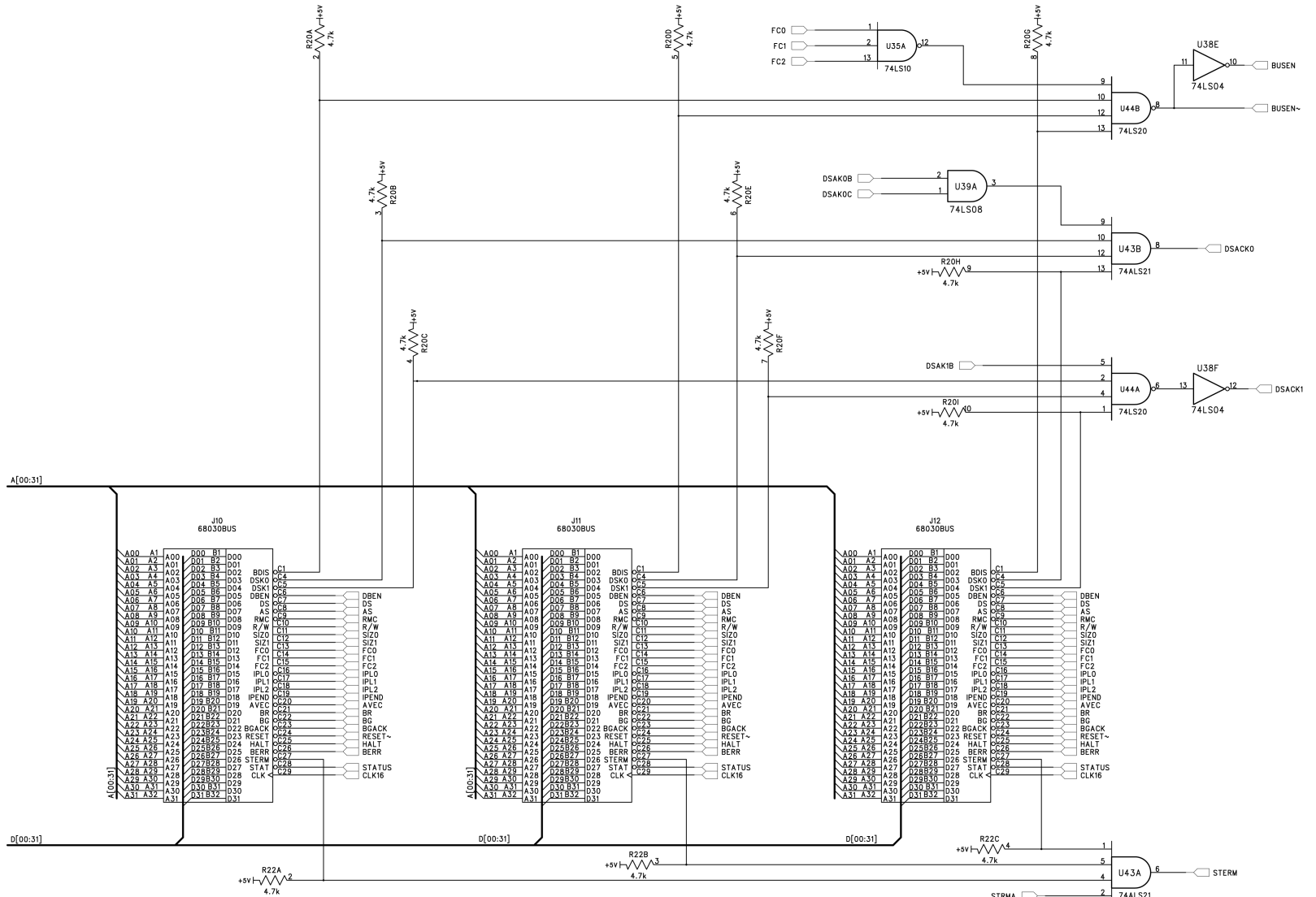
A

D

C

B

A



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DRAWN: hess	DATED: 7/7/93	TITLE: 68030 Bus
CHECKED:	DATED:	PROJECT: 68030 WorkStation
		SHEET: 3 OF 3 REV: B

6

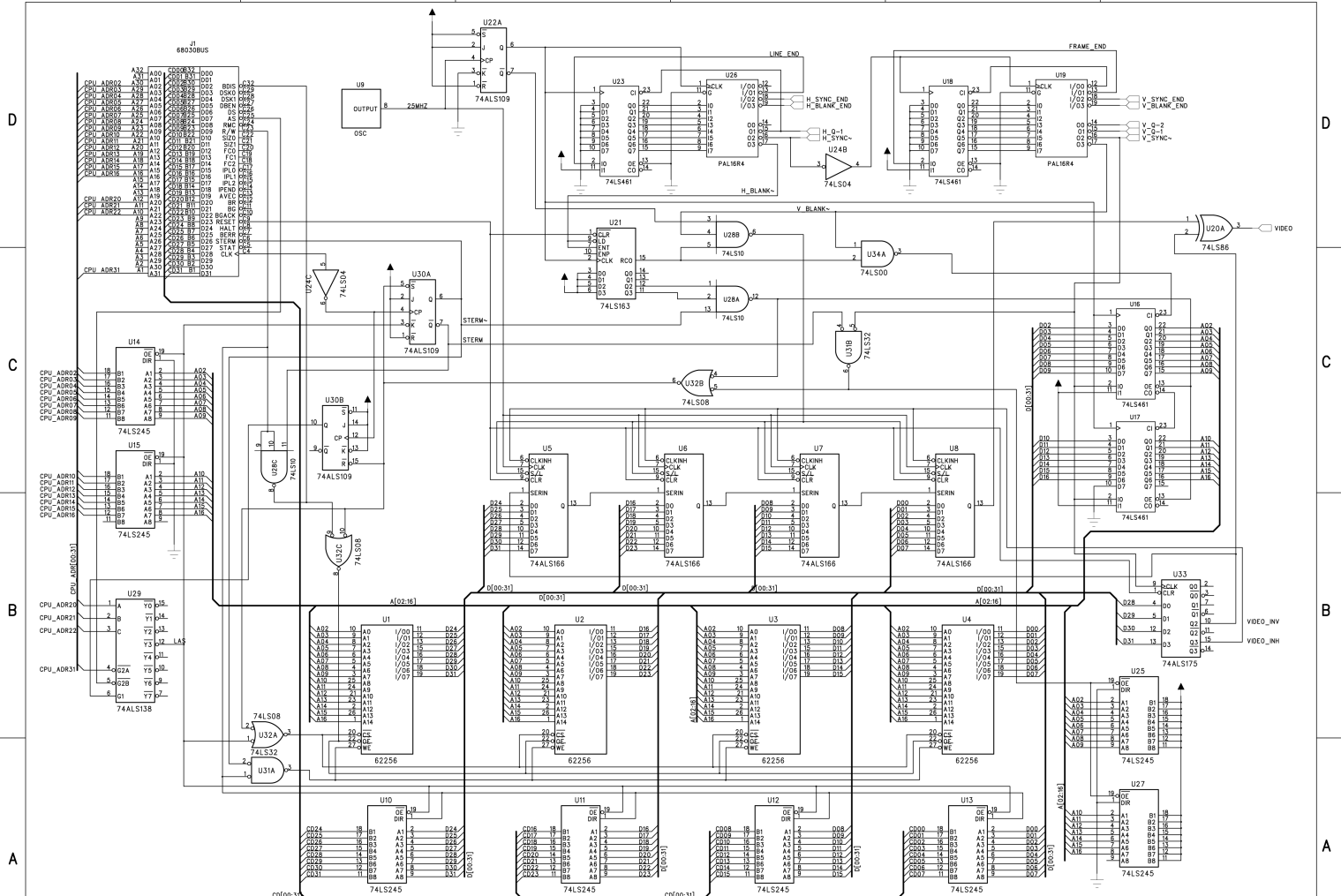
5

4

3

2

1



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DRAWN: hess DATED: 8/3/94

TITLE: Frame Buffer

CHECKED: DATED:

PROJECT: 68030 WorkStation SHEET: 1 OF 1 REV: A