

NOTE 1: J7 - ASYNCHRONOUS OPERATION = NO JUMPERS ON J7-3 THRU J7-10
 SYNCHRONOUS, DTE TO MODEM = J7-5 TO J7-6, J7-7 TO J7-9
 (BOTH CLOCKS RECEIVED FROM MODEM)
 SYNCHRONOUS, DCE TO TERMINAL = J7-3 TO J7-4, J7-5 TO J7-7, J7-8 TO J7-10
 (BOTH CLOCKS DRIVEN FROM Z8530 RXC OUTPUT:
 Z8530 MUST BE PROGRAMMED TO OUTPUT TXC CLOCK)

NOTE 2: ACTIVE PART SWITCH S3 IS DARKENED AF

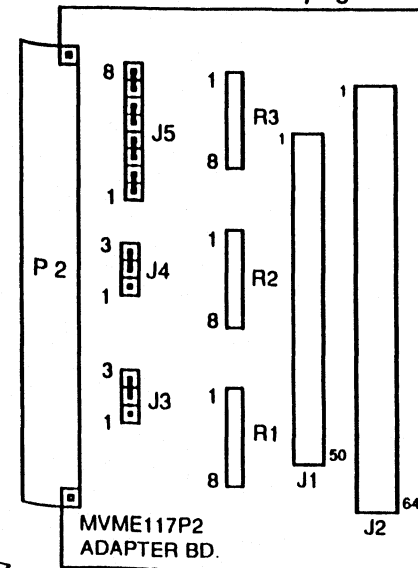
PART NUMBERS:

- MVME117 01-W3371B02 76433096
- MVME117-1 01-W3393B01 76433025 & 76433048
- MVME117-3 01-W3393B03 76433094
- MVME117-3FP 01-W3393B07 76435334
- MVME117-4 01-W3393B04 76433100
- MVME117P2 01-W3567B01 XXXXXXXX

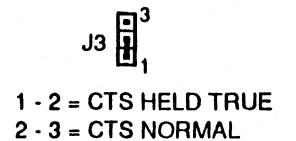
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

PARALLEL PORTSELECTT HEADER

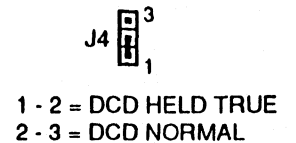
J5 CONNECTIONS	EFFECTIVE CONNECTION	SIGNAL
1 - 2	P2-A19 TO J2-57	P0 - D0
3 - 4	P2-C21 TO J2-60	P0 - D3
5 - 6	P2-A20 TO J2-62	P0 - D2
7 - 8	P2-C20 TO J2-64	P0 - D1



CTS SELECT HEADER

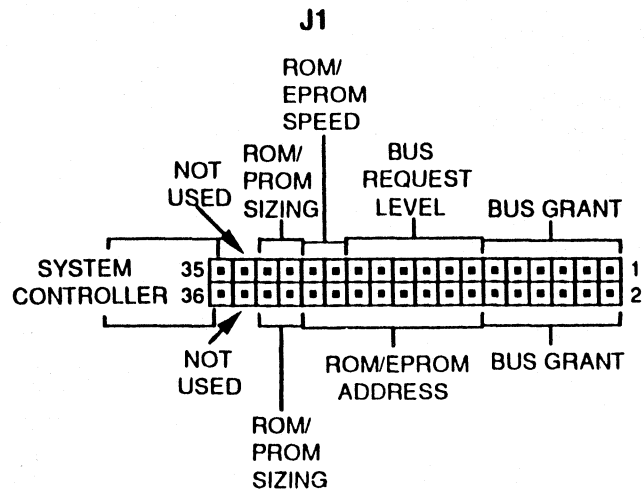


DCD SELECT HEADER



09/11/89

BUS REQUEST, BUS GRANT, ROM/PROM ADDRESS SELECT, ACCESS TIME SELECT, SIZE SELECT, AND SYSTEM CONTROLLER ENABLE



BR3* = J1-21 TO J1-23
 BR2* = J1-19 TO J1-21
 BR1* = J1-15 TO J1-17
 BR0* = J1-13 TO J1-15

BG3IN*, BG3OUT* = J1-1 TO J1-2, 5 TO 6, 7 TO 8, 9 TO 11, 10 TO 12
 BG2IN*, BG2OUT* = J1-1 TO J1-2, 5 TO 6, 7 TO 9, 8 TO 10, 11 TO 12
 BG1IN*, BG1OUT* = J1-1 TO J1-2, 3 TO 5, 4 TO 6, 7 TO 8, 11 TO 12
 BG0IN*, BG0OUT* = J1-1 TO J1-3, 2 TO 4, 5 TO 6, 7 TO 8, 11 TO 12

ROMEPROM PINOUT ADDRESS SELECT:

64K X 8 = J1-14 TO J1-16, J1-18 TO J1-20, J1-26 TO J1-28
 32K X 8 = J1-14 TO J1-16, J1-18 TO J1-20, J1-24 TO J1-26
 16K X 8 = J1-14 TO J1-16, J1-20 TO J1-22, J1-24 TO J1-26
 8K X 8 = J1-20 TO J1-22, J1-24 TO J1-26

ROM/EPROM ACCESS TIME:

J1-25 TO J1-27 INSTALLED FOR FAST 250ns PARTS
 J1-25 TO J1-27 REMOVED FOR SLOW 450ns PARTS

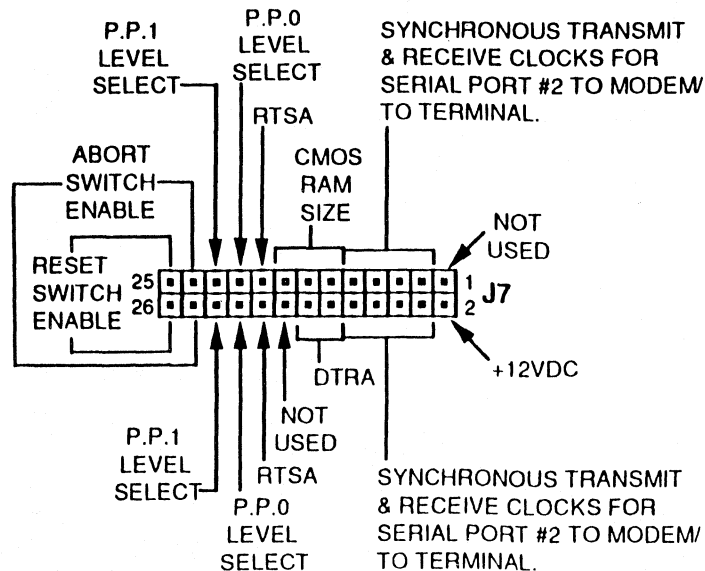
ROM/EPROM SIZE SELECT:

64K X 8 = NO JUMPERS ON J1 PINS 29, 30, 31, AND 32
 32K X 8 = J1-29 TO J1-30
 16K X 8 = J1-31 TO J1-32
 8K X 8 = J1-29 TO J1-30, AND J1-31 TO J1-32

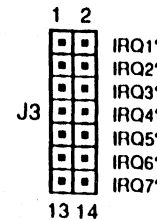
SYSTEM CONTROLLER:

ENABLED = J1-35 TO J1-36 INSTALLED
 DISABLED = J1-35 TO J1-36 REMOVED
 (SET UP AS A SLAVE)

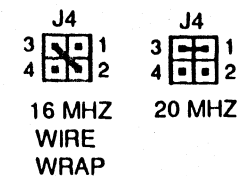
SERIAL PORT JUMPERS

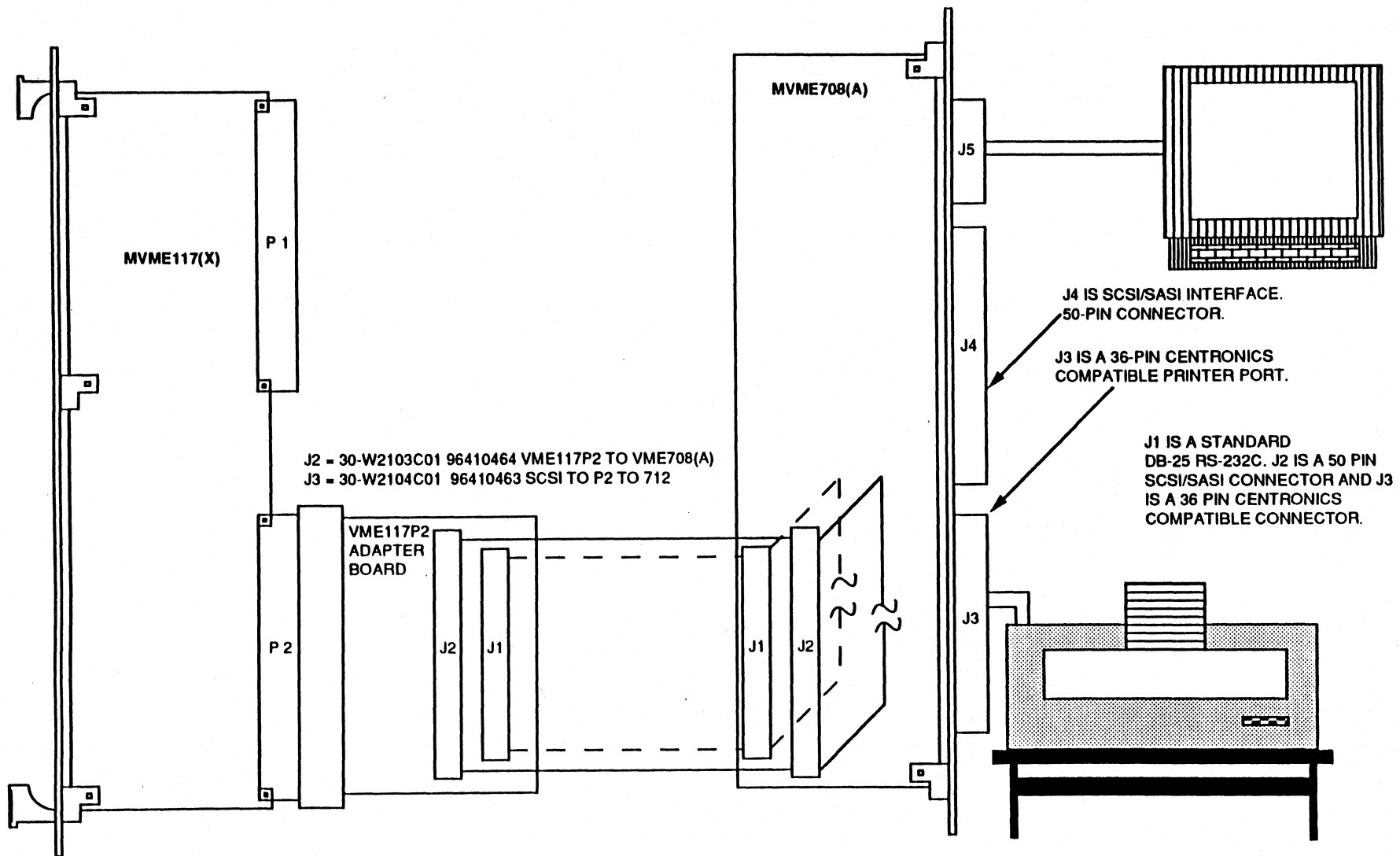


INTERRUPT REQUEST ENABLE



FLOATING POINT PROCESSOR CLOCK SELECT





J2 - 30-W2103C01 96410464 VME117P2 TO VME708(A)
 J3 - 30-W2104C01 96410463 SCSI TO P2 TO 712

VME117P2
 ADAPTER
 BOARD

J4 IS SCSI/SASI INTERFACE.
 50-PIN CONNECTOR.

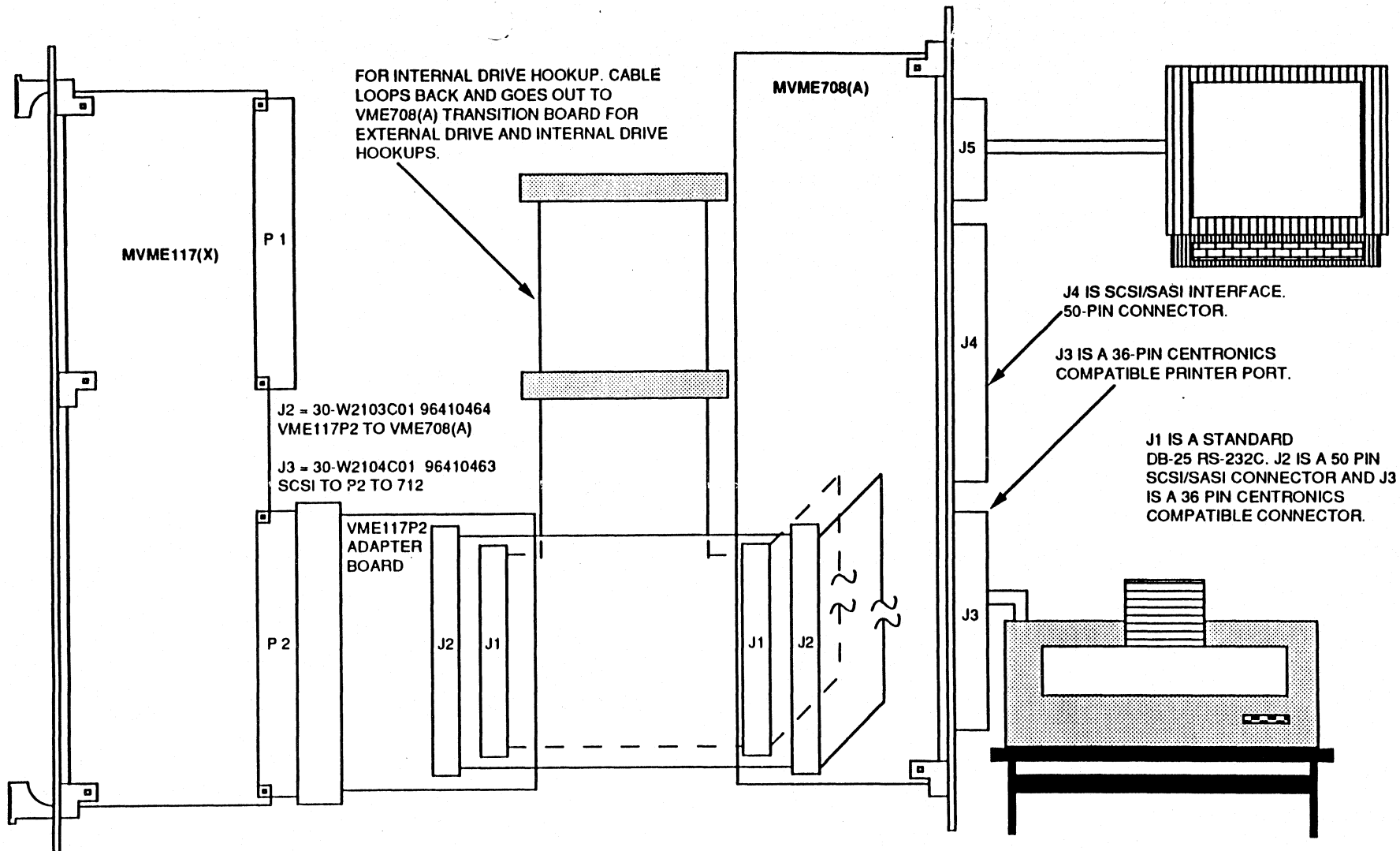
J3 IS A 36-PIN CENTRONICS
 COMPATIBLE PRINTER PORT.

J1 IS A STANDARD
 DB-25 RS-232C. J2 IS A 50 PIN
 SCSI/SASI CONNECTOR AND J3
 IS A 36 PIN CENTRONICS
 COMPATIBLE CONNECTOR.

NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE.
 IT IS USED FOR EXTERNAL DRIVES
 AND IS BEHIND J5 ON THE
 VME708(A) MODULE.
 J1 IS A 50-PIN CONDUCTOR CABLE.
 IT IS USED FOR INTERNAL DRIVES
 OR HOOKING UP OF EXTERNAL SCSI
 DRIVES.

NOTE 2: TERMINATORS MUST BE INSTALLED
 AT THE END OF ALL CABLES.

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NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE. IT IS USED FOR EXTERNAL DRIVES AND IS BEHIND J5 ON THE VME708(A) MODULE. J1 IS A 50-PIN CONDUCTOR CABLE. IT IS USED FOR INTERNAL DRIVES OR HOOKING UP OF EXTERNAL SCSI DRIVES.

NOTE 2: TERMINATORS MUST BE INSTALLED AT THE END OF ALL CABLES.

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