

MVMEPWBCNFG/D3

*PWB &
SYSTEMS
LEVEL*

FSD
BOARD
STRAPPING
GUIDE

RELEASED 4Q, 1991

PREFACE

The VME Board Strapping Guide (B.S.G.) provides configuration information for all Motorola qualified boards and other qualified sub-assemblies for VME Delta System computers and stand-alone boards. Where strapping is software and/or hardware dependent, this fact is so noted.

Reasonable efforts have been made to assure the accuracy of this document. Motorola assumes no liability resulting either from any omissions in this document or from use of information obtained therein. Motorola reserves the right to revise this without being obligated to notify any person of such revision.

Any corrections, updates and/or requests to add information to the strapping guide should be referred to Tedd LeBlond in Tempe Product Support at (602) 438-3107 or FAX (602) 438-3113.

The Lowest Acceptable Level (L.A.L.) for system level boards has been taken over by engineering and reflects the same as the C.R.L. Use the Current Revision Level (C.R.L.) as a reference for board revisions in systems and stand-alone configurations. Board level information is found in the C.R.L./L.A.L. document published on a quarterly basis by Tempe Product Support.

This document supersedes all issues of the Delta Series Board Strapping and Configuration Guide (P/N MVME2616BSG/D1 with all changes). This update includes ALL VME board/system level products. This was requested by the field and done to have one reference source for all strapping information for board and system products.

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Centronics	Data Computer Corporation
Clearpoint	Clearpoint Corporation
Fujitsu	Fujitsu Limited
Imprimis	Imprimis Corporation
Interphase	Interphase Corporation
Kennedy	Kennedy Corporation
Maxtor	Maxtor Corporation
Micropolis	Micropolis Corporation
Pertec	Pertec Peripheral Corporation
Seagate	Seagate Technology
UDS	Universal Data Systems (a Motorola Company)
TEAC	TEAC Corporation

INDEX

PREFACE.....	i
INDEX.....	ii
INTRODUCTION.....	v
SECTION 0. MVME000 SERIES: MVME025, MVME035, MVME050	
SECTION 1. MVME100 SERIES: MVME101, MVME104/05/06/07, MVME110, MVME115M, MVME117, MVME117A, MVME120/21/22/23 MVME130/31/32(XT) SERIES, MVME133 (OLD), MVME133A (NEW), MVME133XT, MVME134, MVME134 SPECIAL, MVME135/36 SERIES, MVME141(-1/-2), MVME143(-1/-2), MVME147 SERIES, MVME147S SERIES, MVME165(A), MVME180, MVME181(-1/-2), MVME188, MVME188 HYPER- MODULES, & MVME188SYSCON, SMM147---	
SECTION 2. MVME200 SERIES: VSB CABLING, MVME200/01, MVME202/222(-1/-2), MVME204(-1/-2), MVME204-2F MVME205, MVME210, MVME211, MVME214, MVME215(-1/-2/-3), MVME224(-1/-2); MVME224A(-1/-2/-3/-4), MVME225(-1/-2), MVME226(-1/-2), MVME230(-1/-2/-3/-4/-5), MVME236(-1/-2/-3), MVME246(-1/-2/-3), MVME288DF.	
SECTION 3. MVME300 SERIES: MVME300, MVME310/15/19, MVME316, MVME320A(-1), MVME320B(-1), MVME322, MVME323(-1/-2), MVME327A, MVME328-1, MVME330(A/B), MVME331-1, MVME332, MVME332XT, MVME333-2, MVME333X.25, MVME334, MVME335, MVME336, MVME337, MVME338, MVME340A, MVME350, MVME355, MVME360, MVME370, MVME 371, MVME372, MVME373, MVME374, MVME390(A/B), MVME391, MVME393, & MVME395.	
SECTION 4. MVME400 SERIES: MVME400, MVME410, MVME420, & MVME435(A).	
SECTION 5. SPECIALS, SERIES:	
SECTION 6. MVME600 SERIES: MVME600/01, MVME605, MVME610, MVME615, MVME620, & MVME625.	
SECTION 7. MVME700 SERIES: MVME701(A), MVME702(A), MVME705(-1/A/A-1/B), MVME707(OLD, NEW & SMM1442), MVME708, MVME709, MVME710 (SMM1437); MVME710F/731(MODEM), MVME711, MVME712(X) SERIES, MVME714(X) SERIES, MVME715P, MVME716, MVME717, MVME718, MVME751, MVME792(A/-1/-2), & MVME794/98 (1/-2). SMM705ATA(1-A, 2-A) \$ RC1-A, SMM712A/B.	
SECTION 8. MVME800 SERIES: MVME82(X), MVME83(X), MVME84(X), MVME85(X), MVME86(X), MVME87(X), & MVME88(X).	

APPENDIX A ESDI HARD DISK DRIVES:

161 MB (182 MB UNFORMATTED 94166-182) CDC DRIVE
380 MB (410 MB UNFORMATTED XT8280) MAXTOR DRIVE
390 MB (442 MB UNFORMATTED 94186-442) CDC DRIVE
701 MB (768 MB UNFORMATTED XT8760) MAXTOR DRIVE

APPENDIX B FLOPPY DRIVES:

655 KB TEAC (SA450 - 5.25 IN. FD-55FV-13-U)
1.2 MB TEAC (SA450 - 5.25 IN. FD-55-GFV-17/GFR-606/GFR-152)
1.2 MB TEAC (SCSI - 5.25 IN. FD55-GS-751)
1.2 MB TEAC W/ OMTI CONTROLLER (SCSI - 5.25 IN. FD-55-GFV-17/GFR-606)
1-4 MB TEAC 135 TPI W/ 5.25 IN BRACKETS (SCSI - 3.5 IN.)
1-4 MB TEAC FD235H-3240 (SCSI 3.5 IN.)
1-4 MB FUJITSU FD-55/235F,G,H,J,SERIES (3.5 IN. SCSI)
2.9 MB TEAC (SCSI - 3.5 IN. FD-235-JS/ FD-235J)

APPENDIX C STREAMING TAPE DRIVES:

60 MB (QIC-02) STREAMING TAPE DRIVE (5.25 IN. ARCHIVE 5945-L2)
60 MB (SCSI) STREAMING TAPE DRIVE (5.25 IN. ARCHIVE 2060S)
150 MB (QIC-02) STREAMING TAPE DRIVE (5.25 IN. ARCHIVE 2150L)
150 MB (SCSI) STREAMING TAPE DRIVE (5.25 IN. ARCHIVE 2150S)
155 MB (SCSI) STREAMING TAPE DRIVE (3.5 IN. MT-2ST/N50)
525 MB (SCSI) STREAMING TAPE DRIVE (5.25 IN. ARCHIVE 2525S)
2.3 GB (SCSI) STREAMING TAPE DRIVE (3.5 IN. EXABYTE 8200)

APPENDIX D SCSI HARD DISK DRIVES:

21 MB SEAGATE HARD DRIVE (5.25 IN. ST125N)
48 MB SEAGATE HARD DRIVE (5.25 IN. ST157N-M)
60 MB SEAGATE HARD DRIVE (3.5 IN. ST177N)
85 MB SEAGATE HARD DRIVE (5.25 IN. ST296N-M)
91 MB CDC HARD DRIVE (5.25 IN. WREN III 94211-106)
104 MB SEAGATE HARD DRIVE (3.5 IN. 94351-126)
135 MB FUJITSU HARD DRIVE (3.5 IN. M2613ESA)
155 MB CDC HARD DRIVE (5.25 IN. WREN III 94161-155)
172 MB SEAGATE HARD DRIVE (3.5 IN. 94351-200S)
180 MB FUJITSU HARD DRIVE (3.5 IN. M2614ESA)
183 MB SEAGATE HARD DRIVE (5.25 IN. WREN V 94221-209)
300 MB CDC HARD DRIVE (5.25 IN. WREN IV 94171-300)
330 MB SEAGATE WREN VI HARD DRIVE (5.25 IN. 94241-383)
330 MB FUJITSU HARD DRIVE (3.5 IN. H/H M2522SA)
520 MB FUJITSU HARD DRIVE (3.5 IN. H/H M2524SA)
600 MB CDC HARD DRIVE (5.25 IN. WREN V 94181-701)
600 MB TOSHIBA CD ROM (5.25 IN. H/H TMX-3201B-MR/TMX3301B)
1.2 GB SEAGATE HARD DRIVE (5.25 IN. WREN VII 94601-1.2G)

APPENDIX E SA450 HARD DISK DRIVES:

40 MB MICROPOLIS HARD DRIVE (5.25 IN. 1304EM)
40 MB TOSHIBA HARD DRIVE (5.25 IN. MK54FA)

- APPENDIX E 70 MB MICROPOLIS HARD DRIVE (5.25 IN. 1325M)
 (Cont.) 70 MB TOSHIBA HARD DRIVE (5.25 IN. MK56A/B)
 85 MB CDC (WREN II) HARD DRIVE (5.25 IN. WREN II 94155-85)
 85 MB MAXTOR HARD DRIVE (5.25 IN. 1085)
 190 MB MAXTOR HARD DRIVE (5.25 IN. 2190)
- APPENDIX F MISCELLANEOUS (TAPE DRIVES, SMDS', ETC...):
 9-TRACK KENNEDY TAPE DRIVE (U92-09662-703 SCSI QUAD DENSITY)
 9-TRACK PERTEC TAPE DRIVE (FS1000 SCSI .5 inch)
 9-TRACK M4 DATA TAPE DRIVE (DUAL & QUAD DENSITY)
 377 MB FUJITSU (SMD) STORAGE MODULE DRIVE (M2333K 8 inch)
- APPENDIX G SYSTEM BOARD POSITIONING:
 SYS1121, 1131, 1132, 1147, 2016, 2316, 2334, 2616, 3204/08, 3304/08,
 3404/08/16, 3604/08, 3640, 3708, 3840, 8408, 8608, & 8864's.
- APPENDIX H BACKPANEL LAYOUTS:
 SYS1132, 1147, 2316, 2334, 2616, 3204/08, 3304/08, 3404/08/16, 3604/08,
 3640, 3708, 3840, 8408, 8440, 8608, 8640, 8840 & 8864's.
- APPENDIX I CARD CAGE LAYOUTS:
 SYS1132, 1147, 2316, 2334, 2616, 3204/08, 3304/08, 3404/08/16, 3604/08,
 3640, 3708, 3840, 8408, 8440, 8608, 8640, 8840 & 8864's.
- APPENDIX J DRIVE PLACEMENT:
 SYS2016, 2316, 2334, 2616, 3204/08, 3304/08, 3404/08/16, 3604/08, 3640,
 3708, 3840, 8408, 8440, 8608, 8640, 8840 & 8864's.
- APPENDIX K TERMINALS:
 LINK, NDS KEYBOARDS, NDS GP-220, MOTOROLA TM-220, TM-228i,
 TM-3100, TM-3200.
- APPENDIX L MPC's:
 MPC100, MPC200, MPC300 DRIVE PLACEMENT

UPDATES IN 2Q91:

INDIVIDUAL CHANGES WERE NOT RECORDED. THIS IS A NEW VERSION CALLED
 MVMEBSGCNFG/D3 MANUAL

Table 3. SYSTEM/BOARD USAGE TABLE

SYSTEMS →	1121	1131	1132	1147	2016	2316	2334	2616	3204	3208	3304	3308	3404	3408	3416	3604	3608	3640	3840	8408	8608	8864		
BOARDS ↘																								
050	X	X																						
121	X																							
131CON		X																						
131DON		X																						
131XT		X			X																			
131XT-1		X																						
131DOF					X																			
132XT			X					X																
132DOF						X																		
134							X																	
141 (-1)																		X	X					
147 (-1)				X						X						X								
147A (-1)				X							X						X							
147RF									X	X														
147S-1									X				X											
147SA-1										X				X										
147SB-1															X									
181-1																					X	X		
188SP-1																							X	
188SP-3																							X	
188DP-1																							X	
188QP-1																							X	
202	X																							
204-2		X	X		X																			
204-2F		X	X		X	X		X																
205			X		X	X		X																
224-1			X	X		X	X	X			X	X				X	X	X	X			X		
224-2			X	X		X	X	X			X	X				X	X	X	X			X		
224F-1									X	X														
224F-2									X	X														
230-1																		X	X					
230-2																		X	X					
236-2																				X				

02/16/90

Table 3. SYSTEM/BOARD USAGE TABLE. (CONTINUED)

SYSTEMS →	1121	1131	1132	1147	2016	2316	2334	2616	3204	3208	3304	3308	3404	3408	3416	3604	3608	3640	3840	8408	8608	8864			
BOARDS ↙																									
236-3																					X				
288DF																							X		
320-1	X	X																							
320A					X																				
320A-1	X	X	X																						
320B					X	X	X	X										X	X						
320B-1	X	X	X	X	X	X	X	X										X							
323 (-1/-2)			X		X	X	X	X										X	X		X				
327A																		X	X	X	X	X			
330-A			X	X	X	X	X	X			X	X				X	X	X	X						
330-B			X	X	X	X	X	X			X	X				X	X	X	X						
331		X																							
332			X		X	X		X																	
332XT			X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X		
333-2			X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X		
333x.25			X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X		
335				X		X	X		X	X	X	X	X	X	X						X				
336			X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X		
350	X	X	X	X	X	X	X	X			X	X						X	X	X					
355					X			X										X	X						
360					X			X										X	X						
372				X					X	X	X	X				X	X	X	X	X	X	X			
373				X					X	X		X				X	X	X	X	X	X	X			
374				X					X	X	X	X	X	X	X	X	X	X	X	X	X	X			
393									X	X			X	X	X						X	X	X		
701A/050	X	X																							
702A/320(X)	X	X																							
705/331		X																							
705A(333)		X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X			
705B													X	X	X										
707(13X)					X	X																			
707A(13X)		X	X					X																	
SMM1442		X																							

VI

02/16/90

Table 3. SYSTEM/BOARD USAGE TABLE. (CONTINUED)

SYSTEMS →	1121	1131	1132	1147	2016	2316	2334	2616	3204	3208	3304	3308	3404	3408	3416	3604	3608	3640	3840	8408	8608	8864			
BOARDS ↘																									
710(332/XT)			X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X		
710F					X	X		X																	
712A(147)													X	X	X	X	X								
712B(147)													X	X	X	X	X								
712M(147)				X					X	X	X	X													
714M(141/18X)																		X	X	X	X	X			
715(335)				X		X	X		X	X	X	X	X	X	X					X					
716(134)							X																		
717(327A)																		X		X	X	X			
731				X			X				X	X				X	X	X	X						
732									X	X			X	X	X					X	X	X			
751(336)			X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X		
772(372)				X					X	X	X	X				X	X	X	X	X	X	X			
792(393)									X	X			X	X	X					X	X	X			
792A(393)									X	X			X	X	X					X	X	X			
820	X																								
822	X																								
823		X																							
832						X																			
833	X	X																							
834	X	X	X																						
835DT			X	X																					
836DT			X	X																					
841					X	X	X	X																	
842						X	X	X										X	X						
843																		X	X						
851					X	X	X	X																	
852				X							X	X	X	X	X	X	X	X	X	X	X	X			
853				X					X	X	X	X	X	X	X	X	X	X	X	X	X	X			
855									X	X			X	X	X										
856													X	X	X			X	X	X	X	X			
858				X					X	X	X	X	X	X	X	X	X	X	X	X	X	X			
862									X	X															

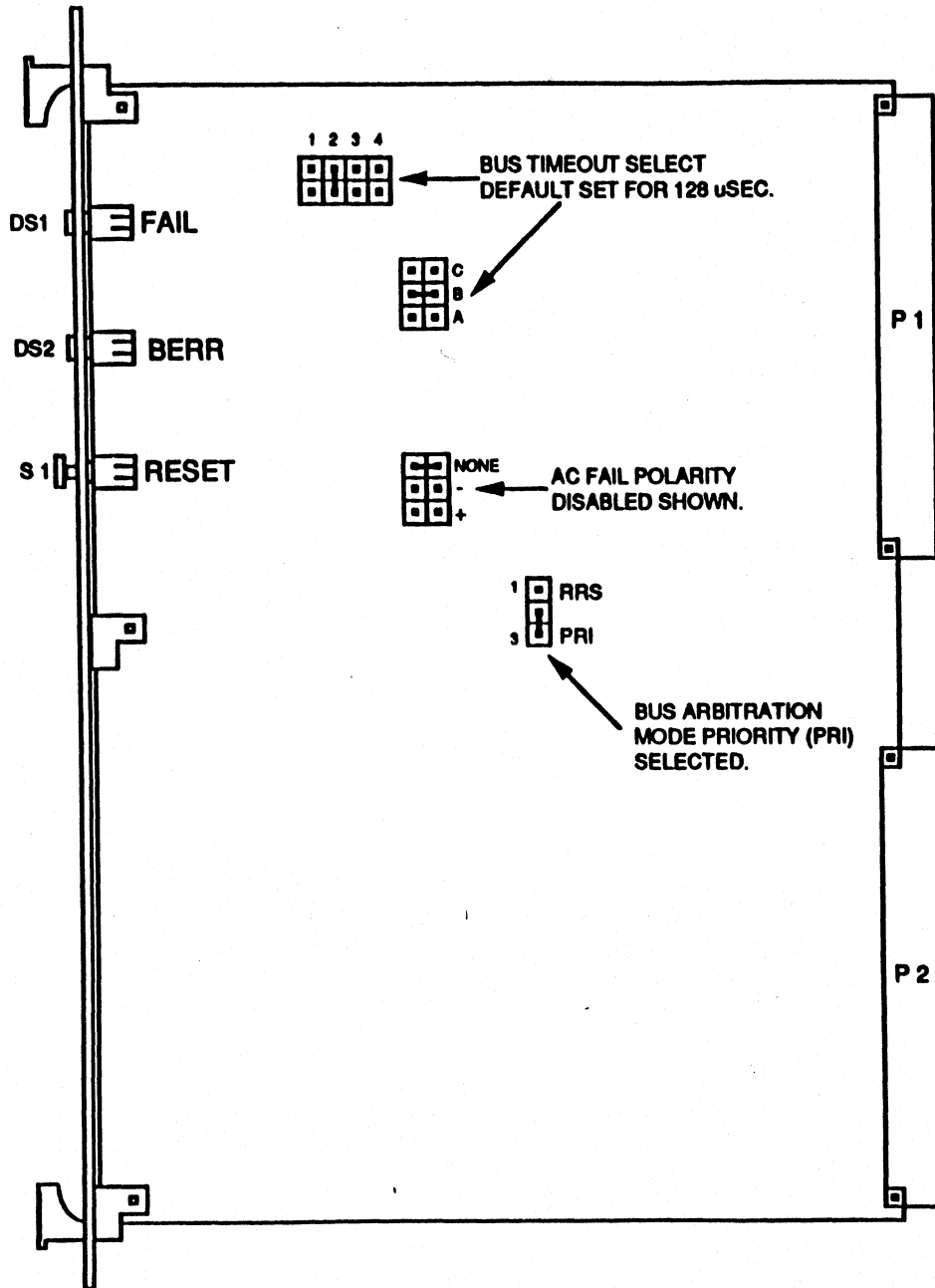
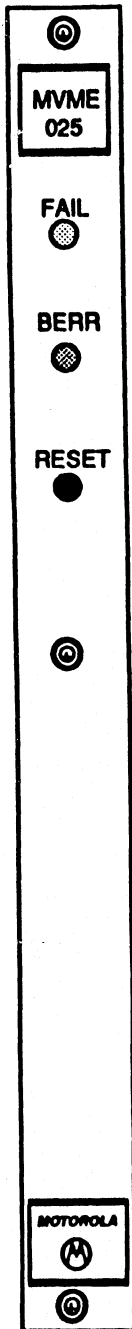
vii

09/18/90

2 GB	1 GB	512 MB	256 MB	128 MB	64 MB	32 MB	16 MB	8 MB	4 MB	2 MB	1 MB	512 KB	256 KB	128 KB	64 KB	32 KB	16 KB	8 KB	4 KB	2 KB	1 KB	512 B	256 B	128 B	64 B	32 B	16 B	8 B	4 B	2 B	1 B
A31	A30	A29	A28	A27	A26	A25	A24	A23	A22	A21	A20	A19	A18	A17	A16	A15	A14	A13	A12	A11	A10	A09	A08	A07	A06	A05	A04	A03	A02	A01	A00
8	4	2	1	8	4	2	1	8	4	2	1	8	4	2	1	8	4	2	1	8	4	2	1	8	4	2	1	8	4	2	1

BIT MAP ADDRESSING WORKSHEET

SECTION 0

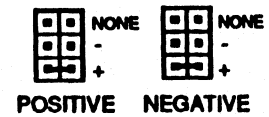


PART NUMBERS:

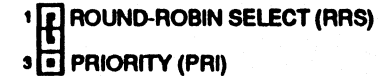
MVME025 01-W3316B01 76432704

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

AC-FAIL POLARITY



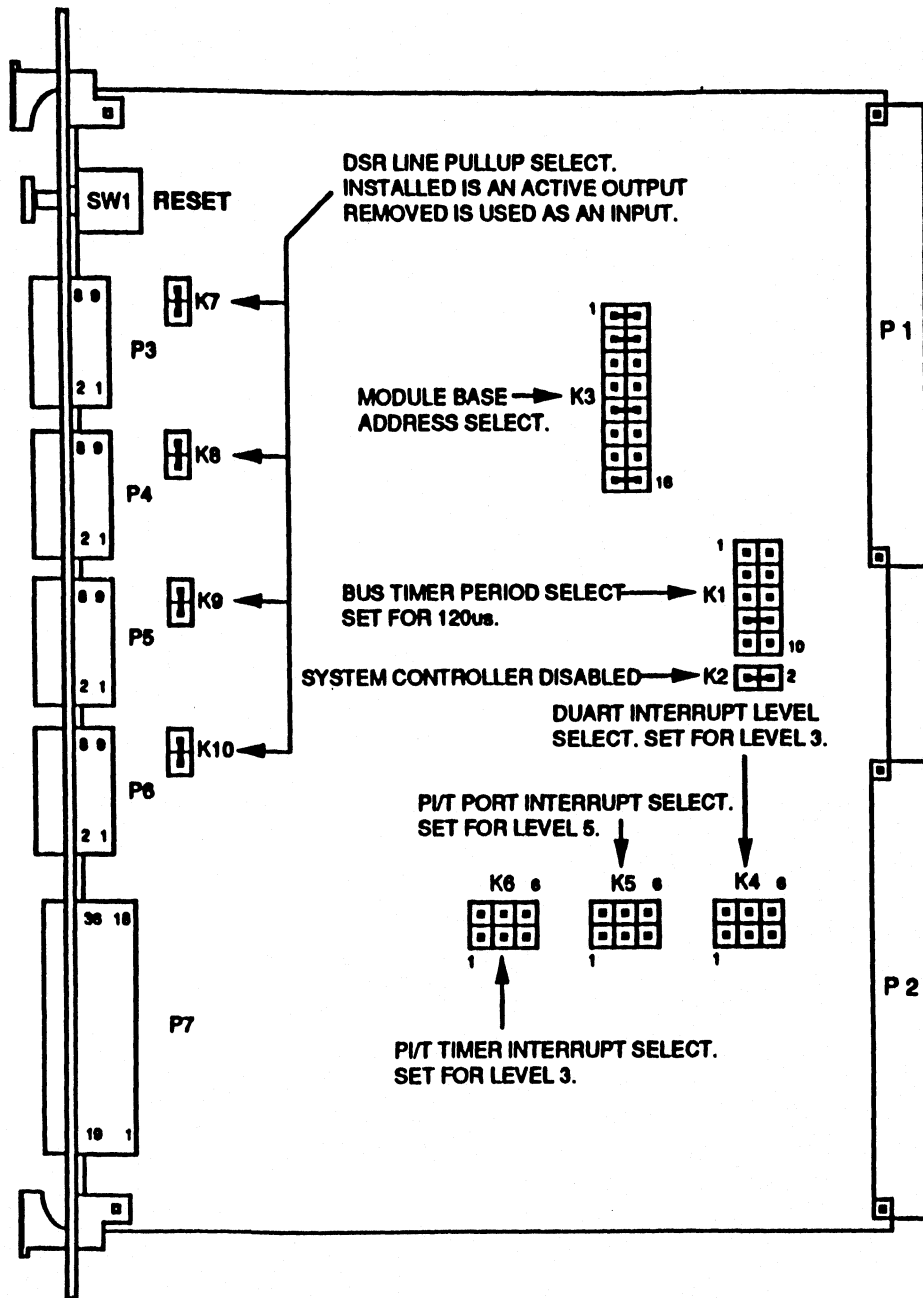
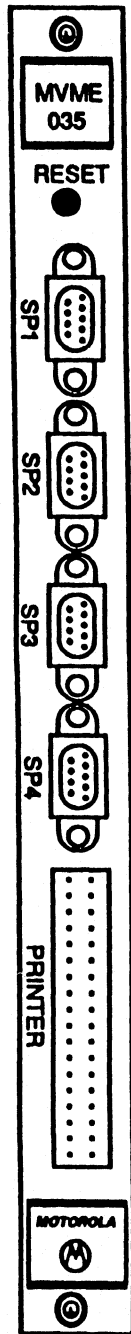
BUS ARBITRATION MODE



BUS TIMEOUT SELECT

1 2 3 4				A B C		
1	0	0	0	4	64	1024
2	0	0	1	8	128	2048
3	0	1	0	16	256	4096
4	1	0	0	32	512	8192

09/11/89



NOTE 1: P3, P4, P5 AND P6 ARE 9-PIN DIN CONNECTORS FOR 4-SERIAL PORTS.

NOTE 2: P7 IS A 36-PIN CENTRONICS PRINTER PORT CONNECTOR.

PART NUMBER:

MVME035 01-W3530B01 76435364 US BUILD

MVME035 01-G3042M01 76435364 EURO BUILD

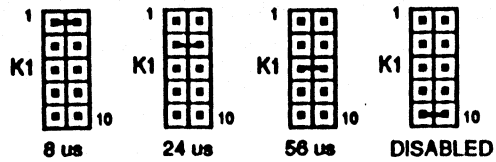
MVME035 01-G3042M02 76433049 EURO BUILD

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

11/29/89

**MVME035
SYSTEM CONTROLLER
WITH
SERIAL/PARALLEL I/O
PAGE 1 OF**

BUS TIMER PERIOD SELECT



SYSTEM CONTROLLER

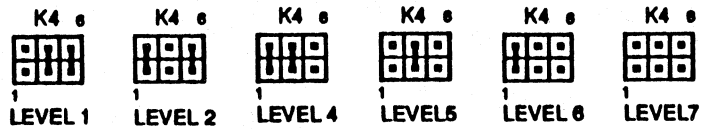


MODULE PORT ADDRESS SELECT

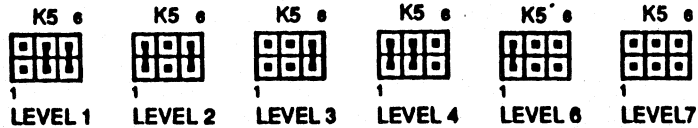
K3	KS CONNECTION	BASE ADDRESS OFFSET
1	A15 1-2	REMOVED \$8000
	A14 3-4	REMOVED \$4000
	A13 5-6	REMOVED \$2000
	A12 7-8	REMOVED \$1000
	A11 9-10	REMOVED \$0800
	A10 11-12	REMOVED \$0400
	A09 13-14	REMOVED \$0200
	A08 15-16	REMOVED \$0100

AXX-0, IF JUMPER IS INSTALLED
 AXX-1, IF JUMPER IS REMOVED

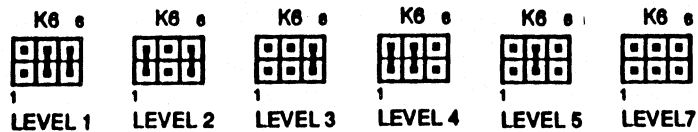
DUART INTERRUPT LEVEL SELECT



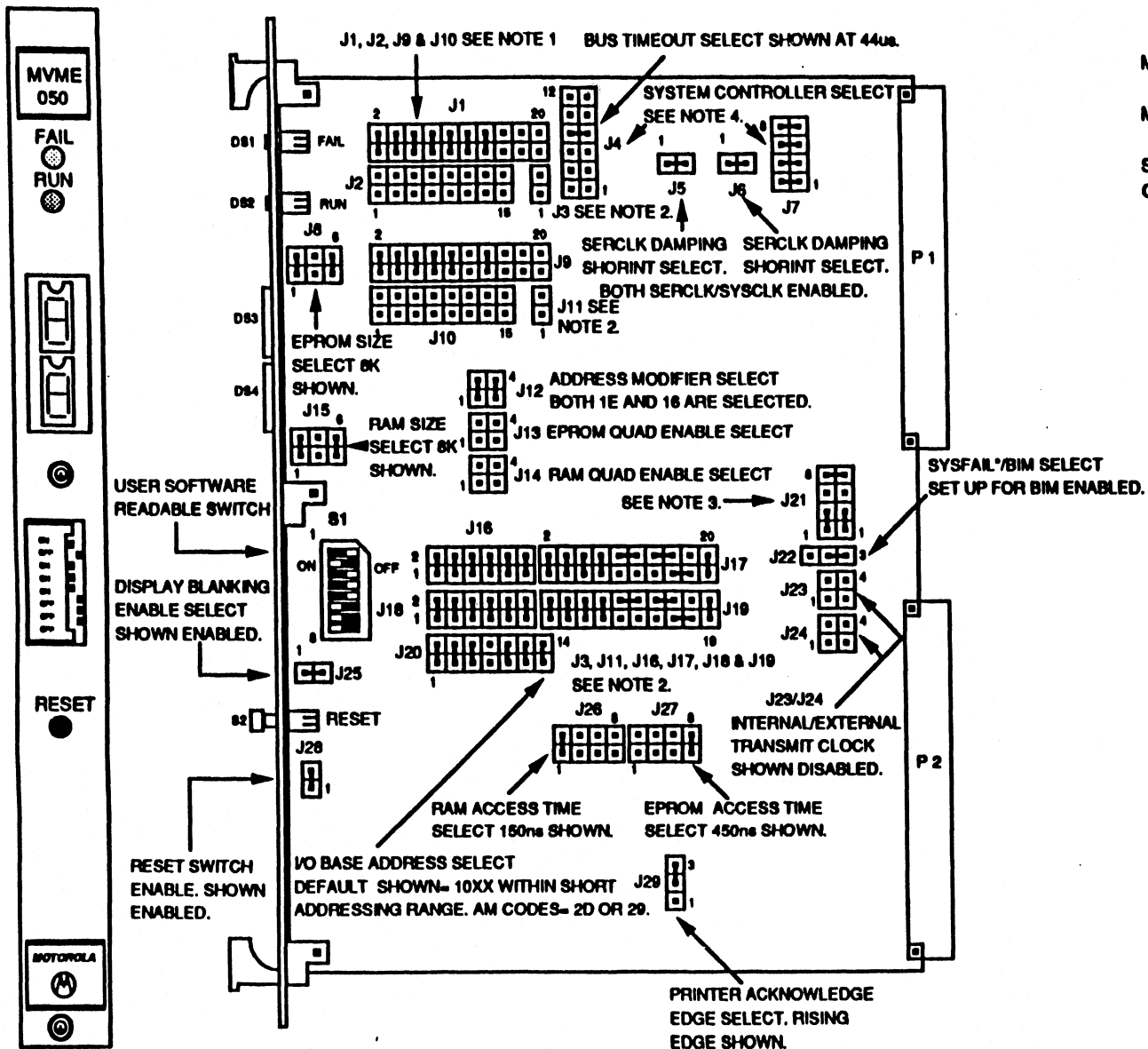
PI/T PORT INTERRUPT SELECT



PI/T TIMER INTERRUPT SELECT



09/11/89



PART NUMBERS:

MVME050 01-W3292B01 96010821 STAND-ALONE PWB

MVME050 01-W3452B01 76432587 SYSTEMS PWB

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: J1 & J2 ARE SET UP FOR \$FFE0000 DEFAULT AND J9 & J10 ARE SET FOR \$FFE8000 DEFAULT. JUMPER IN EQUALS ADDRESS LINE LOW. J1 & J2 ARE FOR EPROM SELECT AND J9 & J10 ARE FOR RAM SELECT.

NOTE 2: J3, J11, J16, J17, J18 & J19 ARE USED IN CONJUNCTION WITH EACH OTHER TO SET UP EITHER RAM OR EPROM CONFIGURATION SIZE SELECT. SEE PAGE 3 OF 3 FOR ALL THE SIZES AND POSSIBILITIES. THE BOARD IS SET UP FOR 8K X 8 RAM DEVICES ON THIS PAGE FOR BOTH QUAD 1 AND QUAD 2.

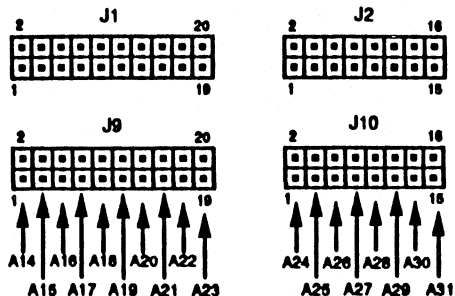
NOTE 3: ON J21 JUMPERS 7-8 ENABLE BATTERY BACKUP. TIME-OF-DAY CLOCK IS SET UP TO SYSTEM POWER WHILE SYSTEM IS ON (2-4) AND FROM BATTERY BACKUP WHEN SYSTEM IS OFF (1-3).

NOTE 4: ON J7, IF THE MODULE IS NOT SYSTEM CONTROLLER, ALL 4 JUMPERS MUST BE REMOVED AND J4 PINS 11-12 JUMPED. IF THE BUS ARBITER (J7 3-4) IS DISABLED, BUS ARBITRATION SIGNALS MUST BE CONFIGURED ON THE BACKPLANE AS THOUGH THE CONTROLLER WAS AN EMPTY SLOT. THE IACK BYPASS JUMPERS MUST REMAIN OPEN. IF VME050 IS A SLAVE, INSTALL J4 11 - 12 AND REMOVE OTHER JUMPERS.

NOTE 5: ACTIVE PART OF SWITCH IS DARKENED AREA.

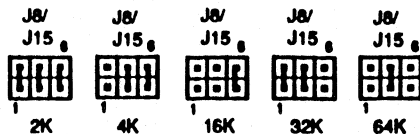
11/28/89

L 1/RAM BASE ADDRESS SELECT



SEE SELECTION ON PAGE 1 FOR AN EXAMPLE

EPROM/RAM SELECT



J8 IS EPROM SELECT AND J15 IS RAM SELECT

EPROM/RAM QUAD ENABLE SELECT



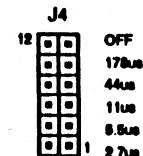
EPROM/RAM QUAD 1= XU8, XU11, XU16 & XU19
EPROM/RAM QUAD 2= XU25, XU28, XU33 & XU36

ADDRESS MODIFIER SELECT ENABLE

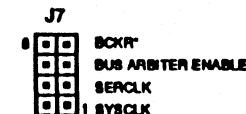


AM1E ENABLE SELECT (EPROM QUAD1)
AM16 ENABLE SELECT (EPROM QUAD 2)

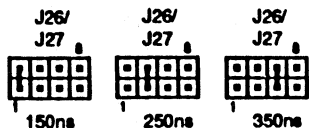
BUS TIMEOUT SELECT



SYSTEM CONTROLLER SELECT

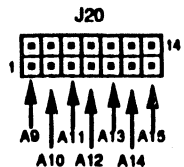


EPROM/RAM ACCESS TIME SELECT



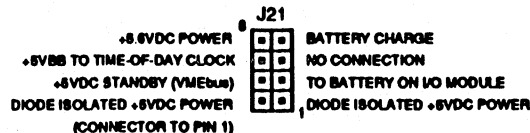
J26 IS RAM ACCESS TIME SELECT
J27 IS EPROM ACCESS TIME SELECT

I/O BASE ADDRESS SELECT



812K BYTE BOUNDARIES

TIME-OF-DAY CLOCK SELECT & BATTERY CHARGE HEADER



SET UP TO POWER TIME-OF-DAY CLOCK FROM +5VDC STANDBY ON VMEbus.

SYSFAIL* OR INTERRUPT SOURCE SELECT



1-2 ENABLES SYSFAIL*.
2-3 ENABLES BUS INTERFACE MODULE (BIM) FOR A SOFTWARE GLOBAL INTERRUPT.

SERIAL PORT 1 AND 2 INTERNAL/EXTERNAL TRANSMIT CLOCK SELECT



1-2 INTERNAL TXC CLOCK.
3-4 EXTERNAL TXC CLOCK.
J23 IS SERIAL PORT 1.
J24 IS SERIAL PORT 2.

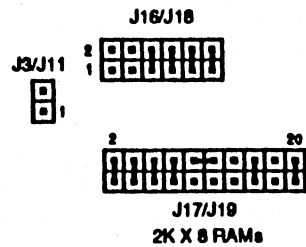
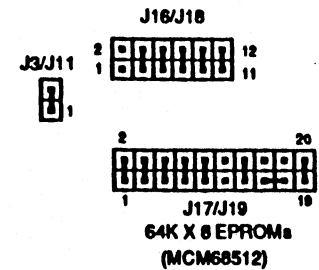
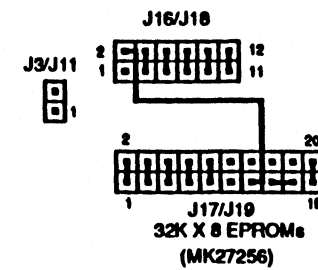
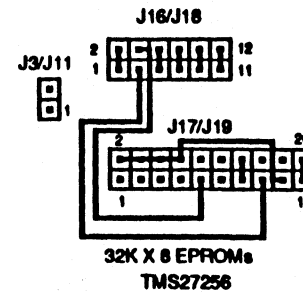
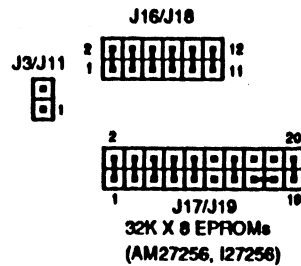
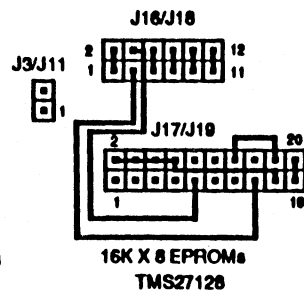
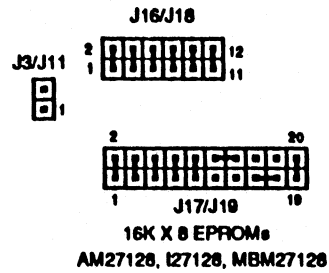
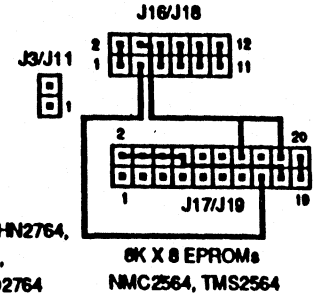
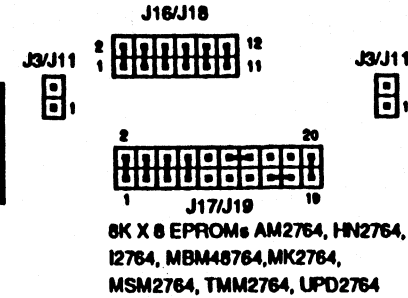
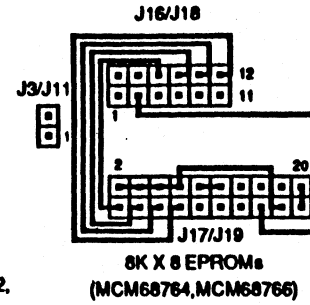
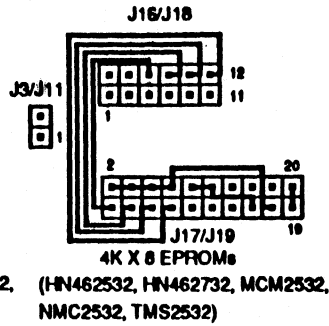
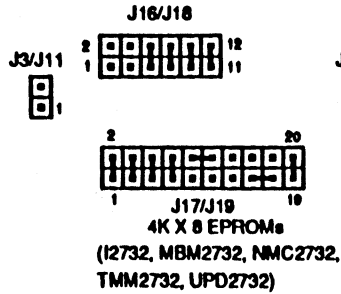
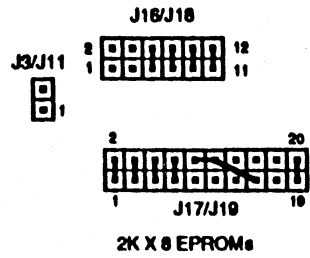
PRINTER ACKNOWLEDGE EDGE SELECT



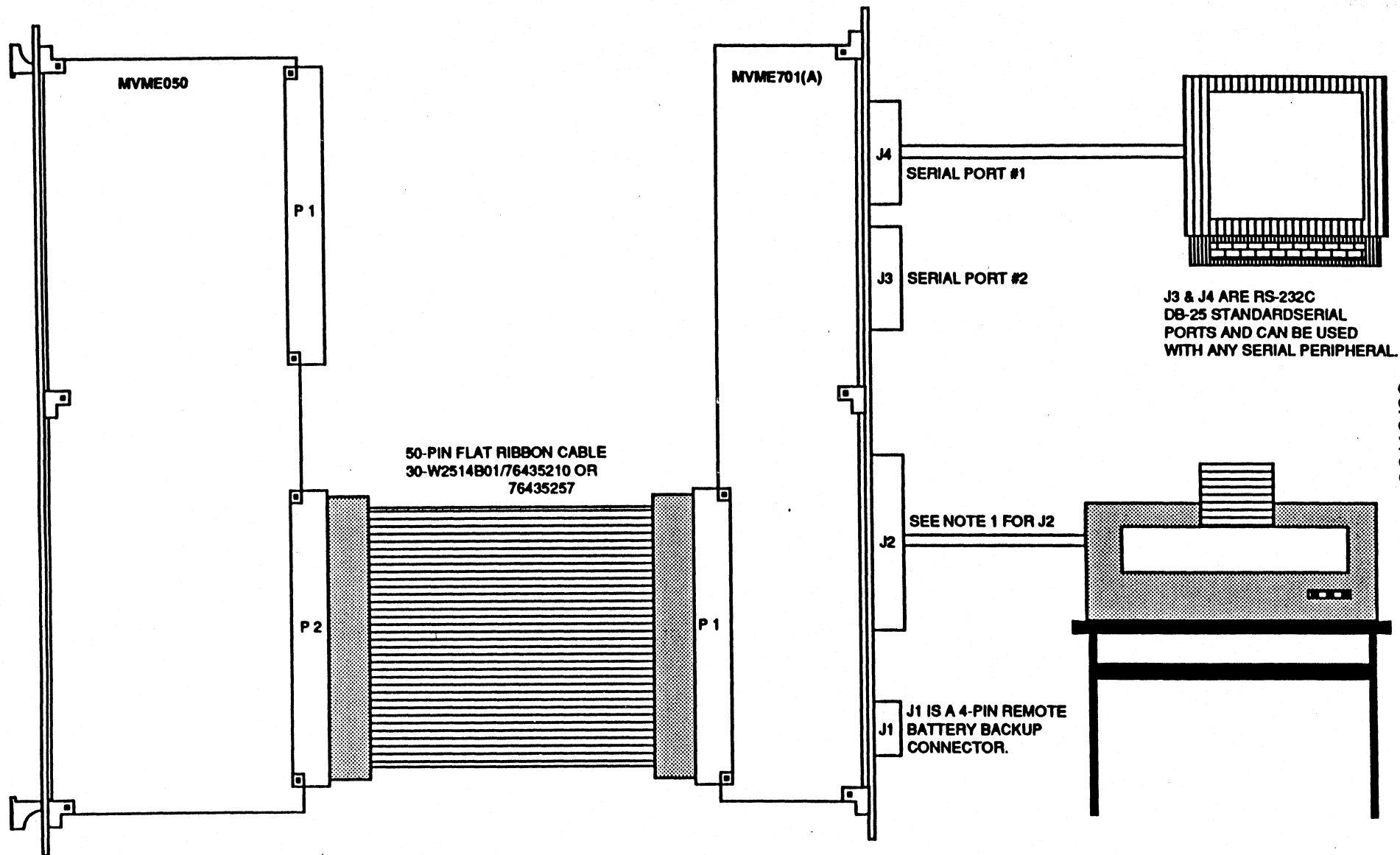
1-2 RISING EDGE
2-3 FALLING EDGE

09/11/89

EPROM/RAM CONFIGURATION HEADERS



09/11/89

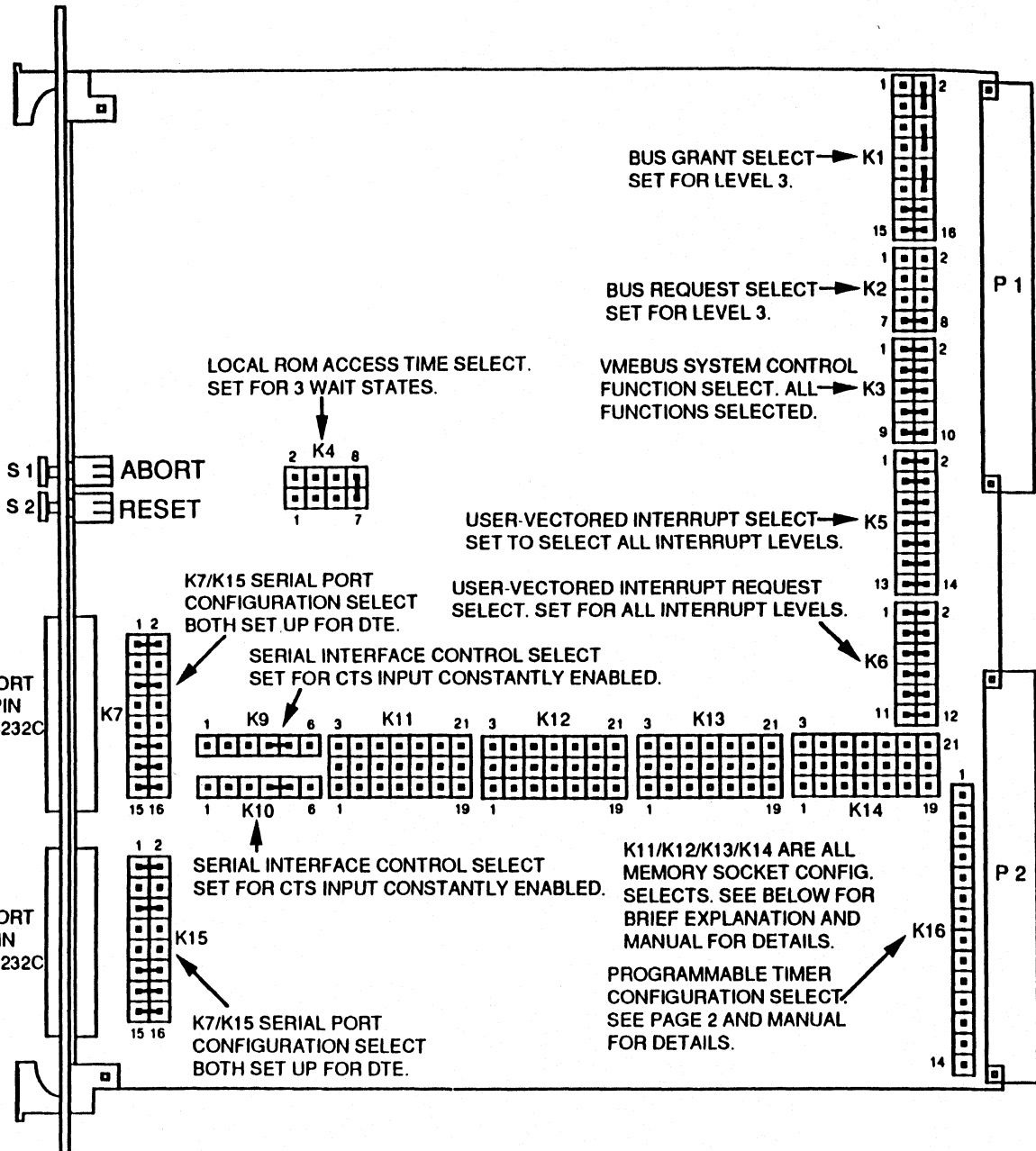
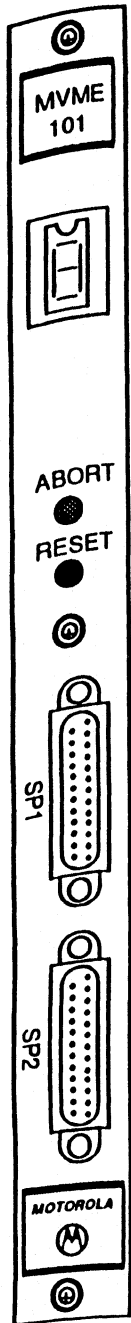


NOTE 1: VME701 HAS A 50-PIN CENTRONICS COMPATIBLE CONNECTOR. VME701A HAS A 36-PIN CENTRONICS COMPATIBLE CONNECTOR.

J3 & J4 ARE RS-232C DB-25 STANDARD SERIAL PORTS AND CAN BE USED WITH ANY SERIAL PERIPHERAL.

09/07/90

SECTION 1



NOTE 1: COULDN'T CHECK PROPER JUMPER CONFIGURATION.
HAD NO MVME101 PROCESSORS IN THE LAB.

PART NUMBERS:

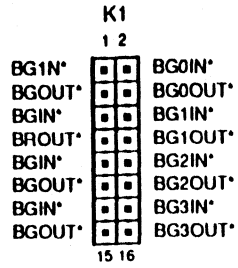
MVME101 01-G3012M01 76431501 EURO BUILD

MVME101 01-W3529B01 76431501 US BUILD

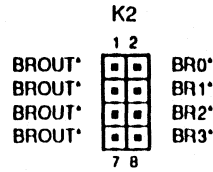
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

09/11/89

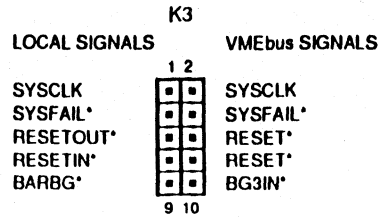
VMEBUS GRANT SELECT



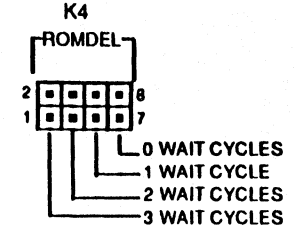
BUS REQUEST SELECT



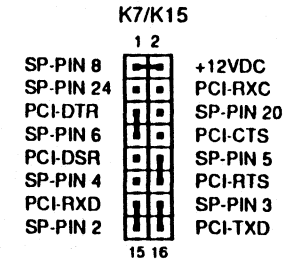
VMEBUS SYSTEM CONTROL FUNCTION SELECT



LOCAL ROM ACCESS TIME SELECT

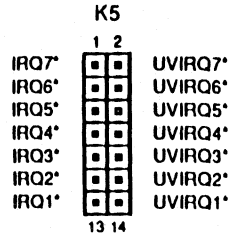


SERIAL PORT CONFIGURATION SELECT

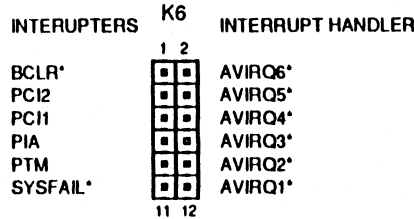


6-8 DSR/CTS CONTROLLED BY DTE
 9-11 DSR/CTS CONTROLLED BY DTE
 7-8 DSR/CTS CONTROLLED BY DCE
 9-10 DSR/CTS CONTROLLED BY DCE
 3-4 OPEN ASYNCHRONOUS DATA TRANSMISSION
 3-4 JUMPED SYNCHRONOUS DATA TRANSMISSION
THIS IS A DCE JUMPER CONFIGURATION.

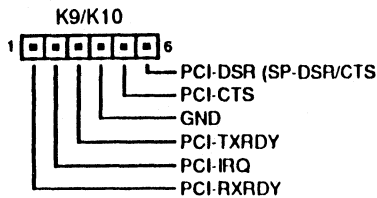
USER-VECTORED INTERRUPT REQUEST SELECT



AUTO-VECTORED INTERRUPT REQUEST SELECT

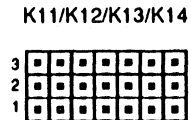


SERIAL INTERFACE CONTROL SELECT



1-2 INTERRUPT ACTIVATED BY RXRDY
 2-3 INTERRUPT ACTIVATED BY TXRDY
 1-2-3 INTERRUPT ACTIVATED BY RXRDY AND TXRDY
 4-5 CTS INPUT CONSTANTLY ENABLED
 5-6 CTS INPUT ENABLED BY PERIPHERAL DEVICE

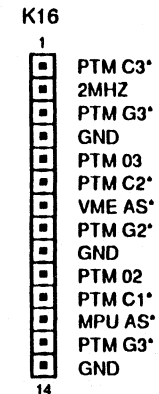
MEMORY SOCKETS CONFIGURATION SELECT



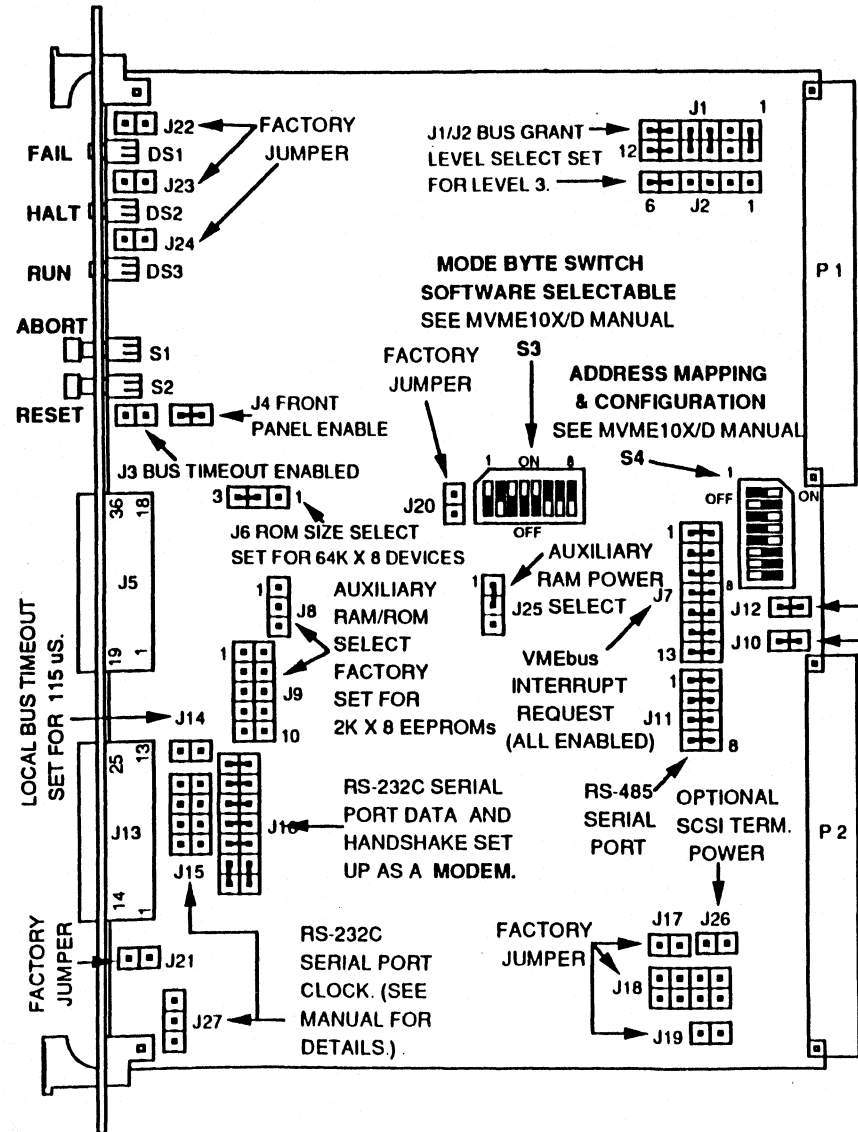
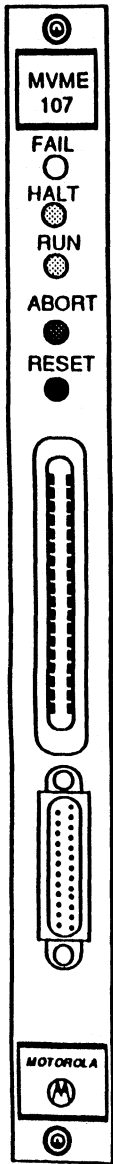
K11 = U53 MEM1U EVEN BYTES
 U61 MEM1L ODD BYTES
 K12 = U54 MEM2U EVEN BYTES
 U62 MEM2L ODD BYTES
 K13 = U55 MEM3U EVEN BYTES
 U63 MEM3L ODD BYTES
 K14 = U56 MEM4U EVEN BYTES
 U64 MEM4L ODD BYTES

THIS IS TOO COMPLEX TO PUT ON THIS SHEET
 REFER TO MANUAL FOR DETAILED EXPLANATIONS.

PROGRAMMABLE TIME CONFIGURATION SELECT



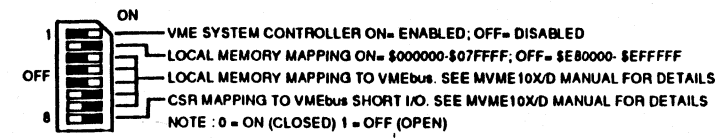
09/11/89



PART NUMBERS:

MVME104	01-W3450B01	76435378	W/ I/O CHANNEL INTERFACE
MVME105	01-W3438B01	76435368	BASIC CPU
MVME106	01-W3438B02	76435367	W/ DISK CONTROLLER THAT SUPPORTS FOUR 5 1/4" FLOPPY DRIVES.
MVME107	01-W3438B03	76435377	/S FULL SCSI BUS SUPPORT

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

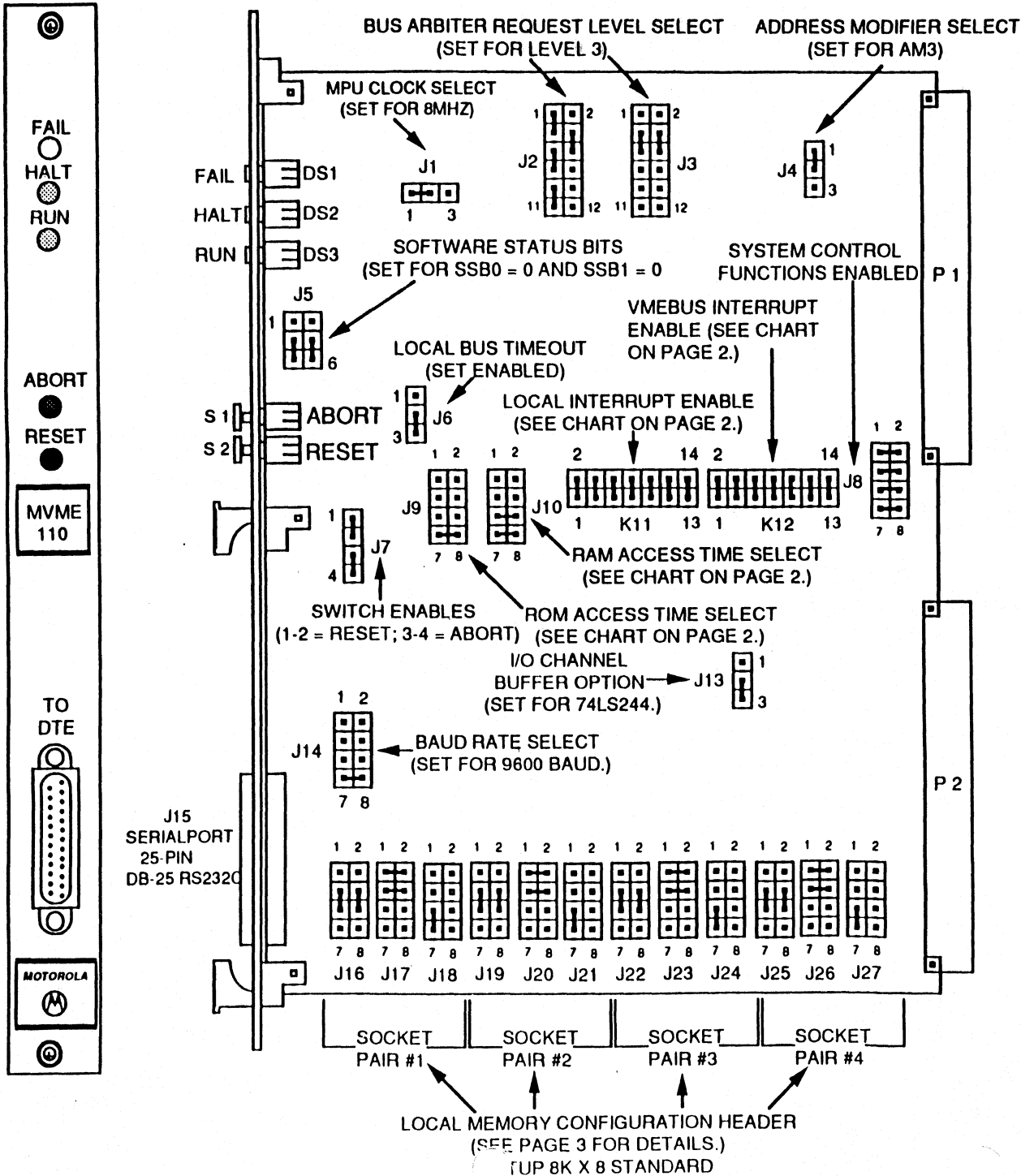


09/11/89

BUS GRANT LEVEL SELECT ALL VERSIONS	VMEbus TIMEOUT (ENABLED) ALL VERSIONS	FRONT PA. SWITCH ENABLE ALL VERSIONS	ROM SIZE SELECT ALL VERSIONS	VMEbus INTERRUPT REQUEST(ENABLED) ALL VERSIONS	AUXILIARY RAM/ROM SIZE SELECT ALL VERSIONS	FACTORY JUMPER VME105/106	FACTORY JUMPER VME107
---	---	--	--	--	--	---	-------------------------------------

RS-232C SERIAL PORT ALL VERSIONS	SYSFAIL INTERRUPT ENABLED ALL VERSIONS	LOCAL BUS TIMEOUT ENABLED ALL VERSIONS	RS-232C SERIAL PORT (TO TERMINAL) ALL VERSIONS	FACTORY JUMPER VME105/106	FACTORY JUMPER VME107	FACTORY JUMPER VME105/106	FACTORY JUMPER VME107	FACTORY JUMPER VME105/106	FACTORY JUMPER VME107
--	--	--	--	---	-------------------------------------	---	-------------------------------------	---	-------------------------------------

FACTORY JUMPER VME105/107	FACTORY JUMPER VME106	FACTORY JUMPER VME105/106	FACTORY JUMPER VME107	FACTORY JUMPER VME104/106	FACTORY JUMPER VME105/107	FACTORY JUMPER VME104/105	FACTORY JUMPER VME106/107	FACTORY JUMPER ALL VERSIONS	AUXILIARY RAM POWER SELECT (+5V) ALL VERSIONS	OPTIONAL SCSI TERMINATOR POWER VME105/106	OPTIONAL SCSI TERMINATOR POWER
---	-------------------------------------	---	-------------------------------------	---	---	---	---	---	---	---	---



PART NUMBERS:

MVME110-1 01-W3047B01 76430442

SMM1377 01-W3047B02 76435338

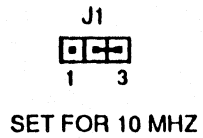
SEE CURRENT RESISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

NOTE 1: BUS ARBITRATION REQUEST LEVEL 0 IS USED
WHENEVER THE VME110 IS USED AS SYSTEM
CONTROLLER.

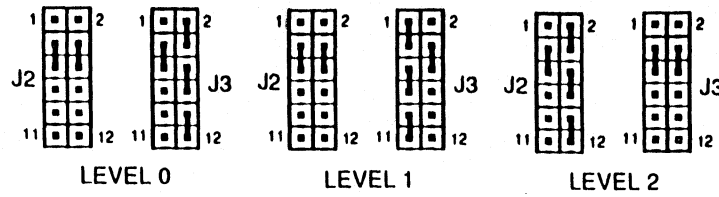
09/07/90

MVME110
VME module
MONOBOARD
MICROCOMPUTER
PAGE 1 OF 3

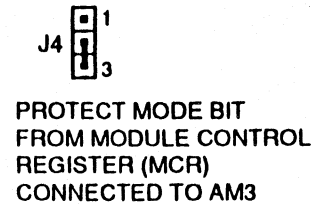
MPU CLOCK SPEED SELECT



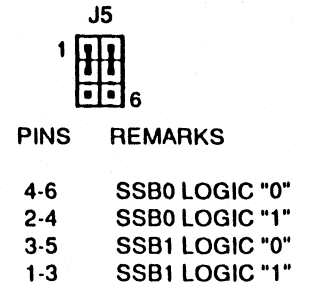
BUS ARBITRATION REQUEST LEVEL SELECT



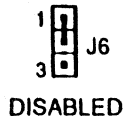
ADDRESS MODIFIER SELECT



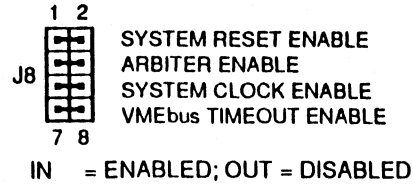
SOFTWARE STATUS BITS



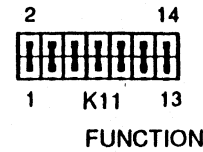
LOCAL BUS TIMEOUT



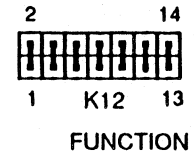
SYSTEM CONTROL FUNCTIONS ENABLE



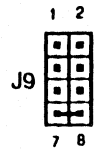
LOCAL INTERRUPT ENABLE



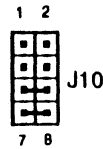
VMEbus INTERRUPT ENABLE



ROM ACCESS TIME SELECT



RAM ACCESS TIME SELECT



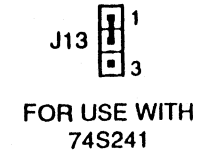
PINS	8MHZ	10 MHZ	WAIT CYCLE
1-2	0-165ns *0-220ns	0-100ns *0-165ns	0
3-4	165-290ns *220-345ns	100-200ns *165-265ns	1
5-6	290-415ns *345-470ns	200-300ns *265-365ns	2
7-8	415-550ns *470-595ns	300-400ns *365-465ns	3

PINS	8MHZ	10 MHZ	WAIT CYCLE
1-2, 3-4	0-165ns	0-100ns	0
5-6, 7-8	165-290ns	100-200ns	1

PIN	FUNCTION
1-2	SYSTEM FAIL TO INTERRUPT LEVEL 7
3-4	PROGRAMMABLE TIMER TO INTERRUPT LEVEL 6
5-6	ACIA TO INTERRUPT LEVEL 5
7-8	I/O INTERRUPT 4 TO INTERRUPT LEVEL 4
9-10	I/O INTERRUPT 3 TO INTERRUPT LEVEL 3
11-12	I/O INTERRUPT 2 TO INTERRUPT LEVEL 2
13-14	I/O INTERRUPT 1 TO INTERRUPT LEVEL 1

PIN	FUNCTION
13-14	INTERRUPT LEVEL 1
11-12	INTERRUPT LEVEL 2
9-10	INTERRUPT LEVEL 3
7-8	INTERRUPT LEVEL 4
5-6	INTERRUPT LEVEL 5
3-4	INTERRUPT LEVEL 6
1-2	INTERRUPT LEVEL 7

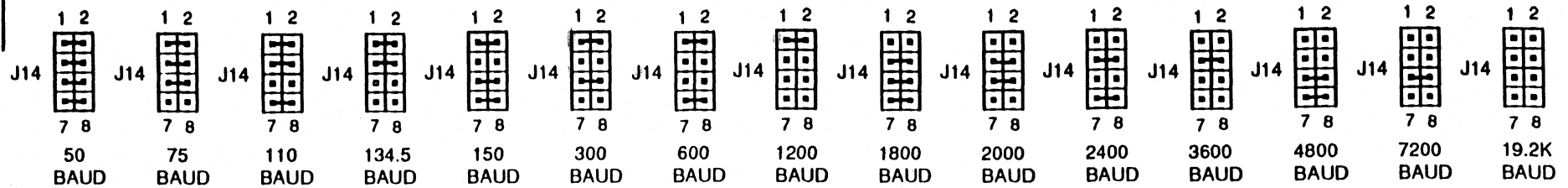
I/O CHANNEL BUFFER OPTION



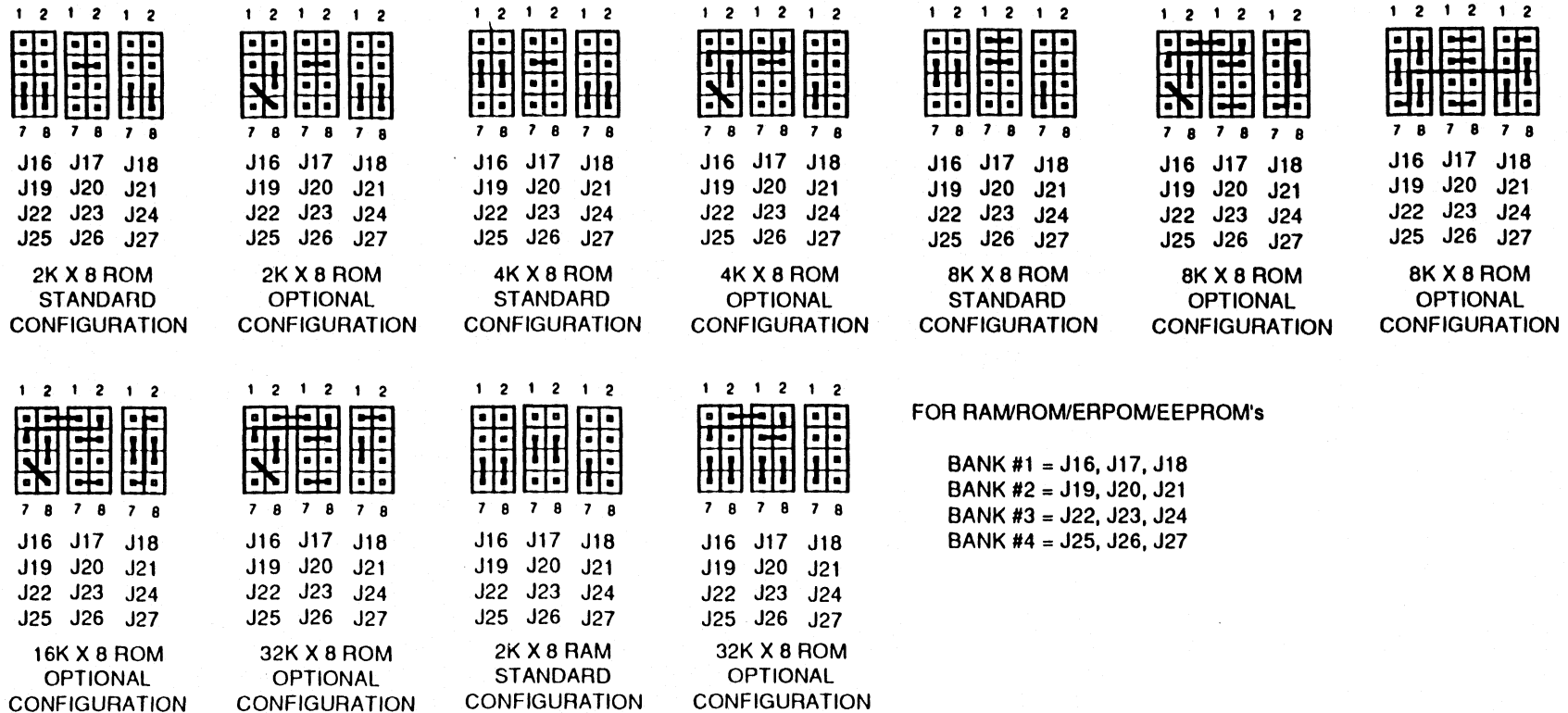
09/07/90

* USED FOR ROM CIRCUITS WHICH REQUIRE AN OPTIONAL MEMORY CONFIGURATION (WIRE-WRAP) JUMPER(S).

BAUD RATE SELECT HEADER



LOCAL MEMORY CONFIGURATION HEADERS



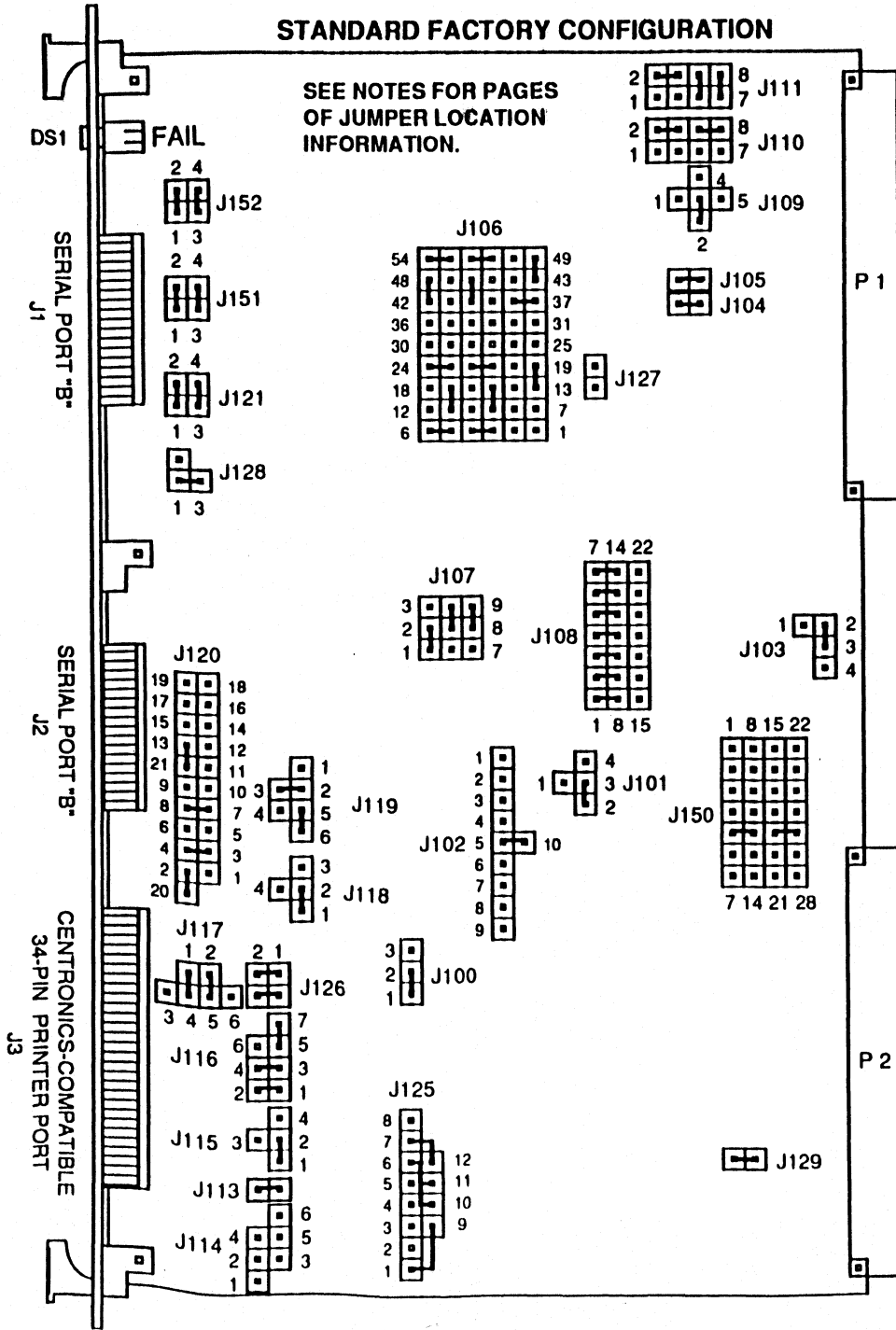
FOR RAM/ROM/ERPOME/EPROM's

- BANK #1 = J16, J17, J18
- BANK #2 = J19, J20, J21
- BANK #3 = J22, J23, J24
- BANK #4 = J25, J26, J27

09/07/90

STANDARD FACTORY CONFIGURATION

SEE NOTES FOR PAGES OF JUMPER LOCATION INFORMATION.



PART NUMBERS:

MVME115M 01-W3317B02 76432703

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

MC68230 PIN	PRINTER LINE	J3 PIN
H2	PRINTER ACKNOWLEDGE	19
PC0	DATA STROBE	1
PB0	BUSY	21
PB1	CHECK (PAPER OUT)	23
PB2	SELECT	25
PC1	PI	29
PC3	FAULT	28

J1 OR J2 PIN	RS-232 PIN	FUNCTION
1	1	PROTECTIVE GROUND
3	2	TRANSMIT DATA (TXD)
5	3	RECEIVE DATE (RXD)
11	6	DATA SET READY (DSR)
13	7	SIGNAL GROUND
14	20	DATA TERMINAL READY (DTR)

NOTE 1: SEE PAGE 2 FOR DETAILS ON J100, J101, J102, J103, J104, J105, 106 AND J107.

NOTE 2: SEE PAGE 3 FOR DETAILS ON J108, J109, J110, J111, J112 NOT USED, J113, J114, J115, J116, J117, J118, J119, J120 AND J126. J122 - J124 NOT USED.

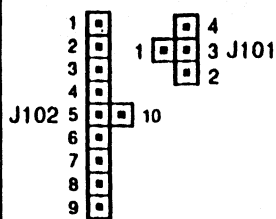
NOTE 3: SEE PAGE 4 FOR J121, J125, J127, J128, J129, J130 - J149 NOT USED, J150, J151, AND J152.

**MVME115M
24-BIT VMEbus
MICROCOMPUTER
PAGE 1 OF 4**

09/11/89

STROBE TIMING

USE THESE JUMPERS IF U59, U60, U61, U65, U66 AND U67 ARE 74S645-1 PARTS

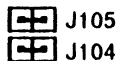


MMU T10 (in nanoseconds)	8 MHZ		10 MHZ	
	J102	J101	J102	J101
0 - 40	10 - 3	3 - 2	0 - 45	10 - 4 3 - 2
40 - 102	10 - 4	3 - 2	45 - 95	10 - 5 3 - 2
102 - 165	10 - 5	3 - 2	95 - 145	10 - 6 3 - 2
165 - 227	10 - 6	3 - 2	145 - 195	10 - 7 3 - 2

USE THESE JUMPERS IF U59, U60, U61, U65, U66 AND U67 ARE 74F245 PARTS

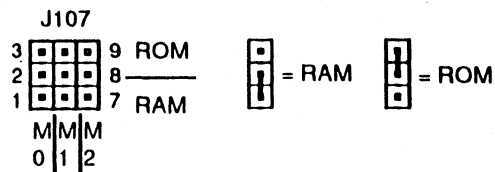
MMU T10 (in nanoseconds)	8 MHZ		10 MHZ	
	J102	J101	J102	J101
0 - 10	10 - 1	3 - 4	0 - 15	10 - 2 3 - 1
10 - 22	10 - 2	3 - 2	15 - 40	10 - 3 3 - 2
22 - 47	10 - 2	3 - 1	40 - 65	10 - 3 3 - 1
47 - 72	10 - 2	3 - 4	65 - 90	10 - 4 3 - 2
72 - 85	10 - 3	3 - 2	90 - 115	10 - 4 3 - 1
85 - 110	10 - 3	3 - 1	115 - 140	10 - 5 3 - 2
110 - 135	10 - 3	3 - 4	140 - 165	10 - 5 3 - 1
135 - 147	10 - 4	3 - 2	165 - 190	10 - 6 3 - 2
147 - 172	10 - 4	3 - 1	190 - 215	10 - 6 3 - 1
172 - 197	10 - 4	3 - 4		
197 - 210	10 - 5	3 - 2		
210 - 235	10 - 5	3 - 1		
235 - 260	10 - 5	3 - 4		

BUS ERROR HANDLING

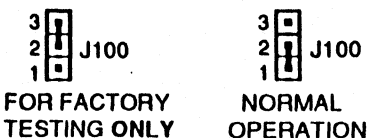


J104 = INSTALLED TO ABORT BUS CYCLES LONGER THAN 256 CPU CLOCKS.
 J105 = INSTALLED TO DETECT VMEbus ERRORS.

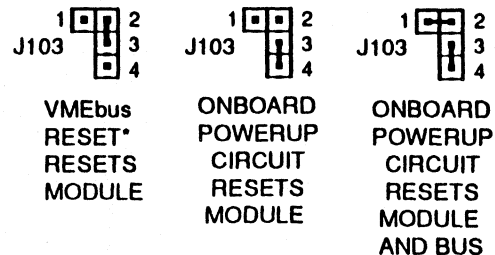
ONBOARD MEMORY ROM/RAM SELECTION



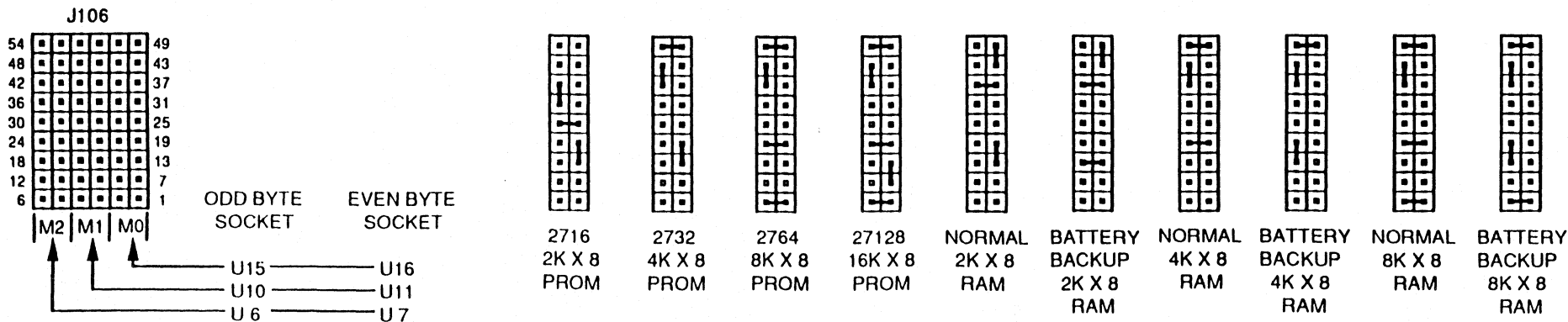
STATE MACHINE CLOCK SPEED



RESET SIGNAL

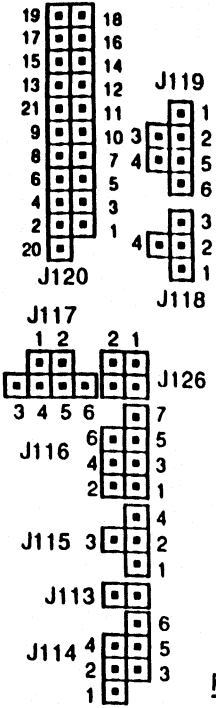


ONBOARD MEMORY CHIP TYPE SELECTION HEADERS. SEE LEFTMOST FIGURE FOR BANK DESCRIPTION.



09/11/89

PARALLEL PORT CONFIGURATION



PORT A	JUMPER	JUMPER	JUMPER
TRANSCEIVER	HEADER	PINS	INSTALLED
ENABLE	J117	4 - 1	ENABLED
DIRECTION	J117	5 - 2	OUTPUT
	J117	5 - 6	INPUT
	J117	5 - 3	BI-DIRECTIONAL

PORT B	JUMPER	JUMPER	JUMPER
TRANSCEIVER	HEADER	PINS	INSTALLED
ENABLE	J119	2 - 3	ENABLED
DIRECTION	J119	5 - 4	OUTPUT
	J119	5 - 6	INPUT
	J119	5 - 1	BI-DIRECTIONAL

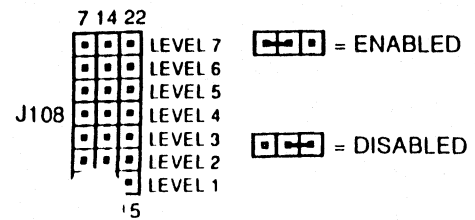
MC68230	JUMPER	JUMPER
LINE	HEADER	PINS
H1	J116	5
H2	J116	3
H3	J116	1
H4	J118	2

FUNCTION OF PC2	JUMPER	JUMPER
	HEADER	PINS
TRANSCEIVER	J119	2 - 3
ENABLE	J119	5 - 4
DIRECTION	J119	5 - 6
	J119	5 - 1

EXTRAS:

- PC2/TIN OF THE MC68030 IS SOFTWARE CONFIGURABLE THRU J114 PIN 2.
- PC3/TOUT OF THE MC68230 IS SOFTWARE CONFIGURABLE THRU J126 PIN3.
- PC4 OF THE MC68230 IS DEDICATED AS SYSFAIL OUTPUT.
- PC5/PIRQ OF THE MC68230 IS SOFTWARE CONFIGURABLE THRU J126 PIN 1.
- PC6/PIACK OF THE MC68230 IS DEDICATED TO PORT-INTERRUPT-ACKNOWLEDGE FUNCTION.
- PC7/TIACK OF THE MC68230 IS DEDICATED TO THE TIMER-INTERRUPT-ACKNOWLEDGE FUNCTION.
- J120 BRINGS IN MANY OF THE PINS FOR THE J3 PRINTER PORT. THE PRINTER PORT IS CENTRONICS-COMPATIBLE. SEE PAGE 1 FOR PINOUTS.

INTERRUPT HANDLER



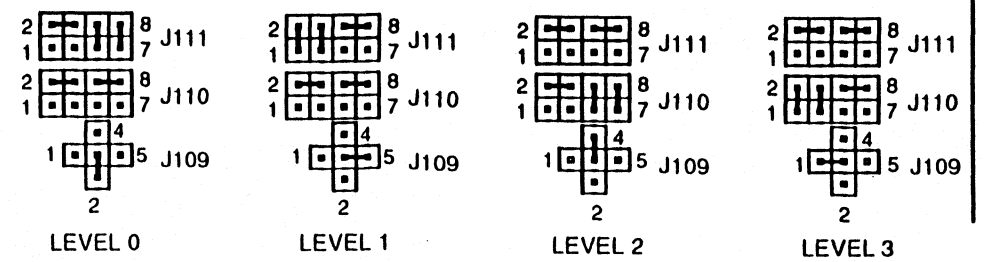
FUNCTION OF PC0	JUMPER	JUMPER
	HEADER	PINS
GENERAL-PURPOSE OUTPUT	J115	2 - 1
GENERAL-PURPOSE INPUT	J115	2 - 4
PORT A/B DIRECTION CONTROL	J115	2 - 3

FUNCTION OF PC1	JUMPER	JUMPER
	HEADER	PINS
GENERAL-PURPOSE OUTPUT	J113	1 - 2
GENERAL-PURPOSE INPUT	J114	3 - 6

USUAL INTERRUPT SOURCE		
JUMPER	HEADER	FUNCTION
J116	2	SERIAL (SCN2681) INTERRUPT
J118	1	SYSFAIL INTERRUPT

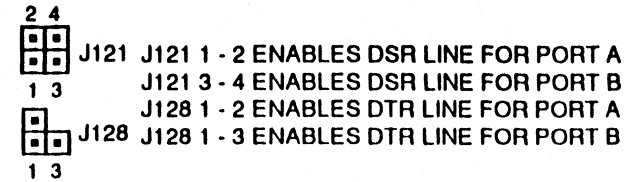
CONNECT		
JUMPER	JUMPER	FUNCTION
HEADER	HEADER	
J115	2 TO J115	1 DATA STROBE (J3 - 1) PC0
J116	3 TO J116	4 PRINTER ACKNOWLEDGE (J3 - 19) H2
J126	1 TO J126	2 PORT INTERRUPT REQUEST PC1 (PIRQ)
J113	1 TO J113	2 INTERMEDIATE LINE
J120	21 TO J120	13 PI (J3 - 29)
J117	1 TO J117	4 ENABLE PORT A TRANSCEIVERS
J117	2 TO J117	5 PORT A DIRECTION = OUT
J119	2 TO J119	3 ENABLE PORT B TRANSCEIVERS
J119	5 TO J119	6 PORT B DIRECTION = IN
J120	7 TO J120	8 BUSY (J3 - 21) PRO
J120	4 TO J120	9 FAULT (J3 - 28) PB3

BUS REQUEST LEVEL

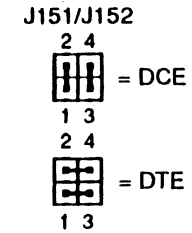


09/11/89

ONBOARD MEMORY ACCESS TIME			FIGURATION					
6 MHZ ACCESS TIME IN NANO-SECONDS			8 MHZ ACCESS TIME IN NANO-SECONDS			10 MHZ ACCESS TIME IN NANO-SECONDS		
			PIN			PIN		
8	□		0 - 166	1	0 - 125	1	0 - 100	1
7	□		166 - 250	2	125 - 187	2	100 - 150	2
6	□	12 SIO	250 - 333	3	187 - 250	3	150 - 200	3
5	□	11 M2	333 - 416	4	250 - 312	4	200 - 250	4
4	□	10 M1	416 - 500	5	312 - 375	5	250 - 300	5
3	□	9 M0	500 - 583	6	375 - 437	6	300 - 350	6
2	□		583 - 666	7	437 - 500	7	350 - 400	7
1	□		666 - 750	8	500 - 562	8	400 - 450	8



SERIAL DATA DIRECTION

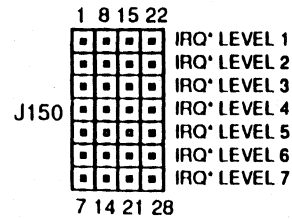


BUS ARBITER OPTION



JUMPER INSTALLED = RELEASE- ON-REQUEST
 JUMPER REMOVED = SINGLE CYCLE

INTERRUPT REQUEST LEVEL

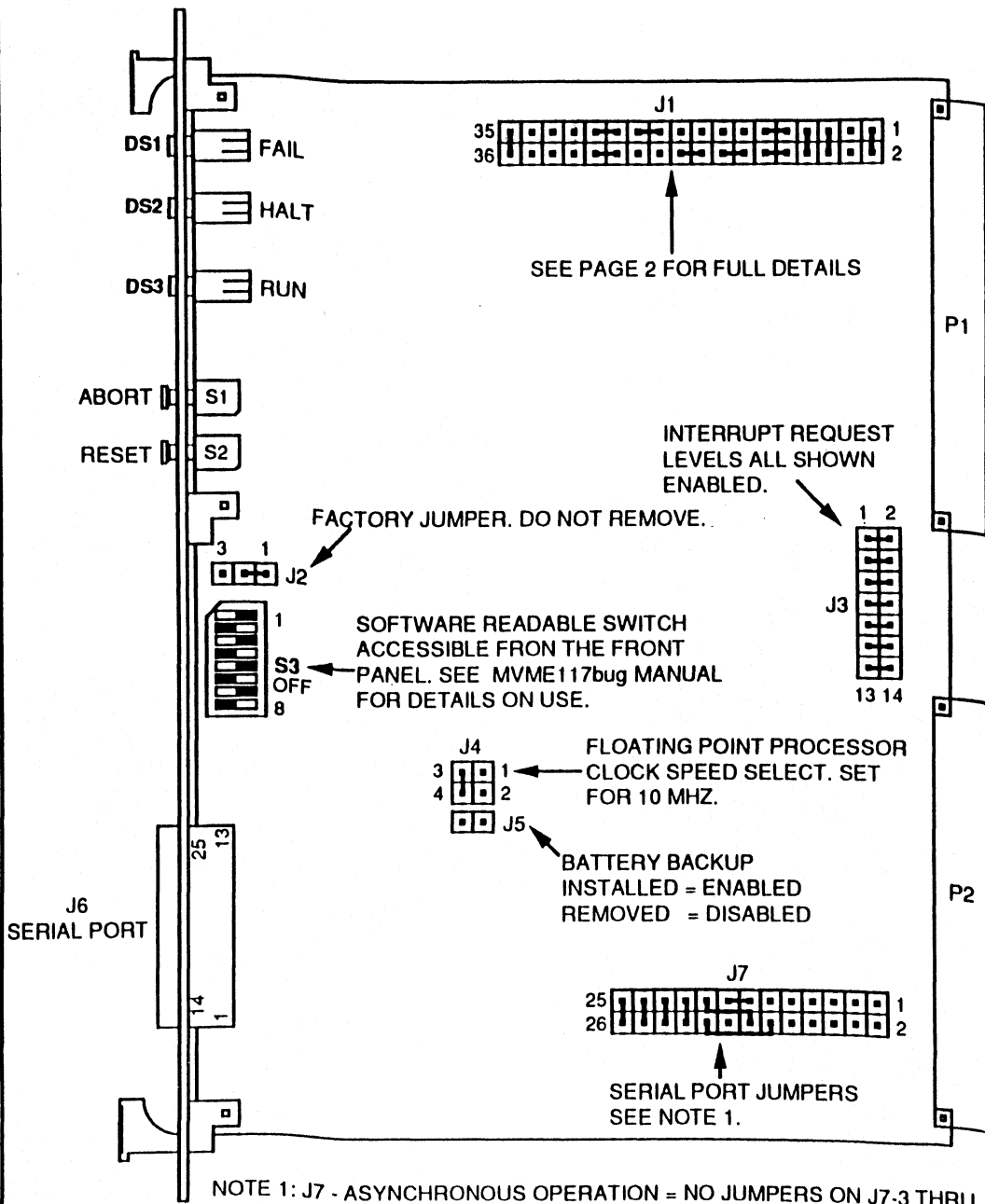
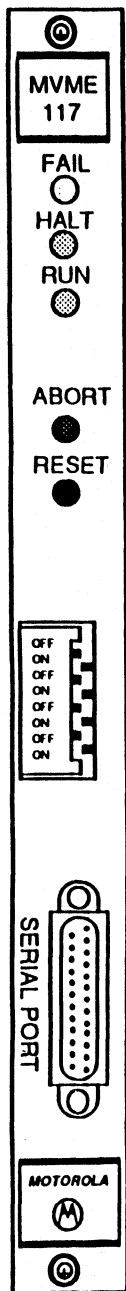


BATTERY BACKUP ENABLE



JUMPER INSTALLED = BATTERY BACKUP ENABLED
 JUMPER REMOVED = BATTERY BACKUP DISABLED

09/11/89



NOTE 1: J7 - ASYNCHRONOUS OPERATION = NO JUMPERS ON J7-3 THRU J7-10
 SYNCHRONOUS, DTE TO MODEM = J7-5 TO J7-6, J7-7 TO J7-9
 (BOTH CLOCKS RECEIVED FROM MODEM)
 SYNCHRONOUS, DCE TO TERMINAL = J7-3 TO J7-4, J7-5 TO J7-7, J7-8 TO J7-10
 (BOTH CLOCKS DRIVEN FROM Z8530 RXC OUTPUT:
 Z8530 MUST BE PROGRAMMED TO OUTPUT TXC CLOCK)

NOTE 2: ACTIVE PART SWITCH S3 IS DARKENED AF

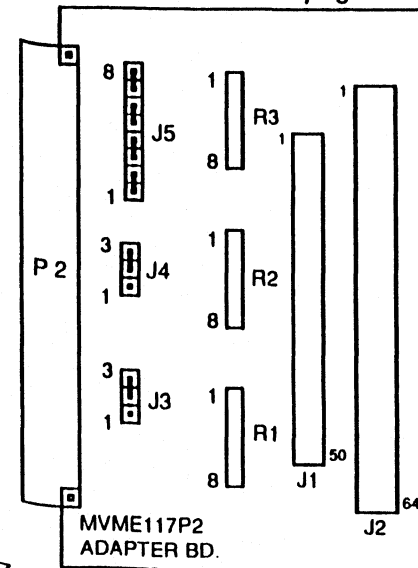
PART NUMBERS:

- MVME117 01-W3371B02 76433096
- MVME117-1 01-W3393B01 76433025 & 76433048
- MVME117-3 01-W3393B03 76433094
- MVME117-3FP 01-W3393B07 76435334
- MVME117-4 01-W3393B04 76433100
- MVME117P2 01-W3567B01 XXXXXXXX

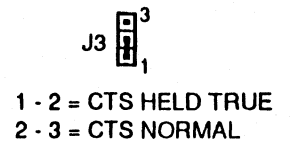
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

PARALLEL PORTSELECTT HEADER

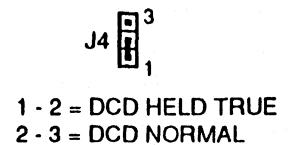
J5 CONNECTIONS	EFFECTIVE CONNECTION	SIGNAL
1 - 2	P2-A19 TO J2-57	P0 - D0
3 - 4	P2-C21 TO J2-60	P0 - D3
5 - 6	P2-A20 TO J2-62	P0 - D2
7 - 8	P2-C20 TO J2-64	P0 - D1



CTS SELECT HEADER

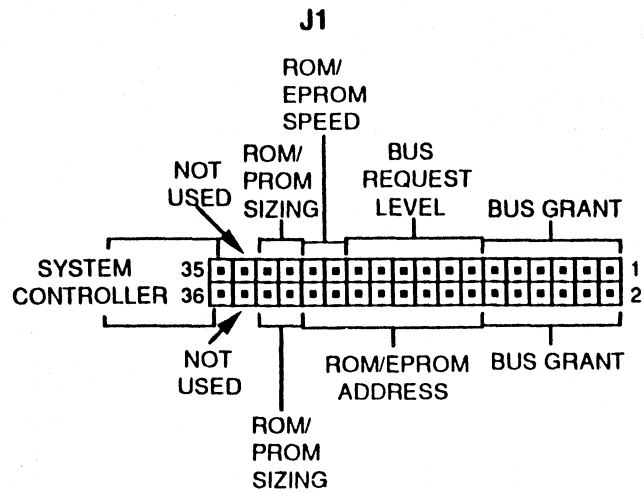


DCD SELECT HEADER



09/11/89

BUS REQUEST, BUS GRANT, ROM/PROM ADDRESS SELECT, ACCESS TIME SELECT, SIZE SELECT, AND SYSTEM CONTROLLER ENABLE



BR3* = J1-21 TO J1-23
 BR2* = J1-19 TO J1-21
 BR1* = J1-15 TO J1-17
 BR0* = J1-13 TO J1-15

BG3IN*, BG3OUT* = J1-1 TO J1-2, 5 TO 6, 7 TO 8, 9 TO 11, 10 TO 12
 BG2IN*, BG2OUT* = J1-1 TO J1-2, 5 TO 6, 7 TO 9, 8 TO 10, 11 TO 12
 BG1IN*, BG1OUT* = J1-1 TO J1-2, 3 TO 5, 4 TO 6, 7 TO 8, 11 TO 12
 BG0IN*, BG0OUT* = J1-1 TO J1-3, 2 TO 4, 5 TO 6, 7 TO 8, 11 TO 12

ROMEPROM PINOUT ADDRESS SELECT:

64K X 8 = J1-14 TO J1-16, J1-18 TO J1-20, J1-26 TO J1-28
 32K X 8 = J1-14 TO J1-16, J1-18 TO J1-20, J1-24 TO J1-26
 16K X 8 = J1-14 TO J1-16, J1-20 TO J1-22, J1-24 TO J1-26
 8K X 8 = J1-20 TO J1-22, J1-24 TO J1-26

ROM/EPROM ACCESS TIME:

J1-25 TO J1-27 INSTALLED FOR FAST 250ns PARTS
 J1-25 TO J1-27 REMOVED FOR SLOW 450ns PARTS

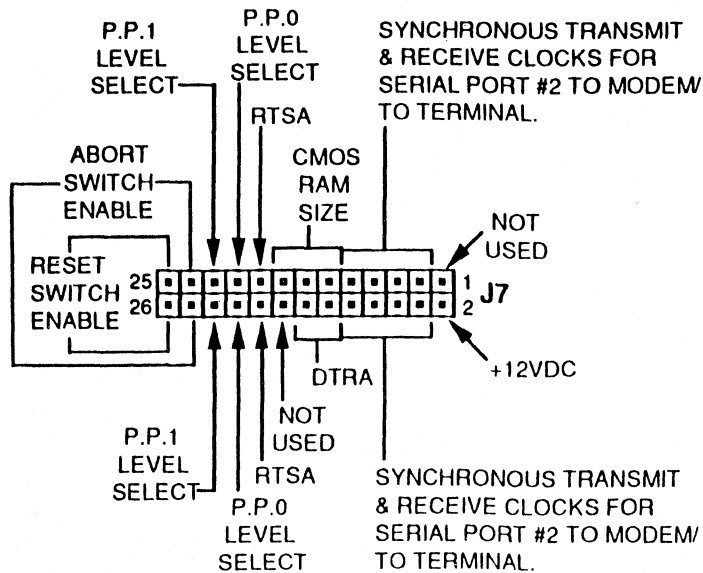
ROM/EPROM SIZE SELECT:

64K X 8 = NO JUMPERS ON J1 PINS 29, 30, 31, AND 32
 32K X 8 = J1-29 TO J1-30
 16K X 8 = J1-31 TO J1-32
 8K X 8 = J1-29 TO J1-30, AND J1-31 TO J1-32

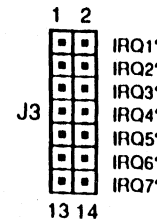
SYSTEM CONTROLLER:

ENABLED = J1-35 TO J1-36 INSTALLED
 DISABLED = J1-35 TO J1-36 REMOVED
 (SET UP AS A SLAVE)

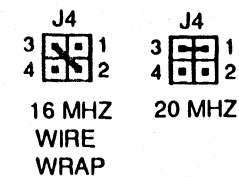
SERIAL PORT JUMPERS

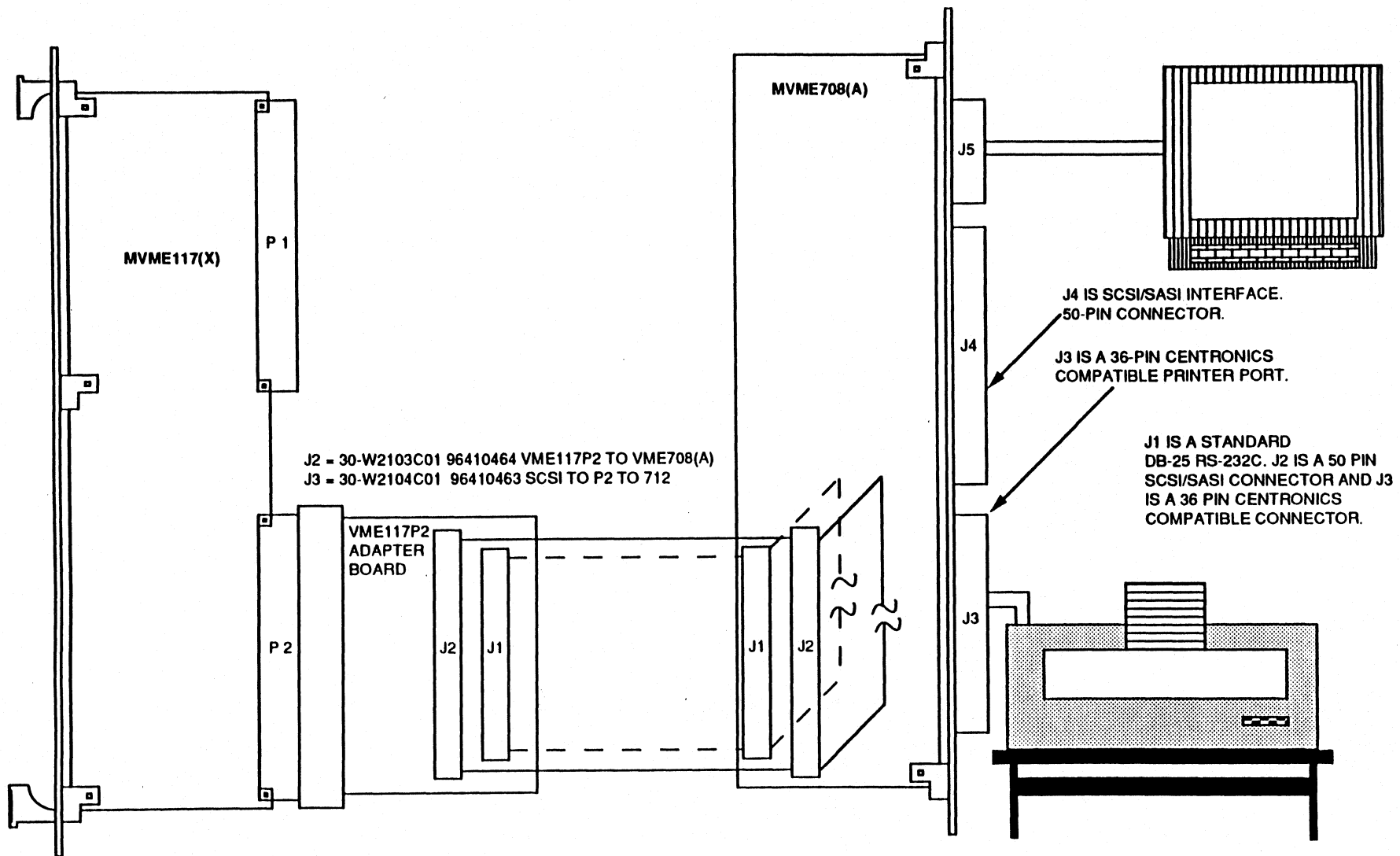


INTERRUPT REQUEST ENABLE



FLOATING POINT PROCESSOR CLOCK SELECT

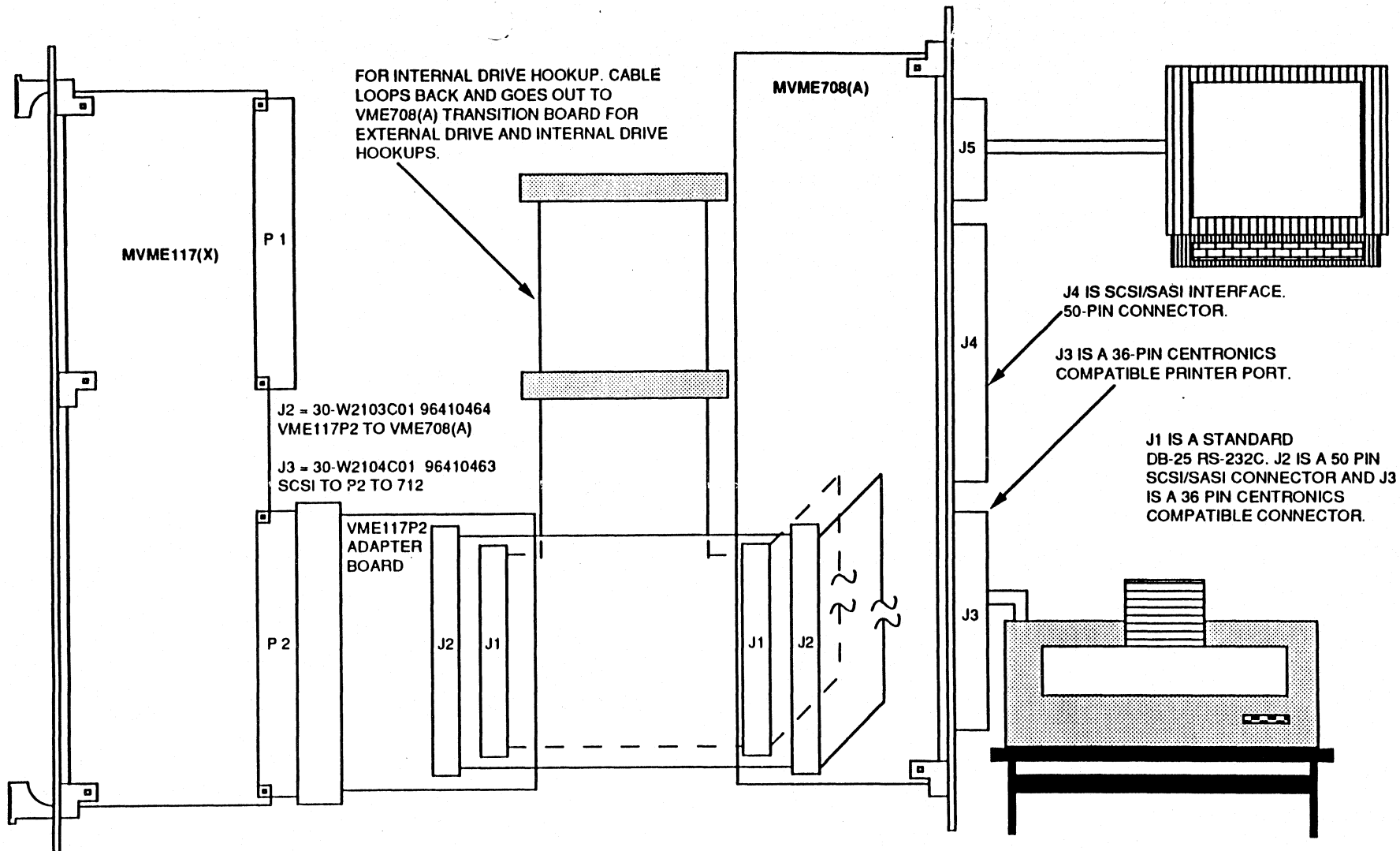




NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE.
IT IS USED FOR EXTERNAL DRIVES
AND IS BEHIND J5 ON THE
VME708(A) MODULE.
J1 IS A 50-PIN CONDUCTOR CABLE.
IT IS USED FOR INTERNAL DRIVES
OR HOOKING UP OF EXTERNAL SCSI
DRIVES.

NOTE 2: TERMINATORS MUST BE INSTALLED
AT THE END OF ALL CABLES.

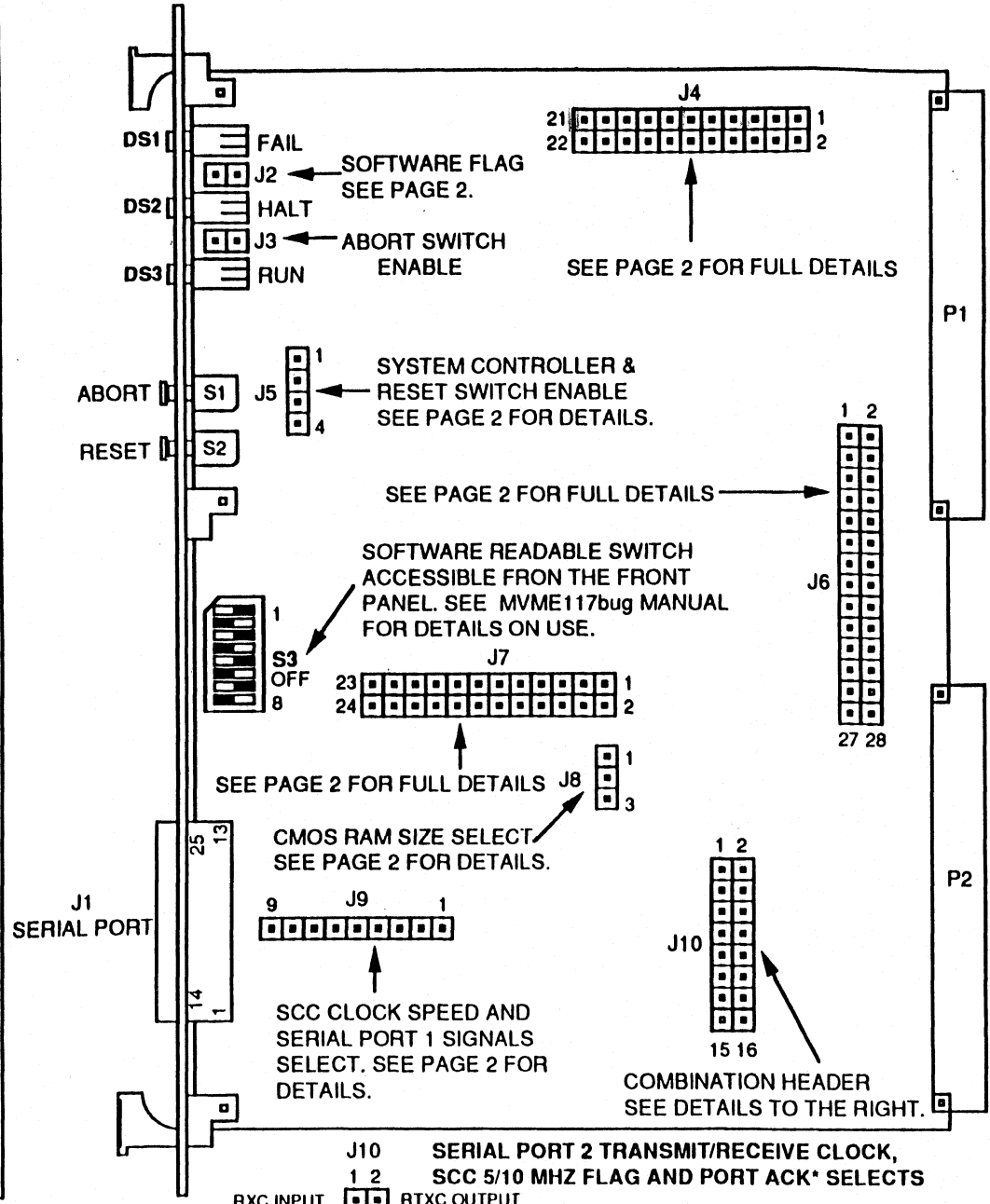
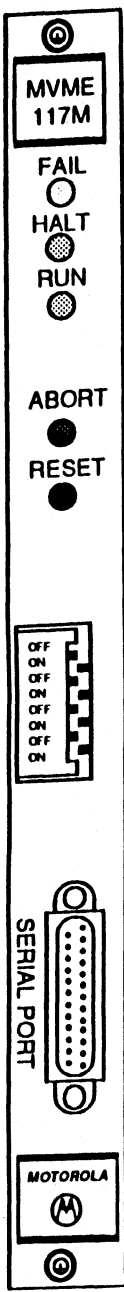
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NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE. IT IS USED FOR EXTERNAL DRIVES AND IS BEHIND J5 ON THE VME708(A) MODULE. J1 IS A 50-PIN CONDUCTOR CABLE. IT IS USED FOR INTERNAL DRIVES OR HOOKING UP OF EXTERNAL SCSI DRIVES.

NOTE 2: TERMINATORS MUST BE INSTALLED AT THE END OF ALL CABLES.

09/11/89



PART NUMBERS:

MVME117A 01-W3472B01 76435334
 MVME117P2 01-W3567B01 XXXXXXXX

SEE CURRENT REVISION LEVEL (CRL)
 FOR CURRENT REVISION INFORMATION.

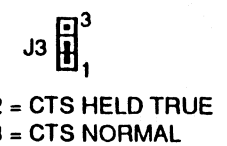
NOTE 1: ACTIVE PART OF SWITCH S3 IS DARKENED AREA.

09/11/89

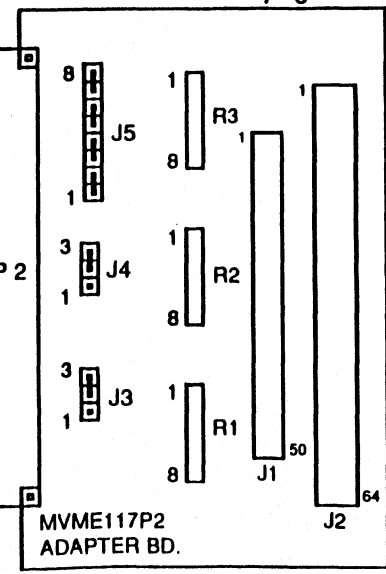
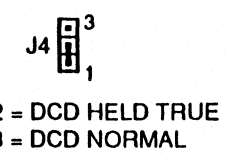
PARALLEL PORT SELECTT HEADER

J5 CONNECTIONS	EFFECTIVE CONNECTION	SIGNAL
1 - 2	P2-A19 TO J2-57	P0 - D0
3 - 4	P2-C21 TO J2-60	P0 - D3
5 - 6	P2-A20 TO J2-62	P0 - D2
7 - 8	P2-C20 TO J2-64	P0 - D1

CTS SELECT HEADER



DCD SELECT HEADER



J10 SERIAL PORT 2 TRANSMIT/RECEIVE CLOCK, SCC 5/10 MHZ FLAG AND PORT ACK* SELECTS

RXC INPUT	1	RTXC OUTPUT	2
RTXC OUTPUT	3	RXC INPUT TXC INPUT	4
TXC INPUT	5	TXC INPUT	6
TRXC OUTPUT	7	TRXC OUTPUT	8
5/10 MHZ FLAG	9	5/10MHZ FLAG	10
11 - 16 = PARALLEL PORT ACK*	11	11-16 = PARALLEL PORT	16

TO TERMINAL. DRIVE BOTH CLOCKS = J10-1 TO J10-3, 4 TO 6, 7 TO 8 TO MODEM. RECEIVE BOTH CLOCKS = J10-2 TO J10-4, 5 TO 6
 5 MHZ SCC CLOCK SPEED FLAG = J10-9 TO J10-10 INSTALLED
 10 MHZ SCC CLOCK SPEED FLAG = J10-9 TO J10-10 REMOVED
 P0 AND P1 HI TO LOW ACK* EDGE = J10-11 TO J10-13, 12 TO 14
 P0 AND P1 LO TO HI ACK* EDGE = J10-13 TO J10-15, 14 TO 16

**SOFTWARE FLAG
SELECT HEADER**



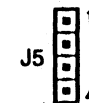
JUMPER INSTALLED = LOW (0)
JUMPER REMOVED = HIGH (1)

**ABC SWITCH
ENABLE**



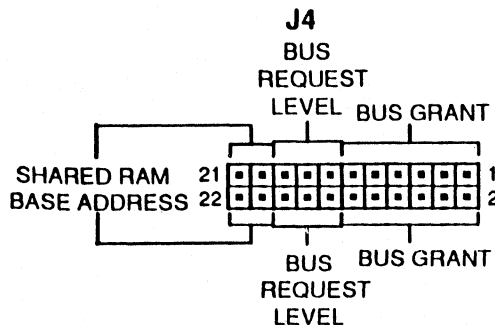
JUMPER INSTALLED = ENABLED
JUMPER REMOVED = DISABLED

SYSTEM CONTROLLER AND RESET ENABLE



J51 TO J5-2 JUMPER INSTALLED = SYSTEM CONTROLLER ENABLED
J51 TO J5-2 JUMPER REMOVED = SYSTEM CONTROLLER DISABLED
J5-3 TO J5-4 JUMPER INSTALLED = RESET SWITCH ENABLED
J5-3 TO J5-4 JUMPER REMOVED = RESET SWITCH DISABLED

BUS REQUEST, BUS GRANT, AND SHARED RAM ADDRESS SELECTS.



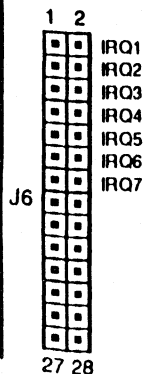
BG3IN*, BG3OUT* = J4-1 TO J4-2, 5 TO 6, 7 TO 8, 9 TO 11, 10 TO 12
BG2IN*, BG2OUT* = J4-1 TO J4-2, 5 TO 6, 7 TO 9, 8 TO 10, 11 TO 12
BG1IN*, BG1OUT* = J4-1 TO J4-2, 3 TO 5, 4 TO 6, 7 TO 8, 11 TO 12
BG0IN*, BG0OUT* = J4-1 TO J4-3, 2 TO 4, 5 TO 6, 7 TO 8, 9 TO 10, 11 TO 12
BR3* = J4-13 TO J4-15
BR2* = J4-15 TO J4-17
BR1* = J4-16 TO J4-18
BR0* = J4-14 TO J4-16
SHARED RAM BASE ADDRESS:
\$000000 = J4-19 TO J4-20, J4-21 TO J4-22
\$200000 = J4-19 TO J4-20
\$400000 = J4-21 TO J4-22
\$600000 = NO JUMPERS INSTALLED

CMOS RAM SIZE SELECT



JUMPER FROM 1 TO 2 = 16K RAM
JUMPER FROM 2 TO 3 = 64K RAM

INTERRUPT REQUEST ENABLE AND EPROM/EEPROM SIZE SELECT



EPROM/EEPROM SIZE SELECT HEADER:

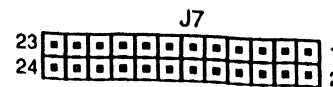
8K, 16K EPROM = J6-15 TO J6-17, 22 TO 24, 23 TO 25 (XU63 AND XU64)
32K EPROM = J6-15 TO J6-17, 21 TO 22, 25 TO 26 (XU63 AND XU64)
64K EPROM = J6-17 TO J6-19, 21 TO 22, 25 TO 26 (XU63 AND XU64)
8K EEPROM = J6-20 TO J6-22, 25 TO 27 (XU63 AND XU64)
32K EEPROM = J6-17 TO J6-18, 20 TO 22, 25 TO 27 (XU63 AND XU64)

**SCC CLOCK SPEED AND
SERIAL PORT 1 SIGNAL HEADER**



PIN 1 = MPUCLK*
PIN 2 = PCLK
PIN 3 = MPUCLK/2
PIN 4 = RTS FROM J1
PIN 5 = RTS TO Z8530A
PIN 6 = +12VDC
PIN 7 = +12VDC
PIN 8 = DTR FROM Z8530A
PIN 9 = DTR FROM J1

**EPROM/EEPROM SIZE SELECT HEADER,
FLOATING POINT PROCESSOR SPEED AND
BATTERY BACKUP ENABLE SELECT HEADER**



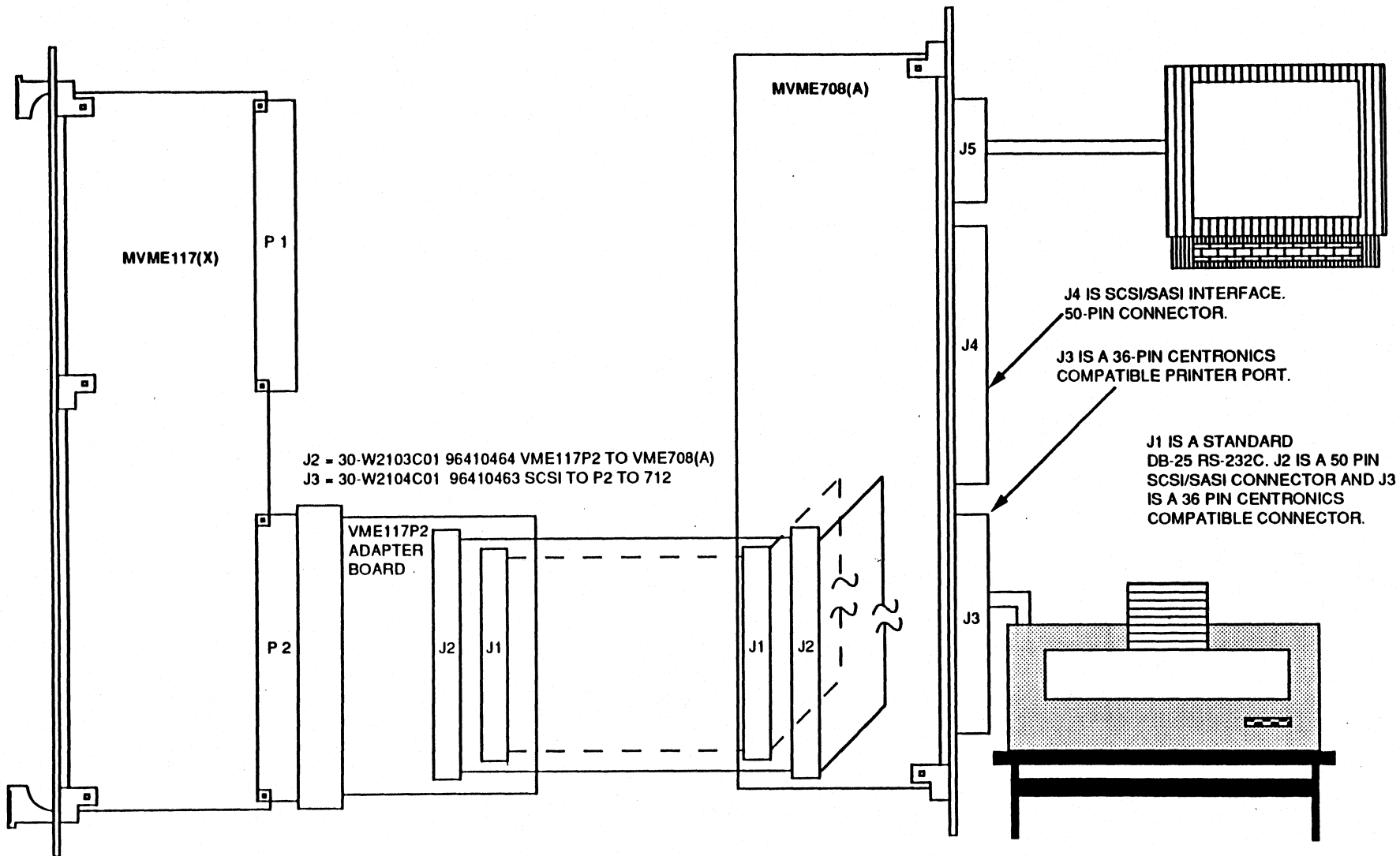
10 MHZ = J7-15 TO J7-17
16 MHZ = J7-17 TO J7-19
20 MHZ = J7-17 TO J7-21
(WIRE WRAP)

EPROM/EEPROM SIZE SELECT HEADER:

8K, 16K EPROM = J7-2 TO J7-4, 7 TO 9, 12 TO 14 (XU71 AND XU72)
32K EPROM = J7-3 TO J7-4, 7 TO 8, 12 TO 14 (XU71 AND XU72)
64K EPROM = J7-3 TO J7-4, 7 TO 8, 10 TO 12 (XU71 AND XU72)
8K EEPROM = J7-4 TO J7-6, 5 TO 7 (XU71 AND XU72)
32K EEPROM = J7-4 TO J7-6, 5 TO 7, 11 TO 12 (XU71 AND XU72)

J7-23 TO J7-24 JUMPER INSTALLED = BATTERY BACKUP ENABLED
J7-23 TO J7-24 JUMPER REMOVED = BATTERY BACKUP DISABLED

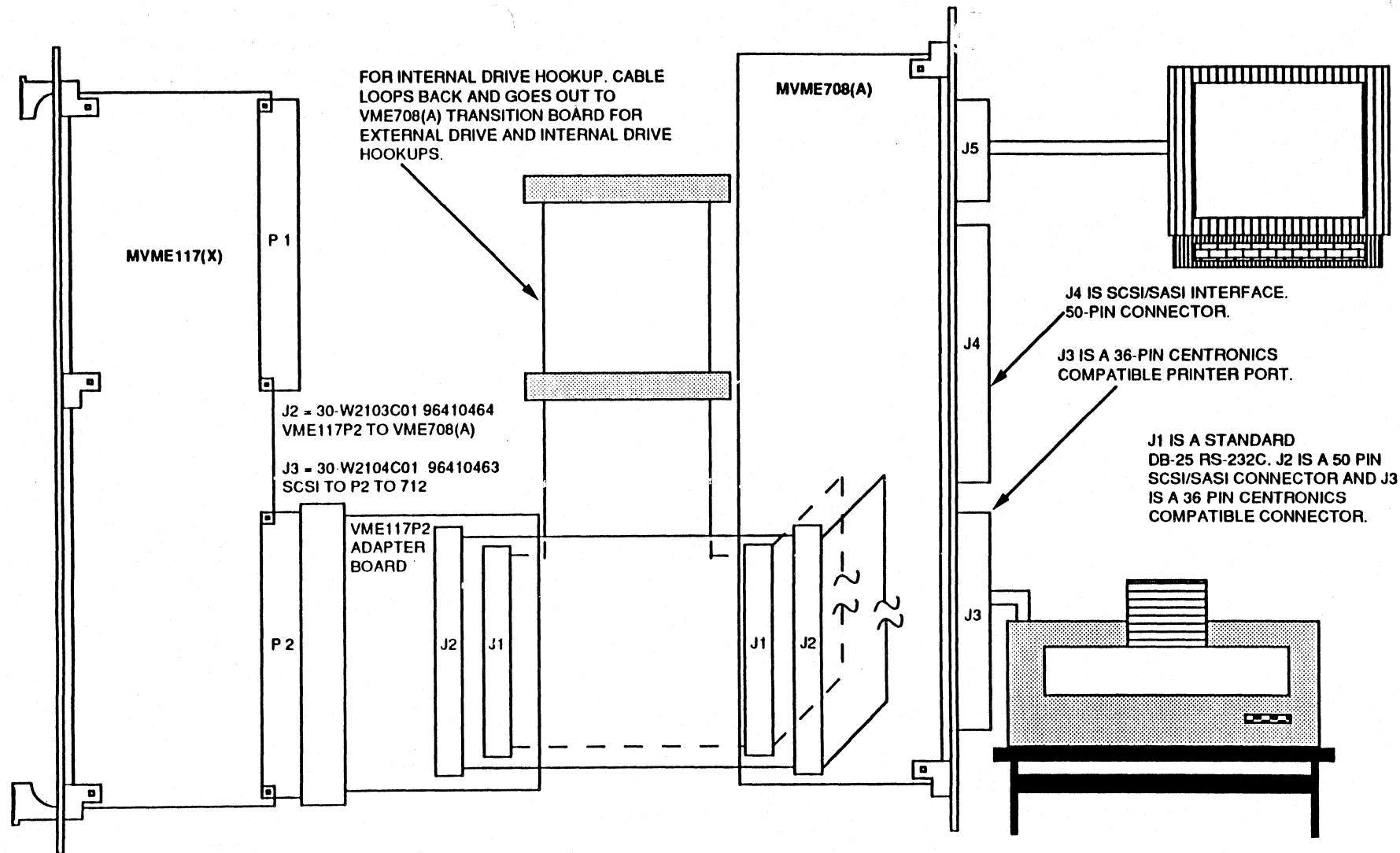
09/11/89



NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE.
 IT IS USED FOR EXTERNAL DRIVES
 AND IS BEHIND J5 ON THE
 VME708(A) MODULE.
 J1 IS A 50-PIN CONDUCTOR CABLE.
 IT IS USED FOR INTERNAL DRIVES
 OR HOOKING UP OF EXTERNAL SCSI
 DRIVES.

NOTE 2: TERMINATORS MUST BE INSTALLED
 AT THE END OF ALL CABLES.

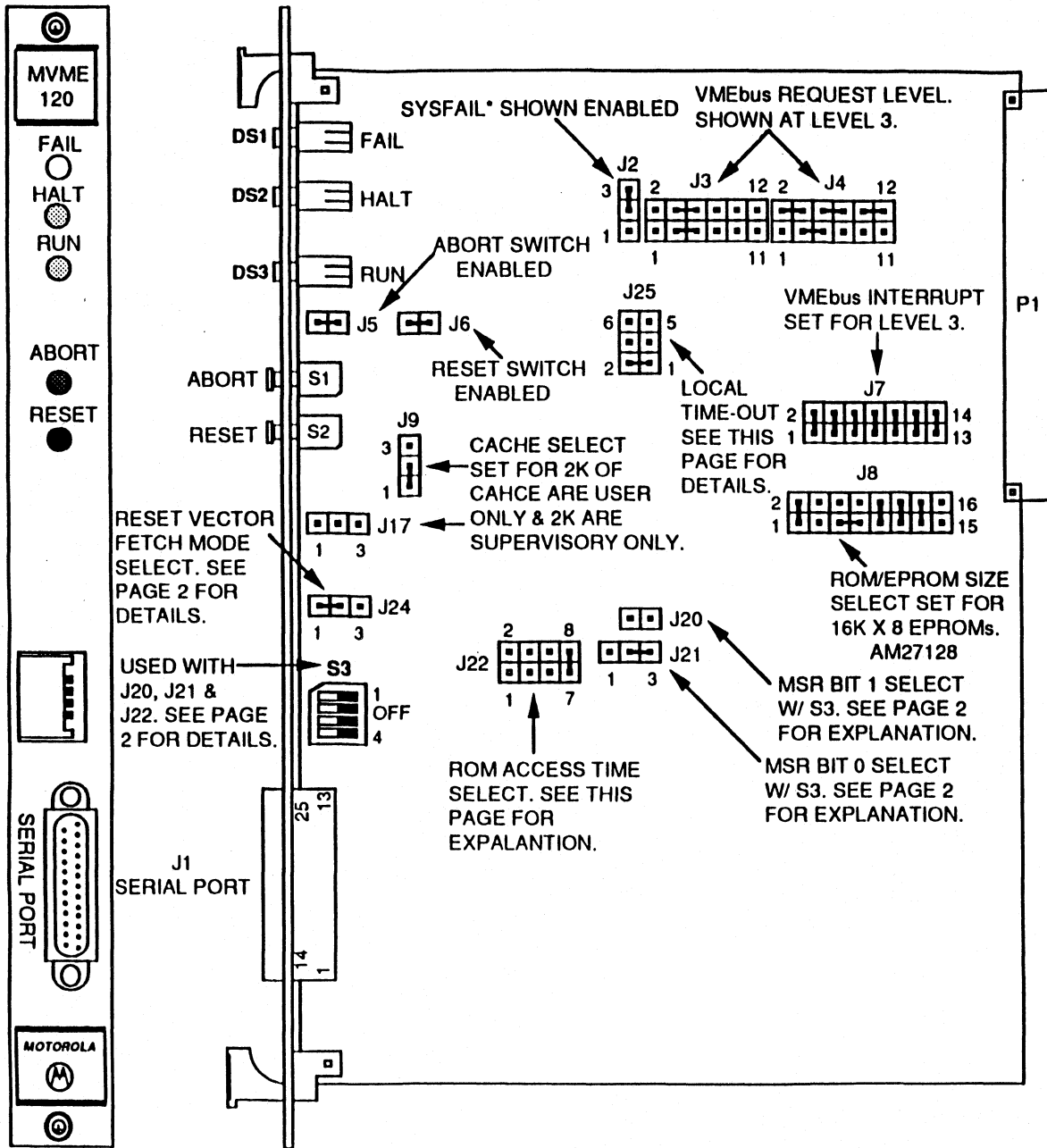
09/11/89



NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE. IT IS USED FOR EXTERNAL DRIVES AND IS BEHIND J5 ON THE VME708(A) MODULE. J1 IS A 50-PIN CONDUCTOR CABLE. IT IS USED FOR INTERNAL DRIVES OR HOOKING UP OF EXTERNAL SCSI DRIVES.

NOTE 2: TERMINATORS MUST BE INSTALLED AT THE END OF ALL CABLES.

09/11/89



NOTE 1: THE FOLLOWING JUMPERS ARE NOT SHOWN ON THIS PWB: J10 - J16, J18, J19, AND J23.

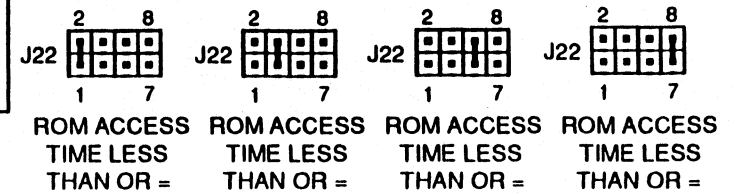
NOTE 2: ACTIVE PART OF SWITCH S3 IS DARKENED AREA.

PART NUMBERS:

- MVME120 01-W3293B01 76432565/2586
- MVME121 01-W3293B02 76432566
- MVME122 01-W3293B03 76432567
- MVME123 01-W3293B04 76432568

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

ROM ACCESS TIME SELECT HEADER



ROM ACCESS TIME LESS THAN OR = ROM ACCESS TIME LESS THAN OR = ROM ACCESS TIME LESS THAN OR = ROM ACCESS TIME LESS THAN OR =

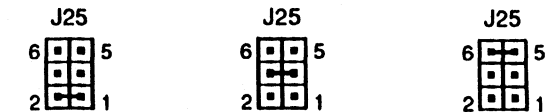
MVME120, 121 AND 123

N/A 250 ns 350 ns 450 ns

MVME122

200 ns 400 ns 450 ns 500 ns

LOCAL TIME-OUT SELECT

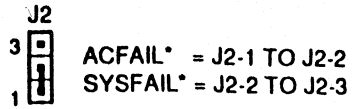


LOCAL TIME-OUT = 103 - 137 MICROSECONDS LOCAL TIME-OUT = 43 - 77 MICROSECONDS LOCAL TIME-OUT = 13 - 47 MICROSECONDS

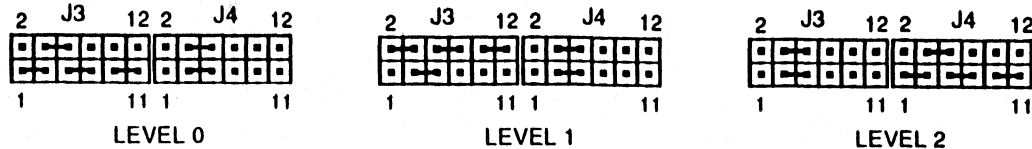
THE VMEbus GLOBAL BUS TIME-OUT MUST BE LESS THAN OR = 110 MICROSECONDS. THE VMEbus GLOBAL BUS TIME-OUT MUST BE LESS THAN OR = 170 MICROSECONDS. THE VMEbus GLOBAL BUS TIME-OUT MUST BE LESS THAN OR = 200 MICROSECONDS.

09/11/89

ACFAIL*/SYSFAIL* SELECT



VMEbus REQUEST LEVEL SELECT HEADERS



ABORT SWITCH ENABLE



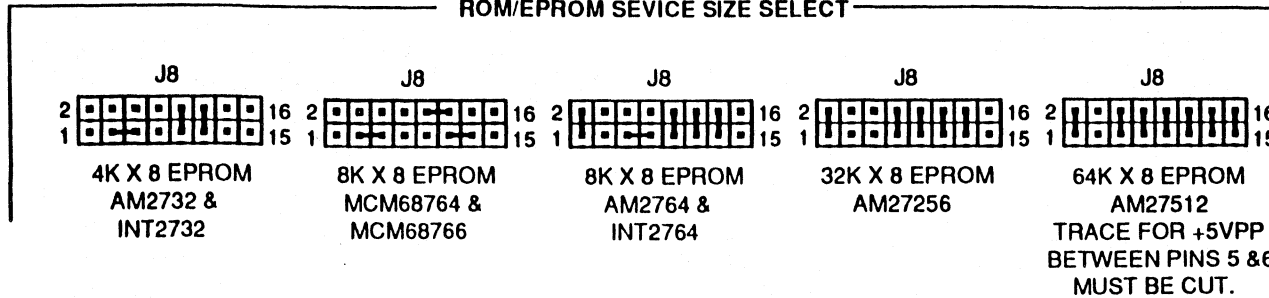
ENABLED IS JUMPER INSTALLED
DISABLED IS JUMPER REMOVED

RESET SWITCH ENABLE

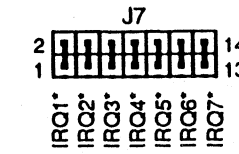


ENABLED IS JUMPER INSTALLED
DISABLED IS JUMPER REMOVED

ROM/EPROM SEVICE SIZE SELECT

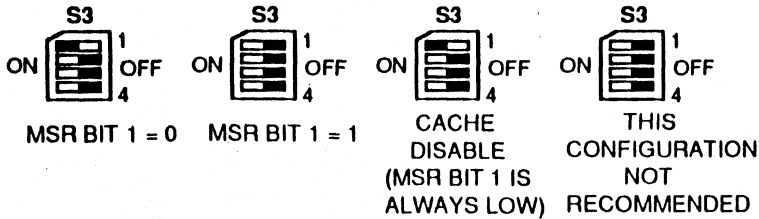


VMEbus INTERRUPT REQUEST SELECT

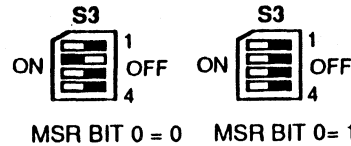
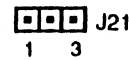


INSTALLED IS ENABLED
REMOVED IS DISABLED

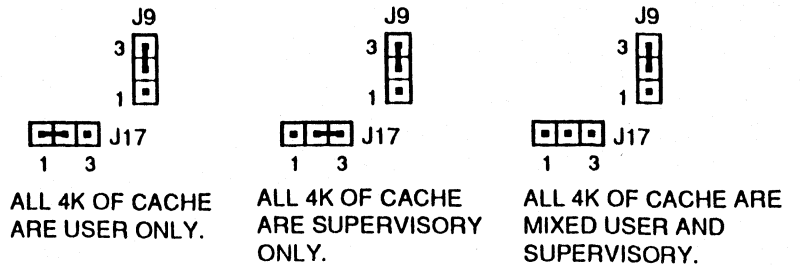
MSR BIT1 SOURCE SELECT HEADER W/ S3



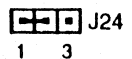
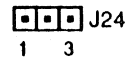
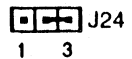
MSR BIT0 SOURCE SELECT HEADER W/ S3



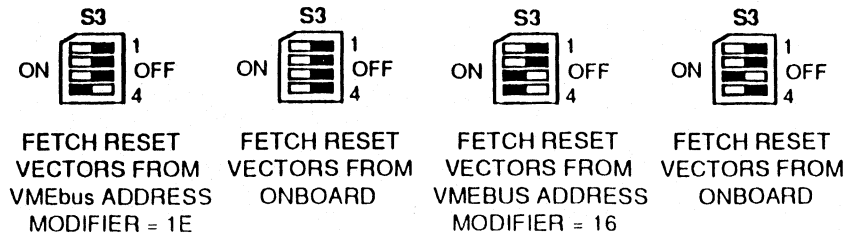
CACHE CONFIGURATION SELECT HEADERS



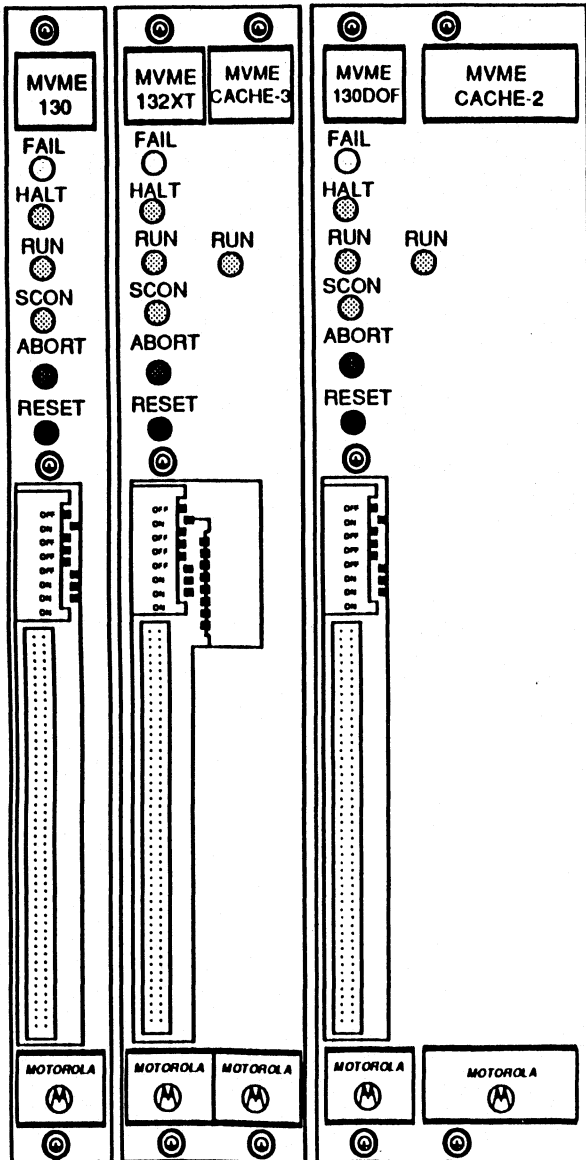
RESET VECTOR FETCH MODE SELECT HEADER



FETCH RESET VECTORS FROM VMEbus (SW3 HAS NO EFFECT.)
NEVER FETCH RESET VECTORS FROM VMEbus (SW3 HAS NO EFFECT.)
SW3-3 AND SW3-4 CONTROL MODE (SEE SW3 BELOW.)

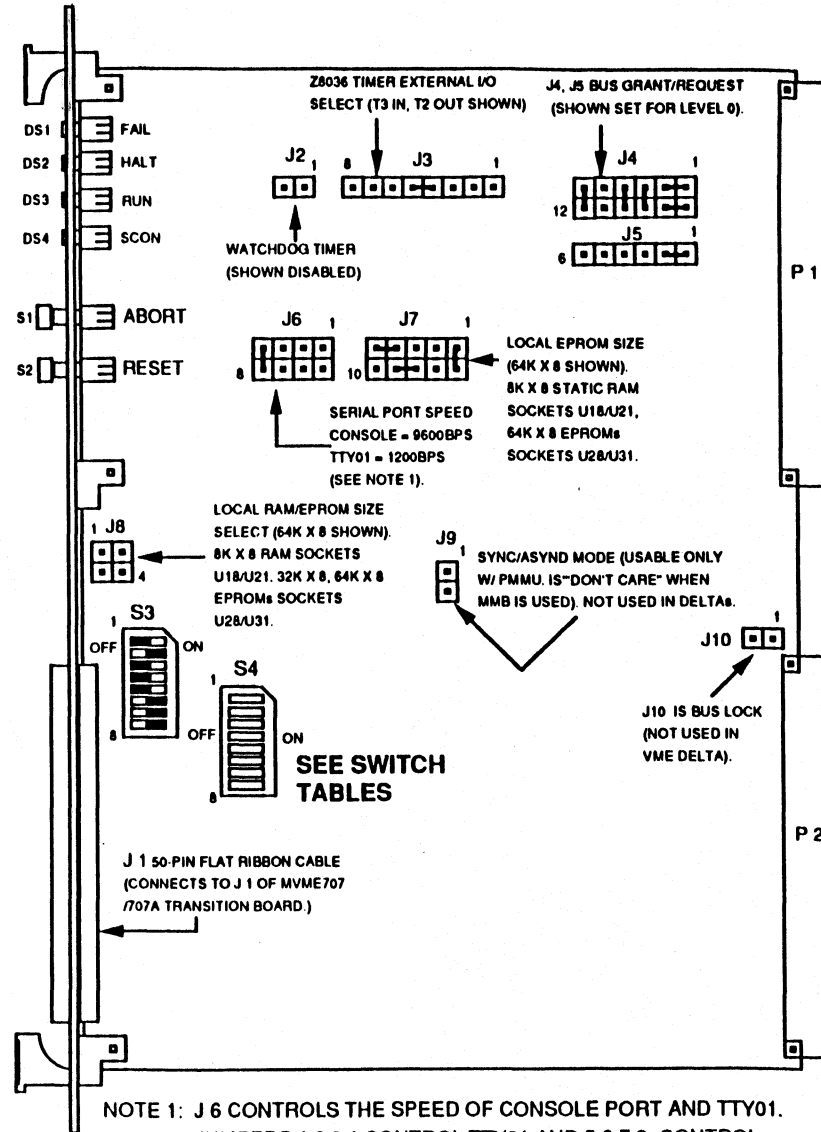


09/11/89



NOTE 2: MVME13()XT BELONGS IN SLOT 1.

NOTE 3: ACTIVE PART OF SWITCH IS DARKENED AREA.



NOTE 1: J 6 CONTROLS THE SPEED OF CONSOLE PORT AND TTY01. JUMPERS 1,2,3,4 CONTROL TTY01 AND 5,6,7,8 CONTROL THE CONSOLE. USE THE TABLE BELOW TO SET SPEEDS.

	7-8 OR 3-4	5-6 OR 1-2
300BPS	IN	IN
1200BPS	IN	IN
2400BPS	OUT	OUT
9600BPS	OUT	OUT

PART NUMBERS:

MVME131XT 01-W1335B03 96010815
 130DOF 01-W3370B11 76435340
 CAC2 01-W3412B02 76435151
 MMB 01-W3383B01 76433104

MVME131DOF 01-W3370B16 96010859
 130DOF 01-W3370B11 76435340
 MMB 01-W3383B01 76435151

MVME132XT 01-W1335B05 96010887
 130DOF 01-W3370B11 76435340
 CAC3 01-W3466B03 76435359

MVME132DOF 01-W3370B14 76435343
 PMMU NA NONE

MVME130BUG 67-W2738B01 NONE
 REVISION 3.2
 NOT INCLUDED ON PWB.

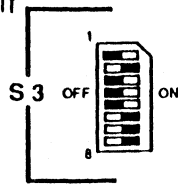
INCLUDES: 51AW5110B13 U31 EVEN
 51AW5110B14 U28 ODD
 51AW4591C30 U60 66431075

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

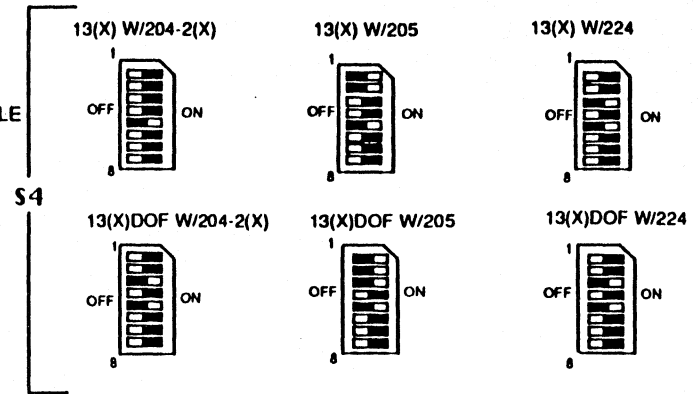
MVME130/31/32 SERIES
32-BIT
MICROPROCESSOR
VME module
 PAGE 4

11/08/91

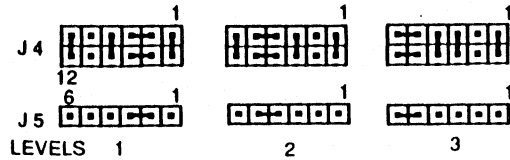
- S3-1 = (ENVIR) OPERATING ENVIRONMENT STATUS BIT
 - S3-2 = (SCON) VMEBUS SYSTEM CONTROLLER
 - S3-3 = (MODE) POWER UP RESET VECTOR
 - S3-4 = (AMSEL) ADDRESS MODIFIER SELECT
 - S3-5 = (ESTRES) HOLD IN RESET
 - S3-6 = (FPSWEN) FRONT PANEL SWITCH ENABLE
 - S3-7 = LOCAL/VMEBUS BUS TIMEOUT SELECT
 - S3-8 = LOCAL/VMEBUS BUS TIMEOUT SELECT
- NOTE: ON = 0, OFF = 1



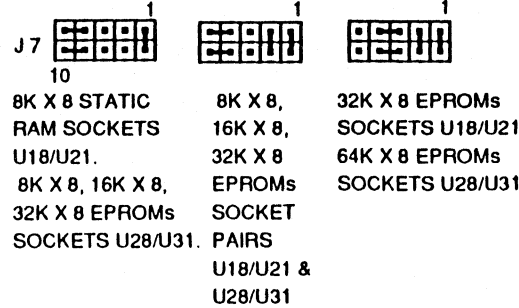
- S4-1 = VMX ENABLE
- S4-2 = VISION 30 ENABLE/DISABLE
- S4-3 = CACHE ENABLE
- S4-4 = D32 DATA WIDTH
- S4-5 = A32 ADDRESS WIDTH
- S4-6 = VMX32BUS DECODER PAL OPTION
- S4-7 = 131 BASE ADDRESS
- S4-8 = 131 BASE ADDRESS



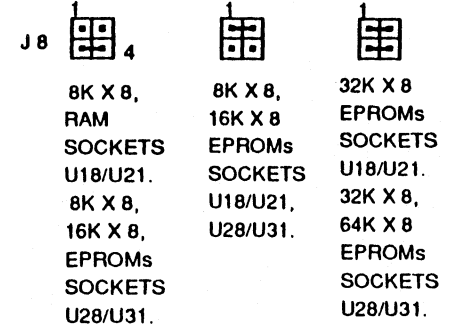
VMEbus GRANT/REQUEST SELECT



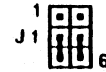
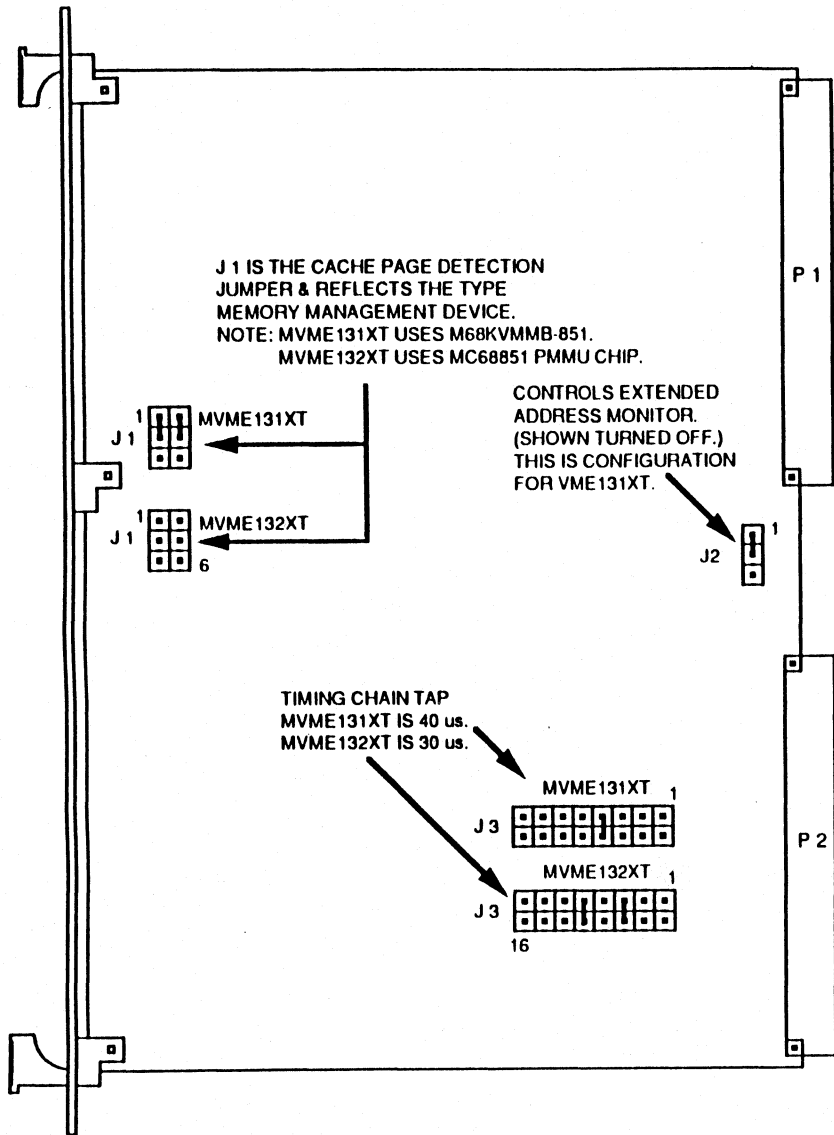
LOCAL EPROM/RAM SELECT



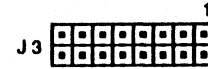
EPROM/RAM CONFIGURATION SELECT



03/14/90



CACHE PAGE DETECTION WITHOUT MMU DEVICE. (W/ MVME131XT OR MVME132XT.)

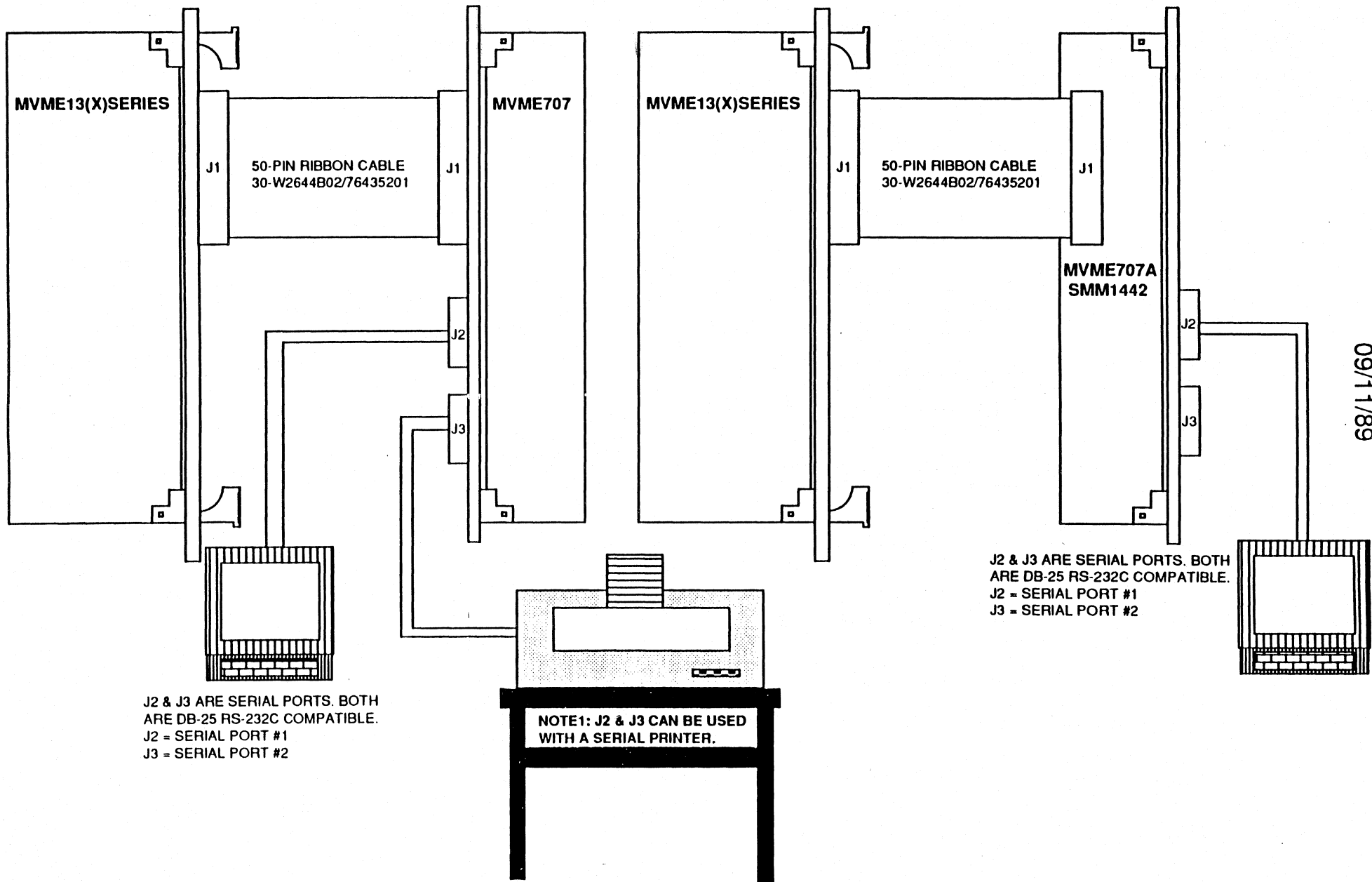


16

TIMING CHAIN TAP WITHOUT MMU.

NOTE: PART NUMBERS AND REVISION CONTROL L.A.L. IS LISTED ON THE MVME130 SERIES PWB DRAWING, PAGE 1 OF 4.

04/17/90



J2 & J3 ARE SERIAL PORTS. BOTH ARE DB-25 RS-232C COMPATIBLE.
 J2 = SERIAL PORT #1
 J3 = SERIAL PORT #2

NOTE1: J2 & J3 CAN BE USED WITH A SERIAL PRINTER.

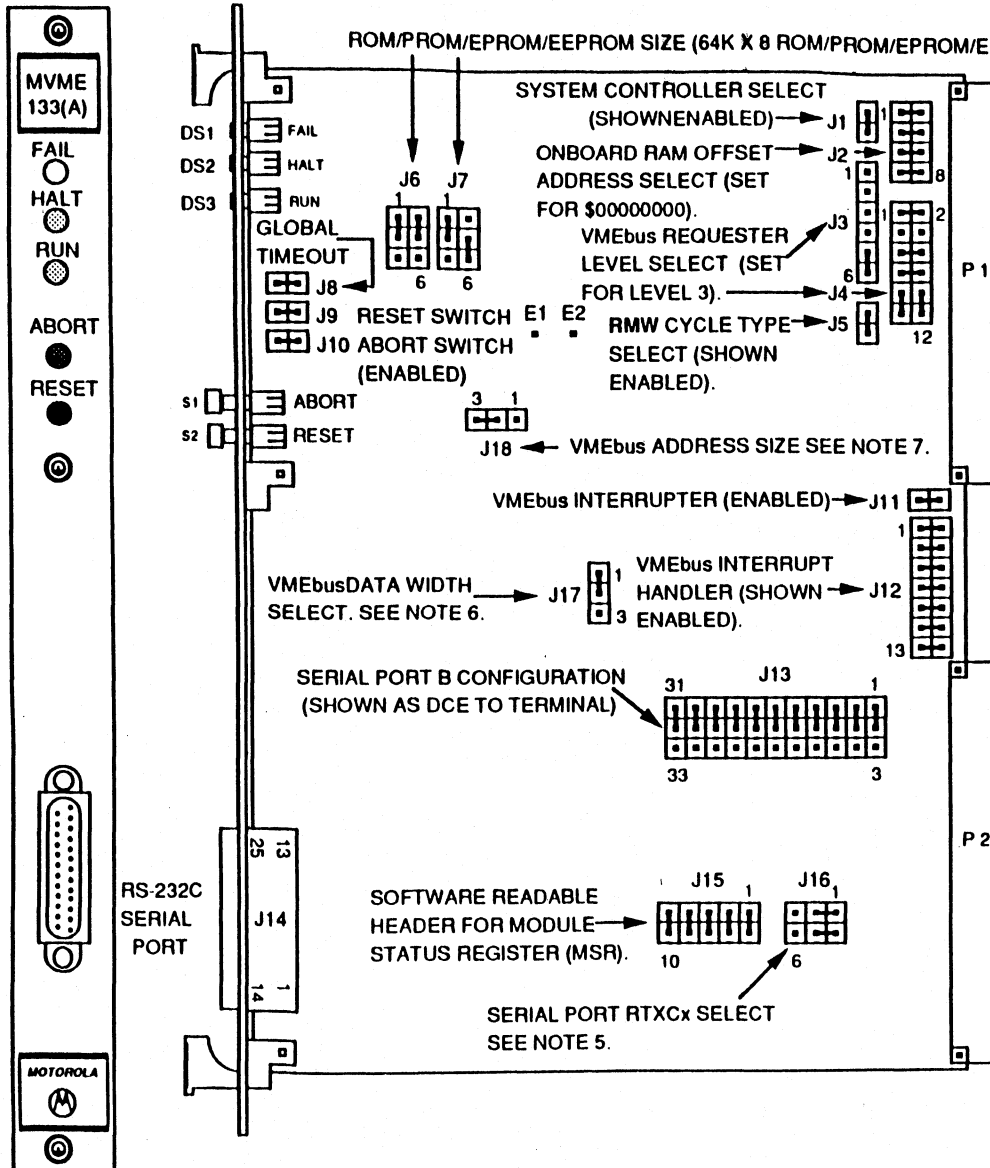
J2 & J3 ARE SERIAL PORTS. BOTH ARE DB-25 RS-232C COMPATIBLE.
 J2 = SERIAL PORT #1
 J3 = SERIAL PORT #2

09/11/89

PART NUMBERS:

MVME133	01-W3434B01	76435171 (OLD VME133)
MVME133	01-W3465B01	76435396 (NEW VME133)
MVME133-1	01-W3434B02	76435333 (OLD VME133-1)
MVME133-1	01-W3465B02	76435397 (NEW VME133-1)
MVME133-2	01-W3434B03	76435350 (OLD VME133-2)
MVME133-2	01-W3465B03	76435398 (NEW VME133-2)
MVME133-3	01-W3434B04	76435351 (OLD VME133-3)
MVME133-3	01-W3465B04	76435399 (NEW VME133-3)
MVME133A	01-W3465B16	76435353 (20 MHZ)
MVME133A-20	01-W3465B15	76435352 (20 MHZ)

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.



NOTE 1: FOR J18, IF VMEbus CONTAINS BOTH 24-BIT AND 32-BIT ADDRESS DEVICES, ONBOARD DRAM RESPONDS ONLY TO 24-BIT ADDRESS ACCESSES.

NOTE 2: VMEbus DATA WIDTH IS DYNAMICALLY CONTROLLED. J17 CONTROLS IT ON THE NEW VME133 (01-W3465BXX).

NOTE 3: THE OLD VME133 (01-W3434BXX) CAN'T CONTROL THE VMEbus ADDRESS SIZE. THE NEW VME133 (01-W3465BXX) CONTROLS IT WITH J18.

NOTE 4: J14 MFP=GPI00 MONITORS DDTR ON J14-20 ON THE OLD VME133 (01-W3434BXX), WHILE THE NEW VME133 (01-W3465BXX) MONITORS IT ON DRTS J14-20.

NOTE 5: J16 IS SET UP WITH RTXCx INPUT FROM ONBOARD 1.23 MHZ. RTXCb INPUT FROM ONBOARD 1.23MHZ.

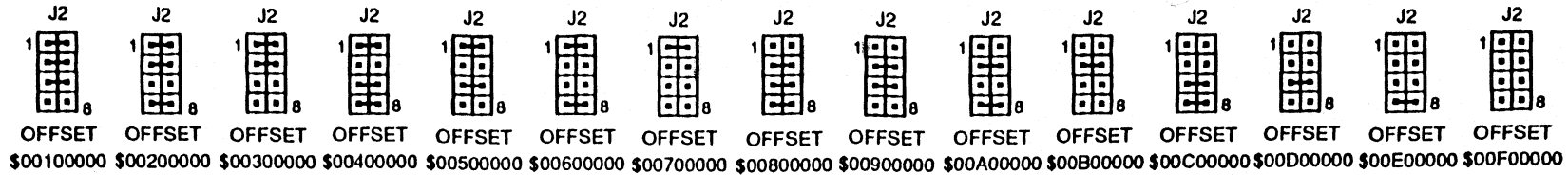
NOTE 6: J17 VMEbus IS 32-BIT DATA WHEN A24 IS LOW AND 16-BIT DATA WHEN A24 IS HIGH.

NOTE 7: J18- VMEbus CONTAINS BOTH 24-BIT AND 32-BIT ADDRESS DEVICES. ONBOARD DRAM RESPONDS ONLY TO 32-BIT ADDRESS ACCESSES.

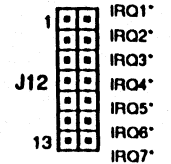
MVME133(A) [NEW]
VMEmodule
32-BIT MONOBOARD
MICROCOMPUTER
 PAGE 2 OF 2

11/08/91

ONBOARD DRAM OFFSET ADDRESS SELECT HEADER

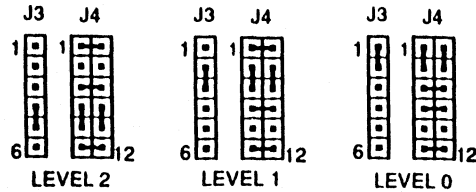


VMEbus INTERRUPT HANDLER SELECT

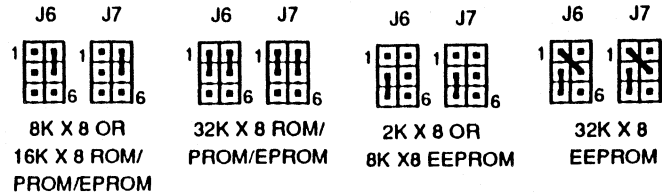


ALL INTERRUPTS (SHOWN DISABLED)

VMEbus REQUESTER LEVEL SELECT

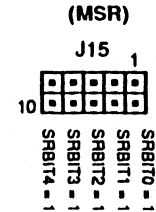


ROM/PROM/EPROM/EEPROM SIZE SELECT

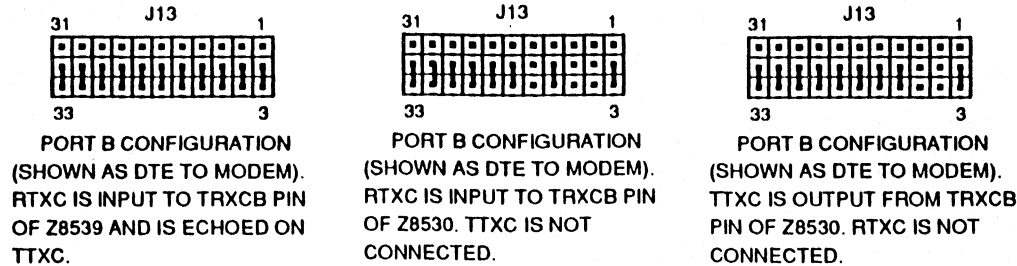


NOTE: J6 IS FOR BANK 1, J7 IS FOR BANK 2

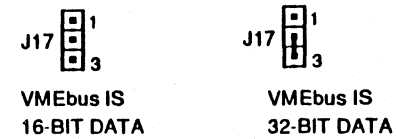
SOFTWARE ADABLE HEADER FOR MODULE STATUS REGISTER (MSR)



SERIAL PORT B CONFIGURATION



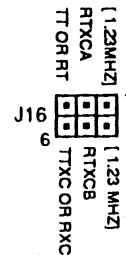
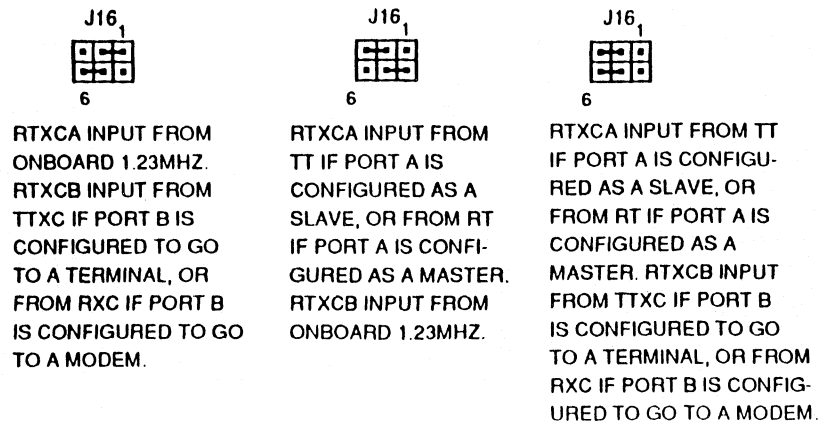
VMEbus DATA WIDTH SELECT



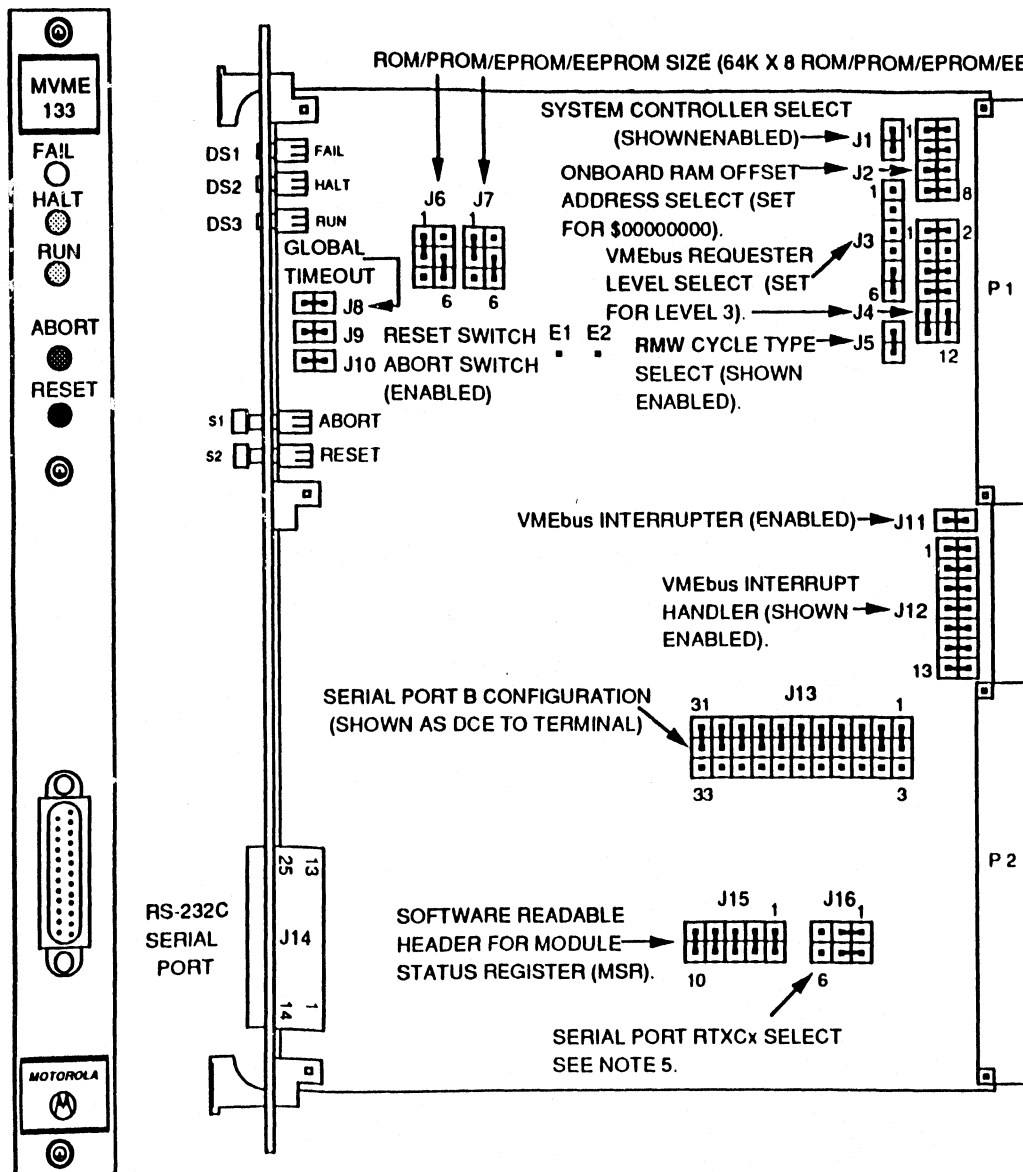
VMEbus ADDRESS SIZE SELECT



SERIAL PORT RTXCx SOURCE SELECT



11/08/91



PART NUMBERS:

MVME133	01-W3434B01	76435171 (OLD VME133)
MVME133	01-W3465B01	76435396 (NEW VME133)
MVME133-1	01-W3434B02	76435333 (OLD VME133-1)
MVME133-1	01-W3465B02	76435397 (NEW VME133-1)
MVME133-2	01-W3434B03	76435350 (OLD VME133-2)
MVME133-2	01-W3465B03	76435398 (NEW VME133-2)
MVME133-3	01-W3434B04	76435351 (OLD VME133-3)
MVME133-3	01-W3465B04	76435399 (NEW VME133-3)

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: FOR J18, IF VMEbus CONTAINS BOTH 24-BIT AND 32-BIT ADDRESS DEVICES, ONBOARD DRAM RESPONDS ONLY TO 24-BIT ADDRESS ACCESSES.

NOTE 2: VMEbus DATA WIDTH IS DYNAMICALLY CONTROLLED. J17 CONTROLS IT ON THE NEW VME133 (01-W3465BXX).

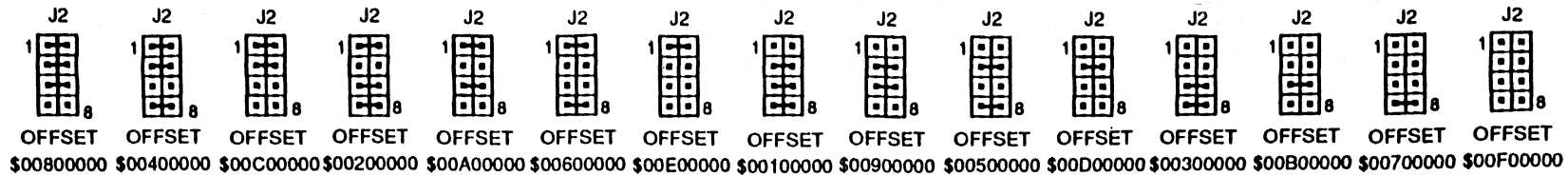
NOTE 3: THE OLD VME133 (01-W3434BXX) CAN'T CONTROL THE VMEbus ADDRESS SIZE. THE NEW VME133 (01-W3465BXX) CONTROLS IT WITH J18.

NOTE 4: J14 MFP-GPI00 MONITORS DDTR ON J14-20 ON THE OLD VME133 (01-W3434BXX), WHILE THE NEW VME133 (01-W3465BXX) MONITORS IT ON DRTS J14-20.

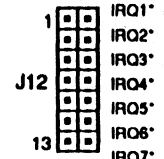
NOTE 5: J16 IS SET UP WITH RTXCA INPUT FROM ONBOARD 1.23 MHZ. RTXCB INPUT FROM ONBOARD 1.23 MHZ.

09/11/89

ONBOARD RAM OFFSET ADDRESS SELECT HEADER

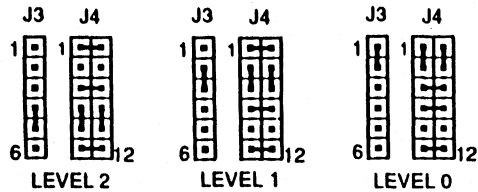


VMEbus INTERRUPT HANDLER SELECT

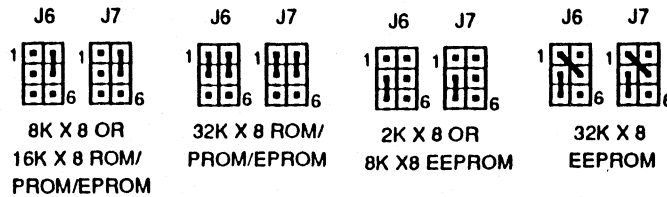


ALL INTERRUPTS (SHOWN DISABLED)

VMEbus REQUESTER LEVEL SELECT

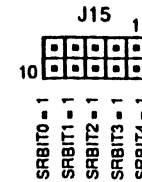


ROM/PROM/EPROM/EEPROM SIZE SELECT

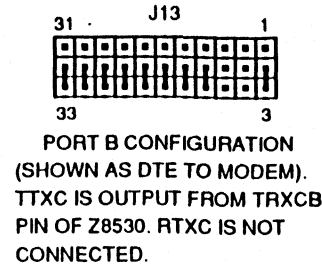
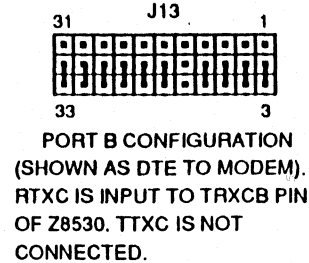
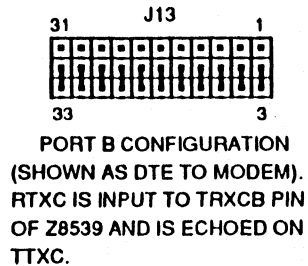


NOTE : J6 IS FOR BANK 1, J7 IS FOR BANK 2

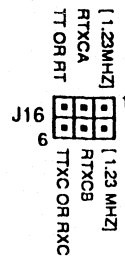
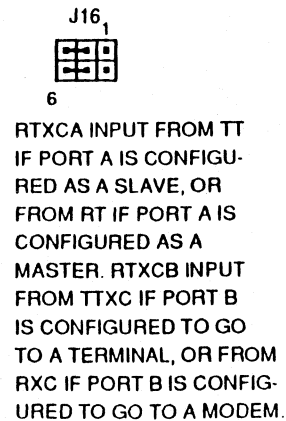
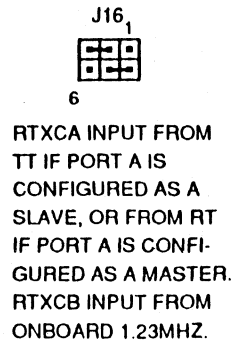
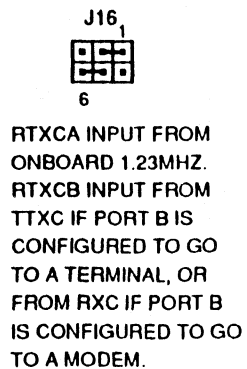
SOFTWARE ADABLE HEADER FOR MODULE STATUS REGISTER (MSR)



SERIAL PORT B CONFIGURATION



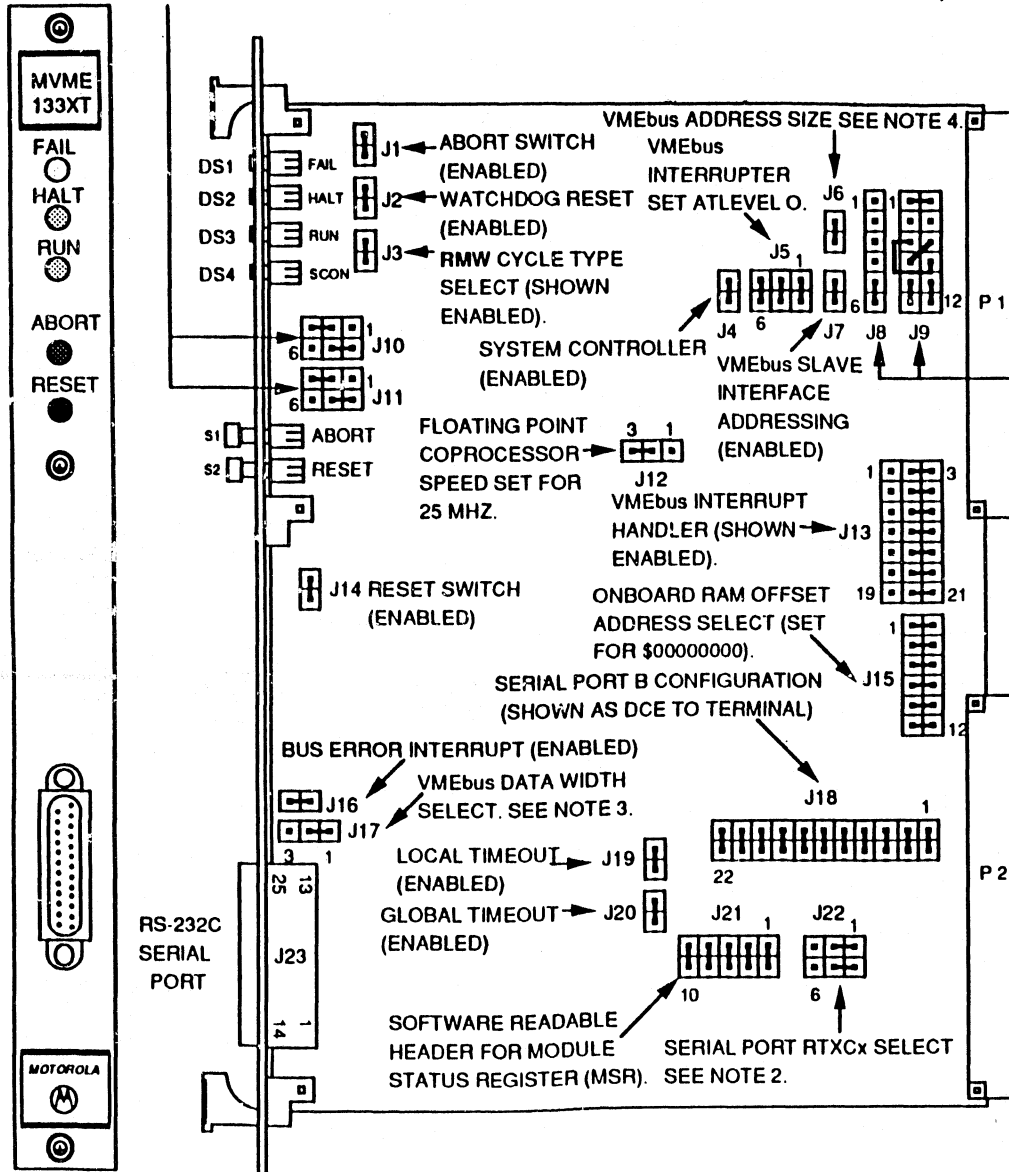
SERIAL PORT RTXCx SOURCE SELECT



09/11/89

MVME133XT 01-W3518B01 96010994

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.



NOTE 1: J14 MFP-GPIO0 MONITORS DDTR ON J14-20 ON THE OLD VME133 (01-W3434BXX), WHILE THE NEW VME133 (01-W3465BXX) MONITORS IT ON DRTS J14-20.

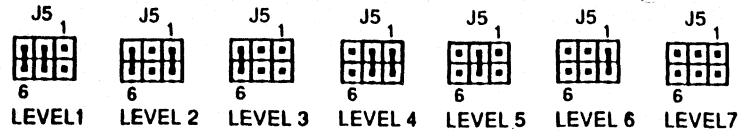
NOTE 2: J22 IS SET UP WITH RTXCA INPUT FROM ONBOARD 1.23 MHZ. RTXCB INPUT FROM ONBOARD 1.23MHZ.

NOTE 3: J17 VMEbus IS 32-BIT DATA WHEN A24 IS LOW AND 16-BIT DATA WHEN A24 IS HIGH.

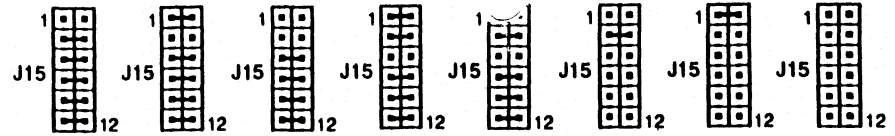
NOTE 4: J6- VMEbus CONTAINS BOTH 24-BIT AND 32-BIT ADDRESS DEVICES. ONBOARD DRAM RESPONDS ONLY TO 32-BIT ADDRESS ACCESSES.

12/14/89

VMEbus INTERRUPTER SELECT



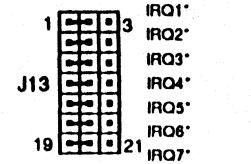
ONBOARD RAM OFFSET ADDRESS SELECT HEADER



\$00400000 \$00800000 \$00C00000 \$01000000 \$01400000 \$0F400000 \$0F800000 \$0FC00000

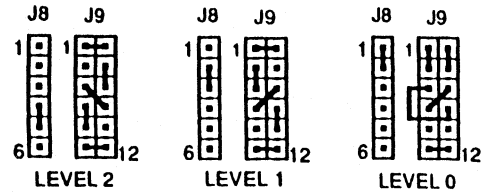
SOME OFFSETS IN BETWEEN AREN'T SHOWN BUT CAN BE FIGURED OUT WITH ABOVE SETUP.

VMEbus INTERRUPT HANDLER SELECT

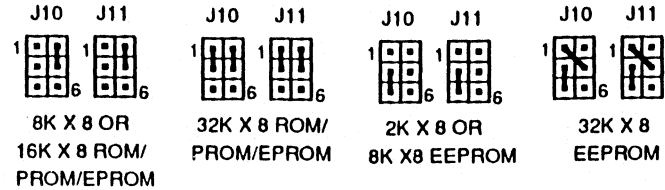


ALL INTERRUPTS (SHOWN DISABLED)

VMEbus REQUESTER LEVEL SELECT

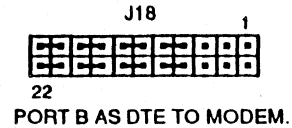


ROM/PROM/EPROM/EEPROM SIZE SELECT

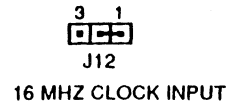


NOTE : J10 IS FOR BANK 1, J11 IS FOR BANK 2

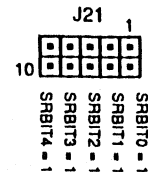
SERIAL PORT B CONFIGURATION



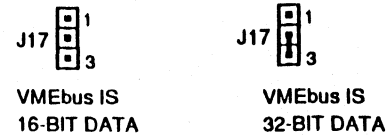
FLOATING POINT COPROCESSOR SPEED SELECT



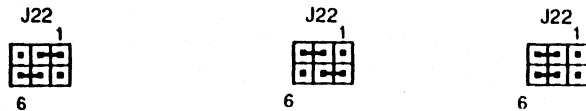
SOFTWARE ADABLE HEADER FOR MODULE STATUS REGISTER (MSR)



VMEbus DATA WIDTH SELECT



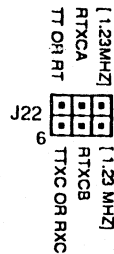
SERIAL PORT RTXCx SOURCE SELECT



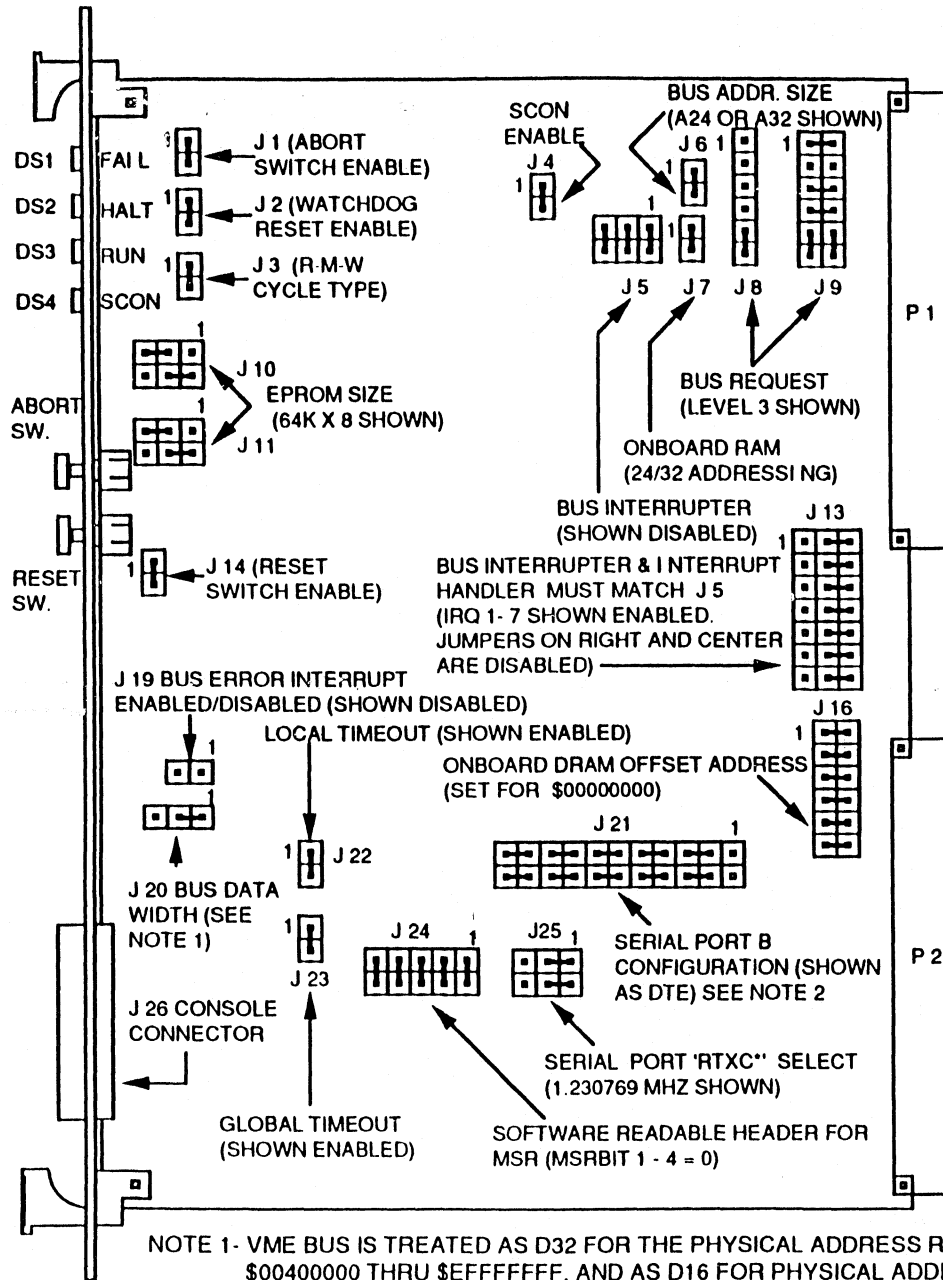
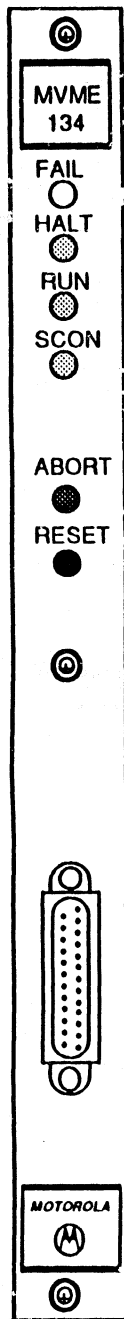
RTXCA INPUT FROM ONBOARD 1.23MHZ. RTXCB INPUT FROM TTXC IF PORT B IS CONFIGURED TO GO TO A TERMINAL, OR FROM RXC IF PORT B IS CONFIGURED TO GO TO A MODEM.

RTXCA INPUT FROM TT IF PORT A IS CONFIGURED AS A SLAVE, OR FROM RT IF PORT A IS CONFIGURED AS A MASTER. RTXCB INPUT FROM ONBOARD 1.23MHZ.

RTXCA INPUT FROM TT IF PORT A IS CONFIGURED AS A SLAVE, OR FROM RT IF PORT A IS CONFIGURED AS A MASTER. RTXCB INPUT FROM TTXC IF PORT B IS CONFIGURED TO GO TO A TERMINAL, OR FROM RXC IF PORT B IS CONFIGURED TO GO TO A MODEM.



12/14/89



NOTE 1- VME BUS IS TREATED AS D32 FOR THE PHYSICAL ADDRESS RANGE \$00400000 THRU \$FFFFFF, AND AS D16 FOR PHYSICAL ADDRESS \$F0000000 THRU \$FFFFFF AND \$FFFF0000 THRU \$FFFFFF.

NOTE 2- IF MODEM IS NOT USED ON PORT B, JUMPER ALL ODD/EVEN PIN PAIRS, I.E., 1-2, 3-4, ETC. OTHER PARAMETERS MAY BE DETERMINED BY J21 BUT ARE NOT STANDARD FOR DELTA SYSTEMS. REFER TO MOTOROLA MVME134 USER'S MANUAL, MVME134, DATED 12/87.

PART NUMBERS:

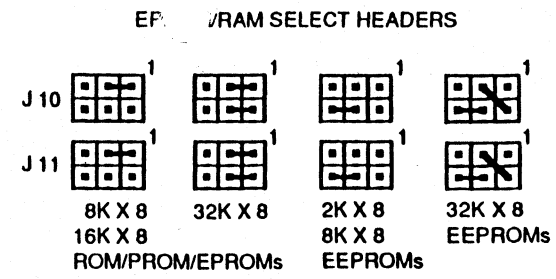
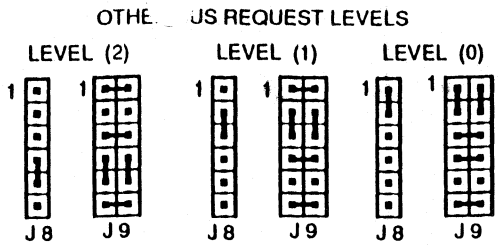
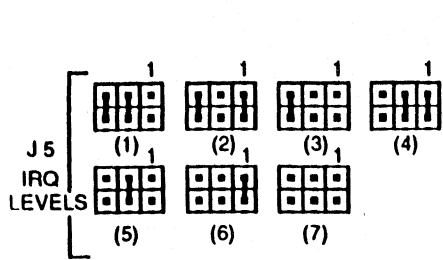
MVME134	01- W3471B02	96010929
MVME134	01- W3471B03	96010929
MVME134	01- W3471B04	96011014
MEZZ	01- W3511B01	NONE
MVME134BUG	67-W5498B01A	NONE
	51AW5371B07 U31	1.0 SA
	51AW5371B08 U12	1.0 SA
	NOT INCLUDED ON PWB.	

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

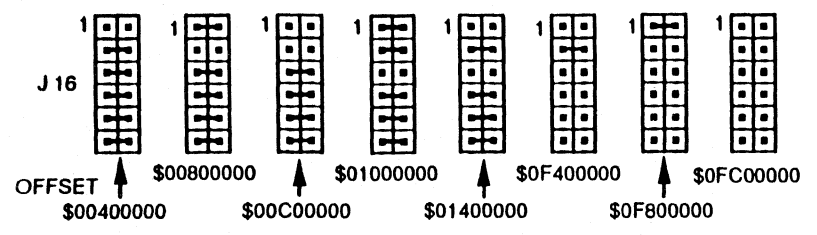
NEED TO KNOW:

1. THIS BOARD IS THE MAIN CPU IN ALL SYS2334'S AND PLUGS INTO THE LEFT-MOST SLOT.
2. THERE ARE NO J 12 OR J 15 HEADER/JUMPERS.
3. J 17 & J 18 PINS 2-3 ARE HARD WIRED FOR 4 MB OF MEMORY, NOT SHOWN.
4. J 20 W/ NO JUMPERS ENABLES 16-BIT OPERATION. THAT MEANS, LWORD* IS DISABLED ALLOWING ONLY 16-BIT WORD TRANSFERS.

11/08/91

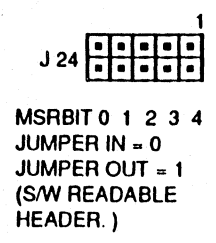


ADDRESS RANGE OFFSET JUMPERS

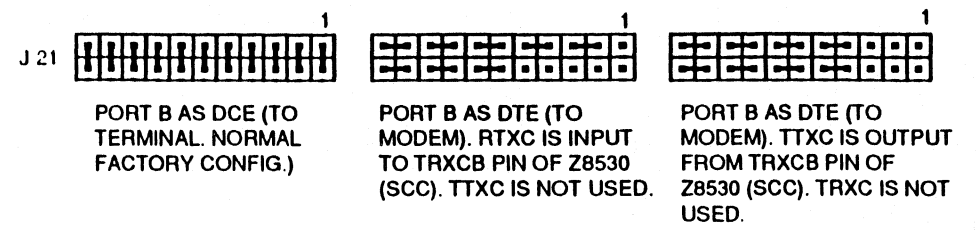


NOTE: ADDRESS RANGES FROM \$01800000 TO \$0F000000 ARE NOT SEEN. LOGICAL SETTING IS ACCOMPLISHED BY FOLLOWING THE SEQUENCE STARTED ABOVE.

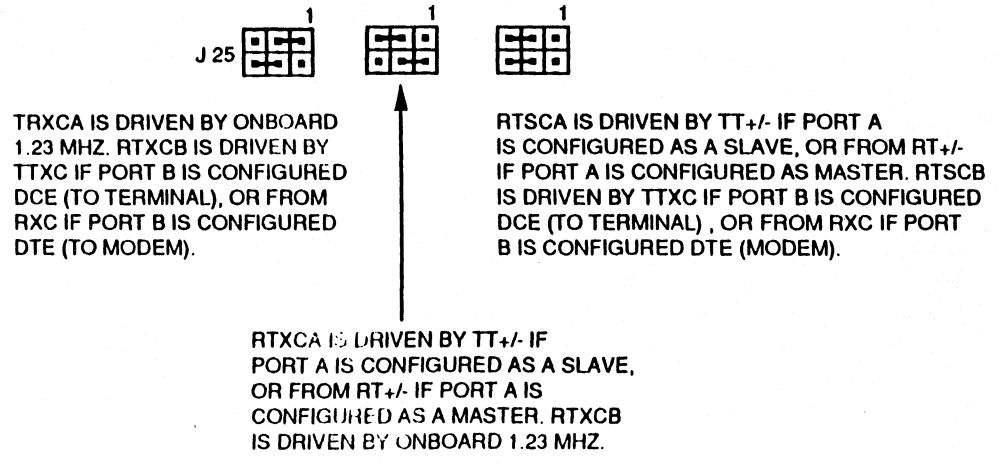
SOFTWARE READABLE HEADER



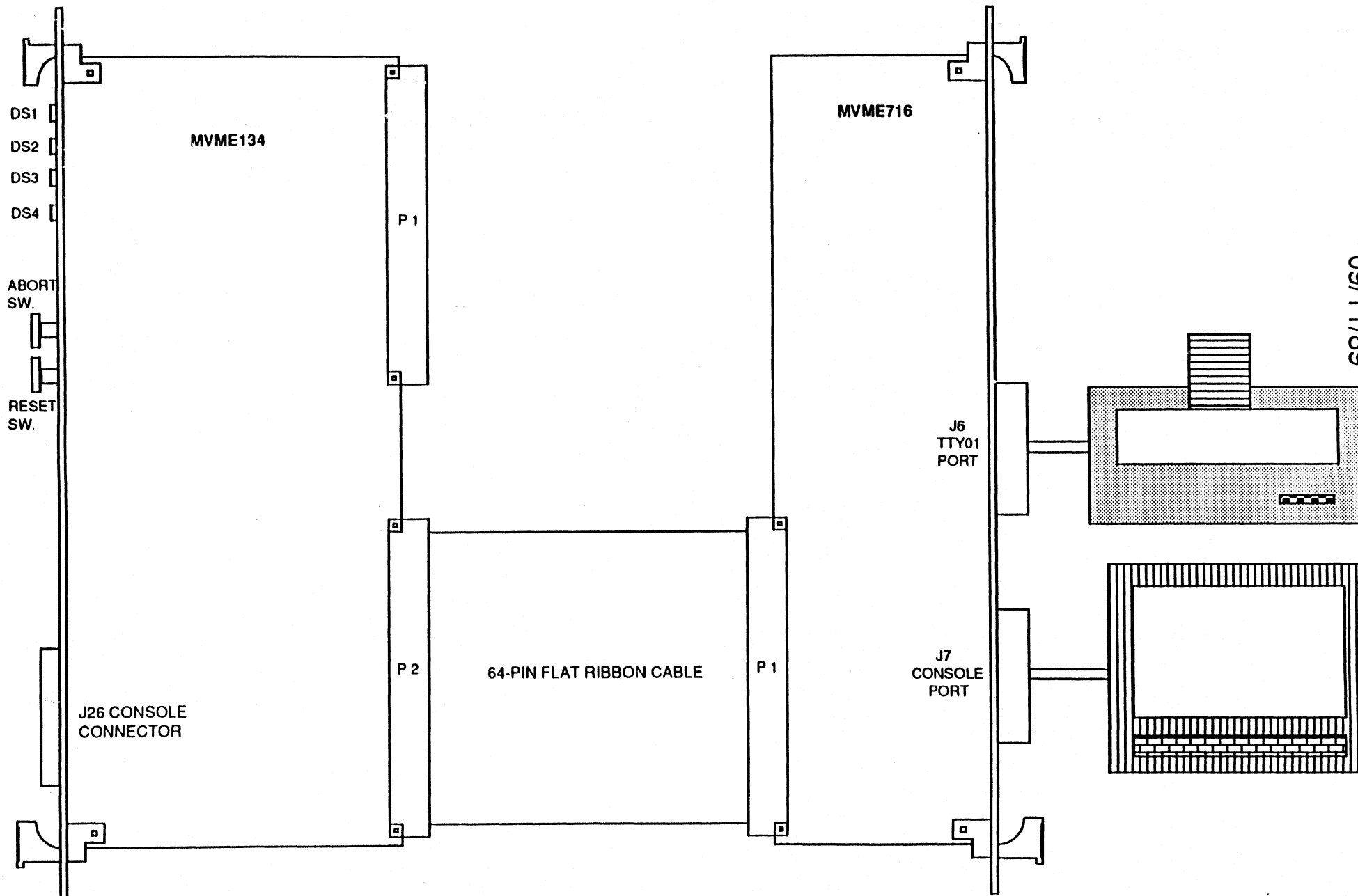
SERIAL PORT B CONFIGURATION



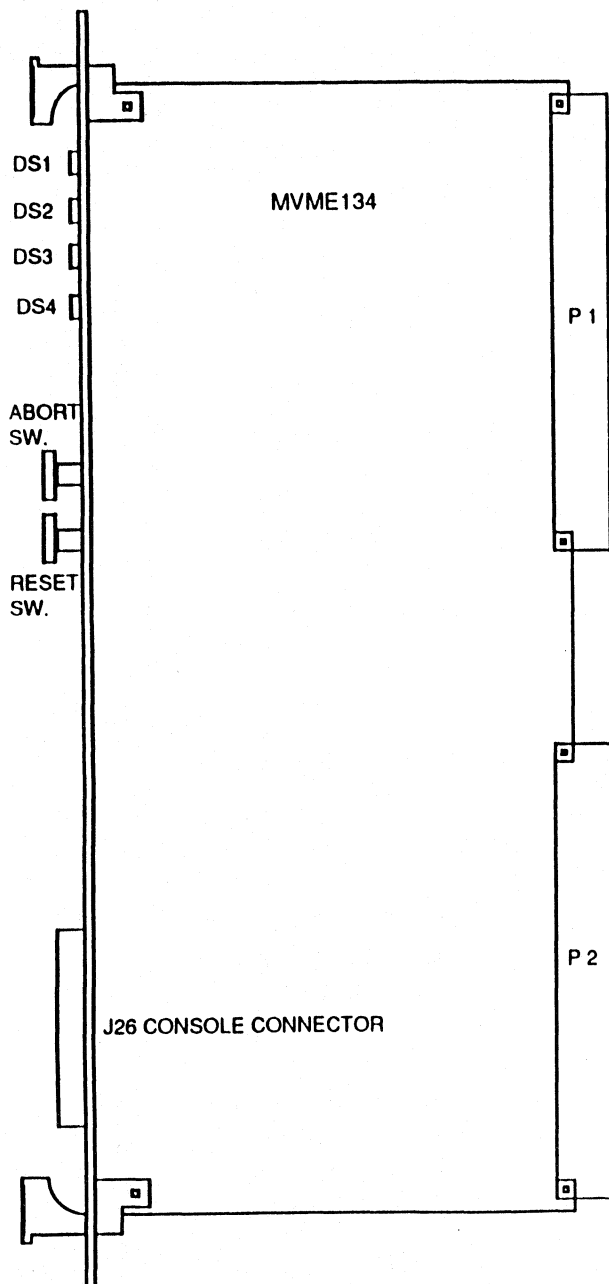
SERIAL PORT RTXC* SELECT



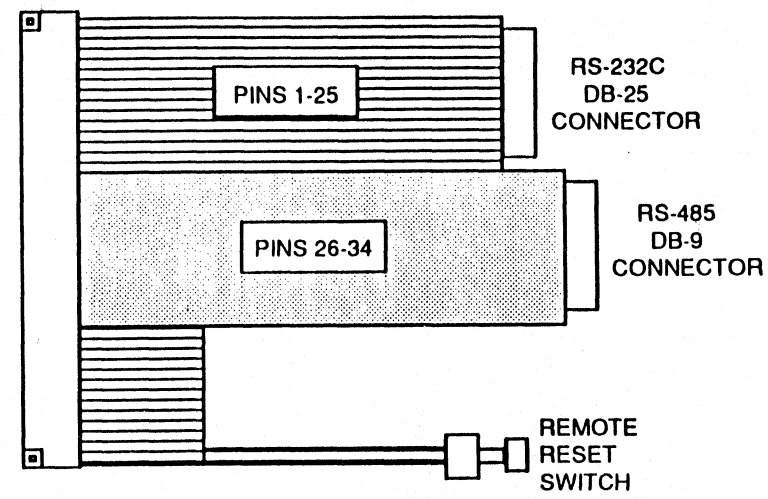
09/11/89



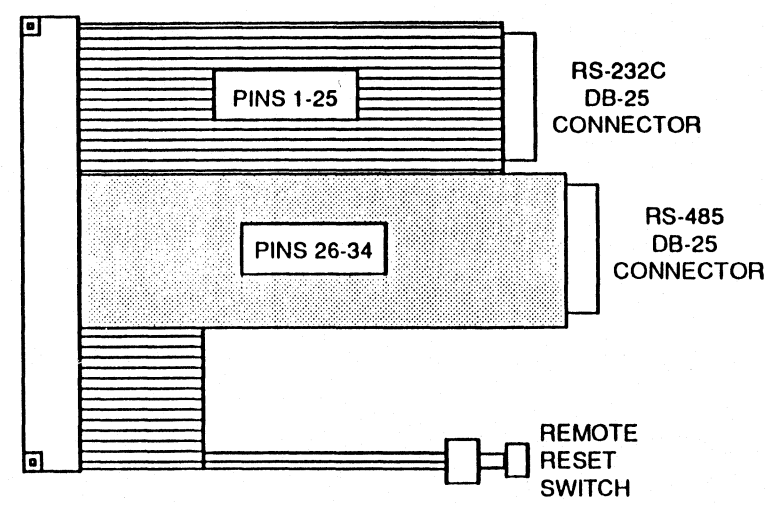
09/11/89



SUGGESTION #1



SUGGESTION #2



NOTE : A 64-PIN MATING CONNECTOR IS USED AND PLUGS INTO P2 ON THE MVME134 BOARD FOR BOTH SUGGESTIONS ABOVE.

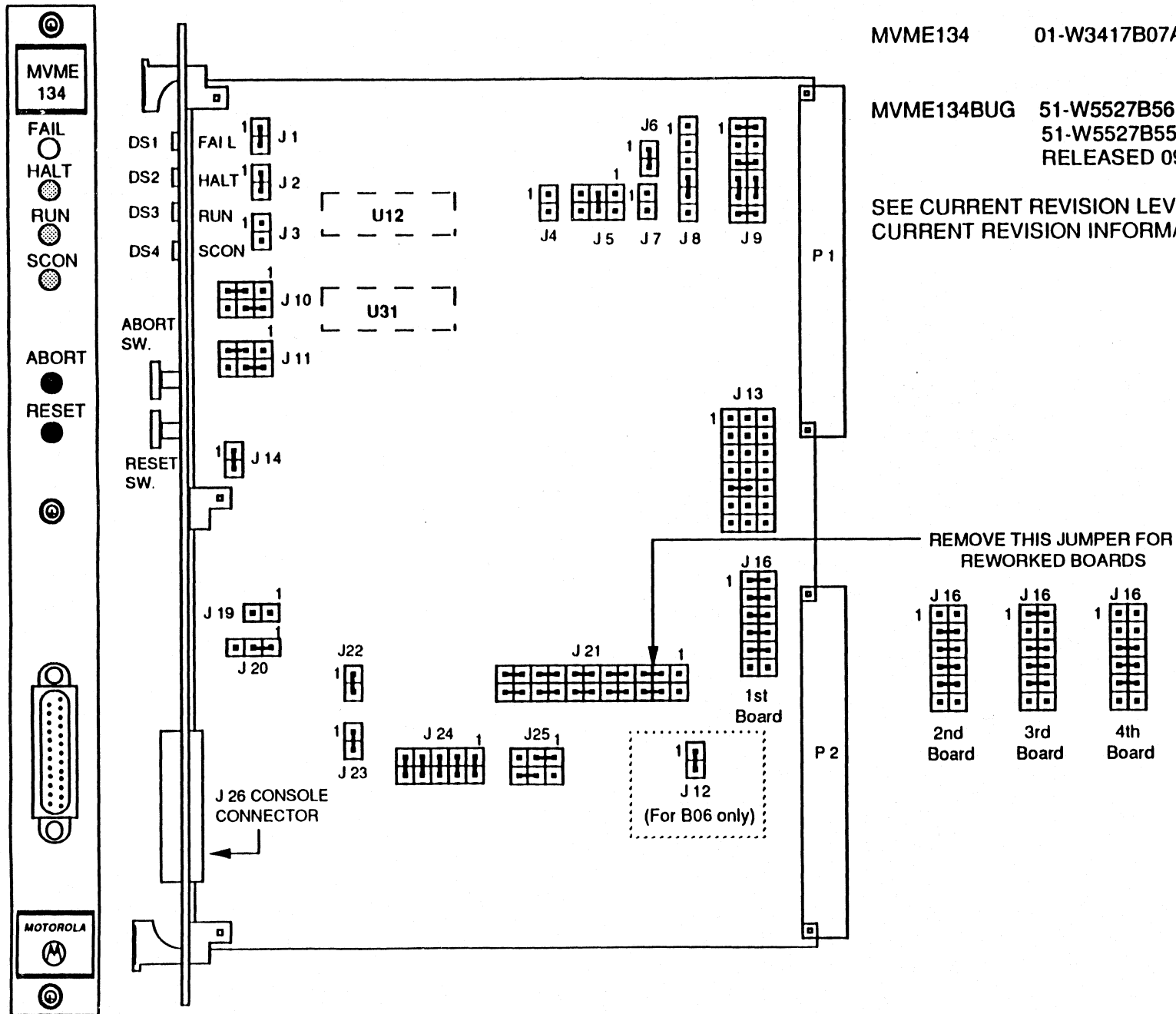
09/11/89

PART NUMBERS:

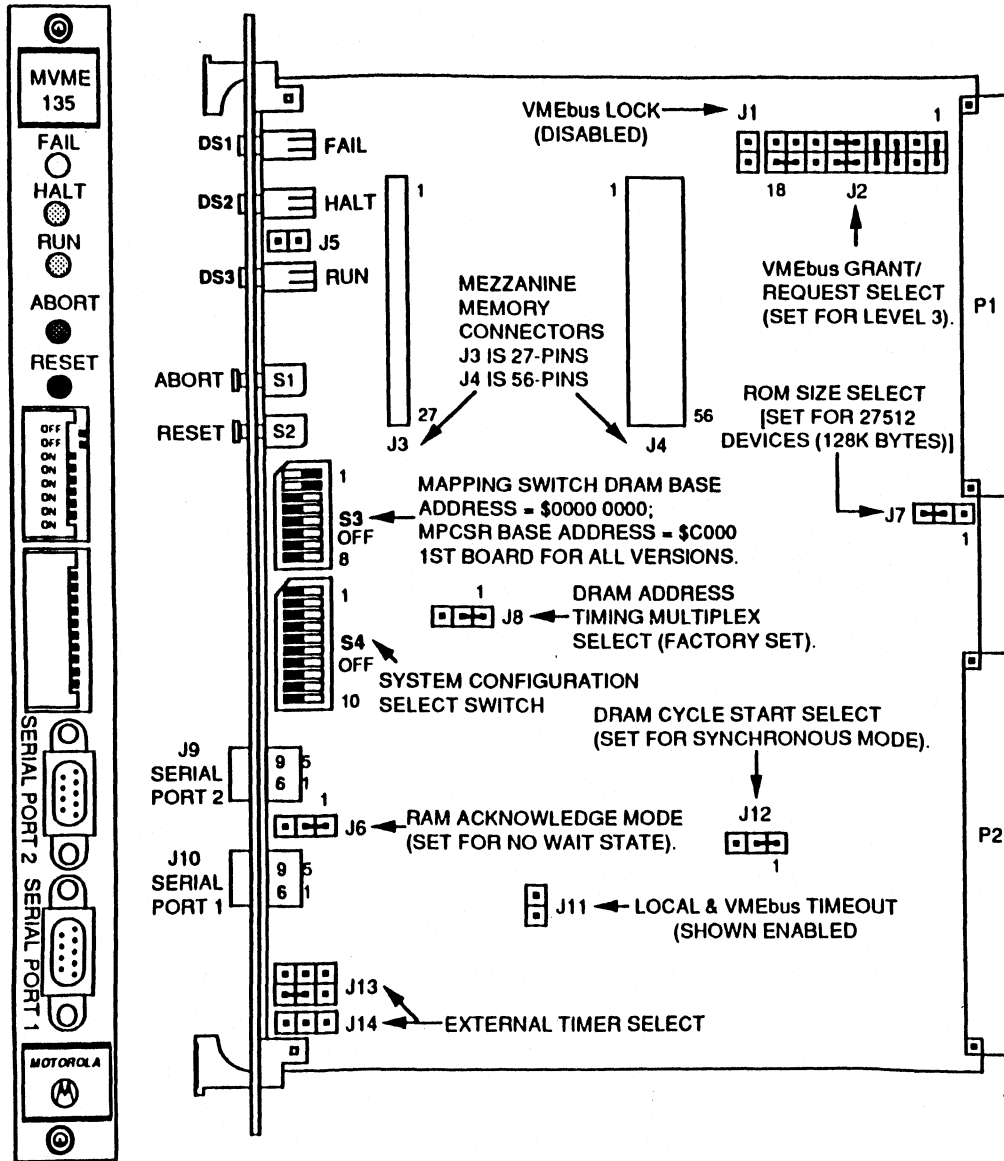
MVME134 01-W3417B07A 96011502

MVME134BUG 51-W5527B56 U31 REV. 2.0
51-W5527B55 U12 REV. 2.0
RELEASED 09/15/88

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.



11/01/91



PART NUMBERS:

MVME135	01-W3440B03	76435381
MVME135-1	01-W3440B07	76435392
MVME135A	01-W3440B11	96010997
MVME135A-1	NOT ON FILE	
MVME135DIFNV	01-W3440B05	76435575
MVME135XT	NOT ON FILE	
MVME135MEZ4	01-W3486B01	96010999

MVME136	01-W3440B04	76435382
MVME136A	01-W3440B08	96010998
MVME136XT	NOT ON FILE	
MVME136MEZZ	NOT ON FILE	

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: J5 IS A FACTORY TEST JUMPER AND IS NOT INSTALLED.

NOTE 2: J9 & J10 ARE BOTH DB-9 RS-232C CONNECTORS.

NOTE 3: ACTIVE PART OF SWITCH IS DARKENED AREA.

09/11/89

BUS GRANT/REQUEST SELECT

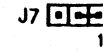


RAM ACKNOWLEDGE MODE SELECT



SET FOR ONE WAIT STATE

ROM SIZE SELECT



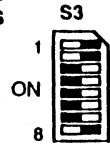
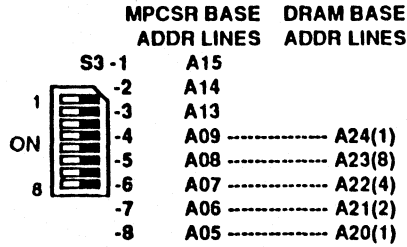
SET FOR 27256K (64K DEVICES)

DRAM CYCLE START SELECT

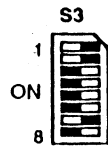


SET FOR ASYNCHRONOUS MODE

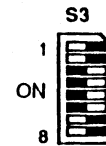
VMEBUS ADDRESS MAPPING SWITCH FOR VME135/135-1/136 ONLY



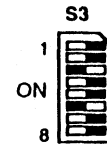
DRAM BASE ADDR = \$0010 0000
MPCSR BASE ADDR = \$C020
2ND BOARD



DRAM BASE ADDR = \$0020 0000
MPCSR BASE ADDR = \$C040
3RD BOARD



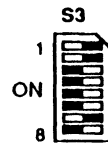
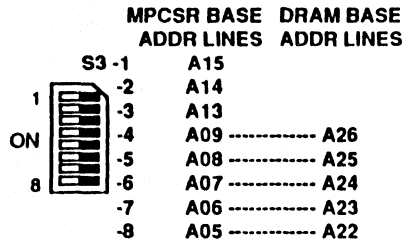
DRAM BASE ADDR = \$0030 0000
MPCSR BASE ADDR = \$C060
4TH BOARD



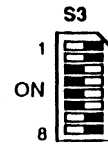
DRAM BASE ADDR = \$0040 0000
MPCSR BASE ADDR = \$C080
5TH BOARD

NOTE : MAPPING IS DONE ON 1 MB BOUNDARIES. THEREFORE 0-32 MB MAPPING ON VME135/135-1/136 IS POSSIBLE.

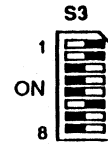
VMEBUS ADDRESS MAPPING SWITCH FOR VME136A ONLY



DRAM BASE ADDR = \$0080 0000
MPCSR BASE ADDR = \$C040
2ND BOARD



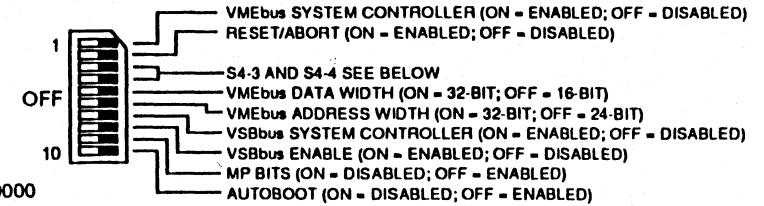
DRAM BASE ADDR = \$00C0 0000
MPCSR BASE ADDR = \$C060
3RD BOARD



DRAM BASE ADDR = \$0100 0000
MPCSR BASE ADDR = \$C080
4TH BOARD

NOTE : MAPPING IS DONE ON 4 MB BOUNDARIES. THEREFORE 0-128 MB MAPPING ON VME136A IS POSSIBLE.

SYSTEM CONFIGURATION SELECT SWITCH



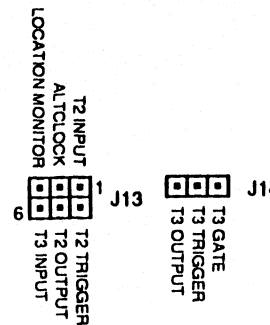
S4-3 OFF = OFF-BOARD VMEbus MEMORY
S4-4 OFF = 135bug EXECUTES IN FIRST

S4-3 OFF = VMEbus BASE
S4-4 ON = 135bug EXECUTES OVER

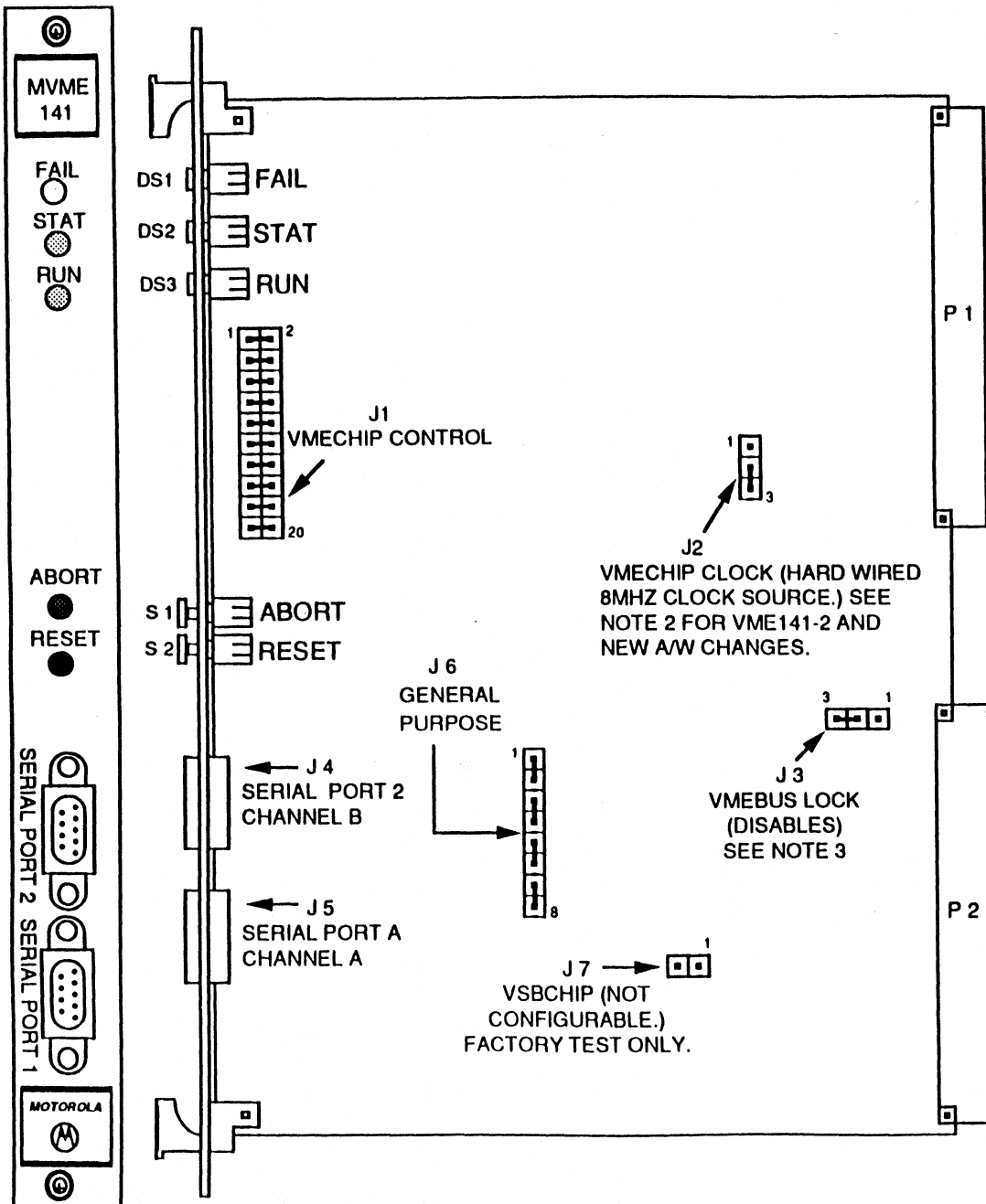
S4-3 ON = DRAM MAPPED AT \$FFX00000
S4-4 OFF = 135bug EXECUTES LOCALLY

S4-3 ON = DRAM MAPPED AT \$00000000
S4-4 ON = 135bug EXECUTES LOCALLY

EXTERNAL TIMER SELECT



09/1/89

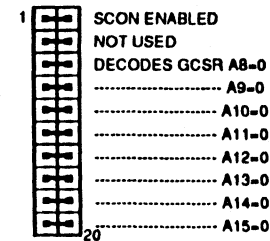


PART NUMBERS:

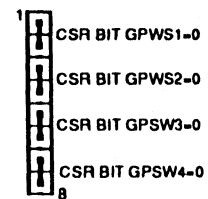
MVME141	25MHZ	01-W3528B01	96010995
MVME141-2	33MHZ	01-W3528B02	96011031
MVME141-3	50MHZ	01-W3528B03	96011242
MVME141	W/ASE F/W	01-WXXXXBXX	96011171
MVMW141-2	W/ASE F/W	01-WXXXXBXX	96011170
MVME141BUG	51-W5895B01	U78 EVEN	NONE
	51-W5895B02	U56 ODD	NONE
		REV. 1.7	INCLUDED ON PWB.

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

J 1 VMECHIP CONTROL



J 6 GENERAL PURPOSE



J2
VMECHIP CLOCK (HARD WIRED 8MHZ CLOCK SOURCE.) SEE NOTE 2 FOR VME141-2 AND NEW A/W CHANGES.

J3
VMEBUS LOCK (DISABLES) SEE NOTE 3

J 6
GENERAL PURPOSE

J 4
SERIAL PORT 2 CHANNEL B

J 5
SERIAL PORT A CHANNEL A

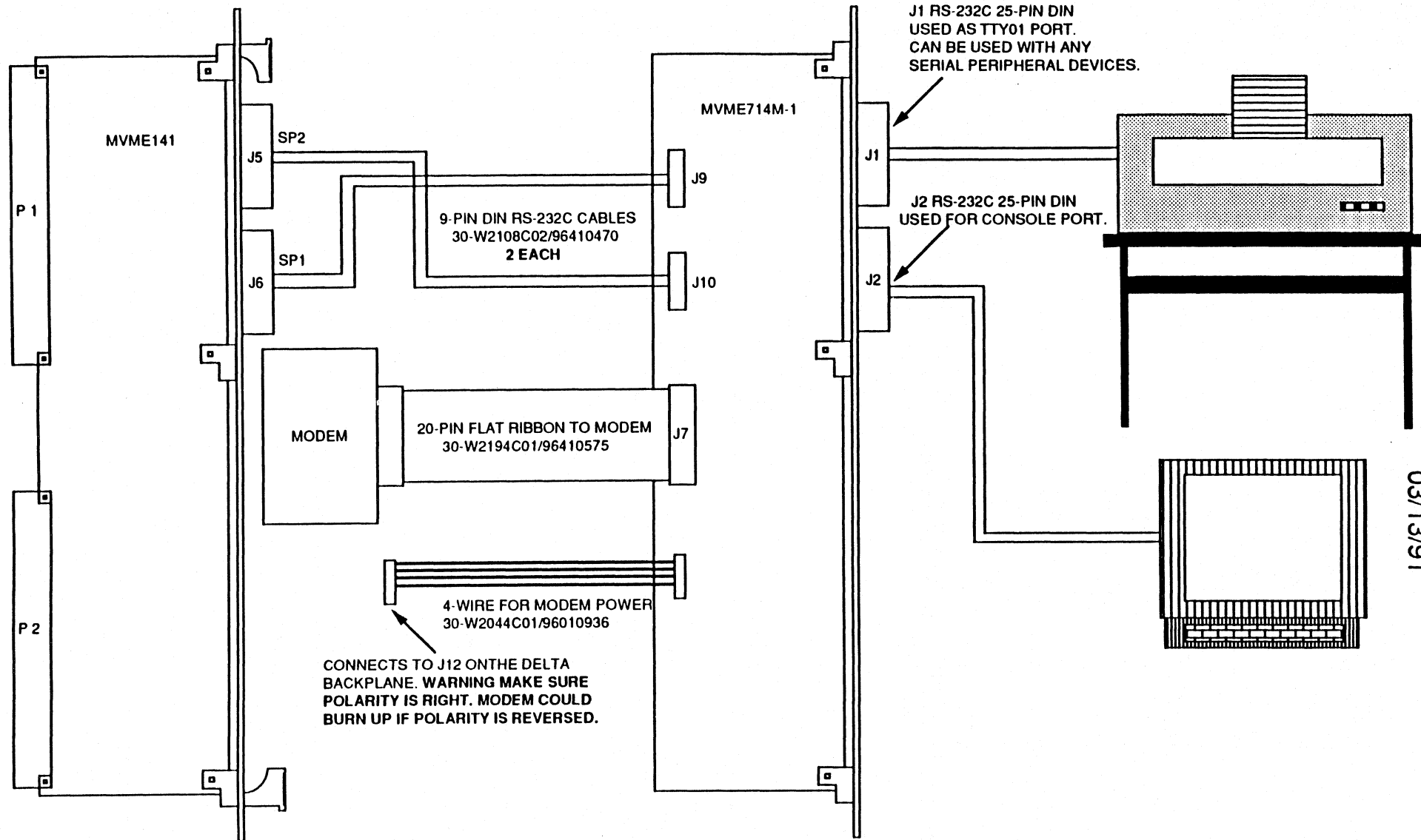
J 7
VSBCHIP (NOT CONFIGURABLE.) FACTORY TEST ONLY.

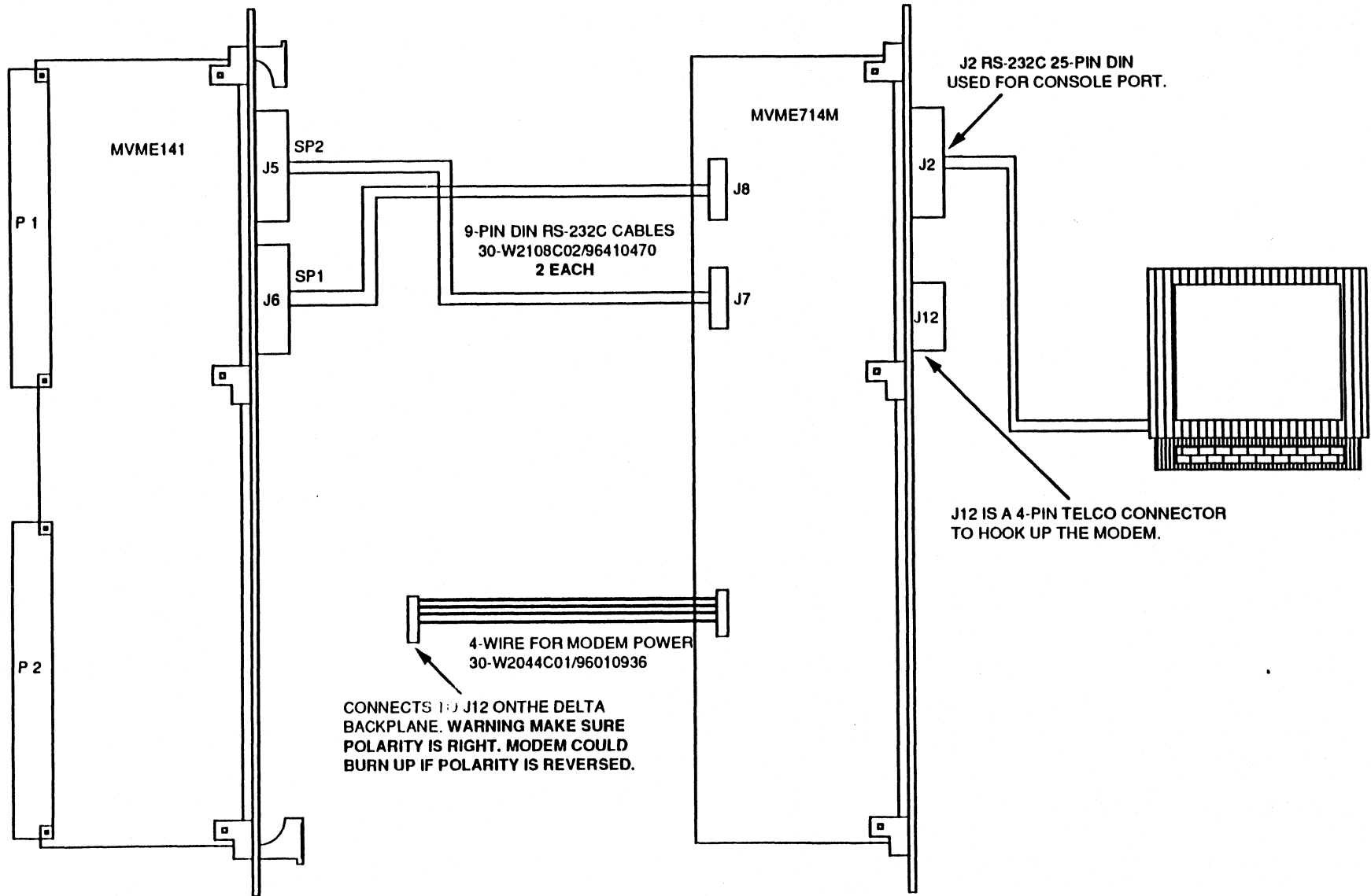
NOTE 1: THIS CONFIGURATION ALSO USED IN SYS3640's.

NOTE 2: J2 IS NOT ON THE VME141-2/-3 OR NEW A/W.

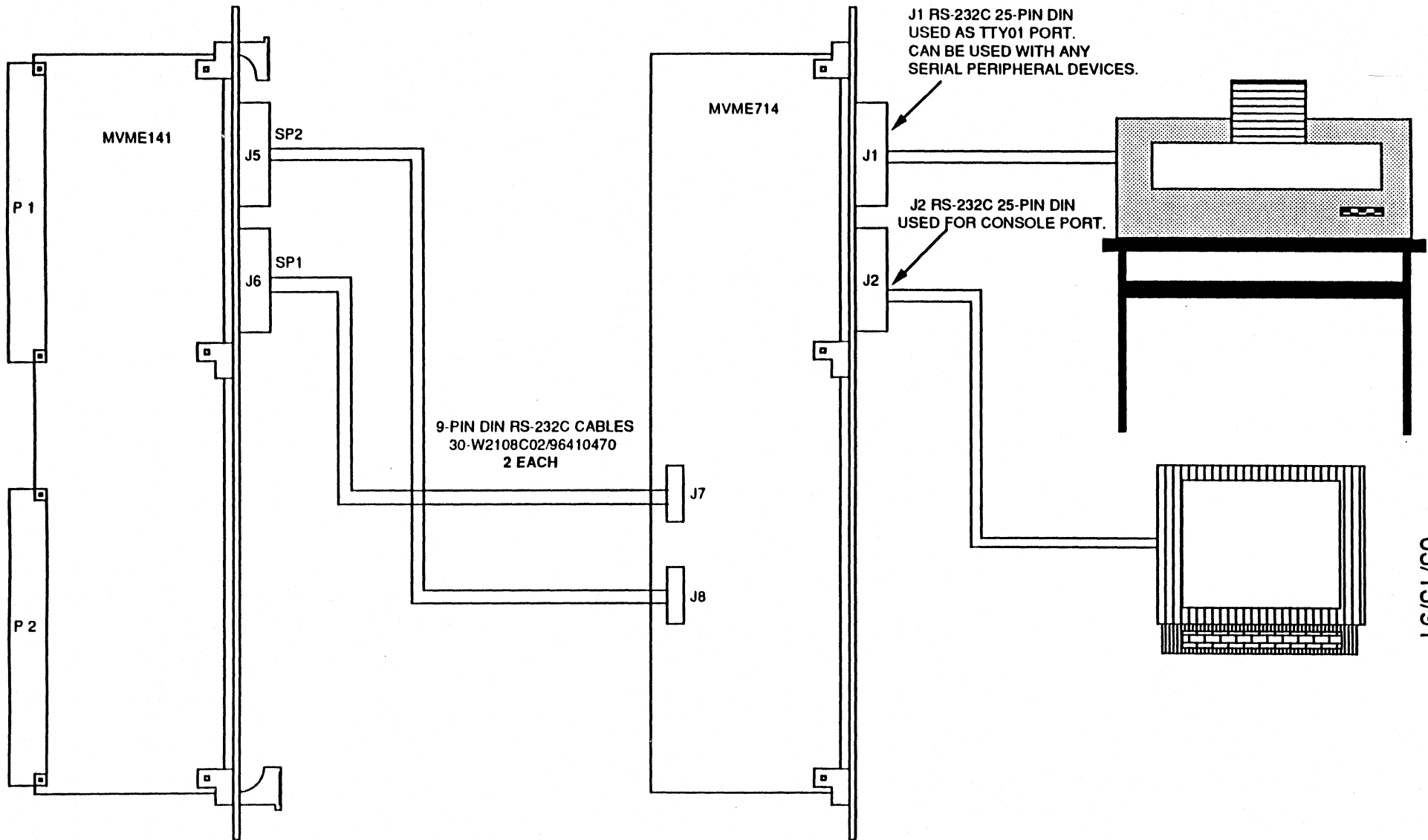
NOTE 3: ON DUAL PORTED BOARDS, PREVENTS ALTERNATE SOURCE FROM GAINING ACCESS OF MEMORY RESOURCE DURING AN INDIVISIBLE BLOCK CYCLE. P2-B3 IS USED AS SIGNAL PIN TO ENABLE IT.

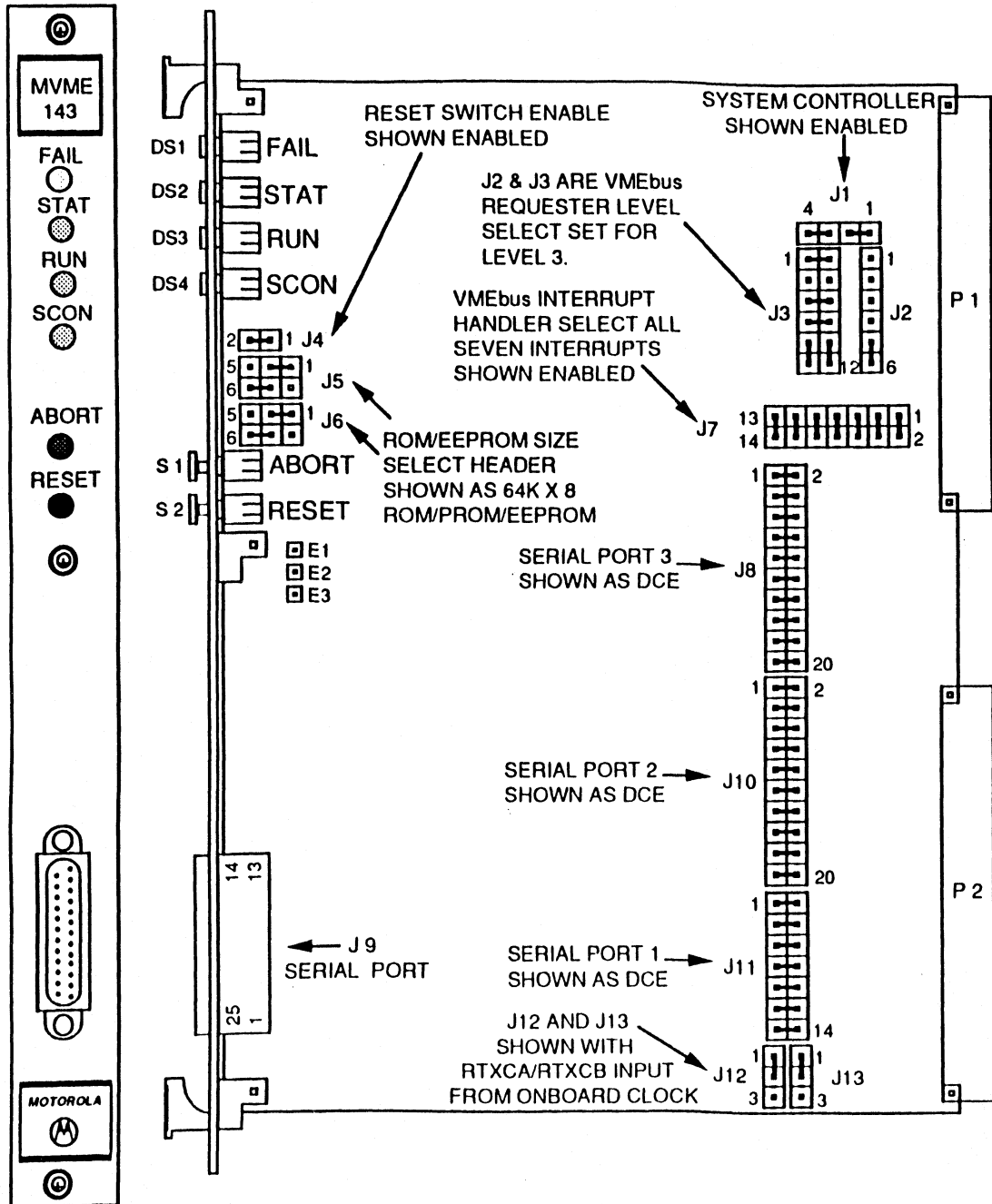
03/13/91





03/13/91





PART NUMBERS:

MVME143 01-W3534B01 96011021

MVME143-1 01-W3534B02 96011057

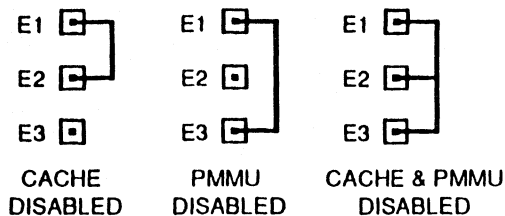
MVME143-2 01-W3534B02 96010919

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

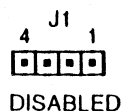
09/11/89

MVME143
**32-BIT VMEbus/
VSB-BASED
MICROCOMPUTER**
PAGE 1 C 1

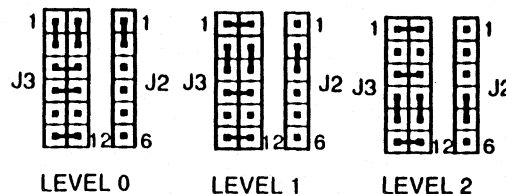
**ONCHIP CACHE AND PMMU
CONTROL SELECT TERMINALS**



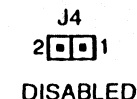
**SYSTEM CONTROLLER
SELECT**



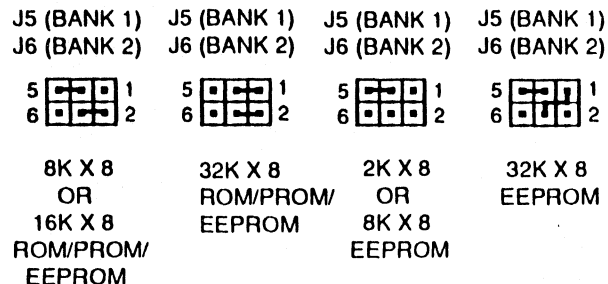
VMEbus REQUEST LEVEL SELECT



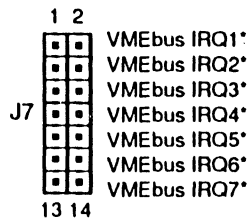
**RESET SWITCH
ENABLE**



ROM/PROM/EEPROM SIZE SELECT

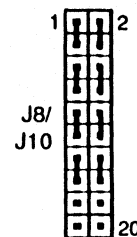


**VMEbus INTERRUPT
HANDLER SELECT**



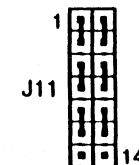
ALL SEVEN INTERRUPTS
SHOWN DISABLED

**SERIAL PORT CONFIGURATION
SELECT HEADER**



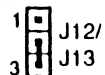
PORTS 2 OR 3
CONFIGURED AS DTE

**SERIAL PORT 1
CONFIGURATION
SELECT HEADER**



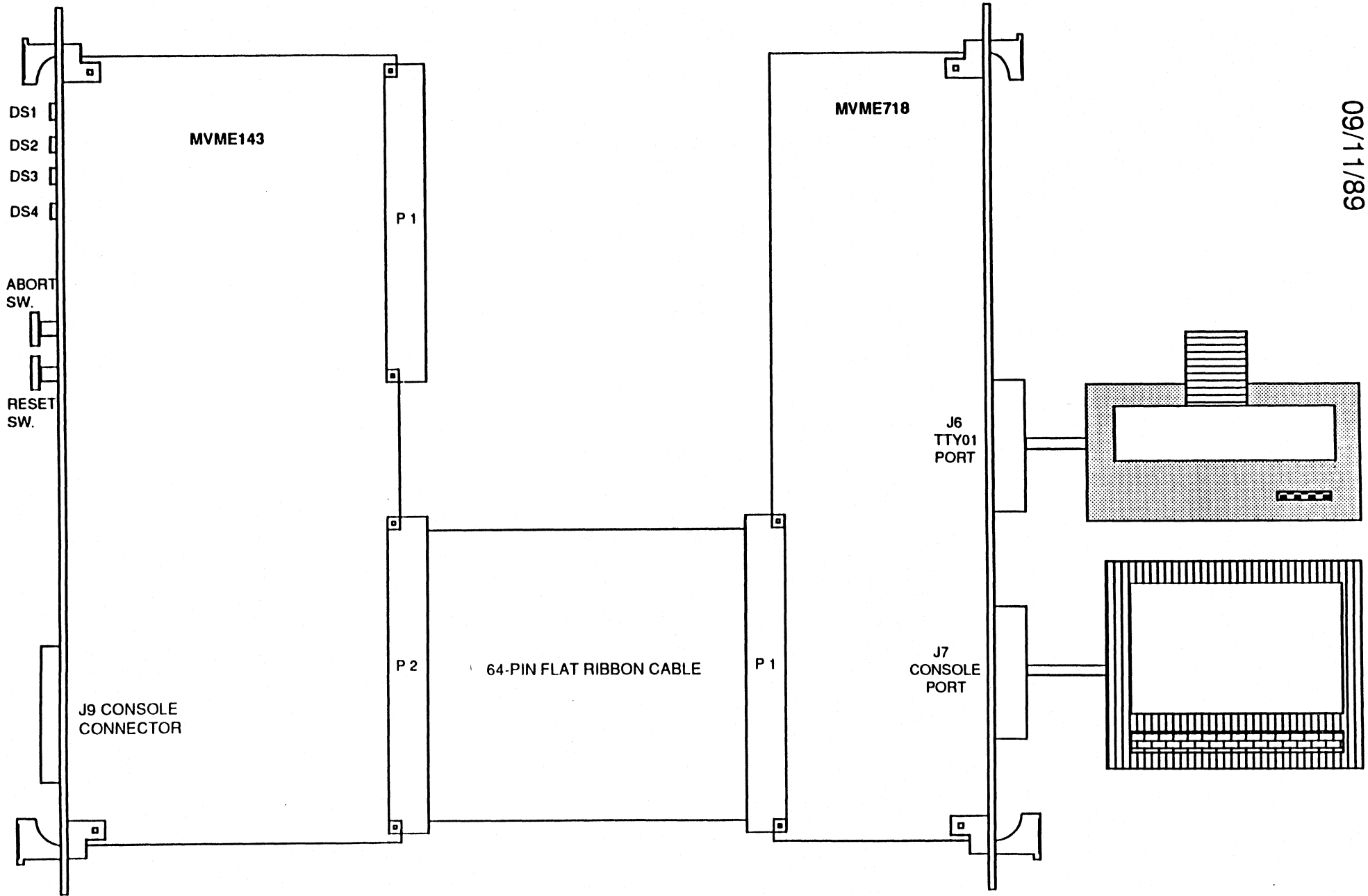
PORT 1
CONFIGURED
AS DTE

**SCC RTXCA AND
RTXCB SOURCE
SELECT HEADER**

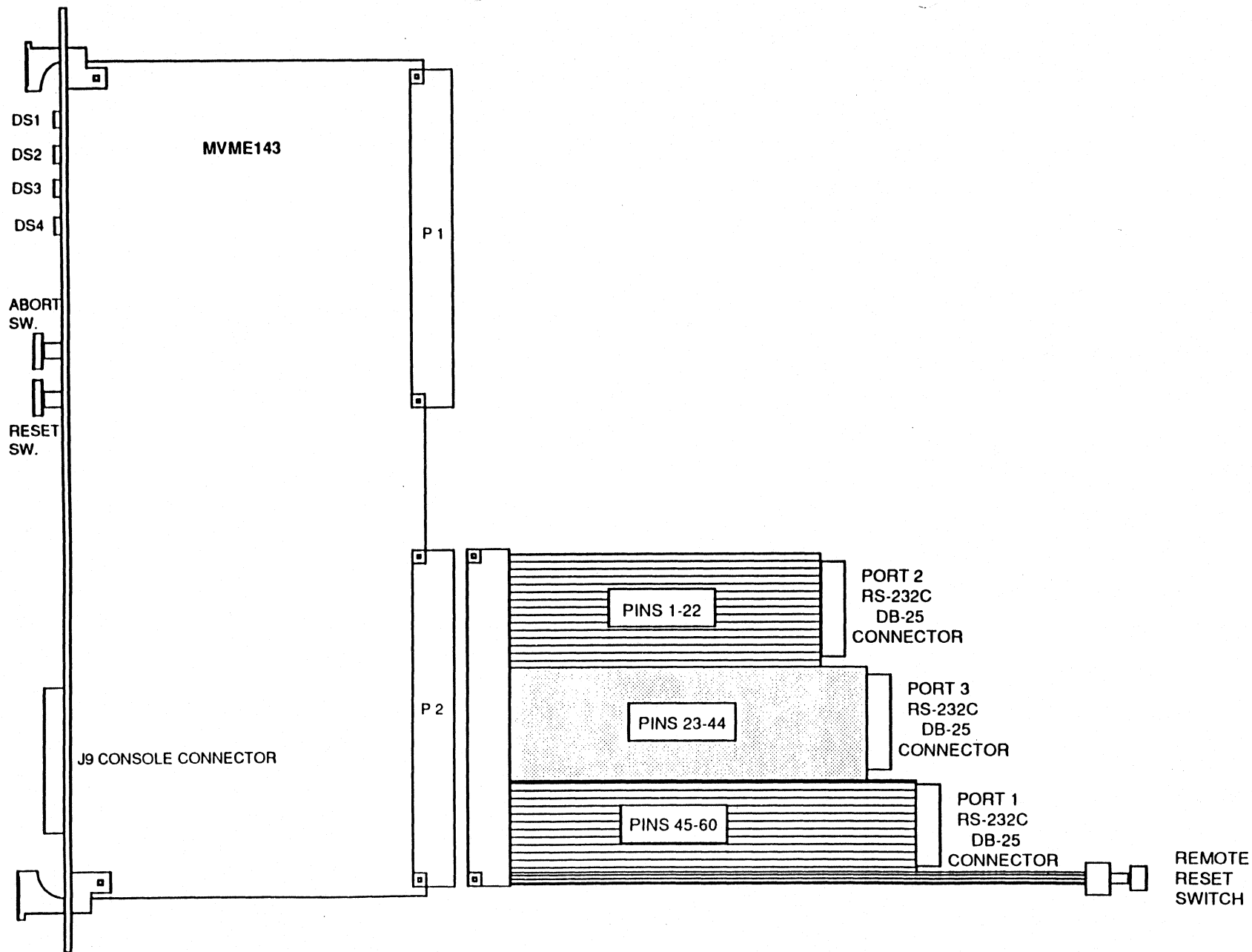


RTXCA AND RTXCB INPUT
FROM TXC02 OR RXC03 IF
PORT 2 OR 3 IS DCE AND
FROM TXC12 OR TXC13 IF
PORT 2 OR 3 IS DTE

09/1/89

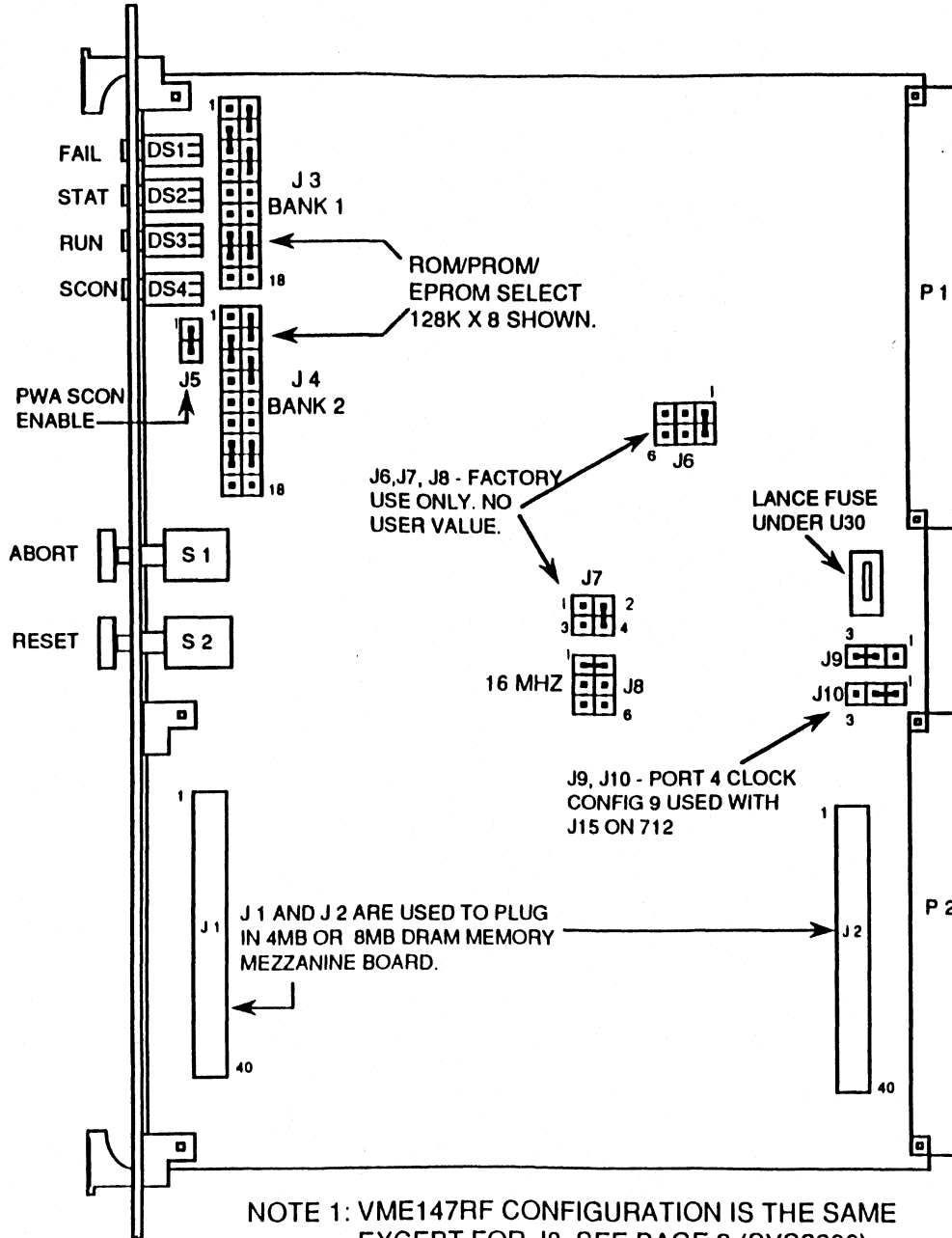
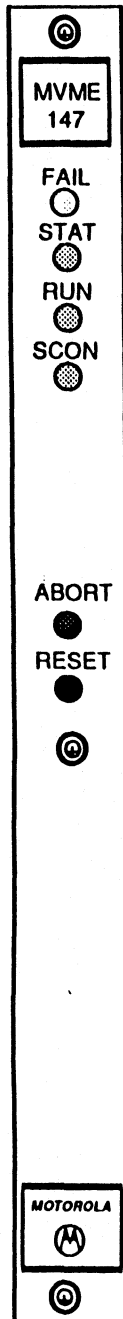


09/11/89



09/11/89

NOTE : A 64-PIN MATING CONNECTOR IS USED AND PLUGS INTO P2 ON THE MVME143 BOARD FOR BOTH SUGGESTIONS ABOVE.



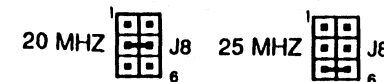
NOTE 1: VME147RF CONFIGURATION IS THE SAME EXCEPT FOR J8. SEE PAGE 3 (SYS3200).

PART NUMBERS:

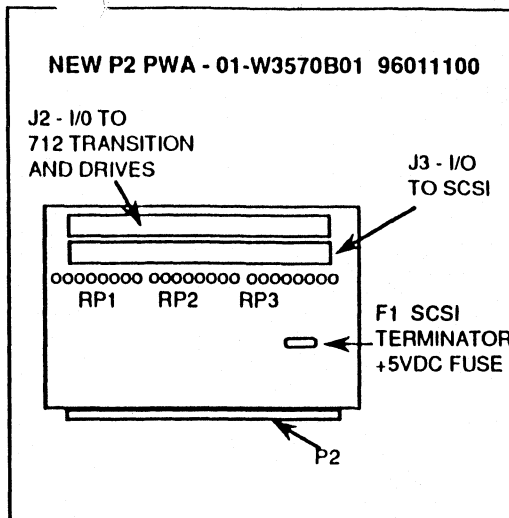
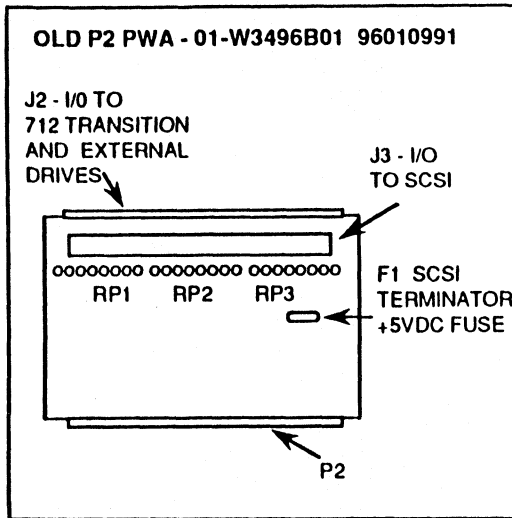
MVME147	4MB, 20MHZ	01-W3520B03	96011003
MVME147A	8MB, 20MHZ	01-W3520B13	96011004
MVME147-1	4MB, 25MHZ	01-W3520B04	96011024
MVME147A-1	8MB, 25MHZ	01-W3520B14	96011017
MVME147RF	4MB, 16MHZ	01-W3520B21	96011007
MVME147A	8MB/PAGE, 20MHZ	01-W3520B15	96011111
MVME147A-1	8MB/PAGE, 25MHZ	01-W3520B16	96011108
MVME147A-1	8MB/PAGE, 25MHZ	01-W3520B17	96011112
MVME147P2	ADAPTER	01-W3469B02	96011006 (OLD)
MVME147P2	ADAPTER	01-W3570B01	96011100 (NEW)
MVME147FW22	67-W2498C01A REV 2.2 NOT INCLUDED WITH PWB. 51-W5741B25 U2, AND 51-W5741B26 U1.		

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

**J6, J7 J8 FOR FACTORY USE ONLY
DO NOT CHANGE.**

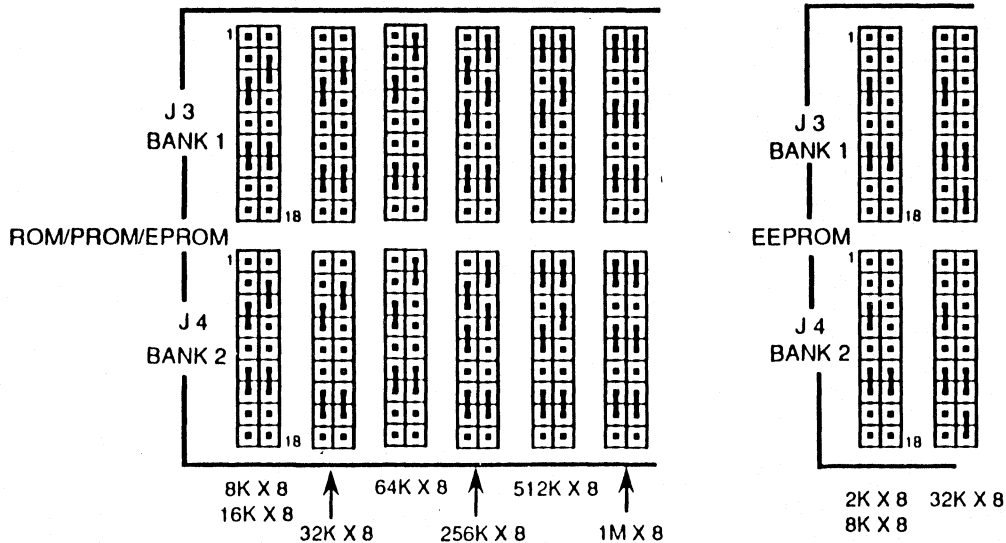


03/13/91

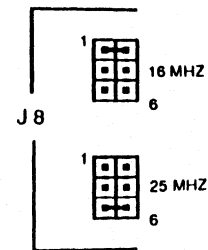


NOTE: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

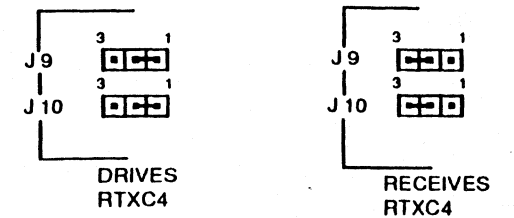
ROM/PROM/EPROM/EEPROM SIZE SELECT HEADERS



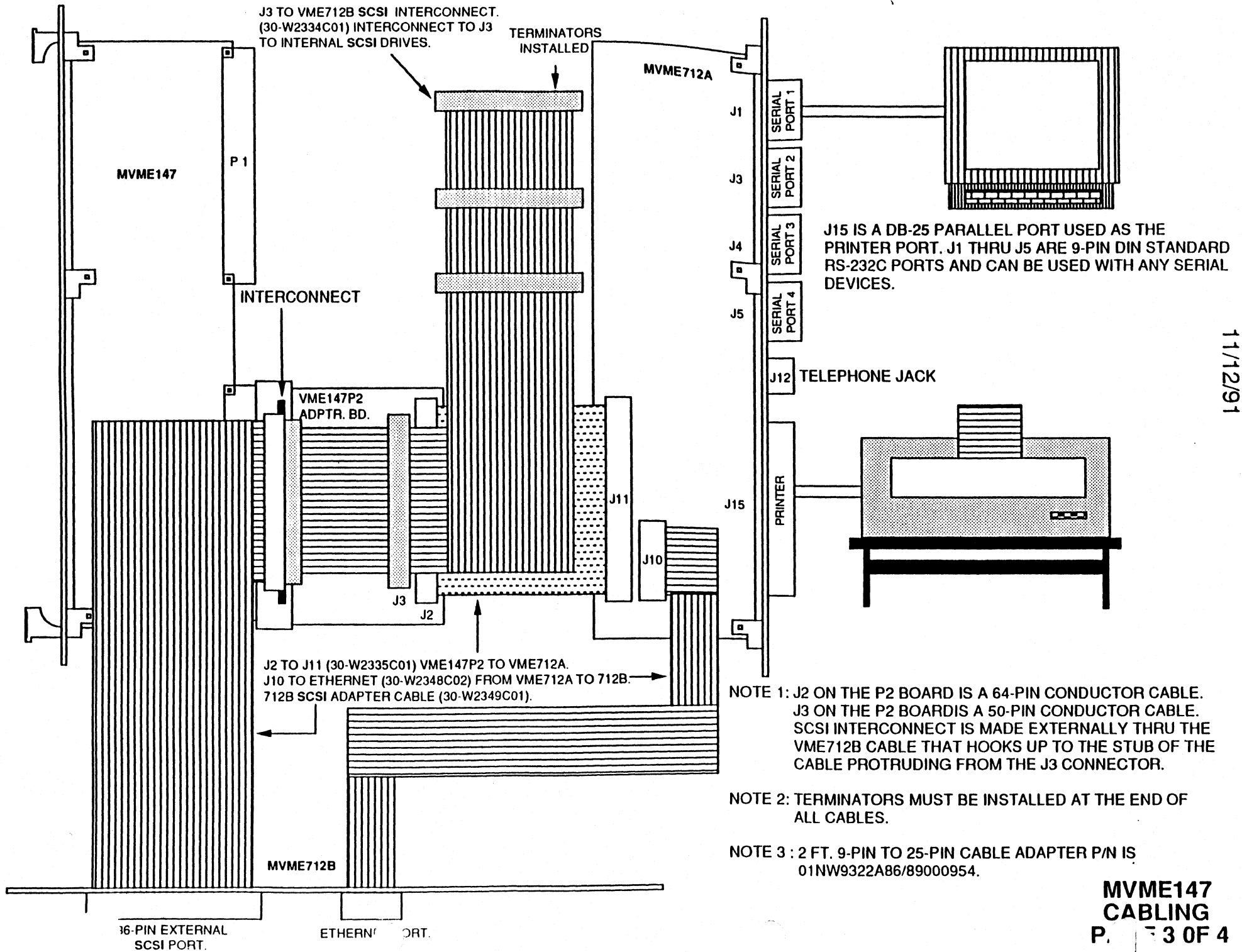
INTERNAL CLOCK FREQUENCY



SERIAL PORT 4 CLOCK CONFIGURATION



03/14/91

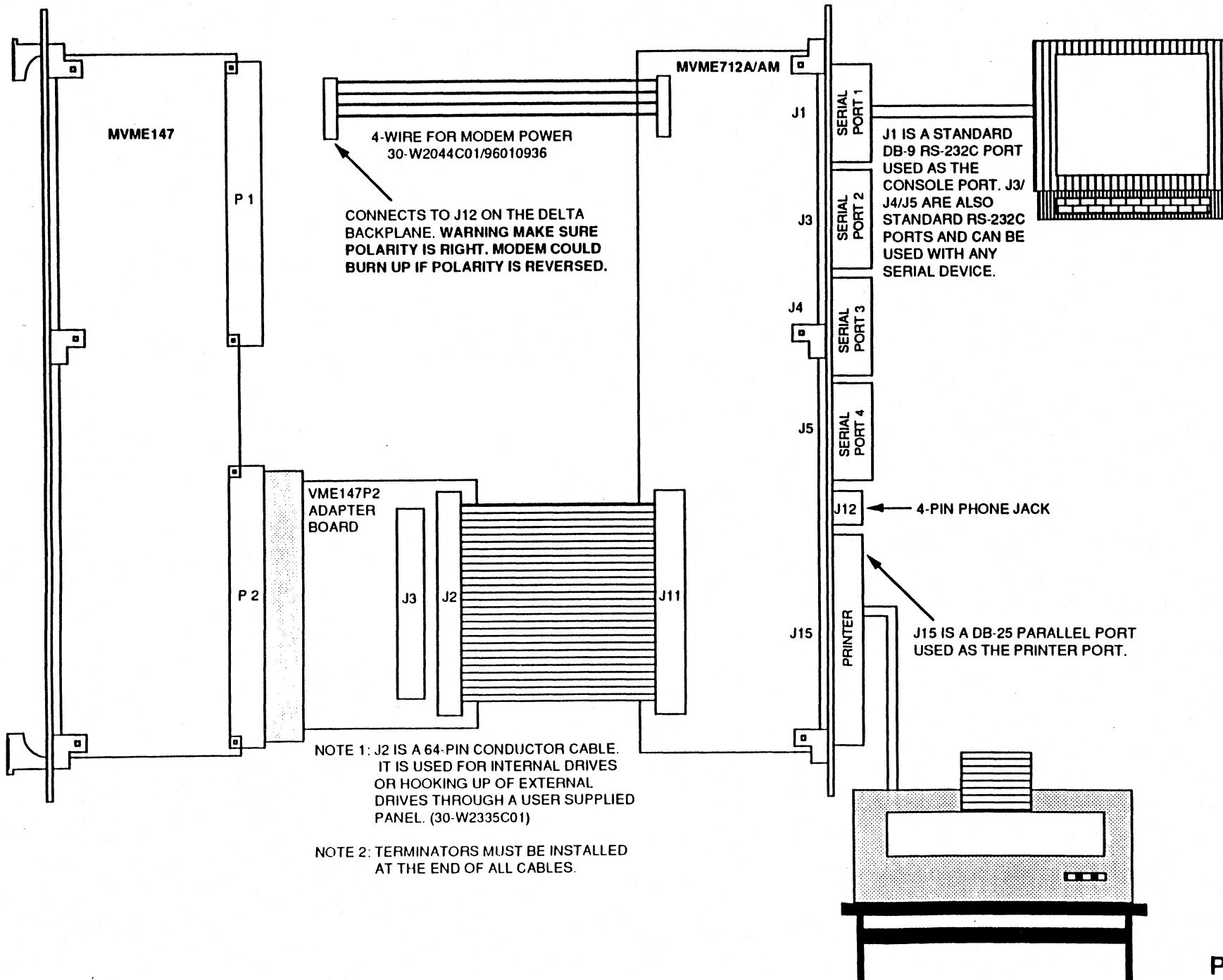


11/12/91

NOTE 1: J2 ON THE P2 BOARD IS A 64-PIN CONDUCTOR CABLE. J3 ON THE P2 BOARD IS A 50-PIN CONDUCTOR CABLE. SCSI INTERCONNECT IS MADE EXTERNALLY THRU THE VME712B CABLE THAT HOOKS UP TO THE STUB OF THE CABLE PROTRUDING FROM THE J3 CONNECTOR.

NOTE 2: TERMINATORS MUST BE INSTALLED AT THE END OF ALL CABLES.

NOTE 3: 2 FT. 9-PIN TO 25-PIN CABLE ADAPTER P/N IS 01NW9322A86/89000954.



4-WIRE FOR MODEM POWER
30-W2044C01/96010936

CONNECTS TO J12 ON THE DELTA
BACKPLANE. WARNING MAKE SURE
POLARITY IS RIGHT. MODEM COULD
BURN UP IF POLARITY IS REVERSED.

VME147P2
ADAPTER
BOARD

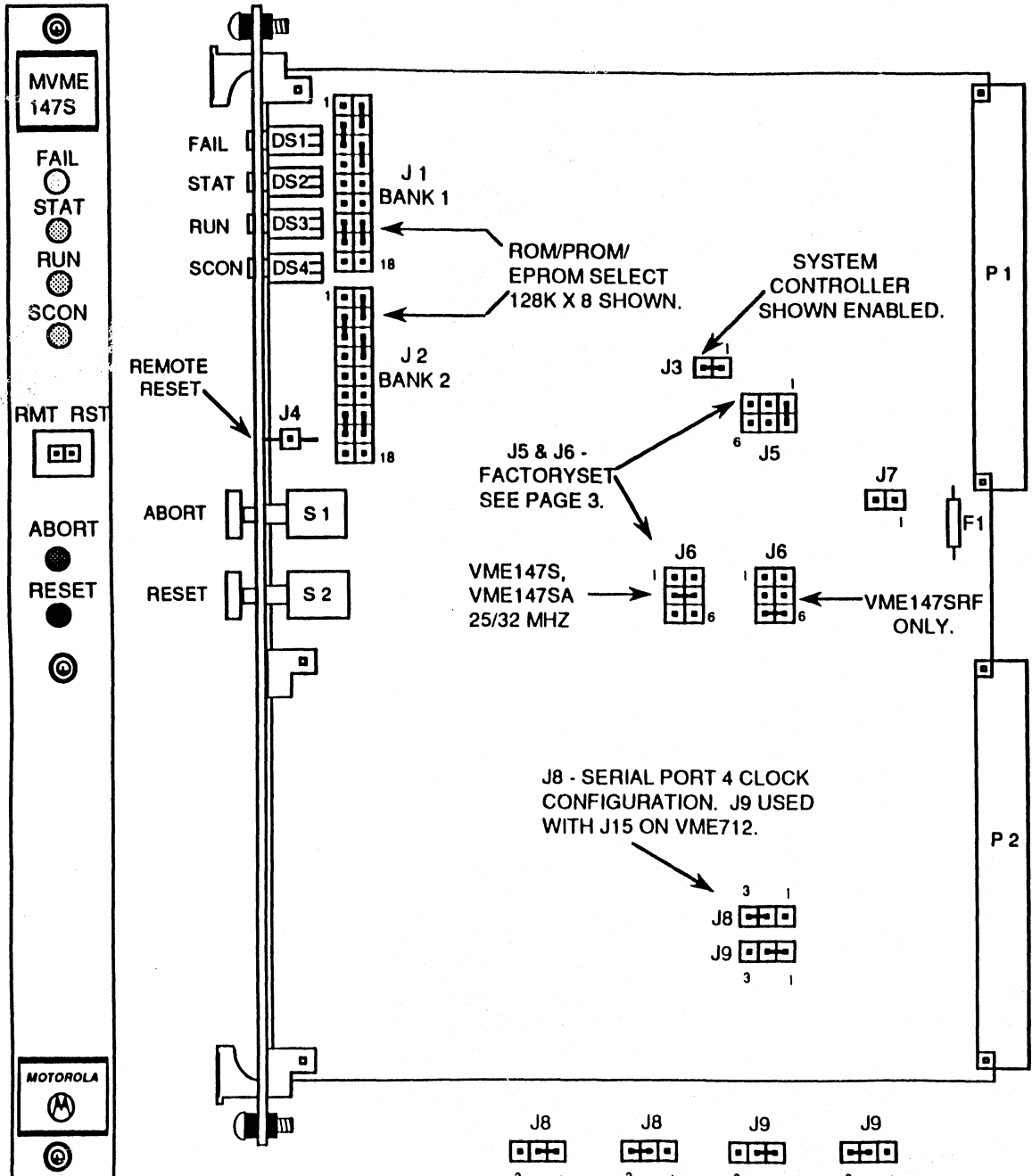
NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE.
IT IS USED FOR INTERNAL DRIVES
OR HOOKING UP OF EXTERNAL
DRIVES THROUGH A USER SUPPLIED
PANEL. (30-W2335C01)

NOTE 2: TERMINATORS MUST BE INSTALLED
AT THE END OF ALL CABLES.

J1 IS A STANDARD
DB-9 RS-232C PORT
USED AS THE
CONSOLE PORT. J3/
J4/J5 ARE ALSO
STANDARD RS-232C
PORTS AND CAN BE
USED WITH ANY
SERIAL DEVICE.

J15 IS A DB-25 PARALLEL PORT
USED AS THE PRINTER PORT.

09/07/90



PART NUMBERS:

MVME147S	4MB, 20MHZ	01-W3577B01	96011177
MVME147SRF	4MB/PAGE, 16MHZ	01-W3577B21	96011135
MVME147S-1	4MB, 25MHZ	01-W3577B02	96011136
MVME147SA	8MB, 20MHZ	01-W3577B11	96011176
MVME147SA-1	8MB, 25MHZ	01-W3577B12	96011137
MVME147SB-1	8MB, 16MHZ	01-W3648B42	96011244
MVME147SC-1	8MB, 32MHX	01-W3577B52	NONE
SVME147SRF	4MB/PAGE, 16MHZ	01-W3577B22	NONE

FUSE, MICRO, 1A AXIAL SCSI BUS 65NW9622A26 66430141
LITTLEFUSE 255001

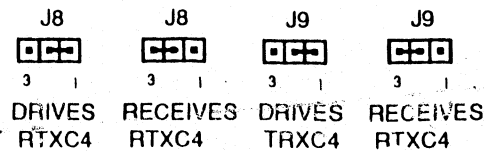
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1 : CONFIGURATION FOR SYS3604/08 & SYS3708'S ARE THE SAME EXCEPT FOR J6. SEE PAGE 2 FOR J6 SETTINGS. J6 IS ACTUALLY J8 FROM THE OLD VME147 THRU-HOLE SERIES BOARDS.

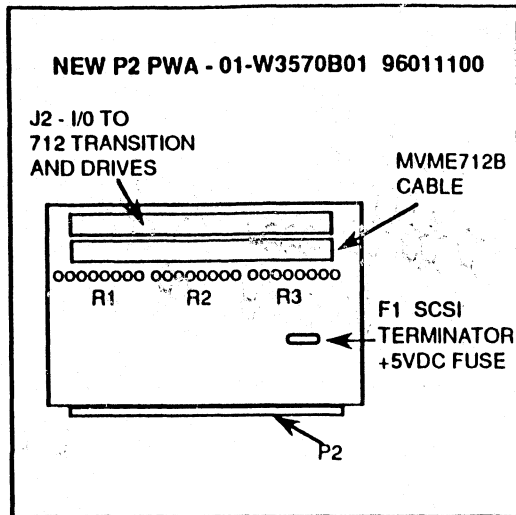
NOTE 2 : THE VME147SRF CONFIGURATION IS THE SAME EXCEPT FOR J6. SEE PAGE 3 (SYS3200).

NOTE 3 : SAME CONFIGURATION FOR SYS3400.

NOTE 4 : 01-W3577B01 AND B11 HAVE J6 JUMPERED FROM 3 - 4.
01-W3577B02 AND B12 HAVE J6 JUMPERED FROM 1 - 2.
01-W3577B21, B22, B31, AND B32 HAVE J6 JUMPERED FROM 5 - 6.



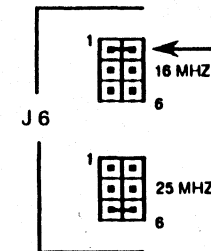
04/16/91



NOTE: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

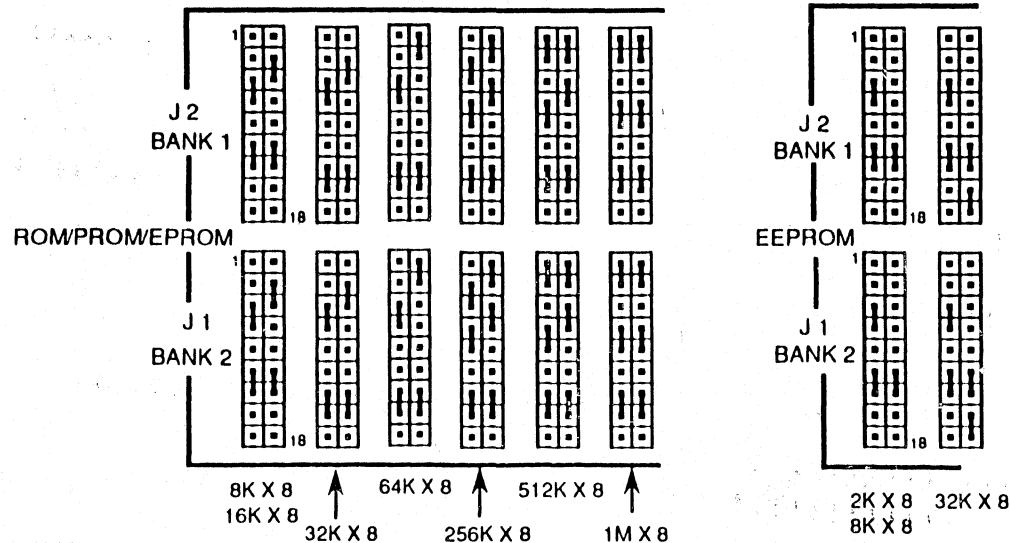
VME147RF,
VME147S,
VME147SA (SYS3708's)

INTERNAL CLOCK FREQUENCY

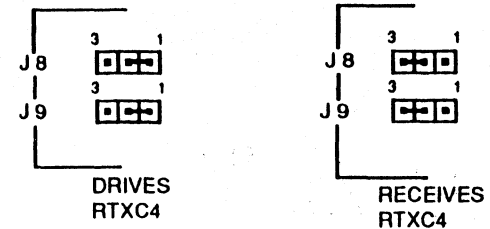


VME147S-1,
VME147SA-1

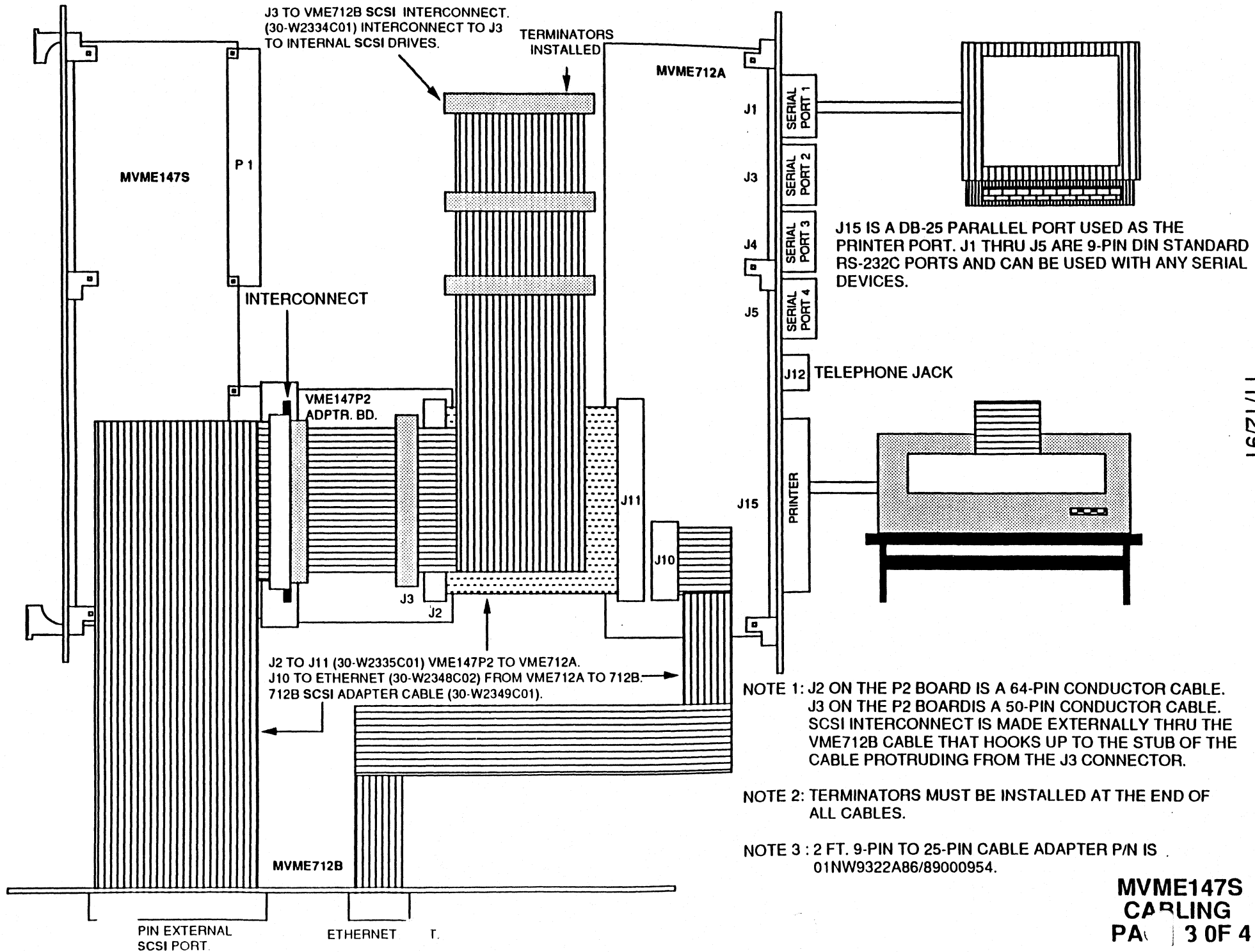
ROM/PROM/EPROM/EEPROM SIZE SELECT HEADERS

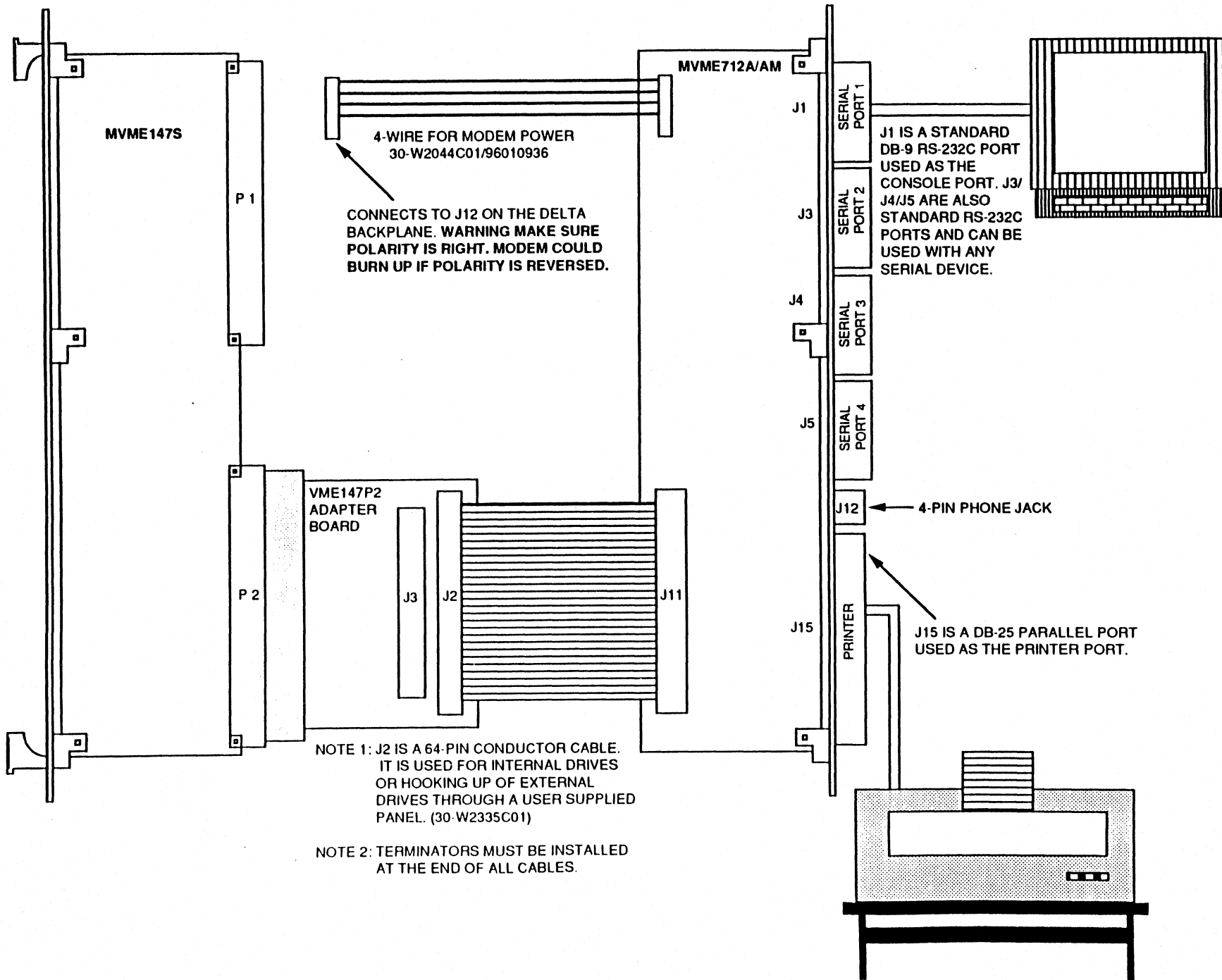


SERIAL PORT 4 CLOCK CONFIGURATION



11/12/91





MVME147S

P 1

P 2

VME147P2
ADAPTER
BOARD

J 3

J 2

MVME712A/AM

4-WIRE FOR MODEM POWER
30-W2044C01/96010936

CONNECTS TO J12 ON THE DELTA
BACKPLANE. WARNING MAKE SURE
POLARITY IS RIGHT. MODEM COULD
BURN UP IF POLARITY IS REVERSED.

NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE.
IT IS USED FOR INTERNAL DRIVES
OR HOOKING UP OF EXTERNAL
DRIVES THROUGH A USER SUPPLIED
PANEL. (30-W2335C01)

NOTE 2: TERMINATORS MUST BE INSTALLED
AT THE END OF ALL CABLES.

J1

J3

J4

J5

SERIAL
PORT 1

SERIAL
PORT 2

SERIAL
PORT 3

SERIAL
PORT 4

J12

J15

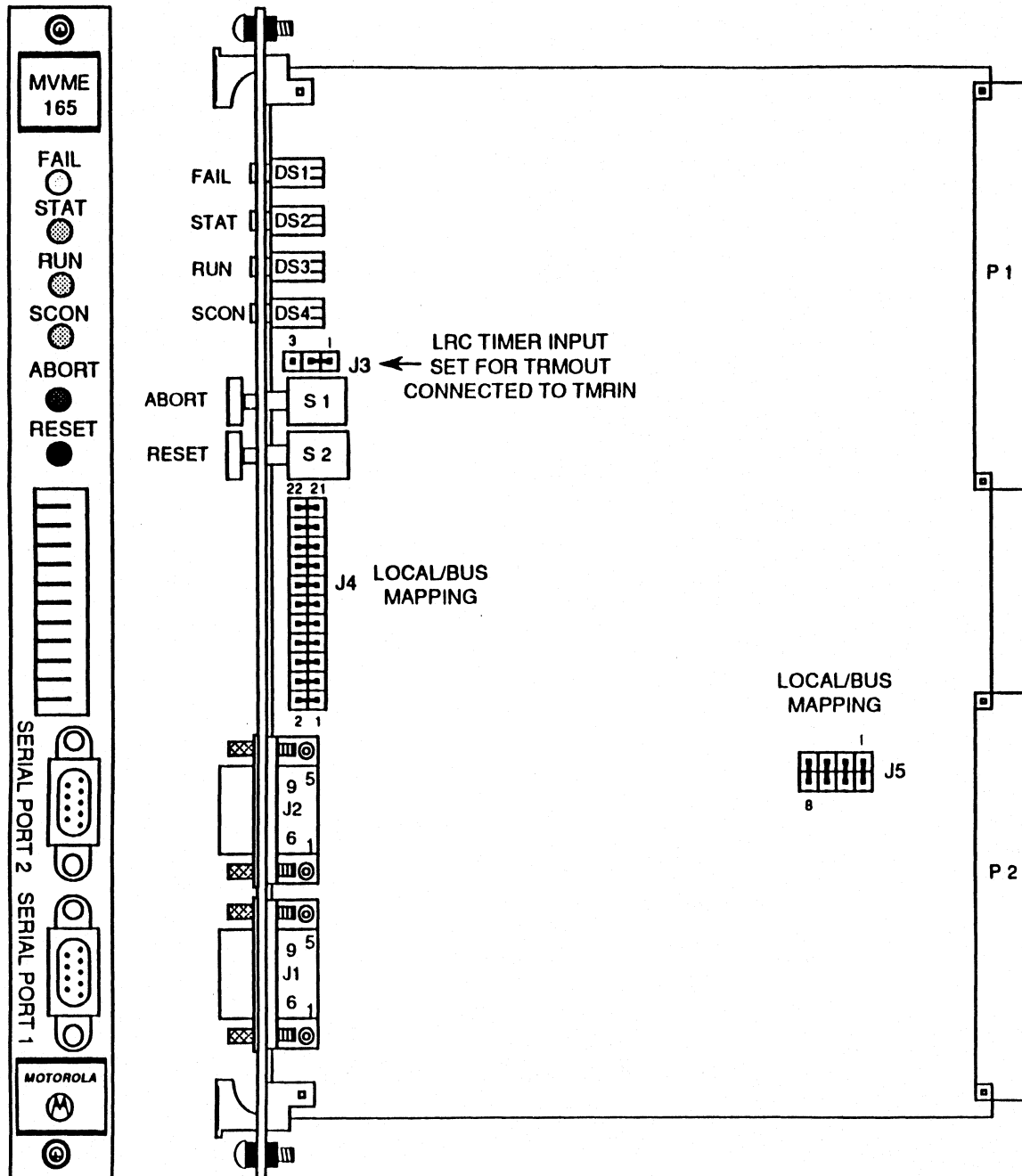
PRINTER

J1 IS A STANDARD
DB-9 RS-232C PORT
USED AS THE
CONSOLE PORT. J3/
J4/J5 ARE ALSO
STANDARD RS-232C
PORTS AND CAN BE
USED WITH ANY
SERIAL DEVICE.

4-PIN PHONE JACK

J15 IS A DB-25 PARALLEL PORT
USED AS THE PRINTER PORT.

06/07/90



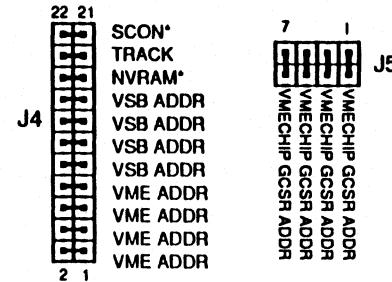
PART NUMBERS:

MVME165 W/4MB 01-W3620B01 76435732

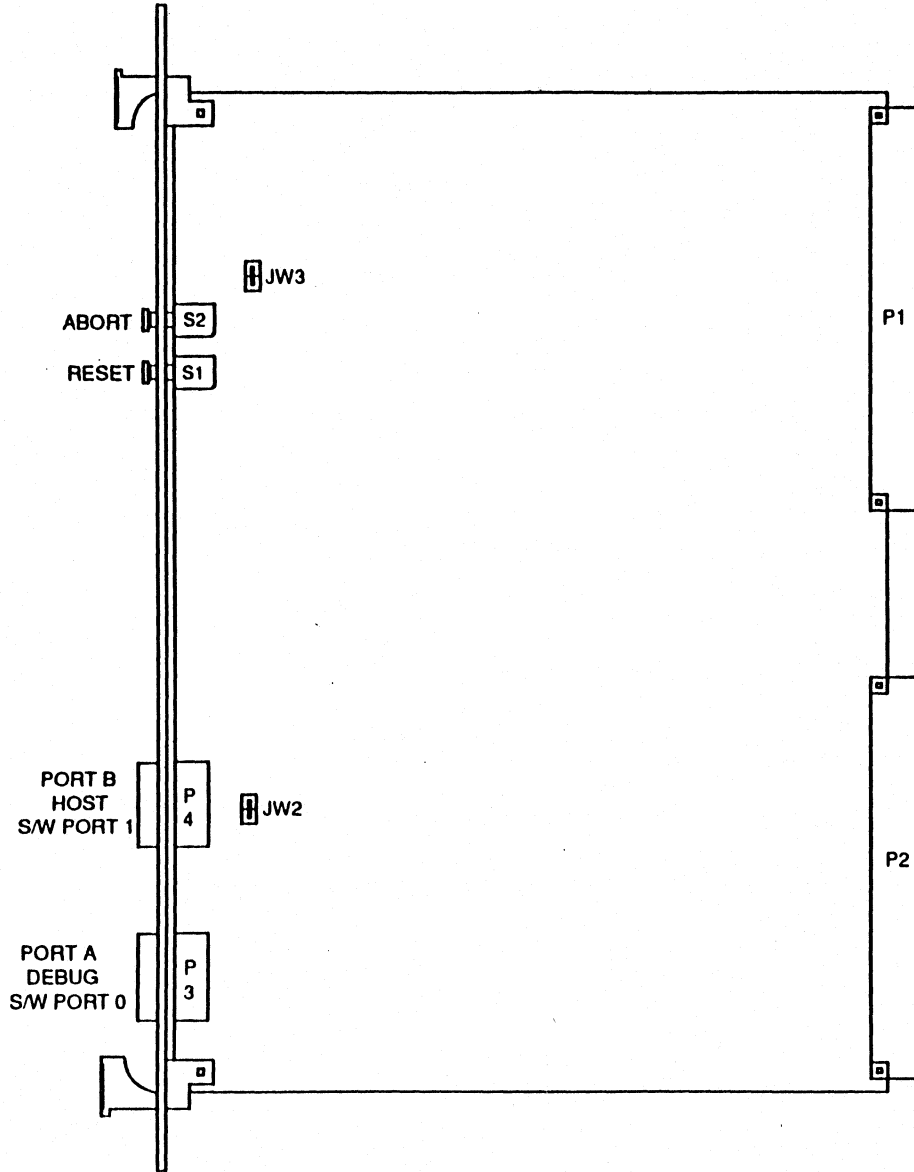
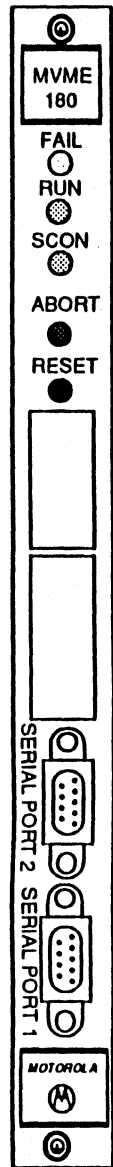
MVME165A W/8MB 01-W3620B11 TBD

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

J4/J5 LOCAL/BUS MAPPING



03/14/91



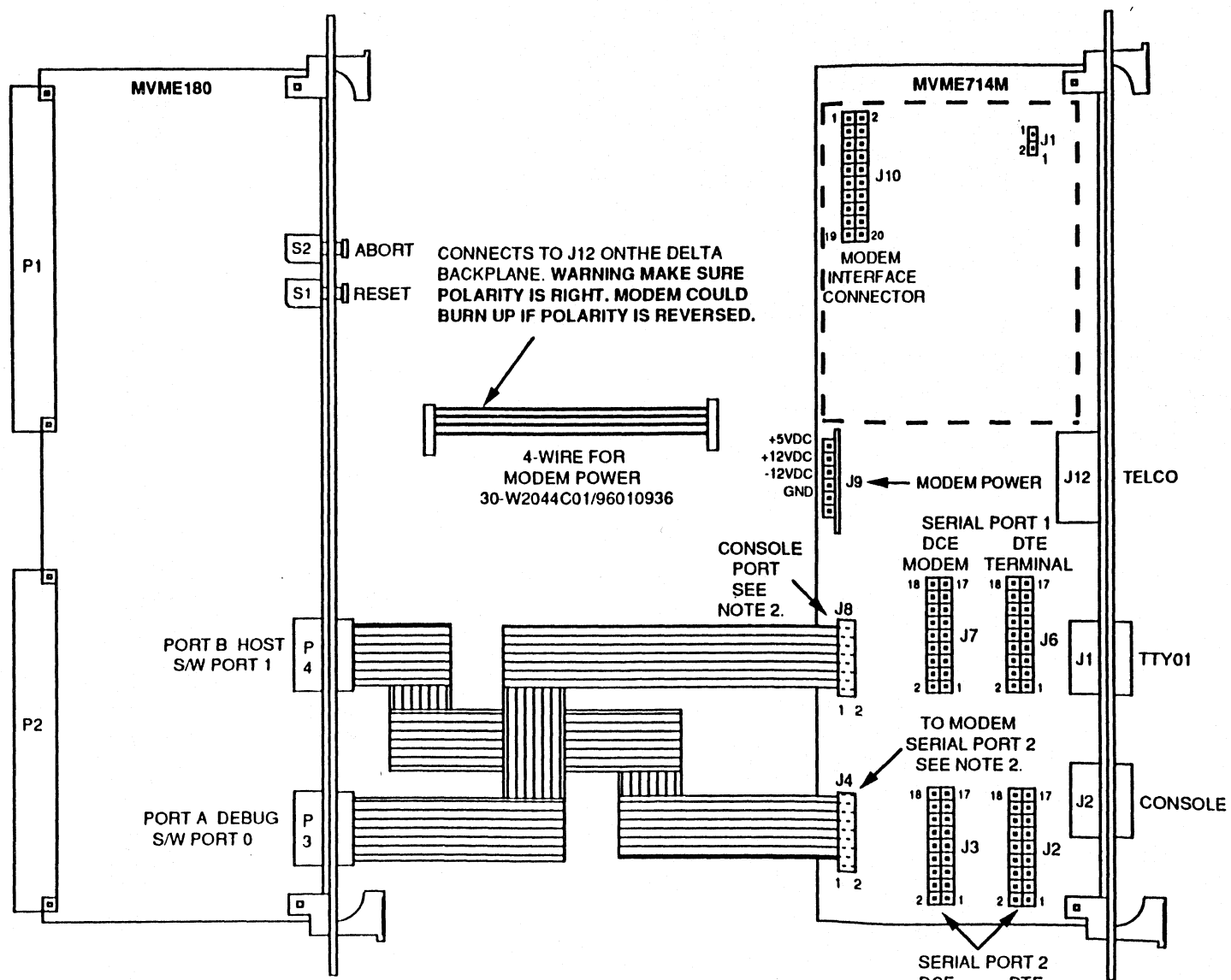
PART NUMBERS:

MVME180 01-W3563B01 96011167

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

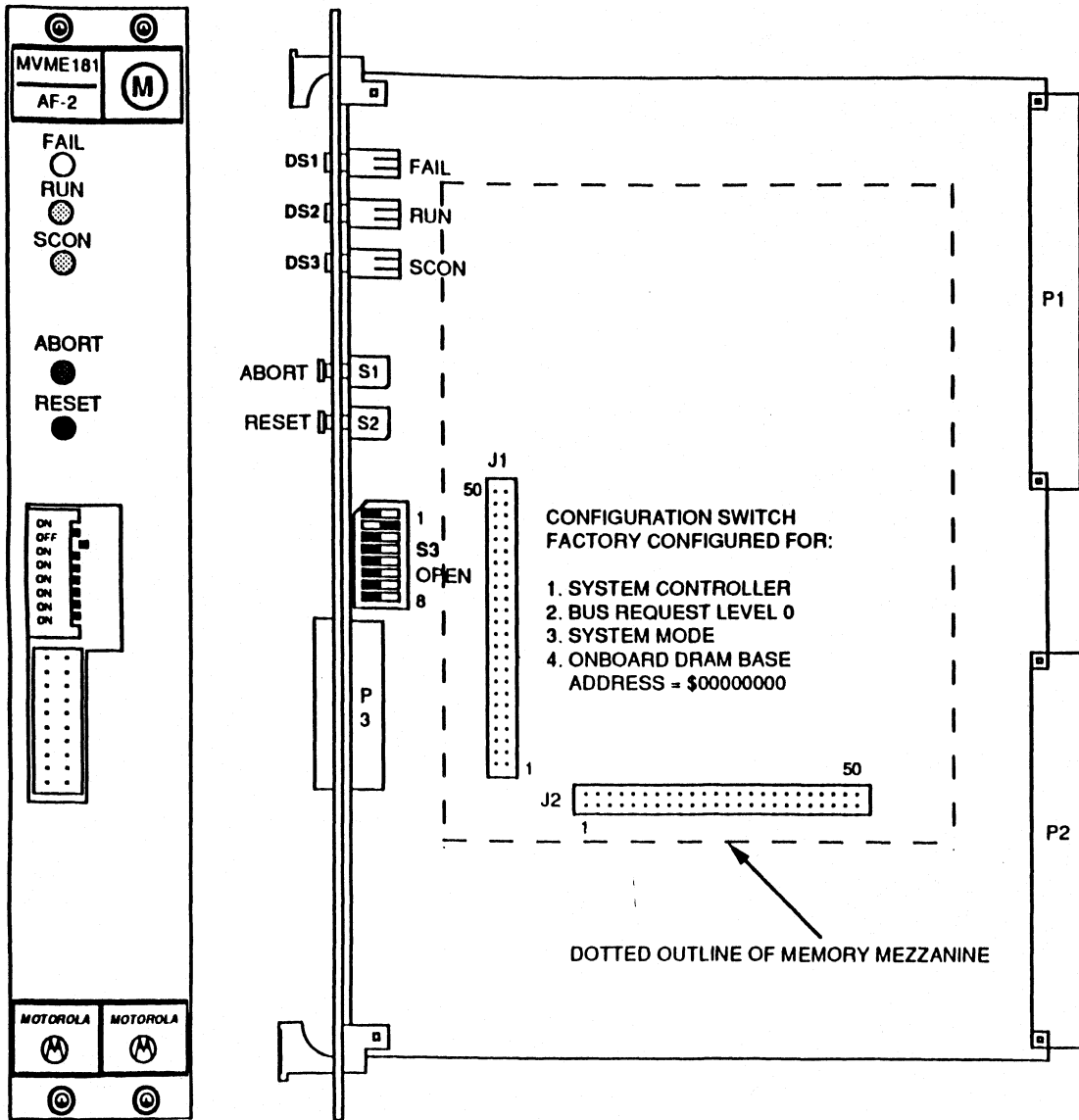
02/23/90

10/30/89



NOTE 1: VME180 IS NOT UNIFORMLY SHOWN. ITS SIZE WAS REDUCED TO FIT ON THIS PAGE AND SHOW ITS HOOKUP TO THE MVME714M TRANSITION BOARD.

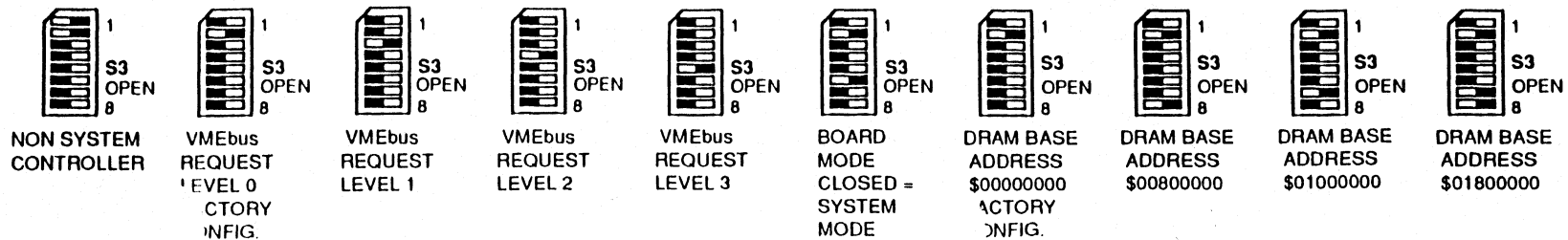
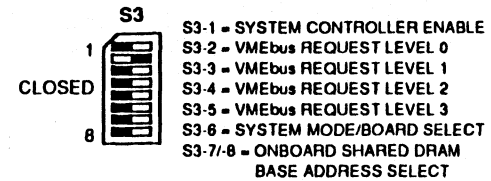
NOTE 2: CABLING GOES FROM VME180 SERIAL PORTS 1 & 2 TO VME714M AND PLUGS INTO J4 AND J8 CONNECTORS FOR INTERFACE TO THE FRONT CONNECTORS OF THE VME714M.



PART NUMBERS:

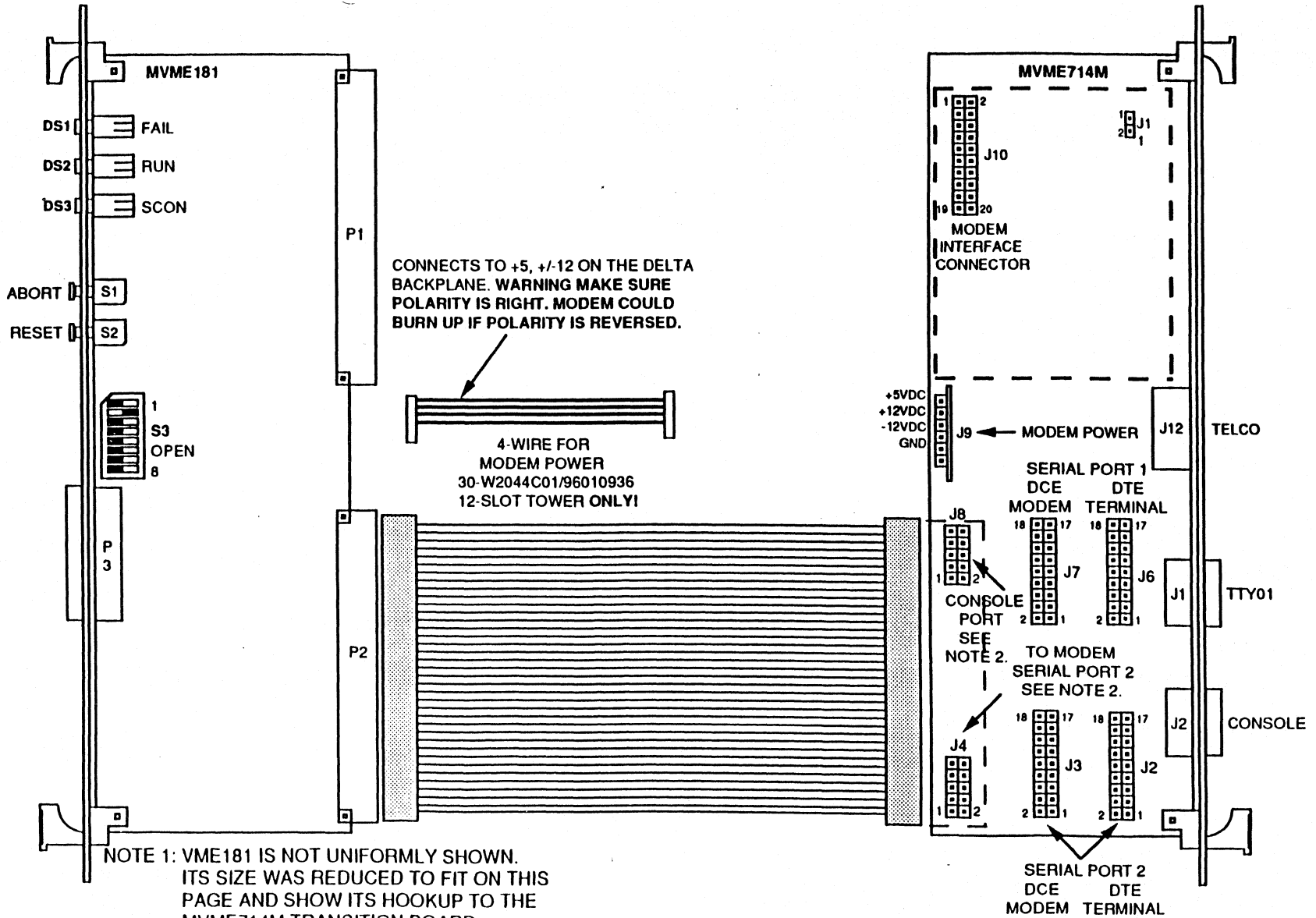
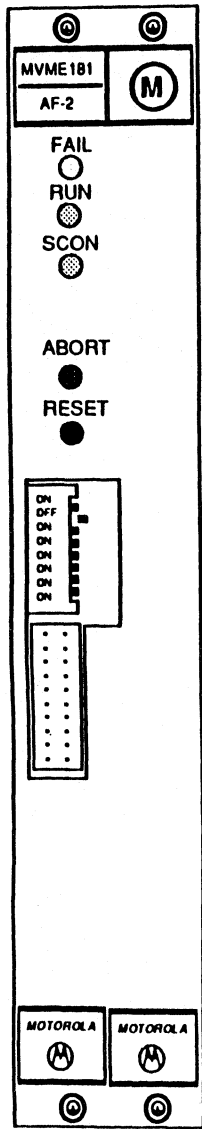
**MVME181 PROTO ONLY. NOT FSD SUPPORTED
THIS BOARD IS SUPPLIED FROM MCD ONLY!**

- NOTE 1: THERE ARE NO JUMPERS ON THE MAIN CPU BOARD.
- NOTE 2: THE MEZZANINE HAS NO JUMPERS EITHER AND THE CONFIGURATION IS NOT SHOWN. IT IS OF STANDARD DUAL-EUROCARD FORM FACTOR.
- NOTE3: THE FRONT PANEL IS A DOUBLE WIDE PANEL.
- NOTE 4: ACTIVE PART OF THE SWITCH IS DARKENED AREA.
- NOTE 5: THE MEZZANINE IS AN 8 MB BOARD.
- NOTE 6: THE SERIAL CABLE USED HAS A 20-PIN FLAT CONNECTOR ON THE CPU END AND IS SPLIT TO 2 10-PIN CONNECTORS ON THE VME714 END. AN ALTERNATE SERIAL INTERFACE IS A SPECIAL P2 TO VME714 CABLE (P/N 30-W2799B02)



**MVME181
AKA AF-2, &
MVME180-2
RISK MICRO-COMPUTER
PAGE 1 OF 2**

09/10/89



NOTE 1: VME181 IS NOT UNIFORMLY SHOWN. ITS SIZE WAS REDUCED TO FIT ON THIS PAGE AND SHOW ITS HOOKUP TO THE MVME714M TRANSITION BOARD.

NOTE 2: CABLING GOES FROM VME181 P2 TO VME714M AND PLUGS INTO J4 AND J8 CONNECTORS FOR INTERFACE TO THE FRONT CONNECTORS OF THE TRANSITION BOARD (VME714M).

02/23/90

PART NUMBERS:

- MVME181-1 01-W3572B01 20 Mhz TBD
- MVME181-1 MEZZ 01-W3571B01 20 Mhz TBD
- MVME181-2 01-W3572B02 25Mhz TBD
- MVME181-2 MEZZ 01-W3571B02 25Mhz TBD

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: THERE ARE NO JUMPERS ON THE MAIN CPU BOARD.

NOTE 2: THE MEZZANINE HAS NO JUMPERS EITHER AND THE CONFIGURATION IS NOT SHOWN. IT IS OF STANDARD DUAL-EUROCARD FORM FACTOR.

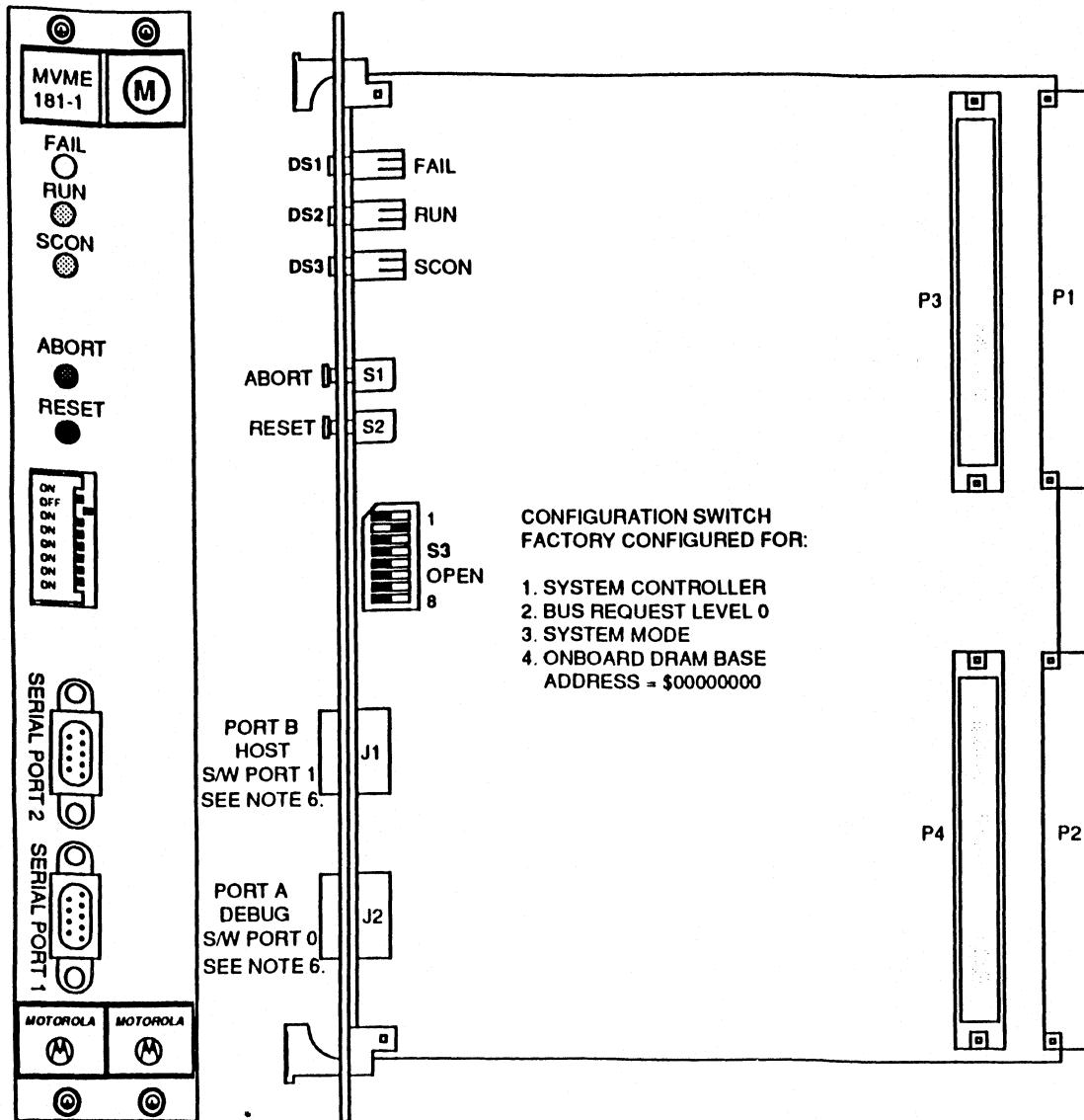
NOTE 3: THE FRONT PANEL IS A DOUBLE WIDE PANEL.

NOTE 4: ACTIVE PART OF THE SWITCH IS DARKENED AREA.

NOTE 5: THE MEZZANINE IS A FULL SIZE 8 MB BOARD.

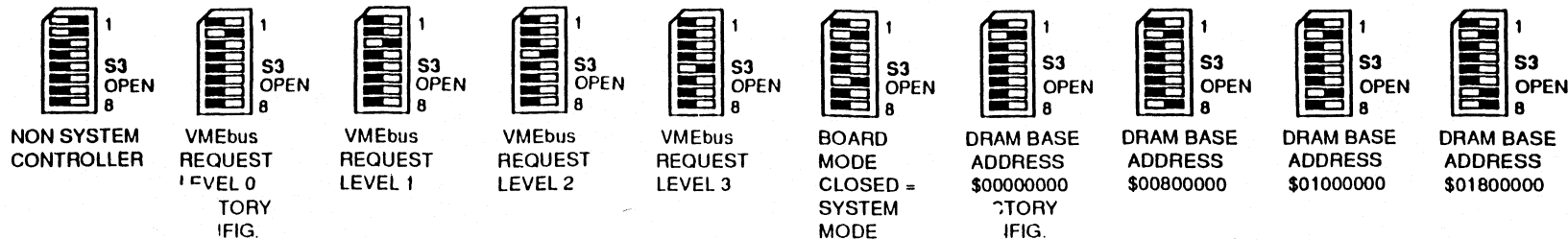
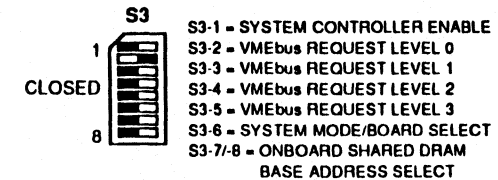
NOTE 6: DESCRIPTION OF SERIAL PORT TERMINOLOGY IS:
 SERIAL PORT 1 IS -- PORT A ON THE SCHEMATICS AND THE DEBUG PORT.
 -- PORT 0 FOR SOFTWARE.
 SERIAL PORT 2 IS -- PORT B ON THE SCHEMATICS AND THE HOST PORT.
 -- PORT 1 FOR SOFTWARE.

NOTE 7: S3 SET THE SAME FOR SYS8408'S.

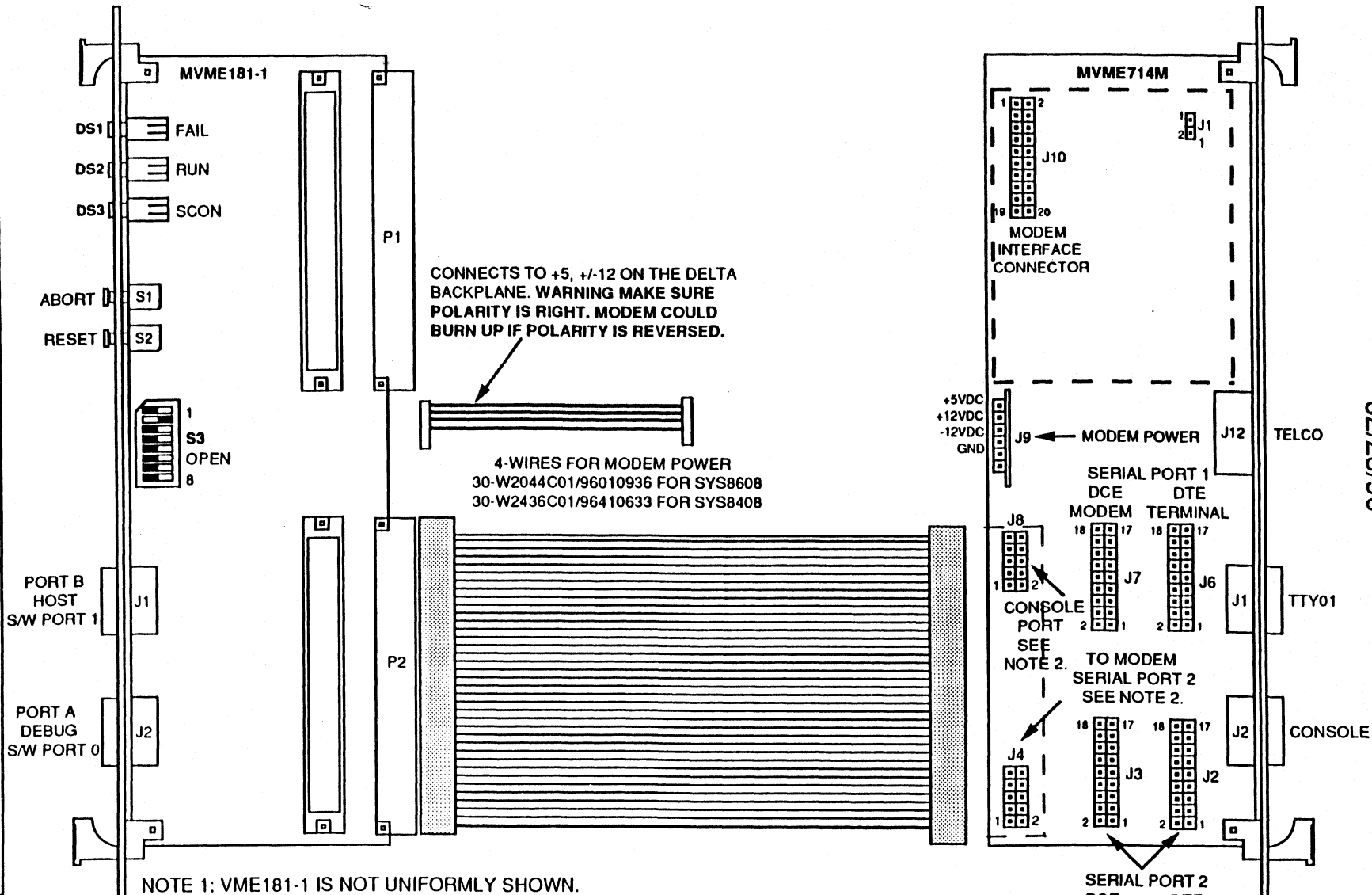
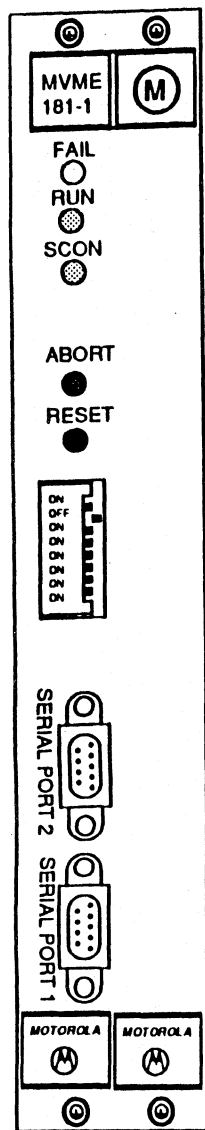


CONFIGURATION SWITCH
FACTORY CONFIGURED FOR:

1. SYSTEM CONTROLLER
2. BUS REQUEST LEVEL 0
3. SYSTEM MODE
4. ONBOARD DRAM BASE ADDRESS = \$00000000



09/10/90



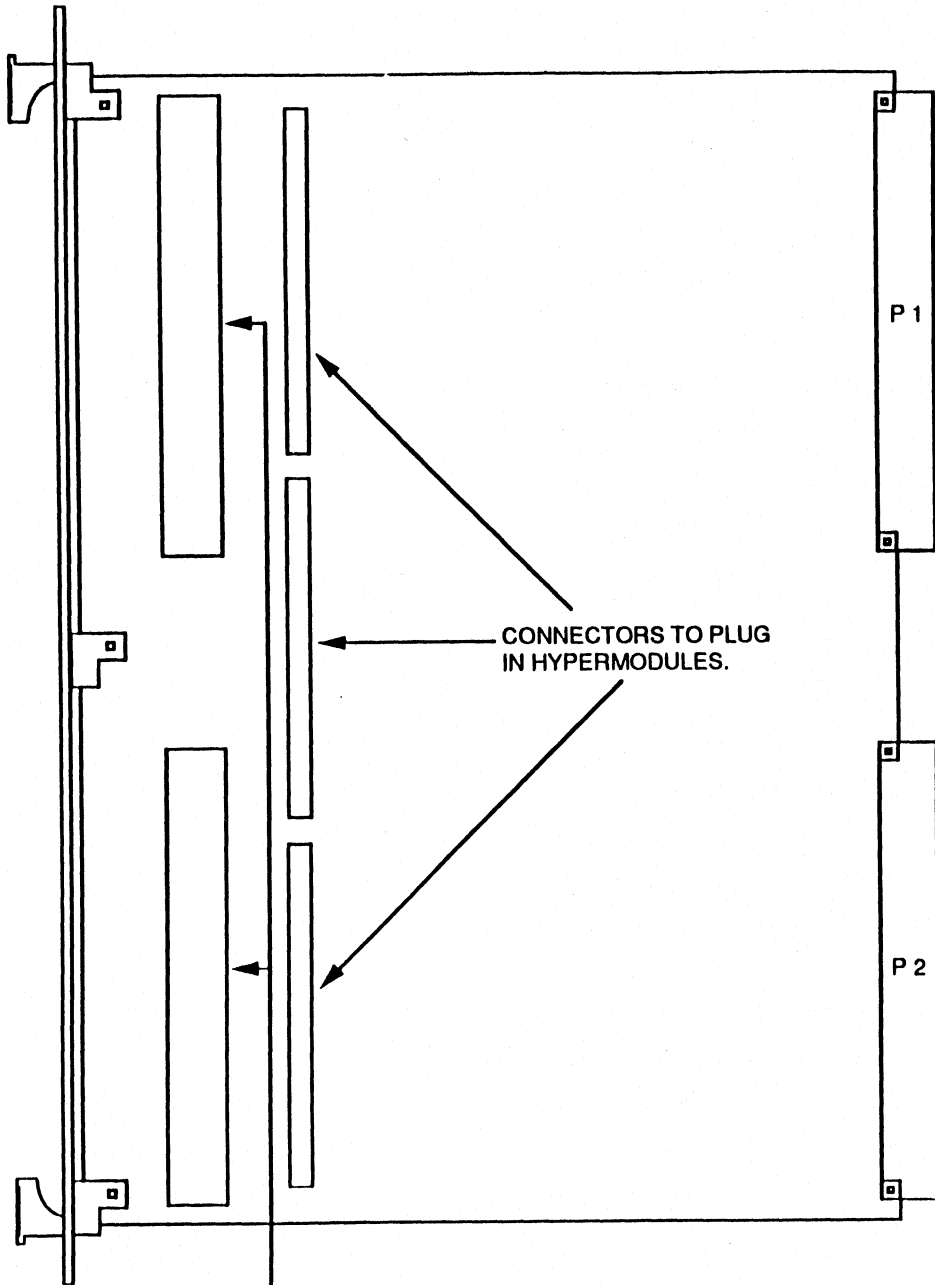
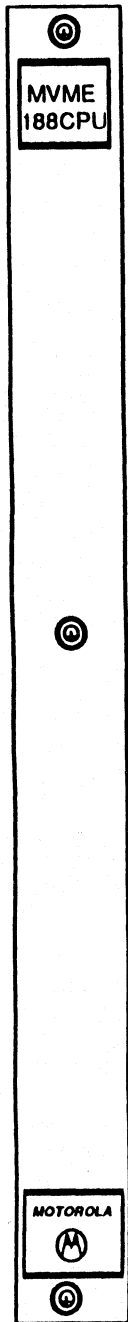
CONNECTS TO +5, +/-12 ON THE DELTA BACKPLANE. WARNING MAKE SURE POLARITY IS RIGHT. MODEM COULD BURN UP IF POLARITY IS REVERSED.

4-WIRES FOR MODEM POWER
 30-W2044C01/96010936 FOR SYS8608
 30-W2436C01/96410633 FOR SYS8408

NOTE 1: VME181-1 IS NOT UNIFORMLY SHOWN. ITS SIZE WAS REDUCED TO FIT ON THIS PAGE AND SHOW ITS HOOKUP TO THE MVME714M TRANSITION BOARD.

NOTE 2: CABLING GOES FROM VME181-1 P2 TO VME714M AND PLUGS INTO J4 AND J8 CONNECTORS FOR INTERFACE TO THE FRONT CONNECTORS OF THE TRANSITION BOARD (VME714M).

02/23/90



CONNECTORS TO PLUG IN HYPERMODULES.

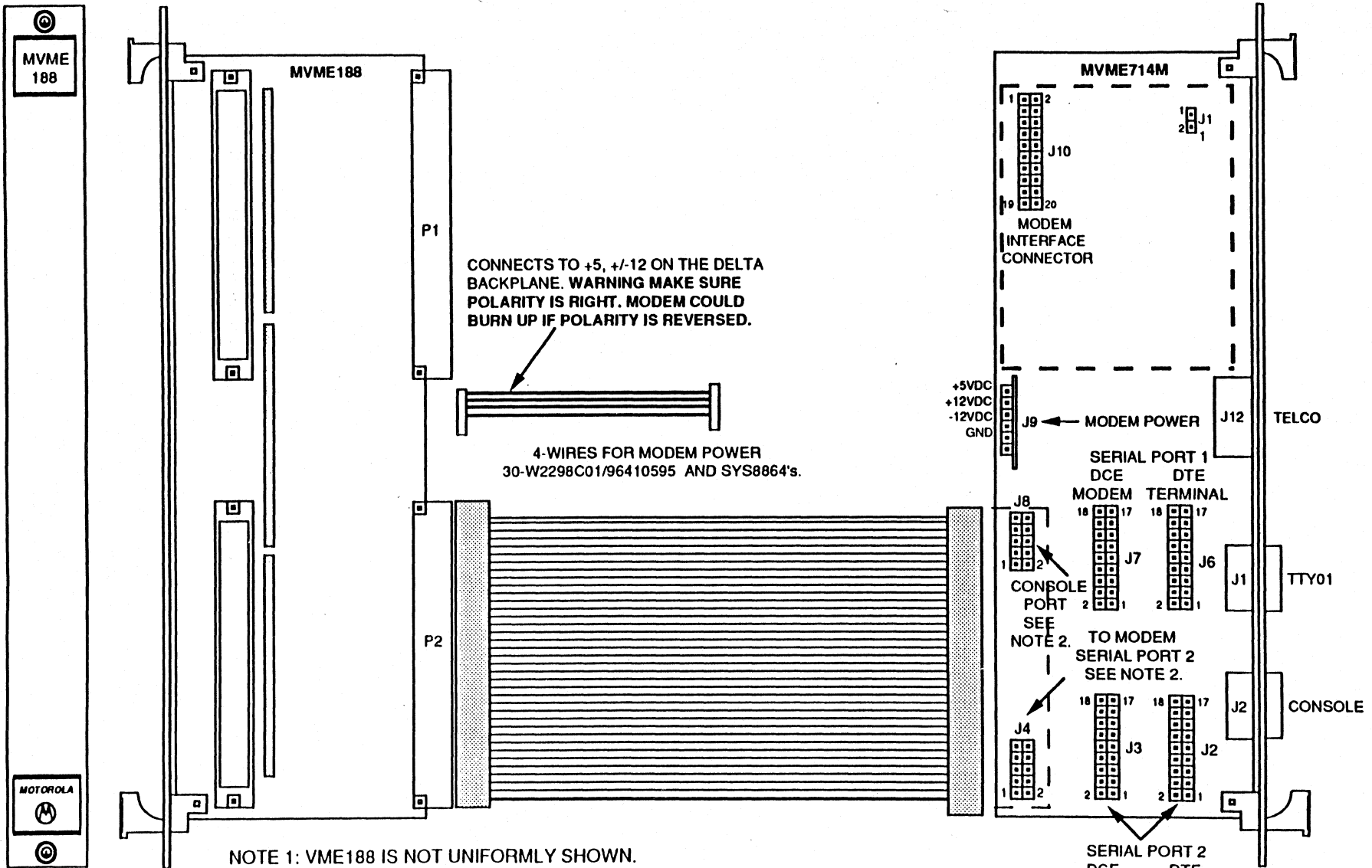
CONNECTORS TO PLUG IN MODULES ON TO THE PRIVATE BUS.

PART NUMBERS:

- MVME188SP-1 01-W2306C01 96011203
- MVME188SP-2 01-W2306C02 96011190
- MVME188DP-1 01-W2306C03 96011202
- MVME188QP-1 01-W2306C04 96011201
- MVME188DP-S3 01-W2306C05 96011193
- MVME188DP-3 01-W2306C06 96011191
- MVME188SP-S3 01-W2306C11 96011194
- MVME188 01-W3546B01 TBD

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

02/23/90

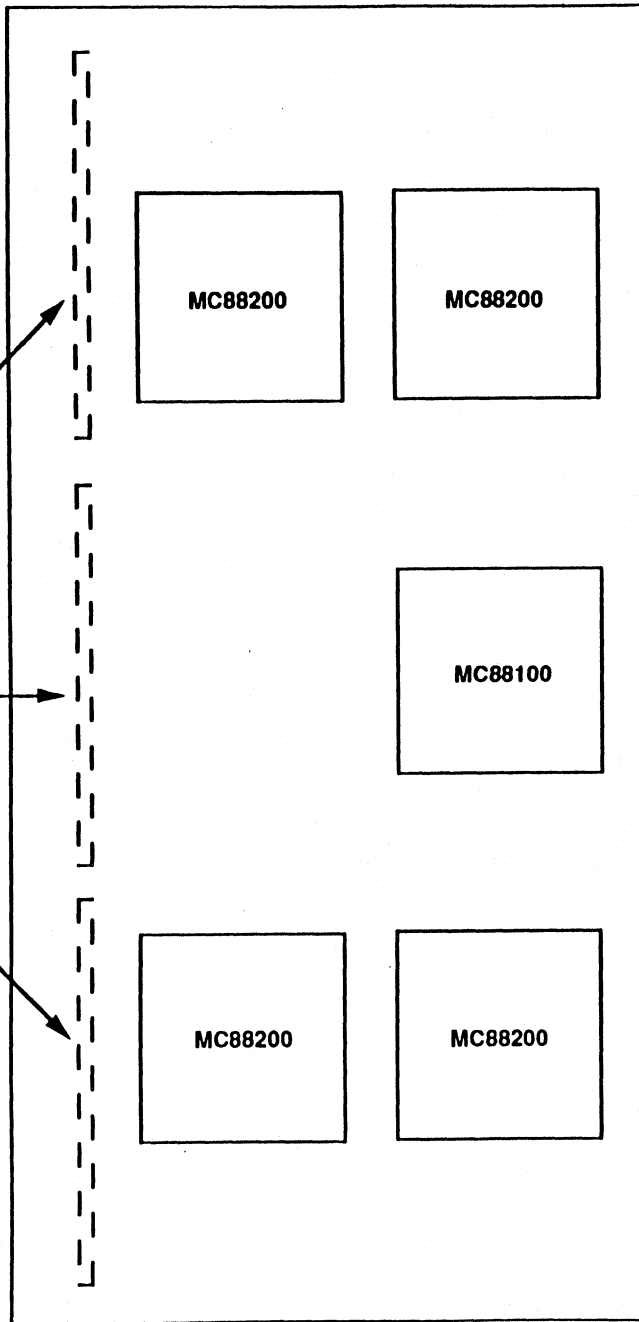


NOTE 1: VME188 IS NOT UNIFORMLY SHOWN. ITS SIZE WAS REDUCED TO FIT ON THIS PAGE AND SHOW ITS HOOKUP TO THE MVME714M TRANSITION BOARD.

NOTE 2: CABLING GOES FROM VME188 P2 TO VME714M AND PLUGS INTO J4 AND J8 CONNECTORS FOR INTERFACE TO THE FRONT CONNECTORS OF THE TRANSITION BOARD (VME714M).

02/23/90

CONNECTORS TO
PLUG INTO THE
MVME188CPU.
DOTTED LINES
ARE SHOWN TO
REFLECT THAT
CONNECTORS
ARE UNDER THE
HYPERMODULE.



PART NUMBERS:

IN KIT # 01-W2306C02 & C11

HM88K-1P64 01-W3576B01 TBD

02/23/90

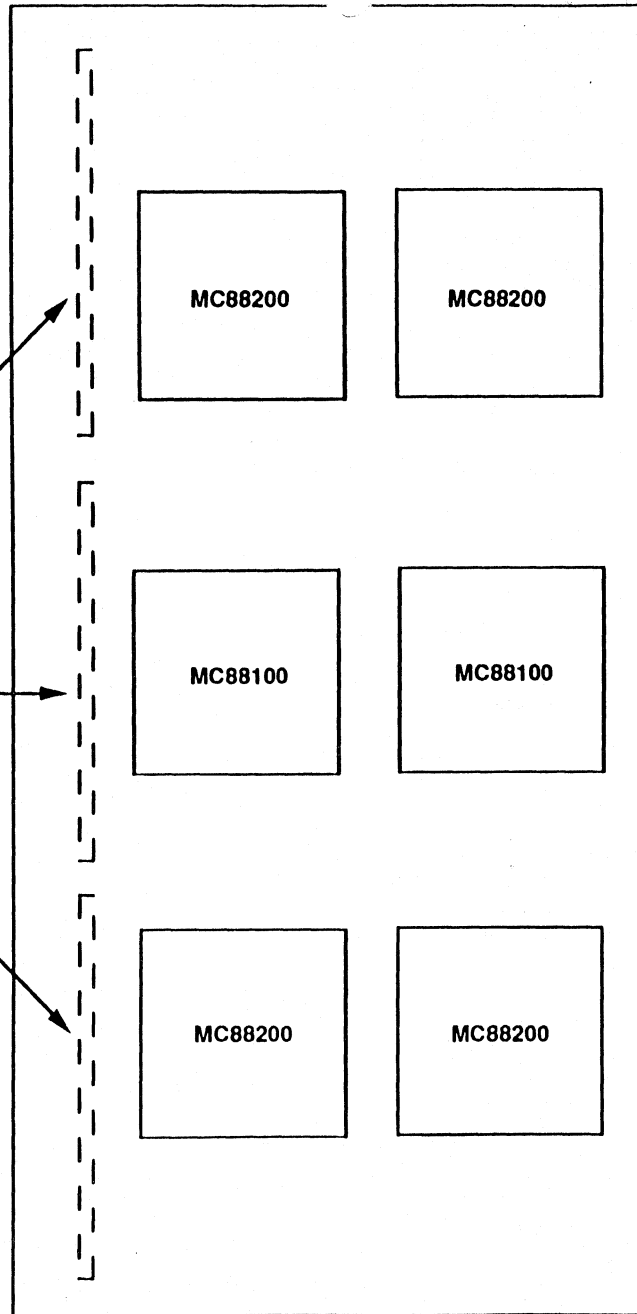
PART NUMBERS:

IN KIT # 01-W2306C05 & C06

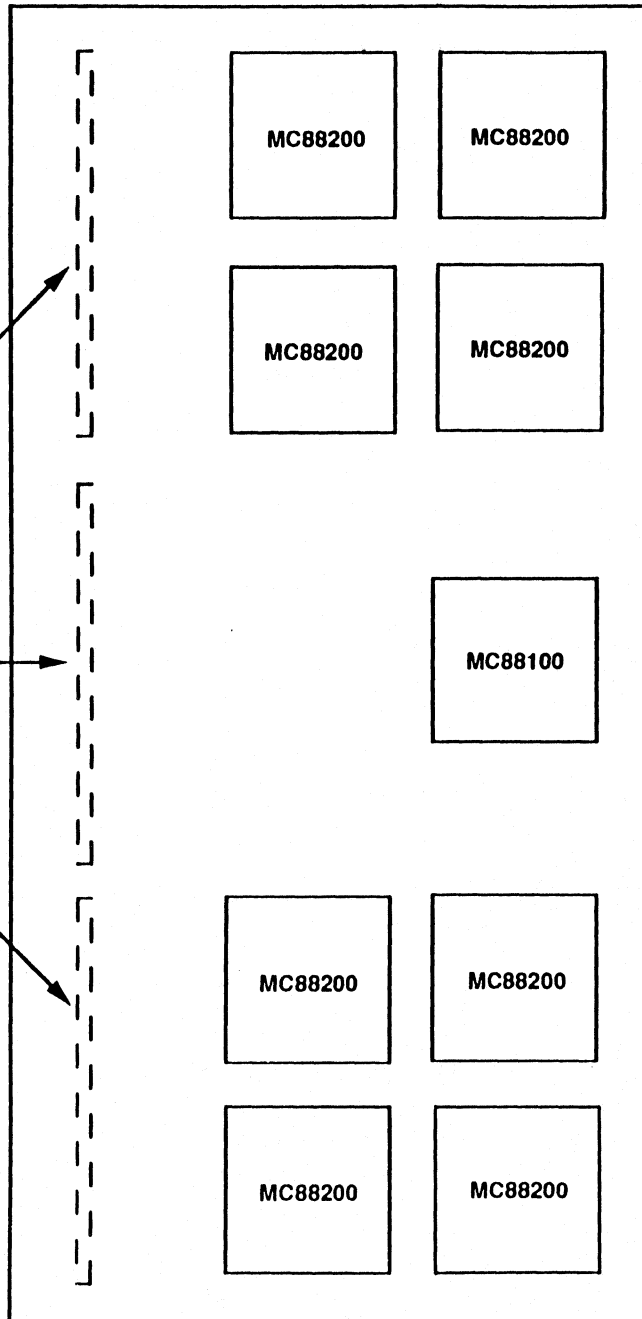
HM88K-2P64 01-W3545B01 TBD

02/23/90

CONNECTORS TO
PLUG INTO THE
MVME188CPU.
DOTTED LINES
ARE SHOWN TO
REFLECT THAT
CONNECTORS
ARE UNDER THE
HYPERMODULE.



CONNECTORS TO
PLUG INTO THE
MVME188CPU.
DOTTED LINES
ARE SHOWN TO
REFLECT THAT
CONNECTORS
ARE UNDER THE
HYPERMODULE.



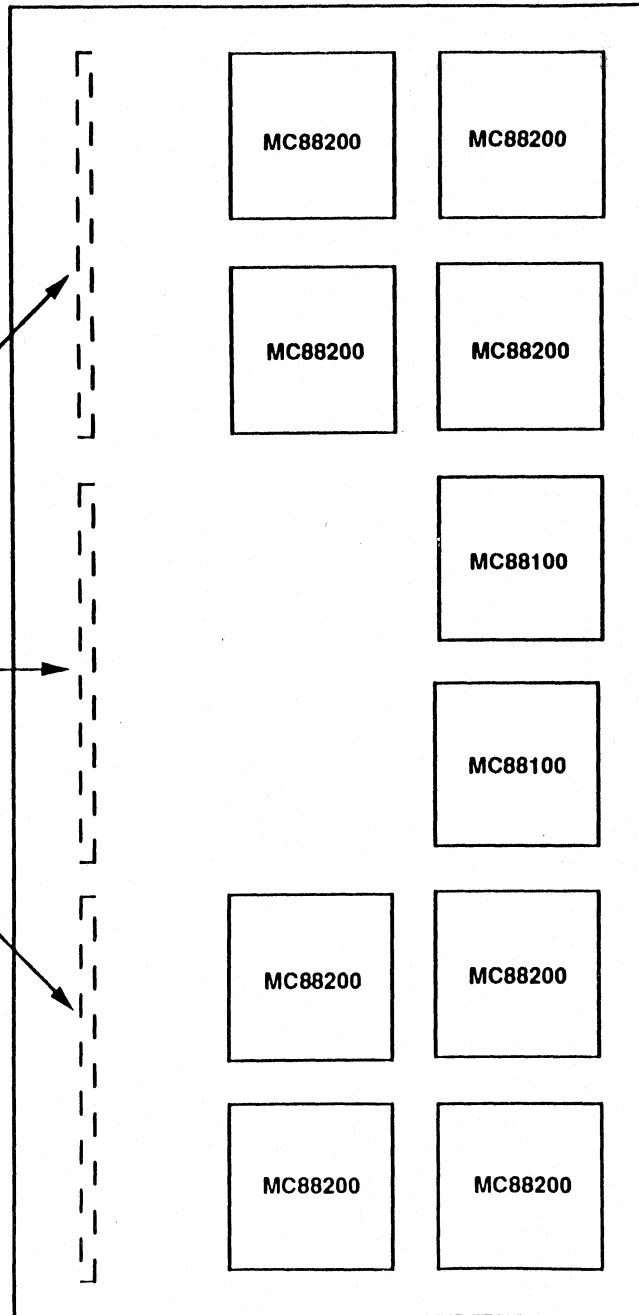
PART NUMBERS:

IN KIT # 01-W2306C01

HM88K-1P128 01-W3554B01 TBD

02/23/90

CONNECTORS TO
PLUG INTO THE
MVME188CPU.
DOTTED LINES
ARE SHOWN TO
REFLECT THAT
CONNECTORS
ARE UNDER THE
HYPERMODULE.



PART NUMBERS:

IN KIT # 01-W2306C03

HM88K-2P128 01-W3556B01 TBD

02/23/90

**HYPERMODULE
2P128
PAGE 6 OF 7**

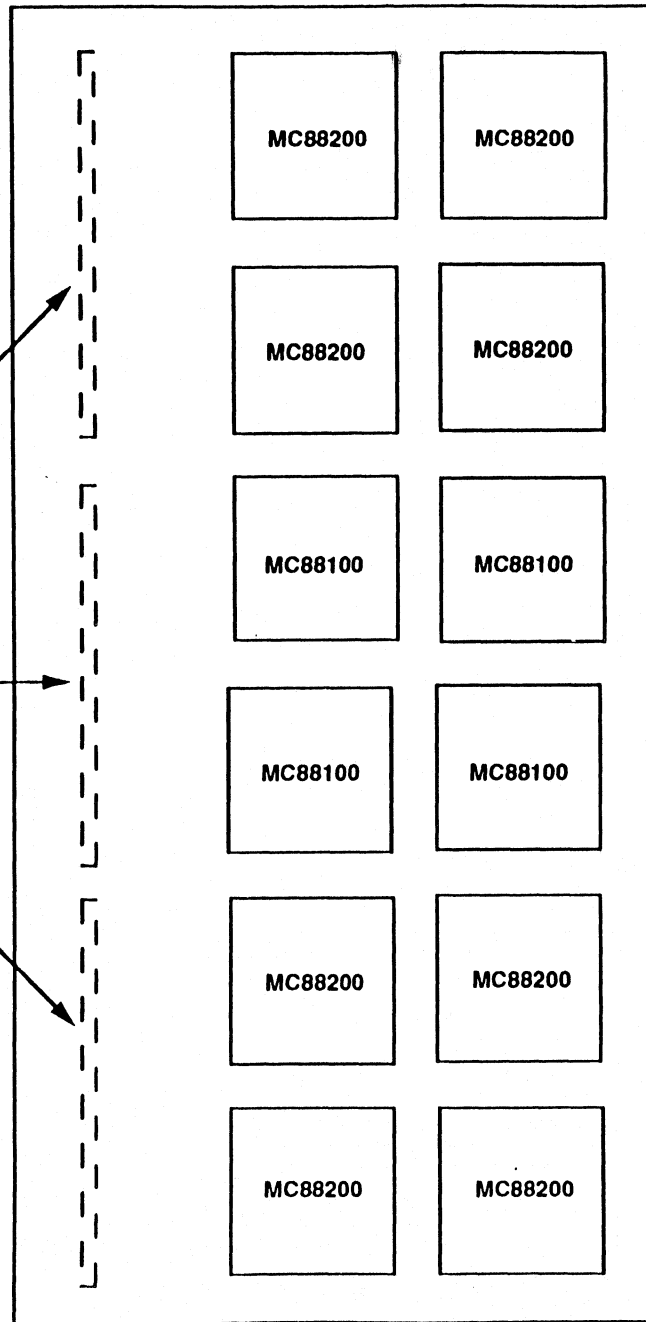
PART NUMBERS:

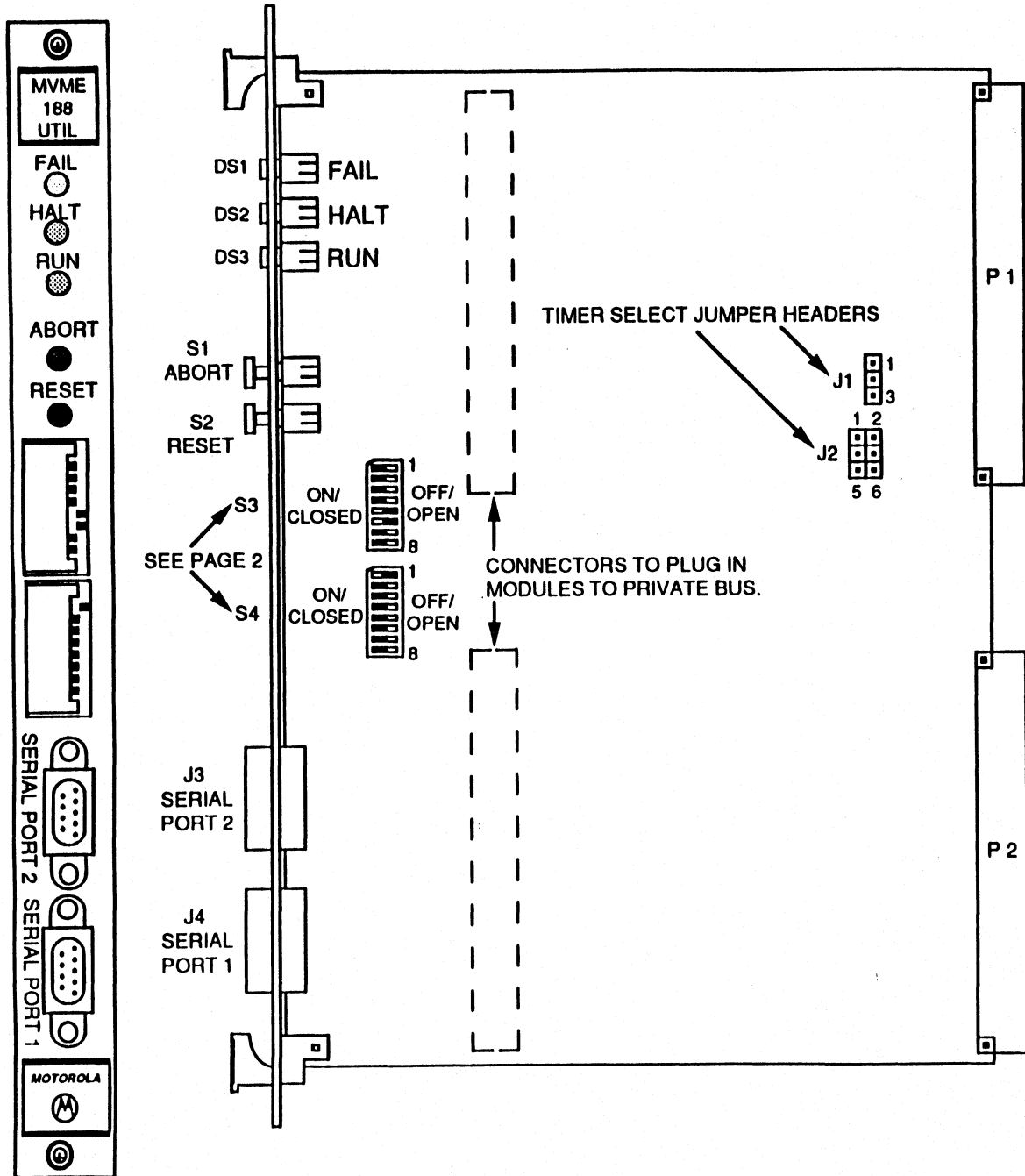
IN KIT # 01-W2306C04

HM88K-4P128 01-W3557B01 TBD

02/23/90

CONNECTORS TO
PLUG INTO THE
MVME188CPU.
DOTTED LINES
ARE SHOWN TO
REFLECT THAT
CONNECTORS
ARE UNDER THE
HYPERMODULE.





PART NUMBERS:

IN KIT # 01-W2306C0(X)

MVME188 UTIL 01-W3547B01 TBD

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

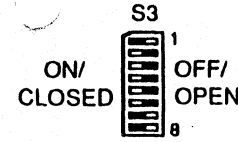
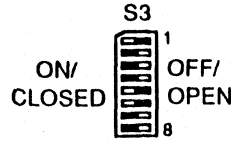
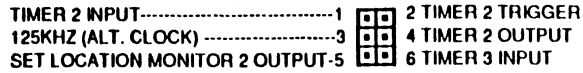
11/12/91

NOTE 1: SWITCHES S3/S4: OPEN = OFF CLOSED = ON.
1ST BOARD SET UP AS SYSTEM CONTROLLER.

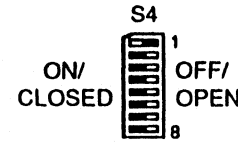
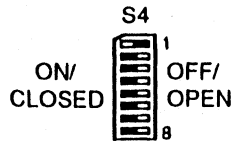
NOTE 2: ACTIVE PART OF SWITCH IS
DARKENED AREA.

NOTE 3: GCSR STANDS FOR "GLOBAL
CONTROL AND STATUS
REGISTER.

J1/J2 EXTERNAL TIMER SELECT JUMPERS



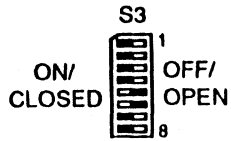
ENV0*-ENV2* ARE :
 S3-2 = ENV0*
 S3-3 = ENV1*
 S3-4 = ENV2*



THIS VME188 SETS ENV0* -ENV2* TO A "1". THE Z8536 CIO PINS PB3-PB5 ARE NOT GROUNDED.

S3-1 OFF = NOT SYSTEM CONTROLLER.

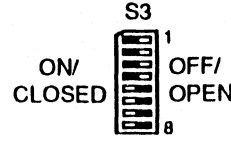
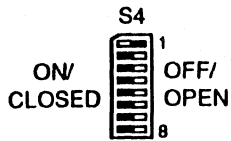
FACTORY CONFIGURATION



GCSR GROUP ADDRESS FUNCTION:

- S3-5 = GRPAD7
- S3-6 = GRPAD6
- S3-7 = GRPAD5
- S3-8 = GRPAD4
- S4-1 = GRPAD3
- S4-2 = GRPAD2
- S4-3 = GRPAD1
- S4-4 = GRPAD0

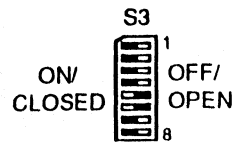
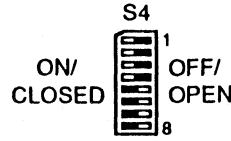
THE MVME188 HAS A GCSR GROUP ADDRESS OF \$0000C800 (A16 ON SHORT I/O SPACE), IS SYSTEM CONTROLLER, AND THE ENV0*-ENV2* TO A "0". THE Z8536 CIO PINS PB3-PB5 ARE GROUNDED.



GCSR GROUP ADDRESS FUNCTION:

- S3-5 = GRPAD7
- S3-6 = GRPAD6
- S3-7 = GRPAD5
- S3-8 = GRPAD4
- S4-1 = GRPAD3
- S4-2 = GRPAD2
- S4-3 = GRPAD1
- S4-4 = GRPAD0

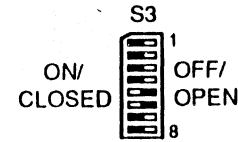
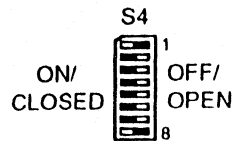
THE MVME188 HAS A GCSR GROUP ADDRESS OF \$0000FF00 (A16 ON SHORT I/O SPACE).



GCSR BOARD ADDRESS FUNCTION:

- S4-5 = BDAD3
- S4-6 = BDAD2
- S4-7 = BDAD1
- S4-8 = BDAD0

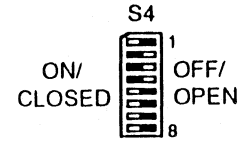
THE MVME188 HAS A GCSR BASE ADDRESS OF \$0000C810.



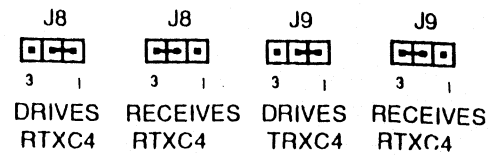
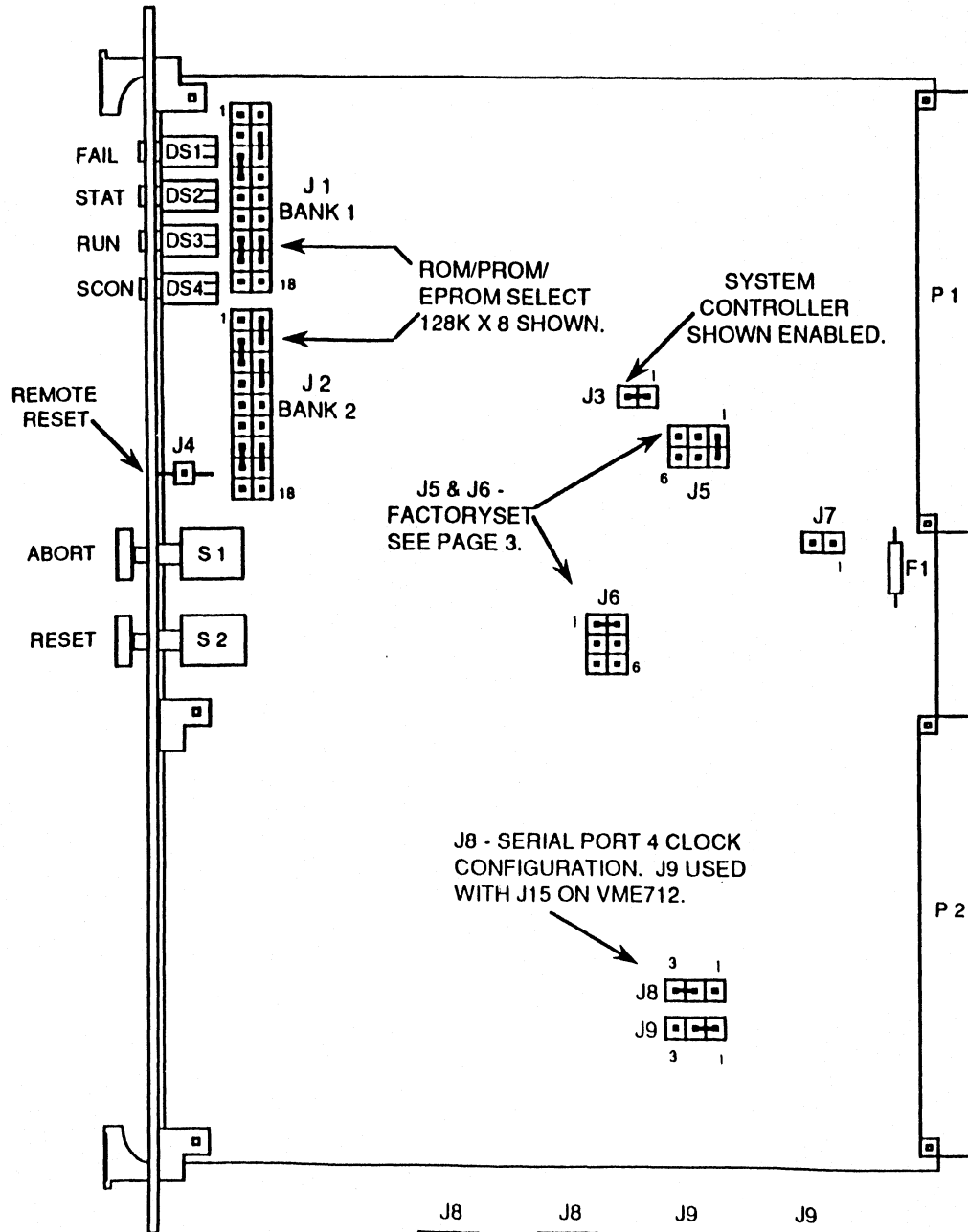
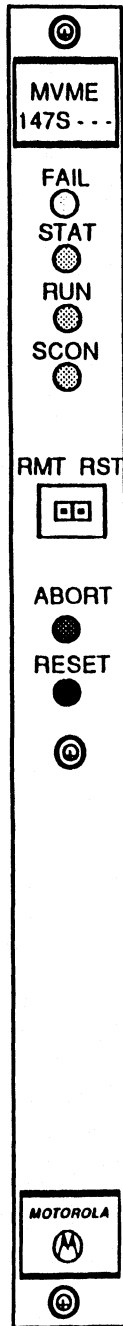
GCSR BOARD ADDRESS FUNCTION:

- S4-5 = BDAD3
- S4-6 = BDAD2
- S4-7 = BDAD1
- S4-8 = BDAD0

THIS MVME188 GCSR BASE ADDRESS OF \$0000C8F0 IS NOT RECOMMENDED.



11/12/91



PART NUMBERS:

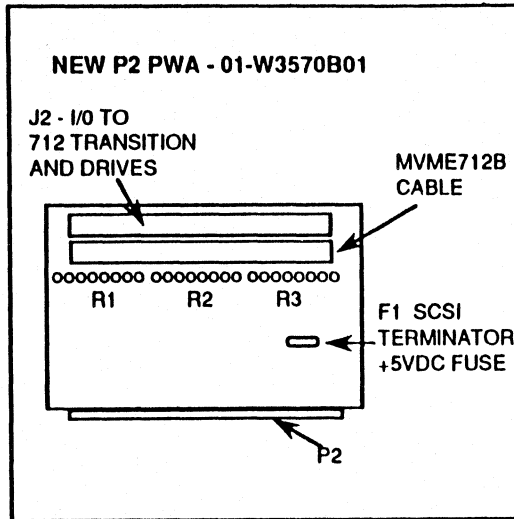
SMM147RP-A 01-W3648B44 TBD

SMM147RC-A 01-W3648B45 TBD

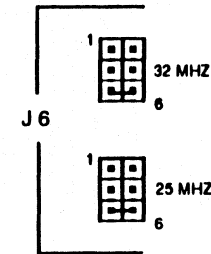
FUSE, MICRO, 1A AXIAL SCSI BUS 65NW9622A26
66430141
LITTLEFUSE 255001

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

04/02/91

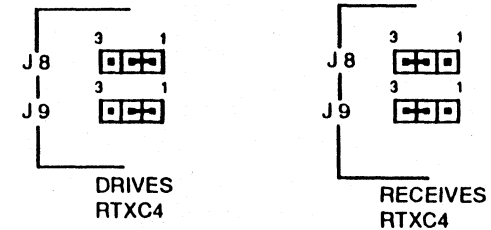


INTERNAL CLOCK FREQUENCY

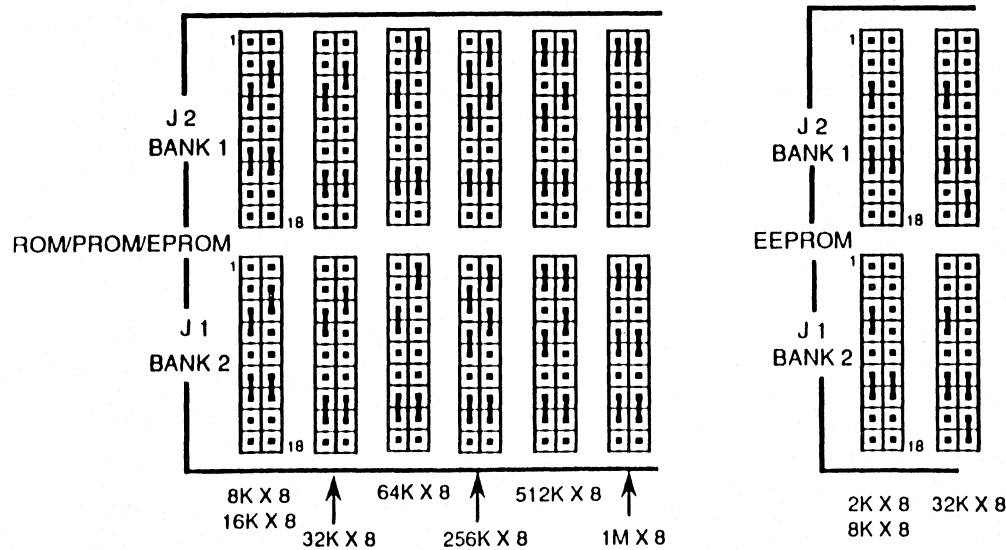


NOTE: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

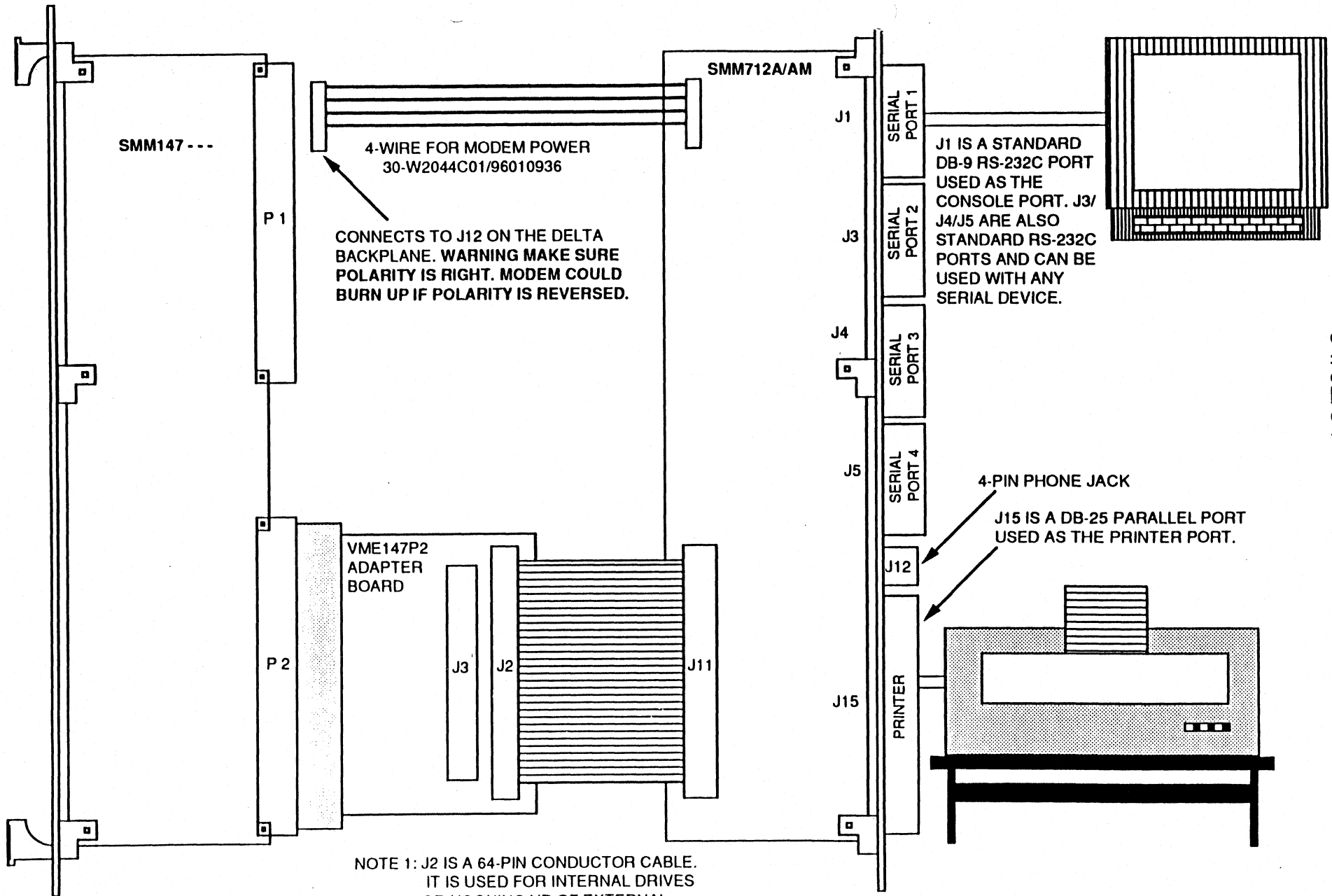
SERIAL PORT 4 CLOCK CONFIGURATION



ROM/PROM/EPROM/EEPROM SIZE SELECT HEADERS



04/02/91



4-WIRE FOR MODEM POWER
30-W2044C01/96010936

CONNECTS TO J12 ON THE DELTA
BACKPLANE. WARNING MAKE SURE
POLARITY IS RIGHT. MODEM COULD
BURN UP IF POLARITY IS REVERSED.

J1 IS A STANDARD
DB-9 RS-232C PORT
USED AS THE
CONSOLE PORT. J3/
J4/J5 ARE ALSO
STANDARD RS-232C
PORTS AND CAN BE
USED WITH ANY
SERIAL DEVICE.

4-PIN PHONE JACK

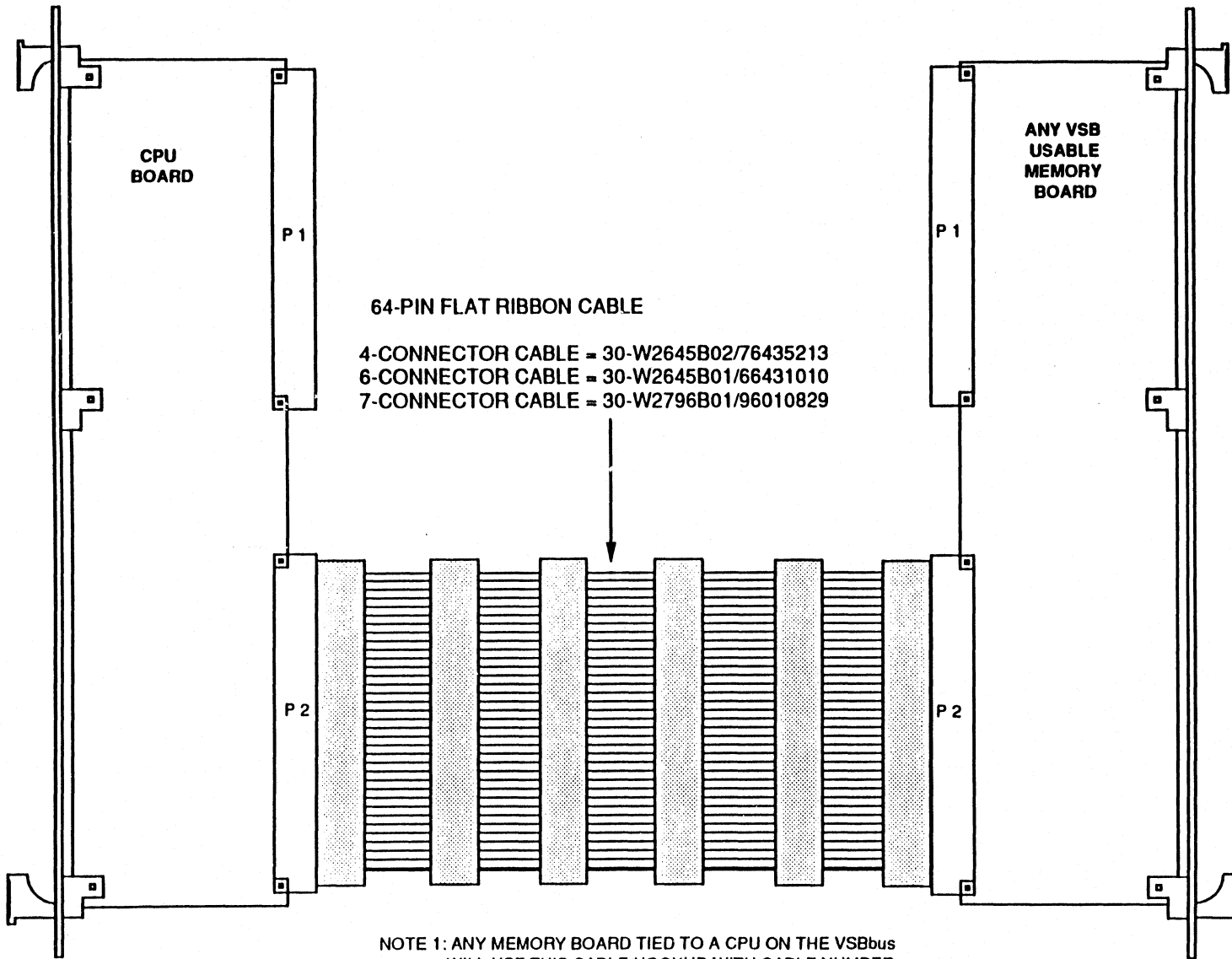
J15 IS A DB-25 PARALLEL PORT
USED AS THE PRINTER PORT.

NOTE 1: J2 IS A 64-PIN CONDUCTOR CABLE.
IT IS USED FOR INTERNAL DRIVES
OR HOOKING UP OF EXTERNAL
DRIVES THROUGH A USER SUPPLIED
PANEL. (30-W2335C01)

NOTE 2: TERMINATORS MUST BE INSTALLED
AT THE END OF ALL CABLES.

04/02/91

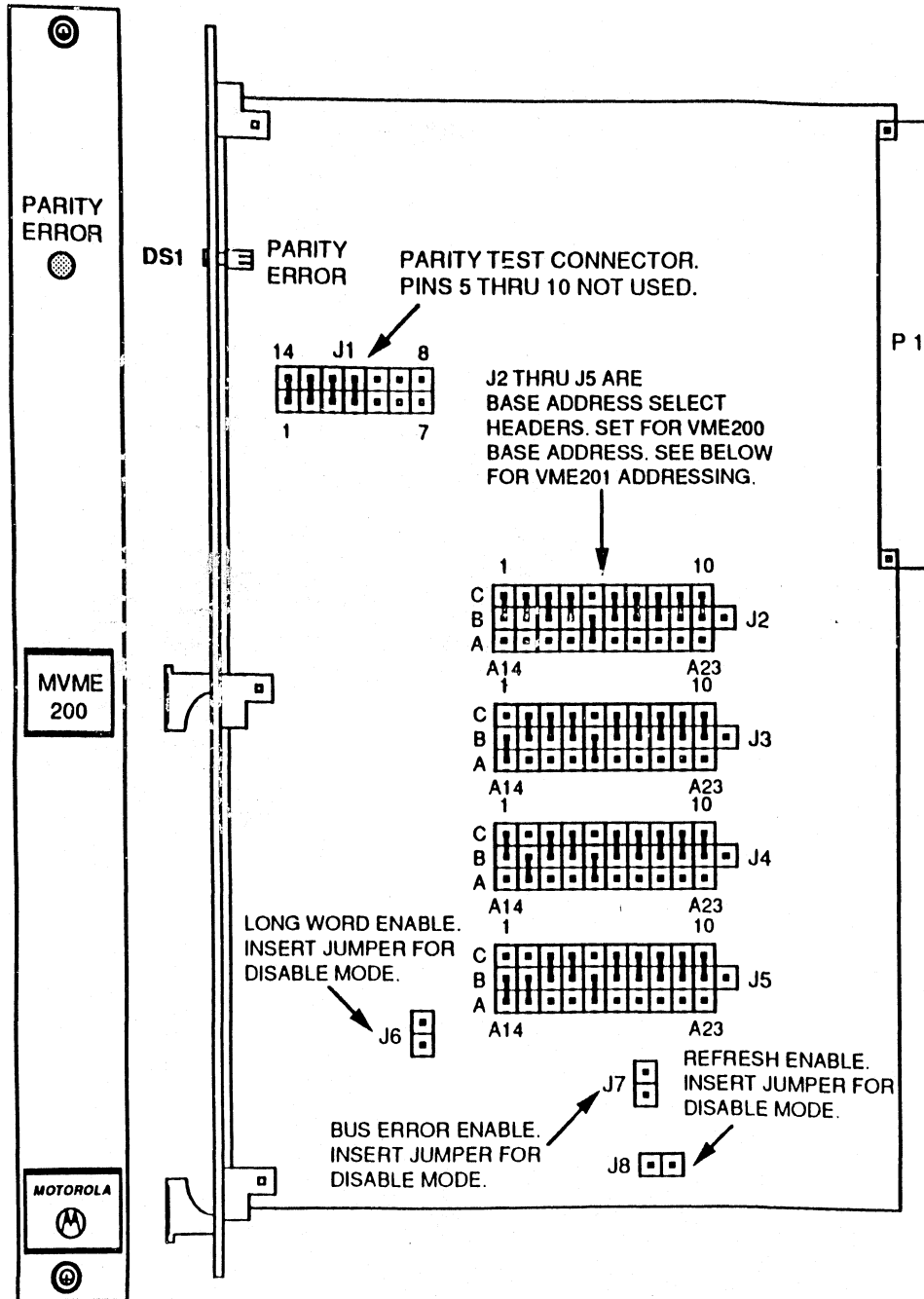
SECTION 2



NOTE 1: ANY MEMORY BOARD TIED TO A CPU ON THE VSBbus WILL USE THIS CABLE HOOKUP WITH CABLE NUMBER LISTED ABOVE.

04/16/90

ANY VSB
 USABLE
 MEMORY
 BOARD
 CABLING
 PA 1 OF 1



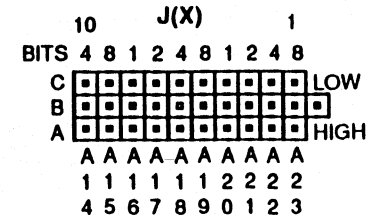
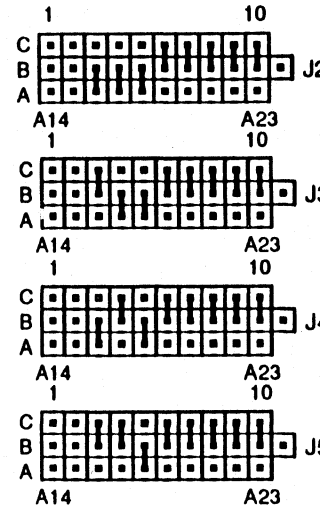
PART NUMBERS:

MVME200 01-W3147B01 (OLD DISCONTINUED) 76430466
 01-W3147B04 (NEW REPLACEMENT) 76432611

MVME201 01-W3147B02 (OLD DISCONTINUED) 76430467
 01-W3147B04 (NEW REPLACEMENT) 76432612

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

VME201 BASE ADDRESS SELECT.
 SEE NOTE 2.



INSERTING JUMPERS BETWEEN ROWS "A" AND "B" ENABLES THE PARTICULAR ADDRESS BIT. ABOVE IS SHOWN THE ADDRESS LINE AND BIT WEIGHT OF THE CORRESPONDING SELECT LINE.

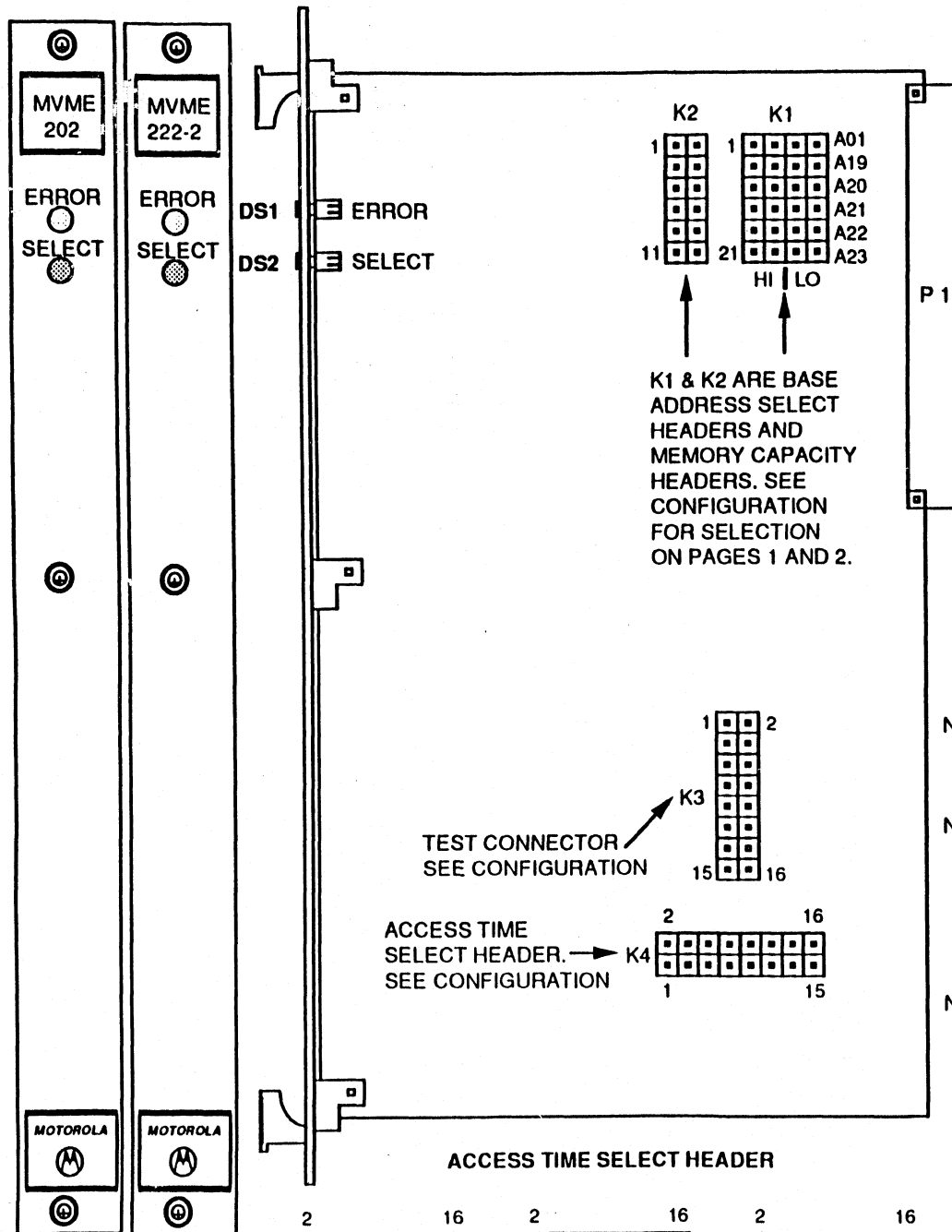
NOTE 1: VME200 ADDRESS IS SET AT: J2 = \$40000, J3 = \$44000, J4 = \$48000 AND J5 = \$4C000.

NOTE 2: VME201 ADDRESS IS SET AT: J2 = \$70000, J3 = \$60000, J4 = \$50000 AND J5 = \$40000.

NOTE 3: TO DISABLE BASE ADDRESS SELECT JUMPERS. INSTALL A JUMPER BETWEEN B10 AND THE OPEN JUMPER ON THE FRONT WHICH SHOULD BE CONSIDERED B11 FOR ALL PRACTICAL REASONS.

NOTE 4: THE BASE ADDRESS IS SELECTABLE ON 16K BOUNDARIES IN THE COMPLETE 16 MEGABYTE ADDRESS MAP.

03/14/91

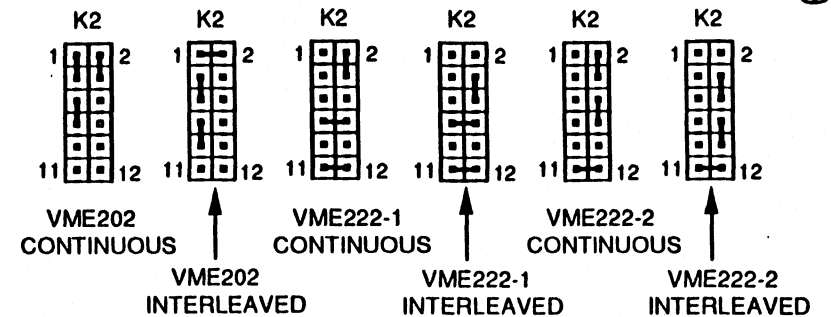


PART NUMBERS:

MVME202	01-G3025M01	76432613	EURO BUILD
	01-W3507B01	76432613	US BUILD
MVME222-1	01-G3025M02	76432614	EURO BUILD
	01-W3507B02	76432614	US BUILD
MVME222-2	01-G3025M03	76432615	EURO BUILD
	01-W3507B03	96011131	US BUILD

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

MEMORY CAPACITY AND ADDRESS MODE SELECT

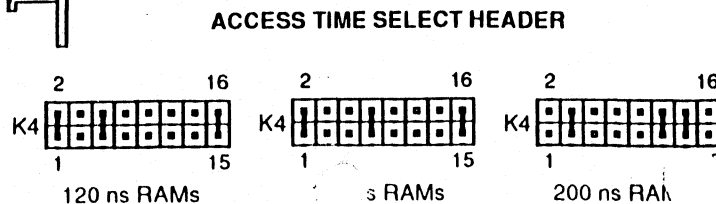
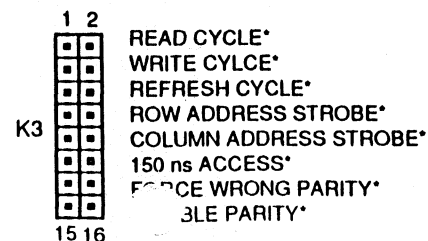


NOTE 1: THE BASE ADDRESS SELECTION IS DEPENDANT ON MODULE CAPACITY (512K, 1M OR 2M) AND ADDRESS MODE (CONTINUOUS OR INTERLEAVED) IN A 16 MEG ADDRESS MAP.

NOTE 2: IN JUMPERING K1, A01 IS SELECTED ONLY WHEN USING INTERLEAVED BOARDS. ONE BOARD SELECTS A01 LOW AND THE OTHER BOARD SELECTS IT HIGH. OTHERWISE, THE JUMPER IS SET IN THE MIDDLE POSITION WHICH DIRECTS THE SIGNAL TO THE RAM CHIP ARRAY AND IS NOT USED FOR ADDRESS DECODING. SEE PAGE 2 FOR EXPLAINED DETAILS.

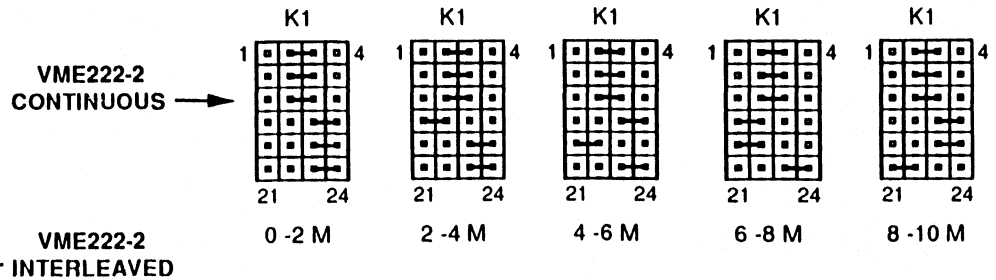
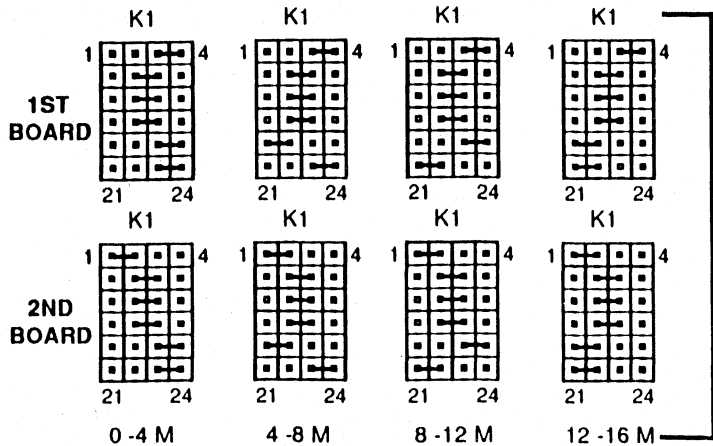
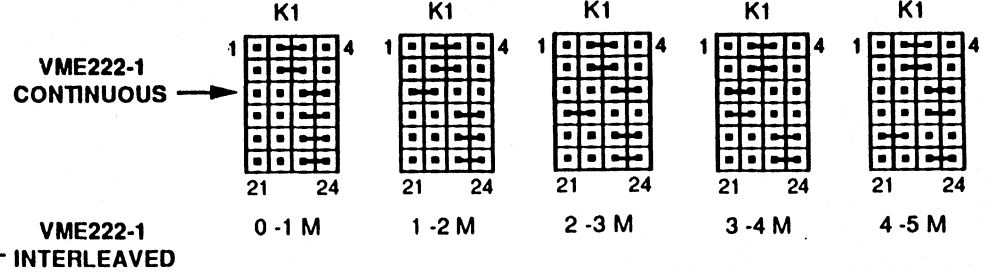
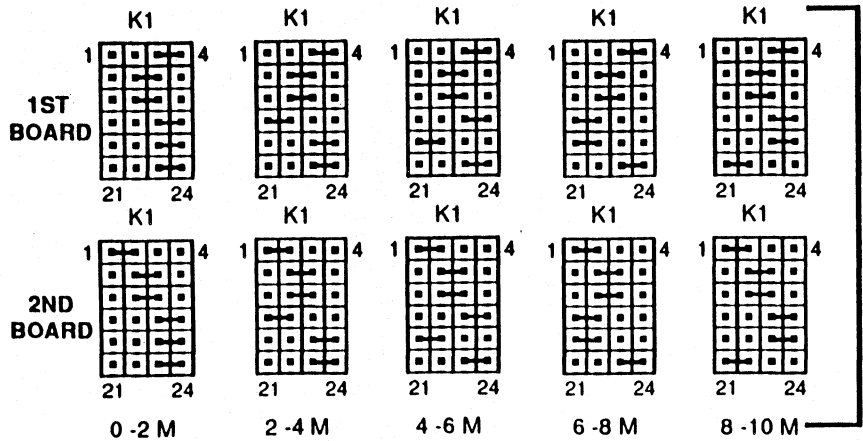
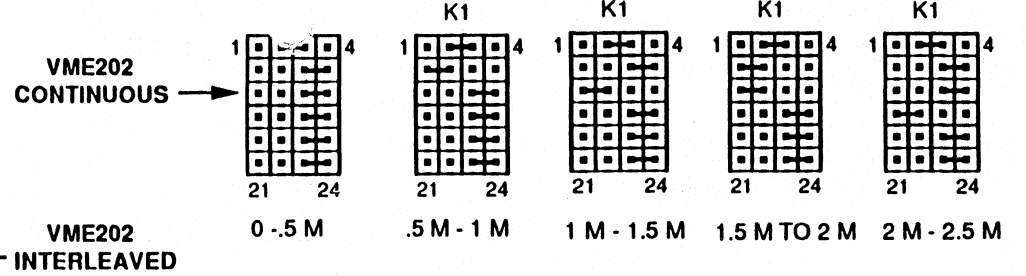
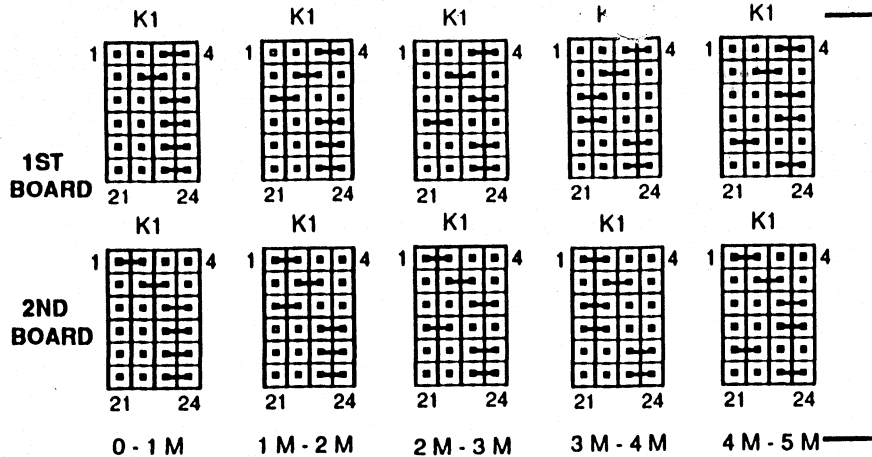
NOTE 3: ADDRESS LINES A19 THRU A21 ARE ALSO DESELECTED THE SAME WAY. SEE PAGE 2 FOR THEIR USE ALSO. ADDRESS LINES USED FOR ADDRESS DECODE ARE SET FOR HIGH TO SELECT A 1 AND LOW TO SELECT A 0.

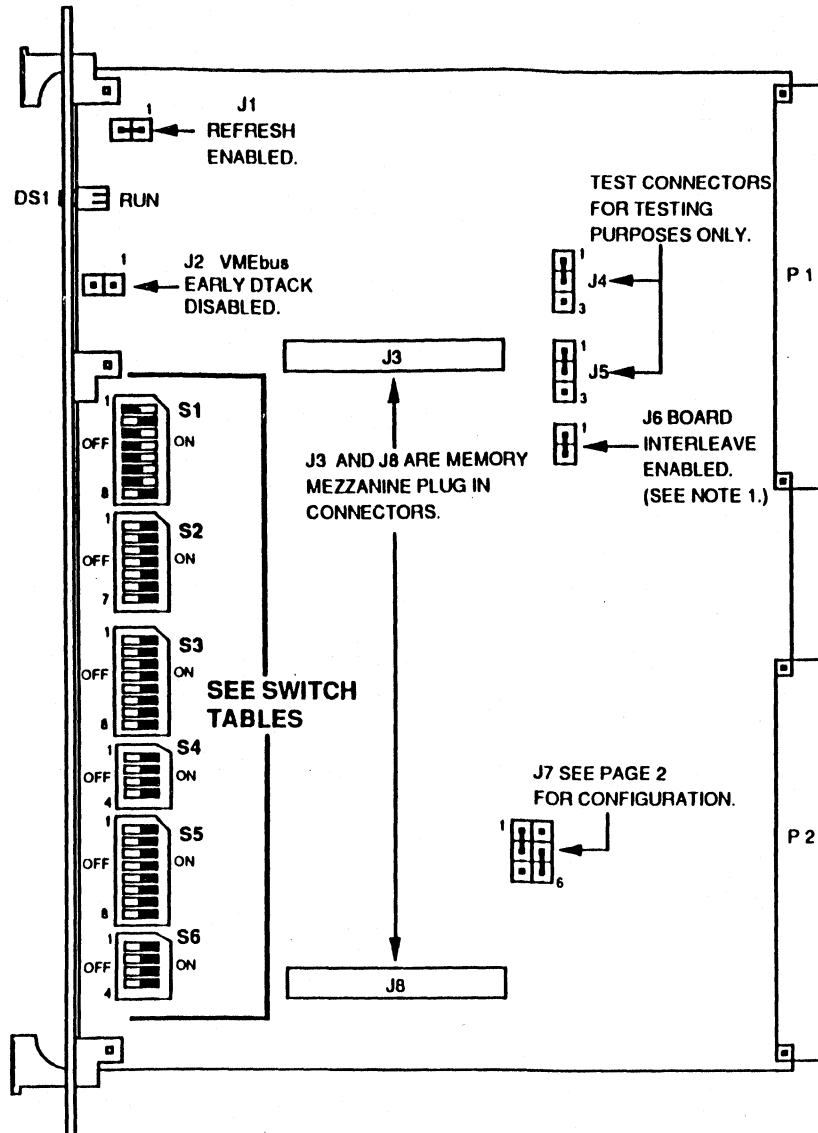
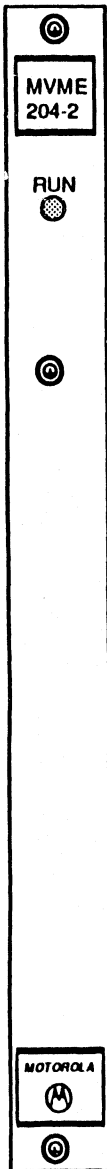
TEST CONNECTOR



1/1/29/89

MVME202/222-1/-2
512K, 1, 2 MB DRAM
MEMORY MODULE
PAGE 1 2





NOTE 1: INTERLEAVING SPEEDS UP MEMORY ACCESS. IF THE FIRST BOARD HAS THE INTERLEAVE JUMPER INSTALLED, THE SECOND SHOULD NOT HAVE THE JUMPER INSTALLED.

NOTE 2: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 3: THE BASE ADDRESS FOR VME204-1 IS SET ON 1 MEG BOUNDARIES AND VME204-2 IS SET ON 2 MEG BOUNDARIES IN THE COMPLETE ADDRESS MAP.

PART NUMBERS:

MVME204-1 01-W3418B01 76433106

MVME204-2 01-W3418B02 96010825

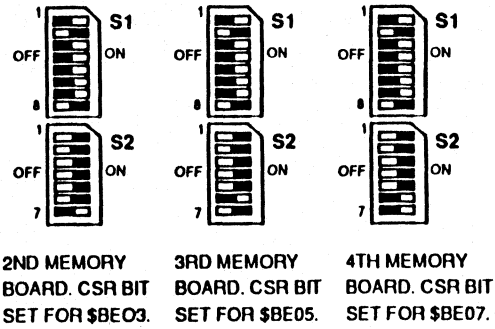
NONE 01-W3424B01 66010022

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

04/16/90

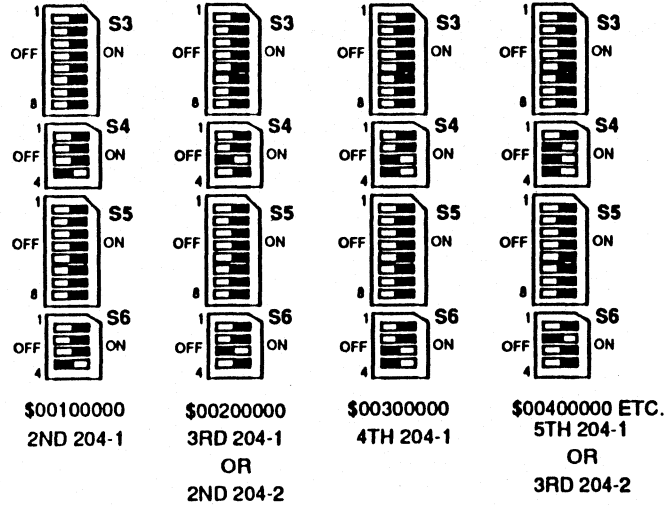
MVME204-1/-2
1, 2 MB DUAL PORTED
DRAM
MEMORY VME module
PAGE 1 2

CSR MAPPING SWITCHES



2ND MEMORY BOARD. CSR BIT SET FOR \$BE03. 3RD MEMORY BOARD. CSR BIT SET FOR \$BE05. 4TH MEMORY BOARD. CSR BIT SET FOR \$BE07.

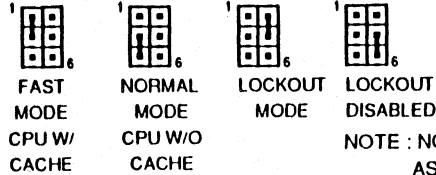
VMEbus/VSBbus SWITCH CONFIGURATION



\$00100000 2ND 204-1 \$00200000 3RD 204-1 \$00300000 4TH 204-1 \$00400000 ETC. 5TH 204-1 OR 3RD 204-2

NOTE: VME204-1 CAN ONLY BE SET ON 1 MB BOUNDARIES.
VME204-2 CAN ONLY BE SET ON 2 MB BOUNDARIES.
SWITCHES S3 AND S4 ARE FOR VMEbus AND
SWITCHES S5 AND S6 ARE FOR VSBbus.

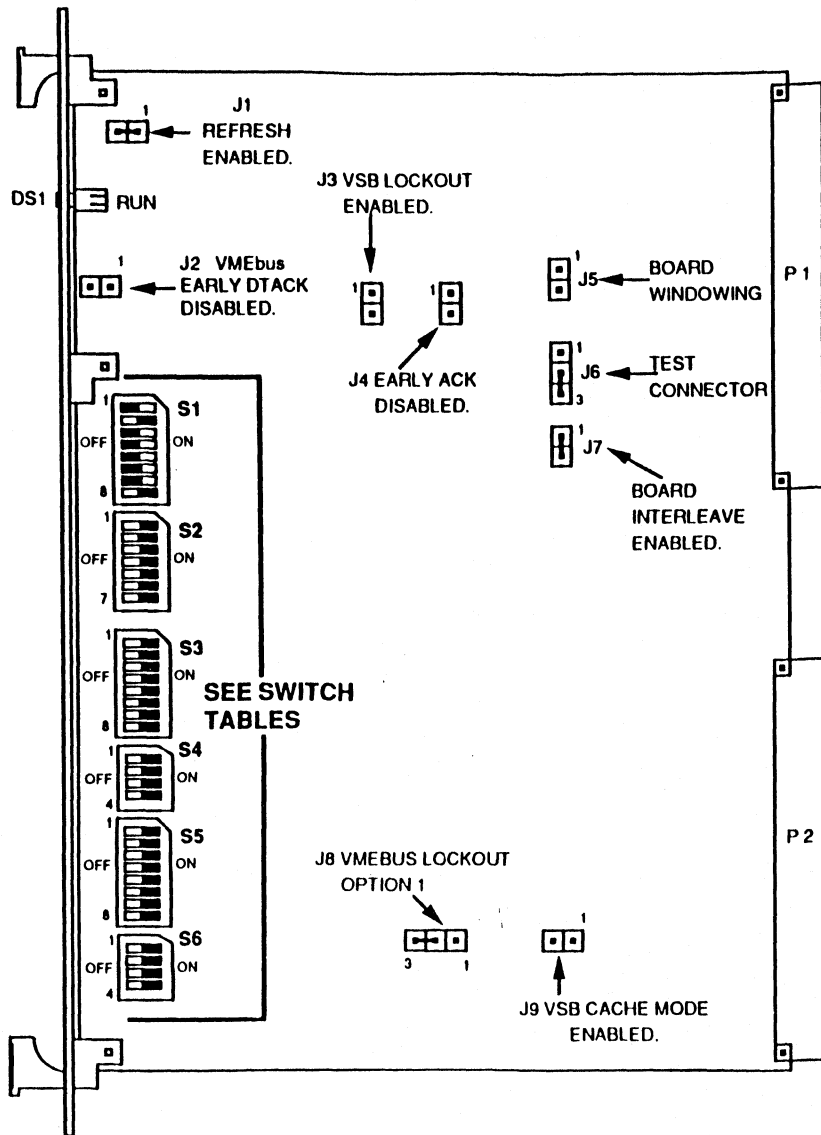
J7 VMX32bus FAST MODE/VMEbus LOCKOUT ENABLE



FAST MODE CPU W/ CACHE NORMAL MODE CPU W/O CACHE LOCKOUT MODE LOCKOUT DISABLED

NOTE : NO INFORMATION AVAILABLE AS TO WHEN THIS SHOULD BE DISABLED.

11/13/91



NOTE 1: WHEN USED WITH AN MVME224-1/2 MEMORY BOARD, THE MVME224-(X) SHOULD BE CLOSEST (JUST TO THE RIGHT OF) THE PROCESSOR BOARD AND THE MVME204-2F, JUST TO THE RIGHT OF THE MVME224-(X), IN THE NEXT CONTIGUOUS MEMORY ADDRESS BLOCK.

NOTE 2: ACTIVE PART OF THE SWITCH IS DARKENED AREA.

NOTE 3: THE BASE ADDRESS IS SELECTABLE ON ANY 2 MEG BOUNDARIES IN THE COMPLETE ADDRESS MAP.

PART NUMBERS:

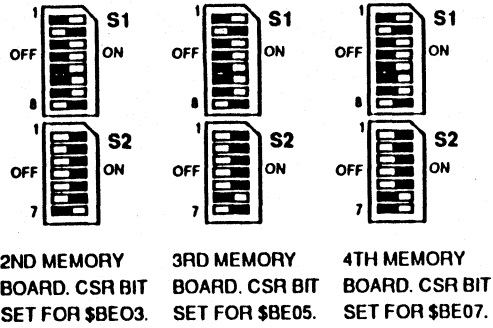
MVME204-2F 01-W3457B01 76435349

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

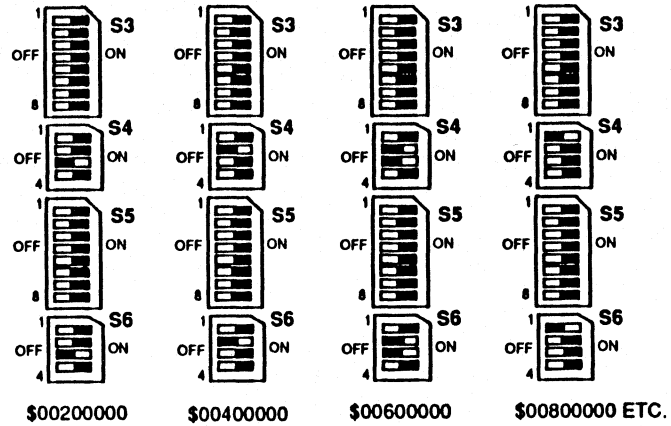
03/14/91

MVME204-2F
2 MB DUAL PORTED
FAST DRAM
MEMORY VMEmodule
 PAGE 1 2

CSR MAPPING SWITCHES

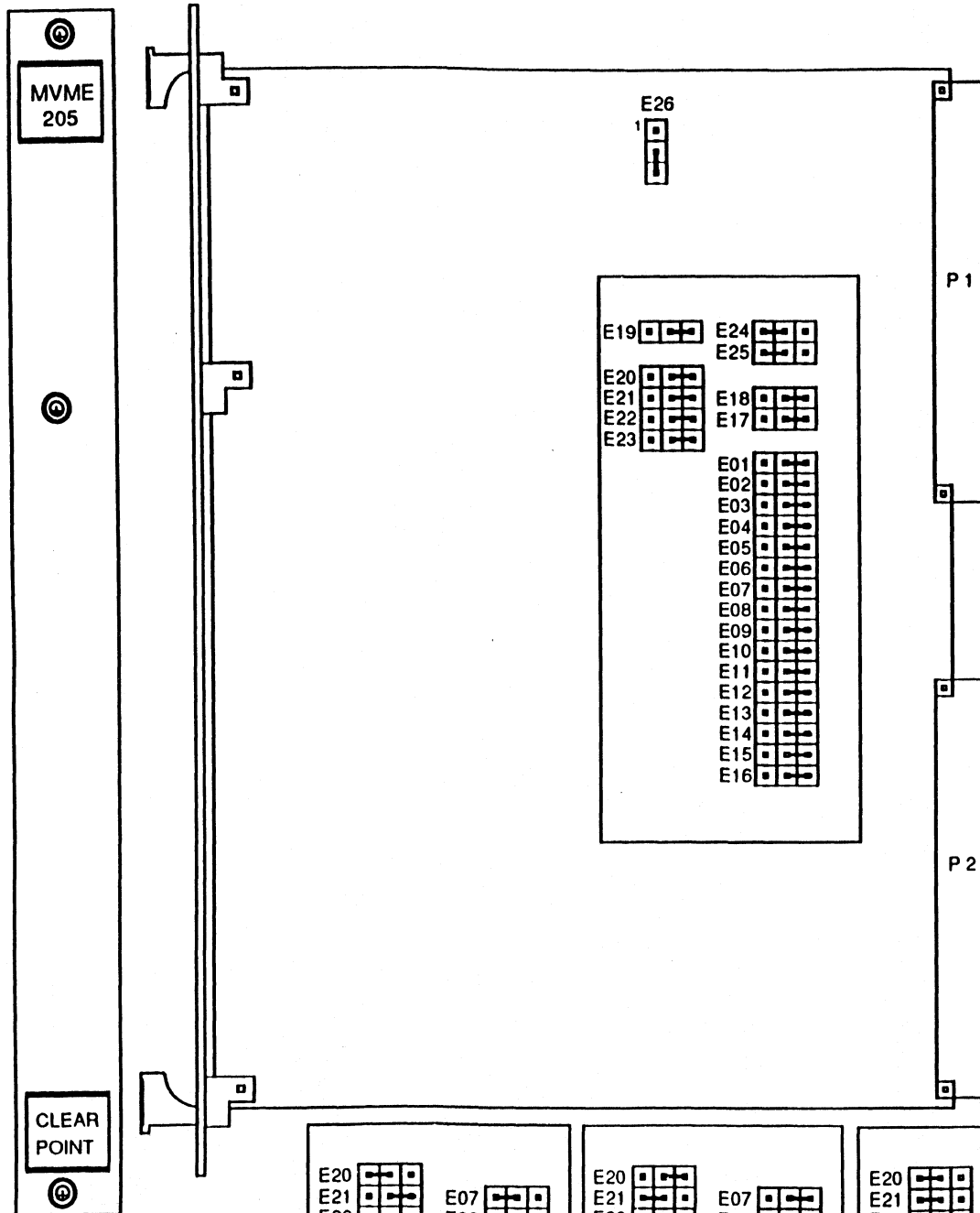


VMEbus/VSBbus SWITCH CONFIGURATION



NOTE: VME204-2F CAN ONLY BE SET ON 2 MB BOUNDARIES.
 SWITCHES S3 AND S4 ARE FOR VMEbus AND
 SWITCHES S5 AND S6 ARE FOR VSBbus.

09/12/89



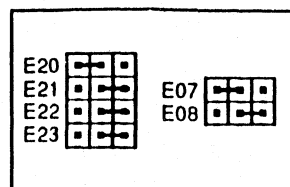
PART NUMBERS:

MVME205 01-W2814B01 96010862
 CLEARPOINT PART# VMERAM

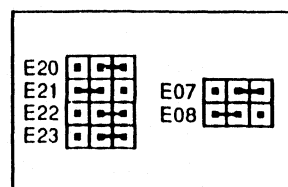
SEE CURRENT REVISION LEVEL (CRL) FOR
 CURRENT REVISION INFORMATION.

NOTE 1: FOR A32 ADDRESSING, SEE MVME13(X) SWITCH
 S4 SETTING ON PAGE 2 OF 4.

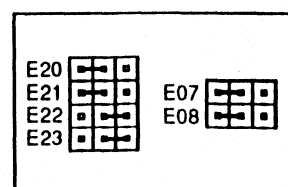
02/23/90



2ND MEMORY BOARD

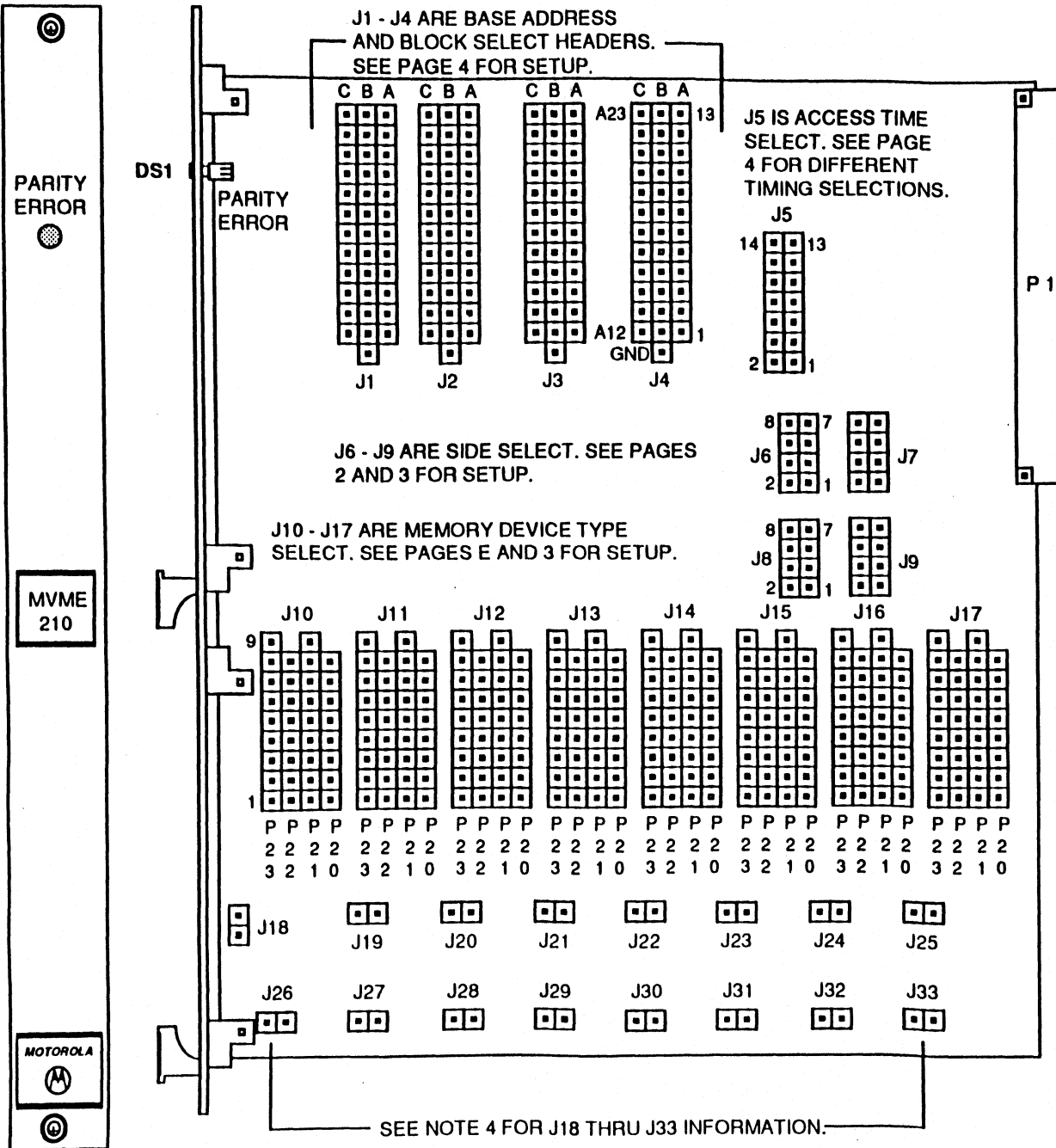


3RD MEMORY BOARD



1 MEMORY BOARD

**MVME205
 4 MB ECC DRAM
 MEMORY VME module
 PAGE 1 OF 1**



PART NUMBERS:

MVME210 01-W3155B01 76430469

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: J1 THRU J4 ARE USED IN CONJUNCTION WITH J6 THRU J9 RESPECTIVELY.

NOTE 2: THE BASE ADDRESS IS DEPENDANT ON THE SIZE OF MEMORY DEVICES BEING INSTALLED IN EACH BLOCK.

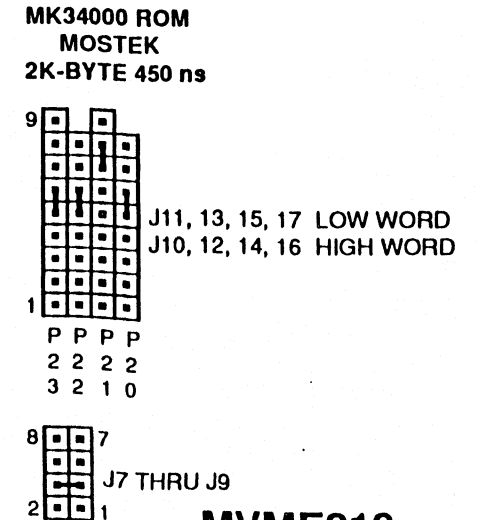
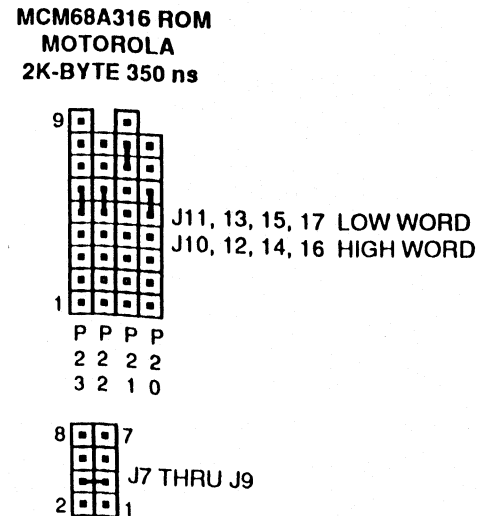
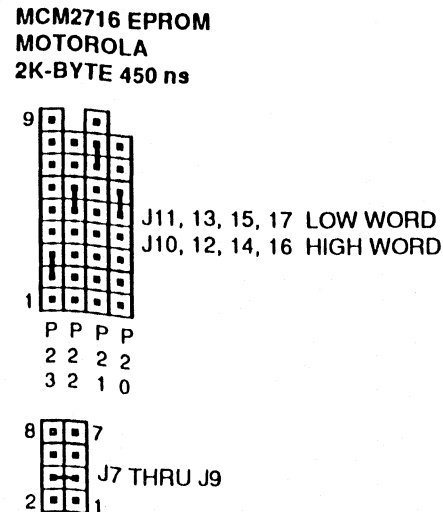
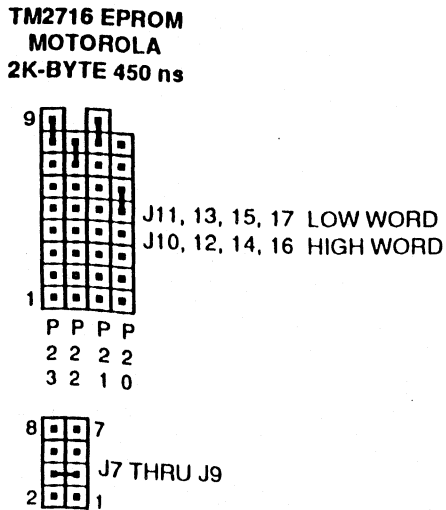
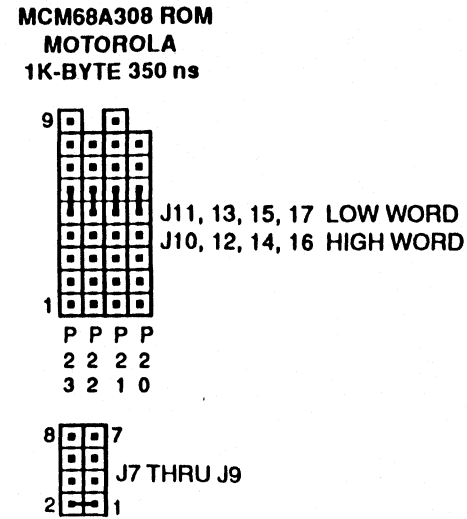
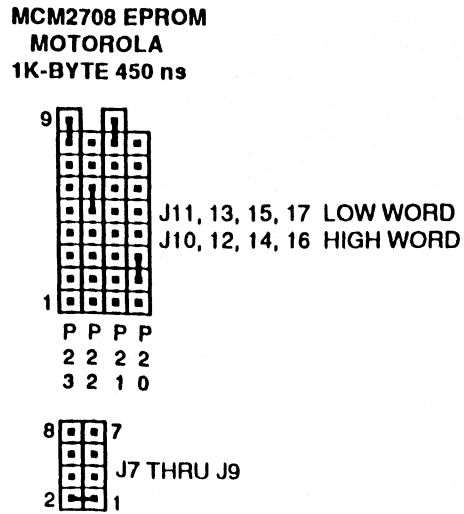
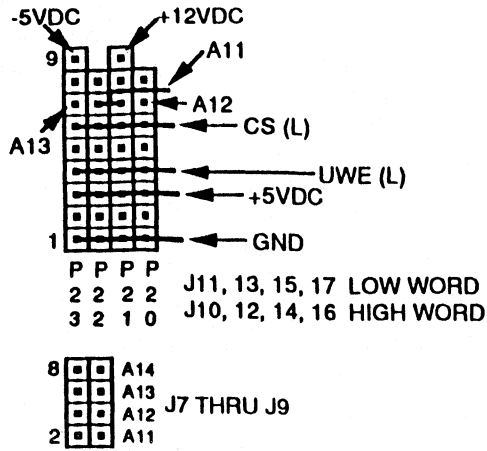
NOTE 3: J6 THRU J9 ROUTE SIGNALS NOT TO BE DECODED FOR BASE ADDRESS SELECTION. SEE PAGE 3 FOR DETAILS ON JUMPER SETTINGS.

NOTE 4: IF ROM/EPROM DEVICES ARE USED, J18 THRU J25 SHOULD HAVE JUMPERS INSTALLED. IF SRAM DEVICES ARE USED, THEN J26 THRU J33 SHOULD HAVE JUMPERS INSTALLED. AT NO TIME CAN BOTH OF THESE JUMPER SERIES BE INSTALLED WITHOUT DAMAGING THE MEMORY BOARD.

09/12/89

**MVME210
16K/32K/64K/128K
SRAM/ROM/EPROM
MEMORY MODULE
PAGE 4 OF 4**

J6 THRU J9 SIDE SELECT AND J10 THRU J17 MEMORY DEVICE TYPE SELECT



09/12/89

J6 THRU J9 SIDE SELECT AND J10 THRU J17 MEMORY DEVICE TYPE SELECT (CONTINUED)

MSM2128 SRAM
OKI
2K-BYTE 200 ns



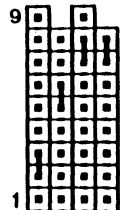
J11, 13, 15, 17 LOW WORD
J10, 12, 14, 16 HIGH WORD

P P P P
2 2 2 2
3 2 1 0



J7 THRU J9

MCM2532 EPROM
MOTOROLA
2K-BYTE 450ns



J11, 13, 15, 17 LOW WORD
J10, 12, 14, 16 HIGH WORD

P P P P
2 2 2 2
3 2 1 0



J7 THRU J9

MEMORY
DEVICE
CAPACITY

ADDRESS LINE FUNCTION

A11 A12 A13 A14

PINS TO BE
JUMPED
J6 - J9

MEMORY DEVICE CAPACITY	A11	A12	A13	A14	PINS TO BE JUMPED J6 - J9
1K-BYTE	SS	X	X	X	1 - 2
2K-BYTE	A	SS	X	X	3 - 4
4K-BYTE	A	A	SS	X	5 - 6
8K-BYTE	A	A	A	SS	7 - 8

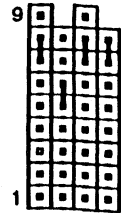
WHERE: A = USED FOR INTERNAL ADDRESSING
SS = USED FOR SIDE SELECT
X = AVAILABLE FOR BLOCK ADDRESS DECODING

NOTES: ADDRESS LINE A01 - A10 ARE ALWAYS USED FOR INTERNAL ADDRESSING.

ADDRESS LINES A15 - A23 ARE ALWAYS USED FOR BLOCK ADDRESS DECODING.

09/12/89

MCM68764 EPROM
MOTOROLA
8K-BYTE 450 ns



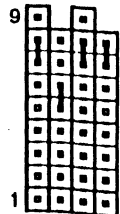
J11, 13, 15, 17 LOW WORD
J10, 12, 14, 16 HIGH WORD

P P P P
2 2 2 2
3 2 1 0



J7 THRU J9

TMS2564 EPROM
TEXAS INSTRUMENT
8K-BYTE 450 ns



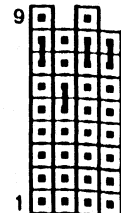
J11, 13, 15, 17 LOW WORD
J10, 12, 14, 16 HIGH WORD

P P P P
2 2 2 2
3 2 1 0



J7 THRU J9

MCM68A364 ROM
MOTOROLA
8K-BYTE 350 ns



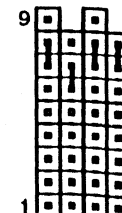
J11, 13, 15, 17 LOW WORD
J10, 12, 14, 16 HIGH WORD

P P P P
2 2 2 2
3 2 1 0



J7 THRU J9

MK36000 ROM
MOSTEK
8K-BYTE 300 ns



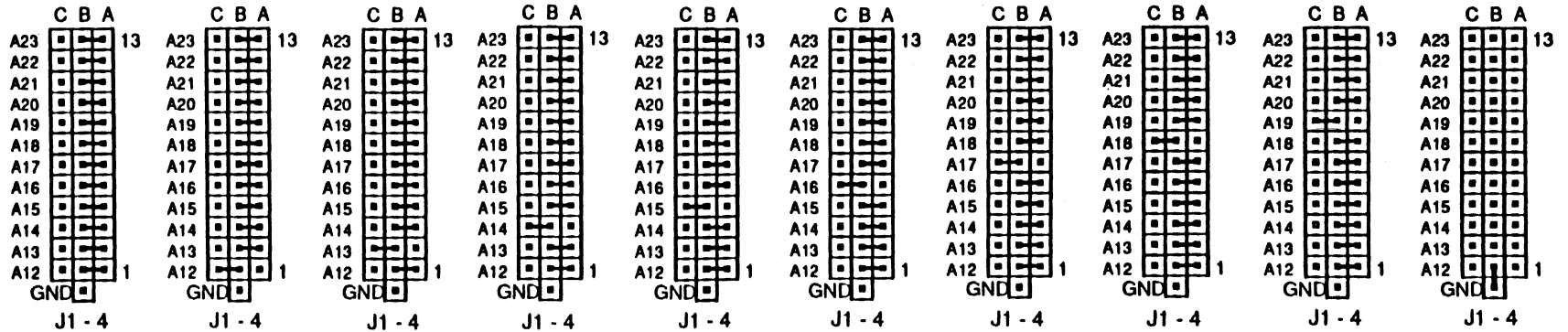
J11, 13, 15, 17 LOW WORD
J10, 12, 14, 16 HIGH WORD

P P P P
2 2 2 2
3 2 1 0



J7 THRU J9

J1 THRU J4 BASE ADDRESS SELE UMPERS



BASE ADDRESS OF \$0000

\$1000

\$2000

\$4000

\$8000

\$10000

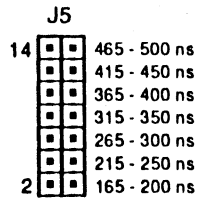
\$20000

\$40000

\$80000

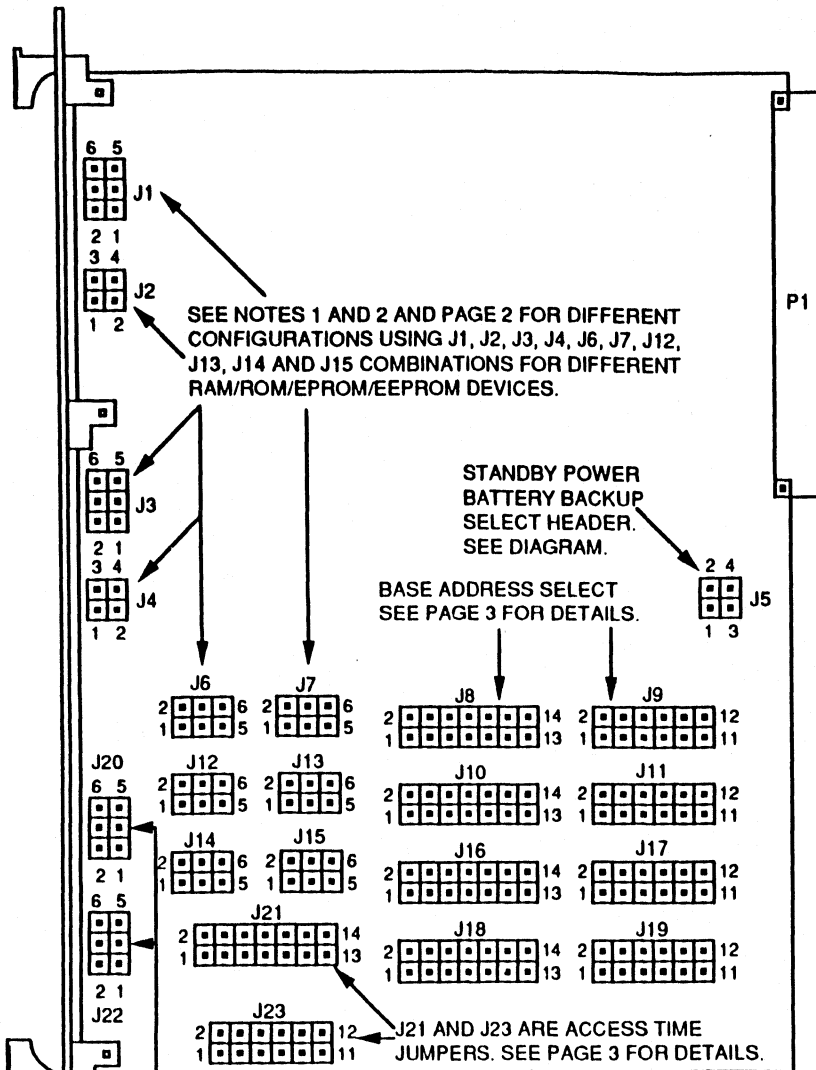
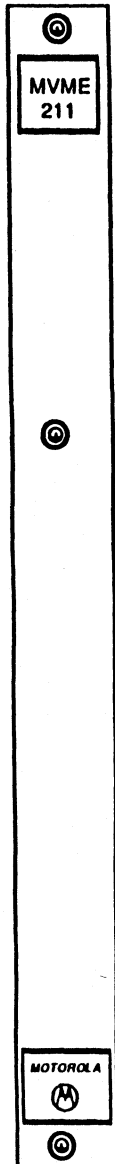
BLOCK DISABLED
WITH 1K-BYTE
BLOCK CHIPS

ACCESS TIME SELECT



DIFFERENT SPEEDS ARE
SELECTED BY INSERTING
JUMPERS STRAIGHT ACROSS.
(i.e. 1 - 2, 3 - 4, 5 - 6, etc.)

09/12/89



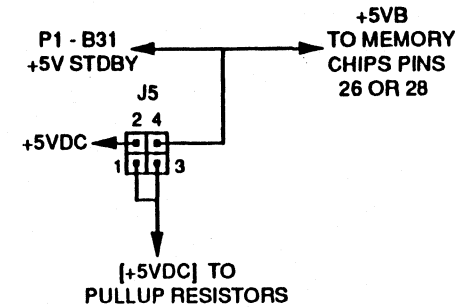
CHIP SIZE SELECT HEADERS.
SEE PAGE 2 FOR DETAILS.
J20 IS FOR BANK 1, J22 IS
FOR BANK 2.

PART NUMBERS:

MVME211 01-W3295B01 76432569

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

STANDBY POWER BATTERY BACKUP SELECT



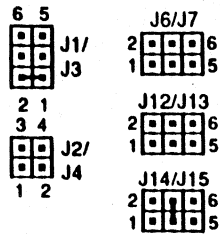
NOTE 1: THE BASE ADDRESS IS DEPENDANT ON THE SIZE OF MEMORY DEVICES BEING INSTALLED IN EACH BLOCK.

NOTE 2: MEMORY ARRAY IS SEPARATED INTO 2 BANKS. BANK 1 CONSISTS OF JUMPERS J1, J2, J6, J12 AND J14. BANK 2 CONSISTS OF JUMPERS J3, J4, J7, J13 AND J15. SEE PAGE 2 FOR CONFIGURATIONS AND SIGNAL NOMENCLATURE.

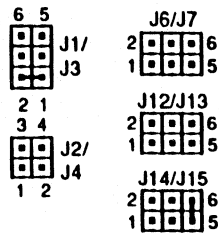
09/12/89

MVME211
32K/64K/128K/
256K/512K/1 MB
RAM/ROM/EPROM
MEMORY VME module
PAGE 1 OF 3

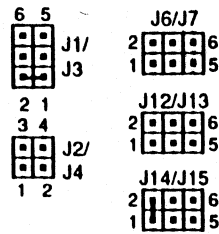
RAM/ROM/EPROM SIZE SIGNAL SELECT HEADERS



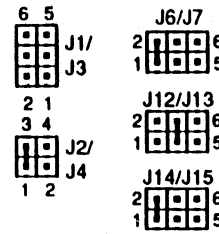
2K X 8 EPROM
INTEL 2716



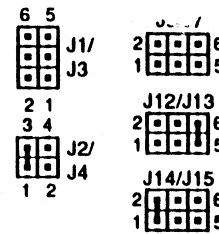
2K X 8 EEPROM
ICOR X2816A



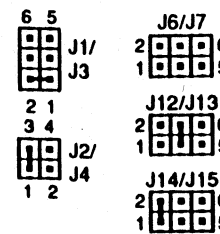
4K X 8 EPROM
INTEL 2732



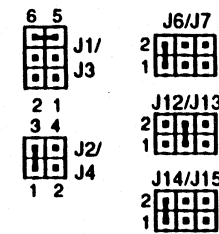
8K X 8 EPROM
INTEL 2764



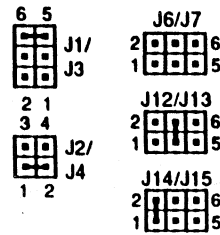
8K X 8 EEPROM
ICOR X2864A



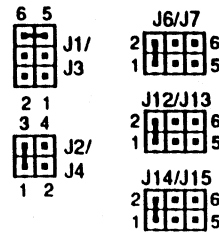
8K X 8 ROM
MOTOROLA MCM68369



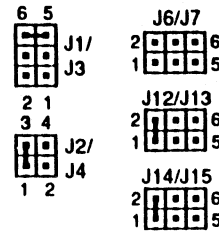
16K X 8 EPROM
INTEL 27128



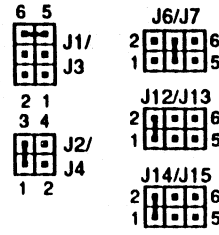
16K X 8 ROM
MOTOROLA MCM63128



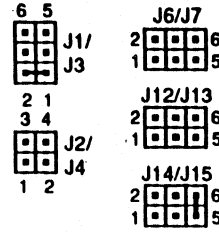
32K X 8 EPROM
INTEL 27256



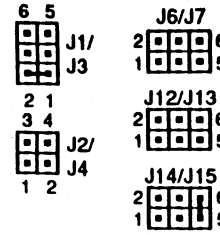
32K X 8 ROM
MOTOROLA MCM63256



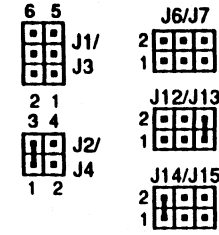
64K X 8 EPROM
INTEL 27512



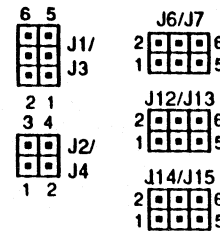
2K X 8 CMOS RAM
TOSHIBA TC5517A



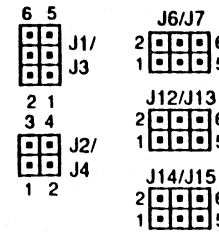
2K X 8 NMOS RAM
MOTOROLA MCM2016H



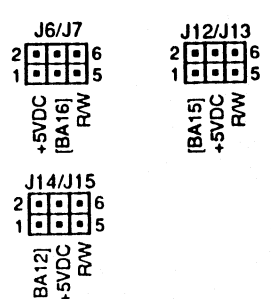
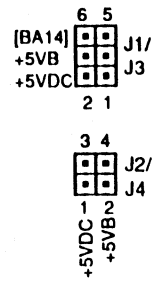
4K X 8 RAM
INDUSTRY STANDARD



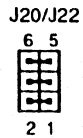
8K X 8 CMOS RAM
TOSHIBA TC5564



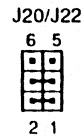
8K X 8 NMOS RAM
INTEL 2186-25



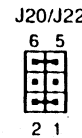
CHIP SIZE SELECT HEADER



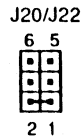
LOGICAL
EQUIVALENT
NUMBER 0
CHIP SIZE
2K X 8



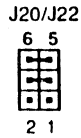
LOGICAL
EQUIVALENT
NUMBER 1
CHIP SIZE
4K X 8



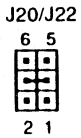
LOGICAL
EQUIVALENT
NUMBER 2
CHIP SIZE
8K X 8



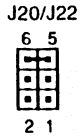
LOGICAL
EQUIVALENT
NUMBER 3
CHIP SIZE
16K X 8



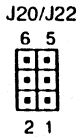
LOGICAL
EQUIVALENT
NUMBER 4
CHIP SIZE
32K X 8



LOGICAL
EQUIVALENT
NUMBER 5
CHIP SIZE
64K X 8

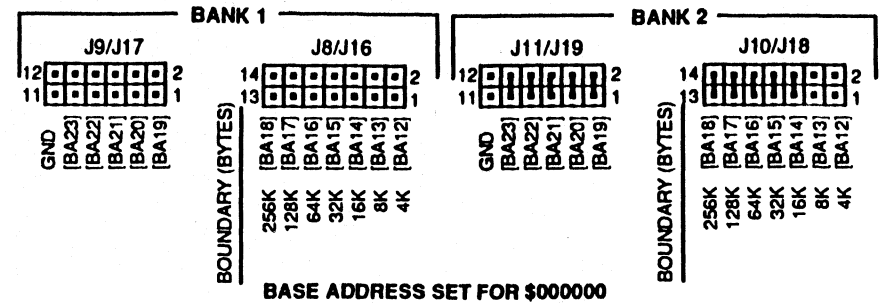
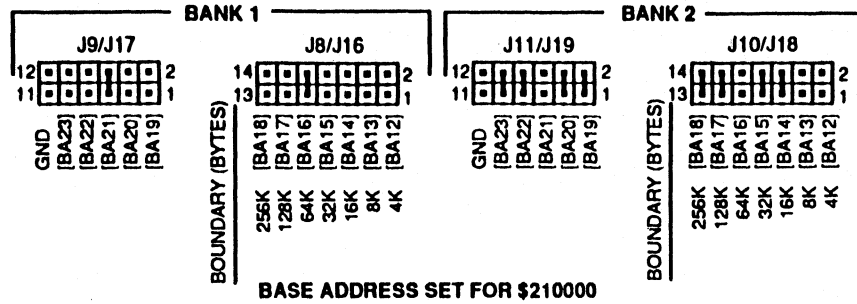


LOGICAL
EQUIVALENT
NUMBER 6
DO NOT USE
THIS OPTION

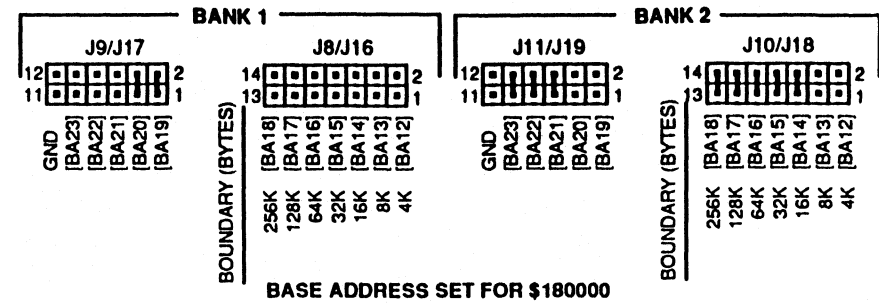


LOGICAL
EQUIVALENT
NUMBER 7
DO NOT USE
THIS OPTION

BASE ADDRESS SELECT HEADERS



MEMORY DEVICE CAPACITY	ADDRESS LINE FUNCTION							
	A12	A13	A14	A15	A16	A17	A18	
2K X 8	S	S	X	X	X	X	X	
4K X 8	A	S	S	X	X	X	X	
8K X 8	A	A	S	X	X	X	X	
16K X 8	A	A	A	S	S	X	X	
32K X 8	A	A	A	A	S	S	X	
64K X 8	A	A	A	A	A	S	S	



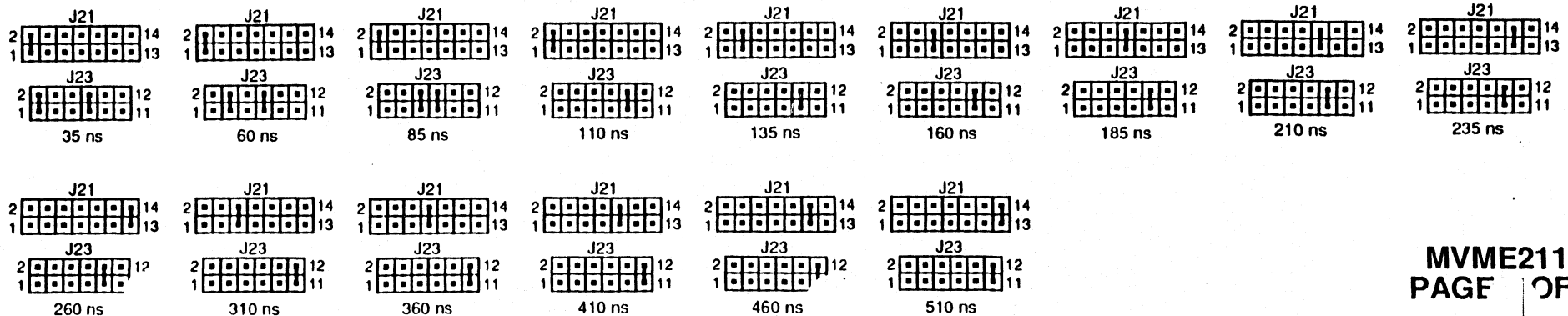
WHERE: A = USED FOR INTERNAL ADDRESSING
 S = USED FOR SOCKET PAIR SELECTION
 X = AVAILABLE FOR BLOCK ADDRESS DECODING

NOTES: ADDRESS LINE A01 - A11 ARE ALWAYS USED FOR INTERNAL ADDRESSING.

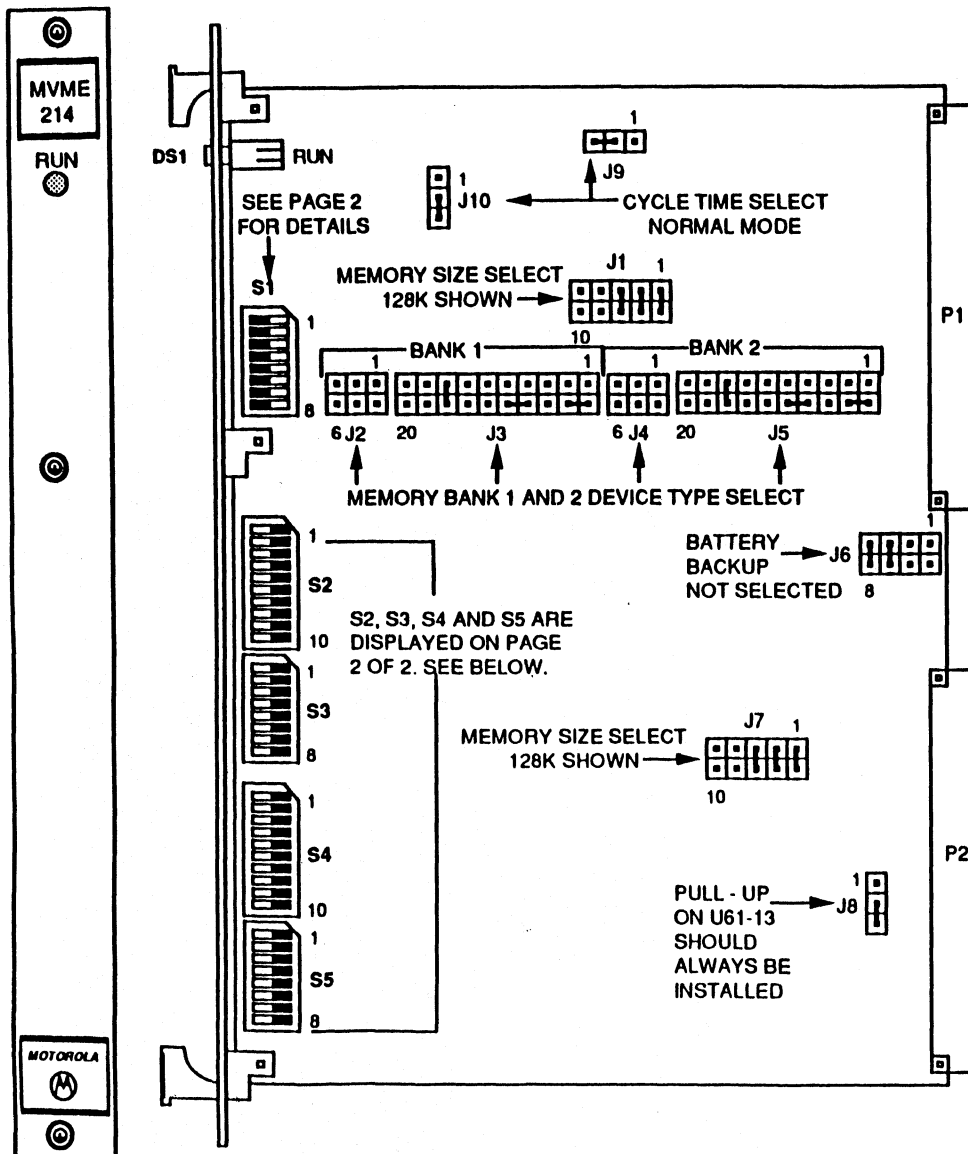
ADDRESS LINES A19 - A23 ARE ALWAYS USED FOR BLOCK ADDRESS DECODING, AND ARE ALWAYS JUMPED EXCEPT WHEN THE BLOCK IS DISABLED.

THE "SOCKET PAIR SELECTION" (S) ADDRESS(ES) MAY BE JUMPED, BUT THIS WILL DISABLE PART OF THE MEMORY BLOCK SELECTED. SUCH DISABLING IS NOT RECOMMENDED BY MOTOROLA.

ACCESS TIME SELECT (NOMINAL ACCESS TIME IN NANoseconds)



09/12/89



PART NUMBERS:

MVME214 01-W3374B01 76433022

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1 : J2 AND J4 ARE JUMPED AS FOLLOWS:

- A14 = PINS 2 TO 4
- +5VDC = PINS 3 TO 4
- A15 = PINS 4 TO 6

J3 AND J5 ARE JUMPED AS FOLLOWS:

- PIN 27 TO GROUND = PINS 19 TO 20
- TO A14 = PINS 17 TO 18
- TO WE* = PINS 15 TO 16
- TO +5VDC = PINS 13 TO 14
- PIN 26 TO GROUND = PINS 10 TO 12
- TO A13 = PINS 9 TO 10
- TO +5VDC = PINS 8 TO 10
- PIN 23 TO +5VDC = PINS 6 TO 4
- TO WE* = PINS 3 TO 4
- TO A11 = PINS 2 TO 4

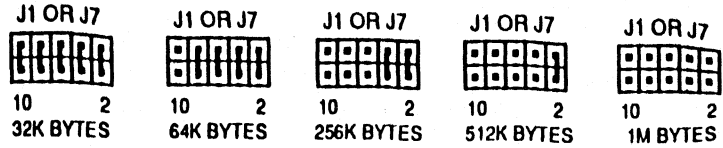
NOTE 2: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 3: THE BASE ADDRESS IS SELECTABLE ON ANY 32K BOUNDARY IN THE COMPLETE 32-BIT ADDRESS MAP. SEE PAGE 2 FOR DETAILS.

09/10/90

**MVME214
32KB TO 1 MB
DUAL PORTED
SRAM
MEMORY VME module
PAGE 1 2**

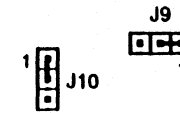
MEMORY SIZE SELECT



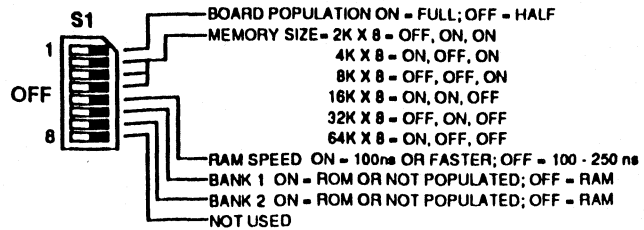
**BATTERY BACKUP
SELECTED**



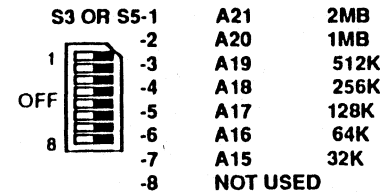
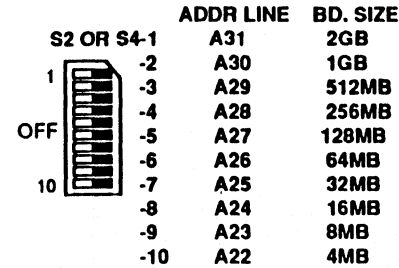
**CYCLE TIME SELECT
OPTIONAL MODE**



MEMORY MODULE CHIP CONFIGURATION



VMEBUS/VSBBUS ADDRESS SELECT SWITCHES

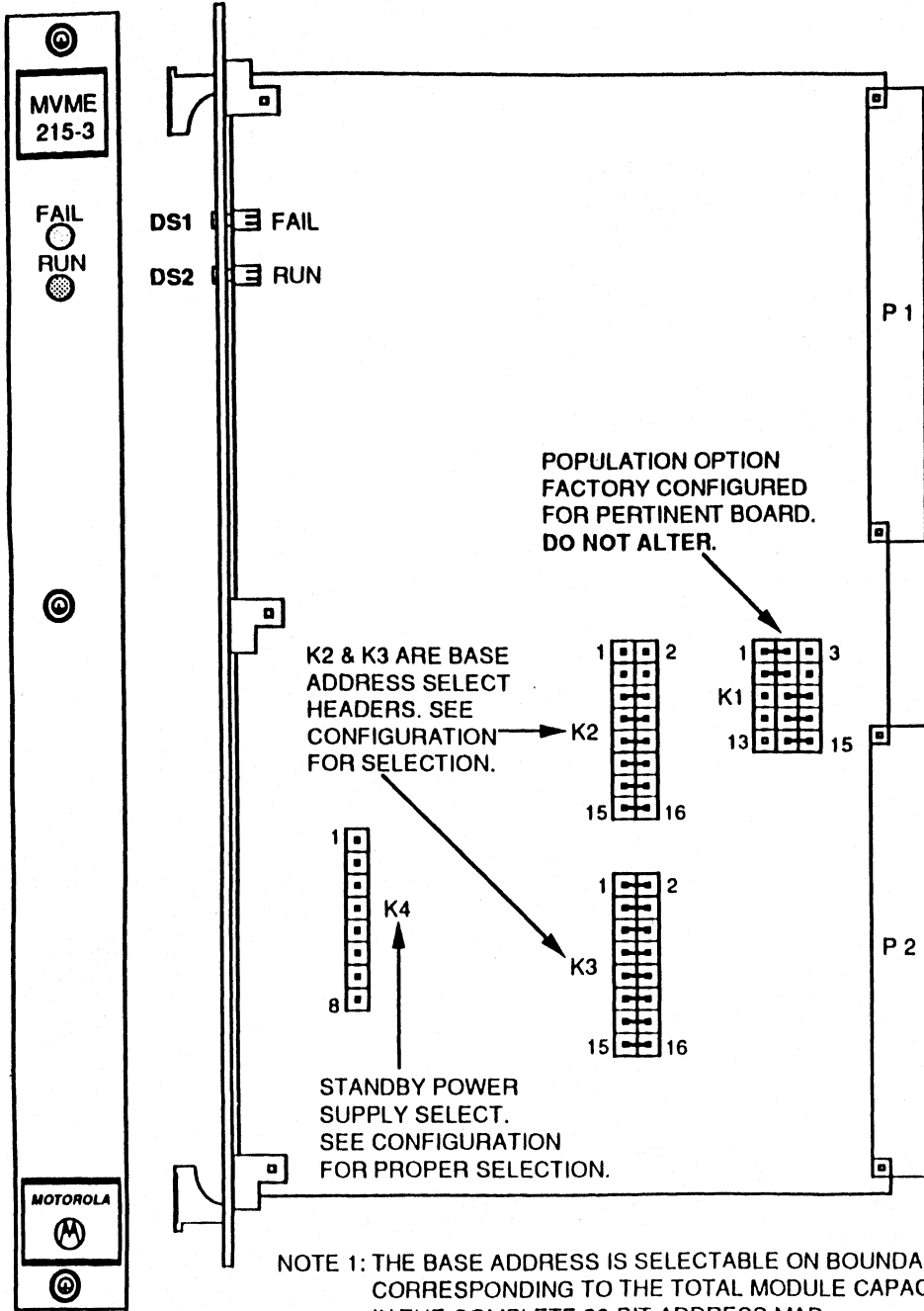


EXAMPLES =	ADDRESS RANGE	MEMORY DEVICES	
		FULL	HALF
1MB -- S3 & S5 - 3 - 7 OFF	0-FFFF	64K	-
512K -- S3 & S5 - 4 - 7 OFF	0-7FFFF	32K	64K
256K -- S3 & S5 - 5 - 7 OFF	0-3FFFF	16K	32K
128K -- S3 & S5 - 6 - 7 OFF	0-1FFFF	8K	16K
64K -- S3 & S5 - 7 OFF	0-FFFF	4K	8K
32K -- S3 & S5 ALL ON	0-7FFF	2K	4K

NOTE 1: A RULE OF THUMB IS TO START WITH ALL SWITCHES IN THE ON POSITION.

NOTE 2: S2 AND S3 ARE VMEBUS (VME A15, A14, ETC.)
S4 AND S5 ARE VMXBUS (VMX A15, A14, ETC.)

09/10/90



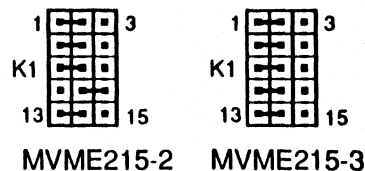
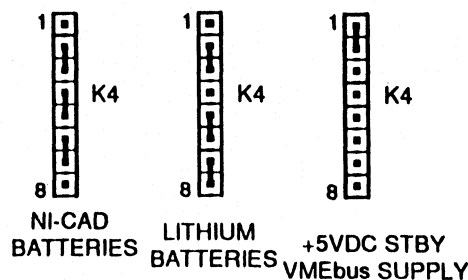
NOTE 1: THE BASE ADDRESS IS SELECTABLE ON BOUNDARIES CORRESPONDING TO THE TOTAL MODULE CAPACITY IN THE COMPLETE 32-BIT ADDRESS MAP.

PART NUMBERS:

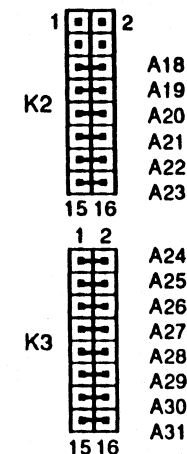
- MVME215-1 01-W3508B01 76435324/96011503
- MVME215-2 01-W3508B02 76435325
- MVME215-3 01-W3508B03 76435326

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

STANDARD POWER SUPPLY SELECT



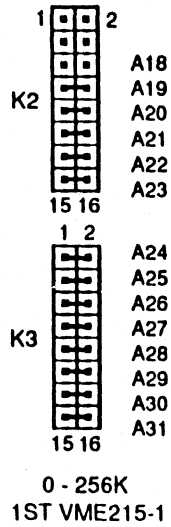
BASE ADDRESS SELECT



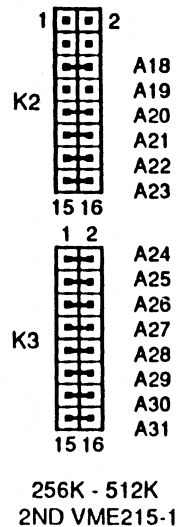
REMOVE JUMPERS FOR TRUE SIGNAL.
(i.e. \$700000, REMOVE A20, A21 A22.)
 VME215-1 256K STANDARD = A18 - A23
 EXTENDED = A18 - A31
 VME215-2 512K STANDARD = A19 - A23
 EXTENDED = A19 - A31
 VME215-3 1MB STANDARD = A20 - A23
 EXTENDED = A20 - A31
 SEE PAGE 2 FOR DETAILS.

11/13/91

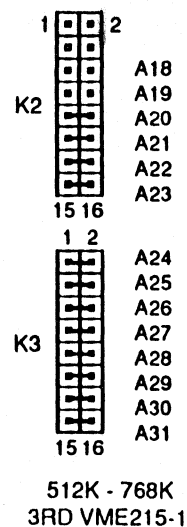
BASE ADDRESS SELECT FOR VME215-1, VME215-2 AND VME215-3



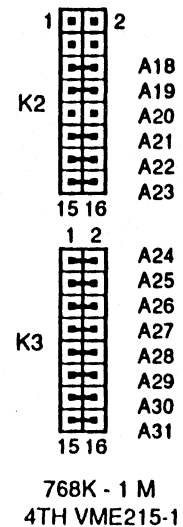
0 - 256K
1ST VME215-1



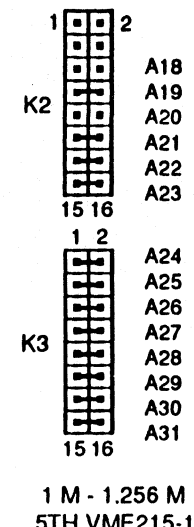
256K - 512K
2ND VME215-1
0 - 512K
1ST VME215-2



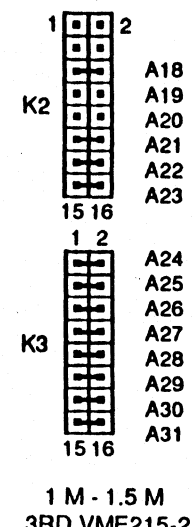
512K - 768K
3RD VME215-1



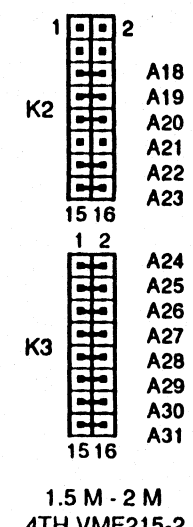
768K - 1 M
4TH VME215-1
512K - 1 M
2ND VME215-2
0 - 1 M
1ST VME215-3



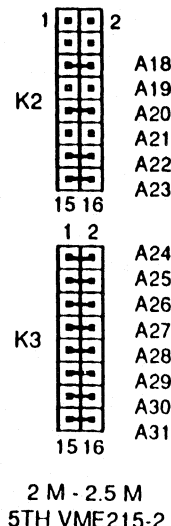
1 M - 1.256 M
5TH VME215-1



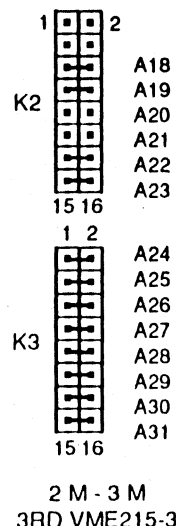
1 M - 1.5 M
3RD VME215-2



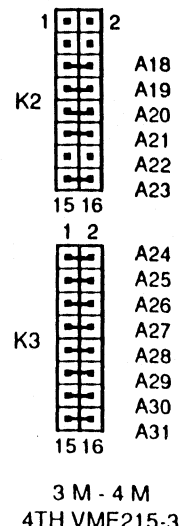
1.5 M - 2 M
4TH VME215-2
1 M - 2 M
2ND VME215-3



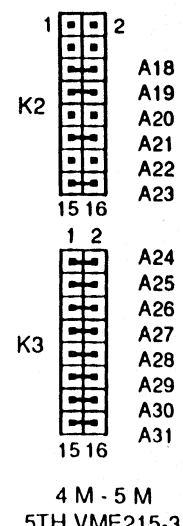
2 M - 2.5 M
5TH VME215-2



2 M - 3 M
3RD VME215-3

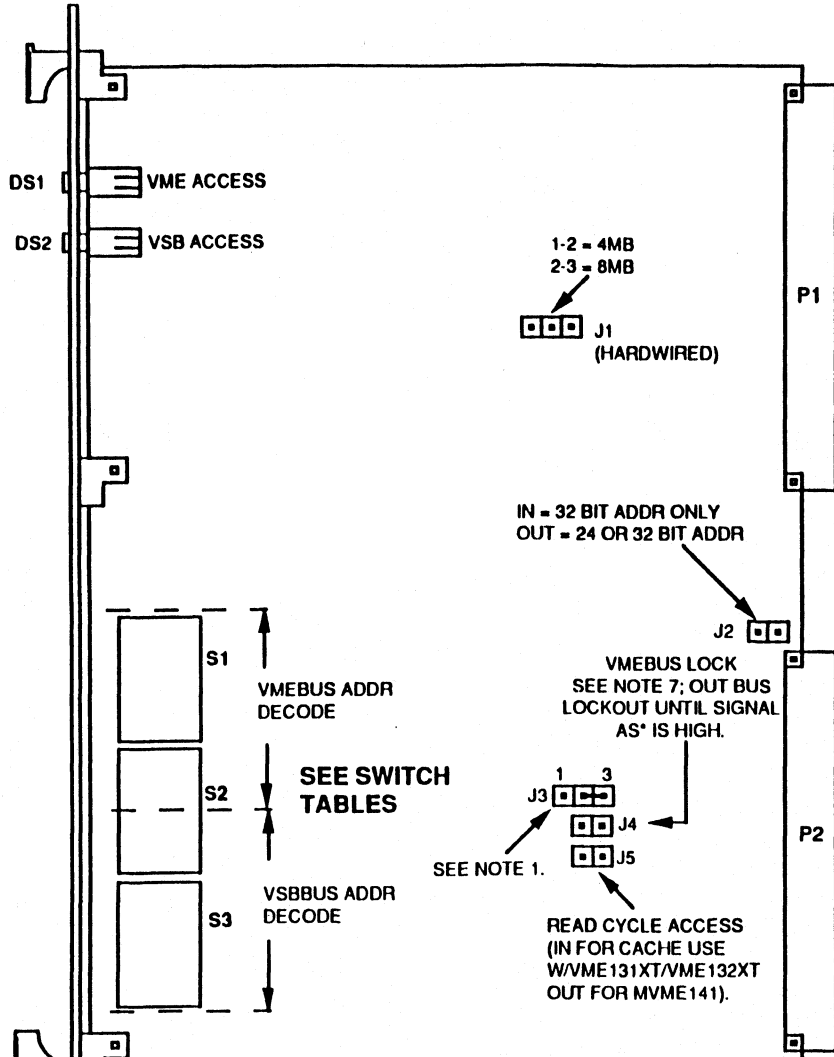
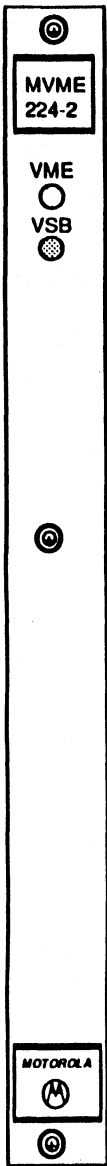


3 M - 4 M
4TH VME215-3



4 M - 5 M
5TH VME215-3

09/12/89



- NOTE 1: J3 IS HARD WIRED PINS 1-2.
- NOTE 2: WHEN USED WITH AN MVME204-2F, THE MVME224-(X) MEMORY BOARDS SHOULD BE CLOSEST (JUST TO THE RIGHT OF) THE PROCESSOR BOARD AND THE MVME204-2F TO THE RIGHT OF THE MVME224-(X) AND THE NEXT CONTIGUOUS MEMORY ADDRESS BLOCK.
- NOTE 3: ACTIVE PART OF SWITCH IS DARKENED AREA.
- NOTE 4: SEE SW1 - SW3 CHARTS FOR PROPER CONFIGURATION FOR SYS3200 SYSTEM.
- NOTE 5: BASE ADDRESS IS SELECTABLE ON 1 MEG BOUNDARY IN COMPLETE 32-BIT ADDRESS MAP.
- NOTE 6: THIS CONFIGURATION ALSO USED IN SYS3604/08's, AND SYS3640's.
- NOTE 7: ON DUAL PORTED BOARDS, PREVENTS ALTERNATE SOURCE FROM GAINING ACCESS OF MEMORY RESOURCE DURING AN INDIVISIBLE BLOCK CYCLE IS THE SIGNAL PIN.

PART NUMBER:

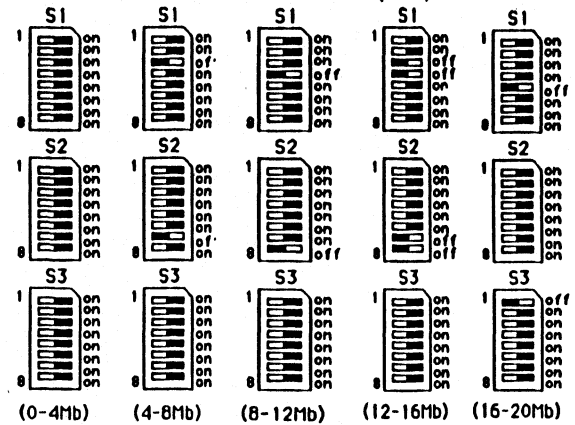
- MVME224-1 01-W3500B01 76435407
- 01-W3500B04 76435614
- MVME224-2 01-W3500B02 76435406
- 01-W3500B03 76435613

* B03 AND B04 BOARDS WERE LIMITED PRODUCTION FOR VME DELTA SYSTEMS ONLY. B01 AND B02 BOARDS ARE FULLY QUALIFIED REPLACEMENTS FOR B03 AND B04.

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

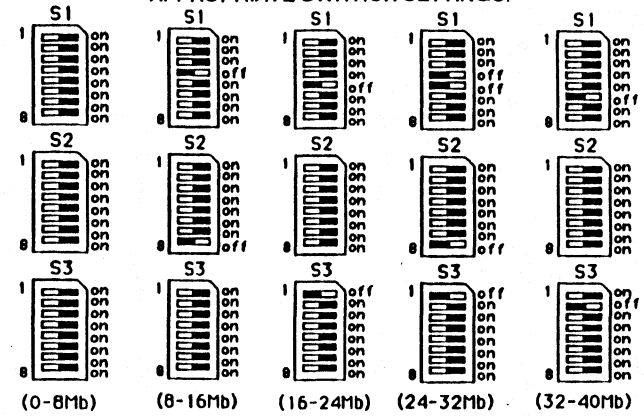
MVME224-1 CSR SWITCHES (MAX 5 ALLOWED)

NOTE: MAXIMUM OF 5 BOARDS ALLOWED BECAUSE OF LENGTH RESTRICTION OF VMX (VSB) CABLE.



MVME224-2 CSR SWITCHES (MAX 5 ALLOWED)

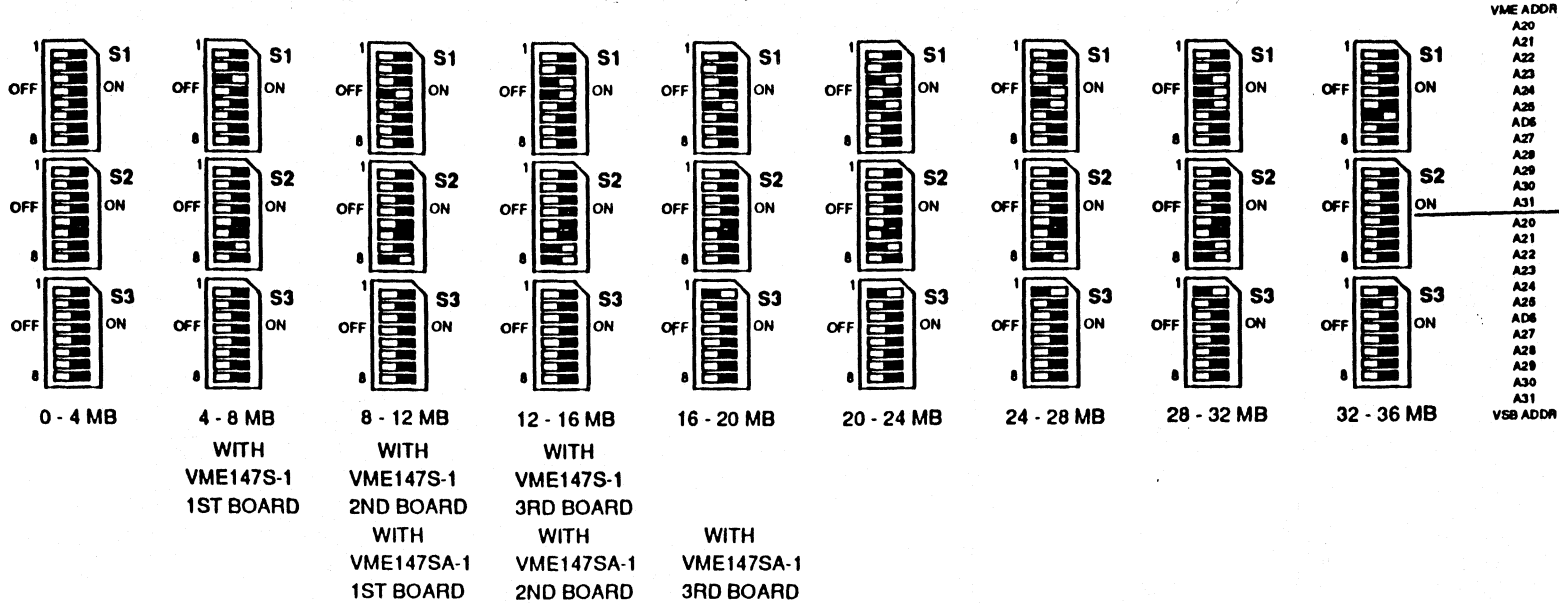
NOTE 7: FOR SYS3400, SEE VME147S-1, VME147SA-1, APPROPRIATE SWITCH SETTINGS.



**MVME224-1/2
4, 8 MB DUAL
PORTED DRAM
MEMORY MOD.
PA 1 OF 3**

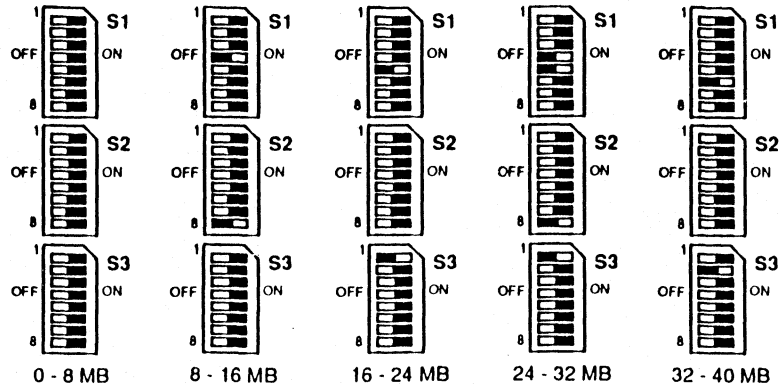
03/13/91

TABLE 2-1. MVME224-1 (4MB) USED WITH MVME131, MVME132, MVME141, MVME147RF AND MVME147S-1(X)-1) SERIES.

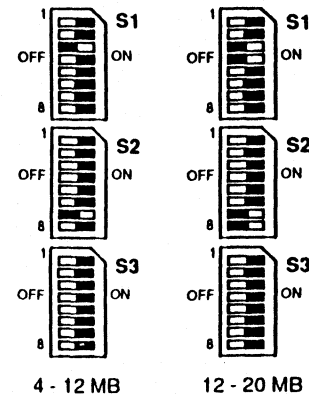


NOTE: REFER TO VME13(X) SERIES SWITCH S4-3 FOR CACHE ENABLE OFF INFORMATION.

TABLE 2-2. MVME224-2 (8MB) USED WITH MVME131, MVME132 AND MVME141.



MVME224-2 (8MB) USED WITH MVME147RF AND MVME147S-1



11/13/91

TABLE 2-3. MVME224-1 (4MB) USED WITH MVME134, MVME147, MVME147RF, MVME147S-1 AND MVME147SA-1.

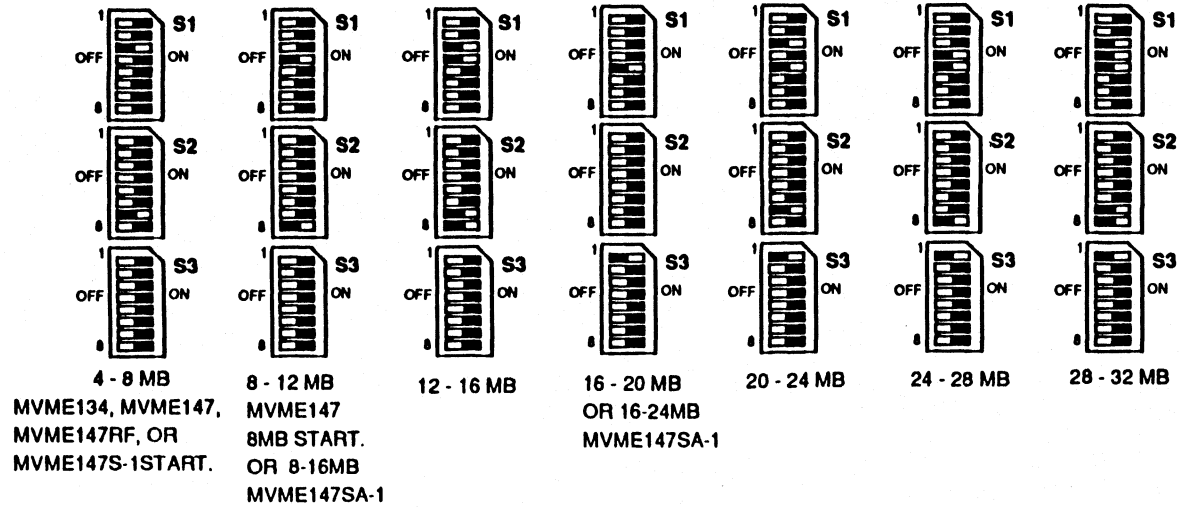
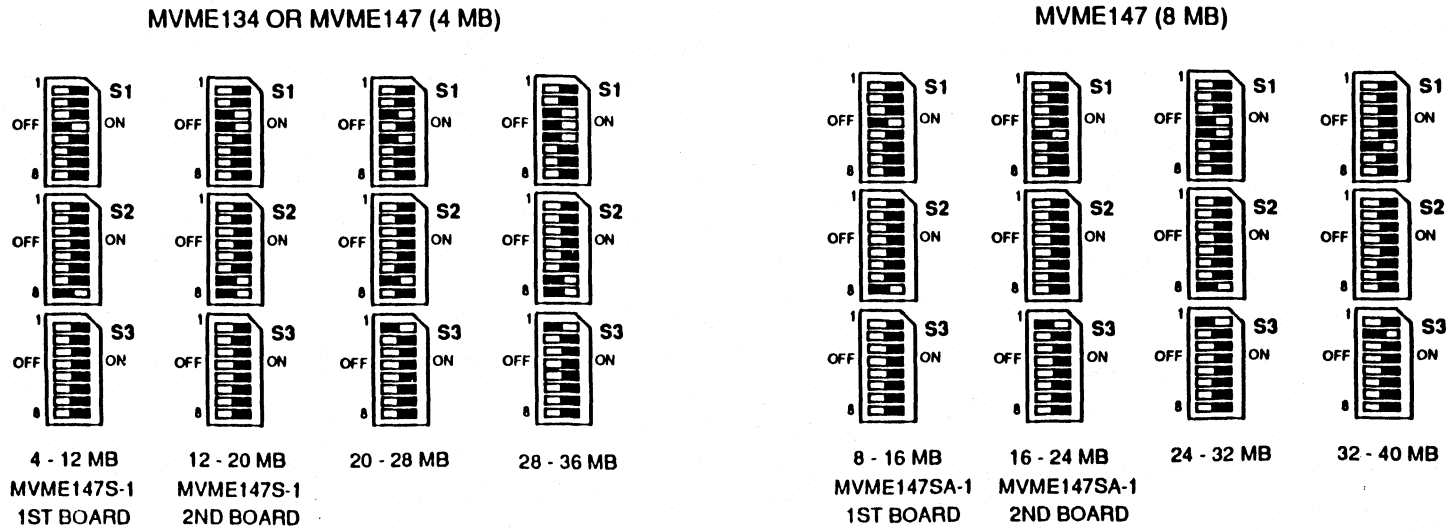
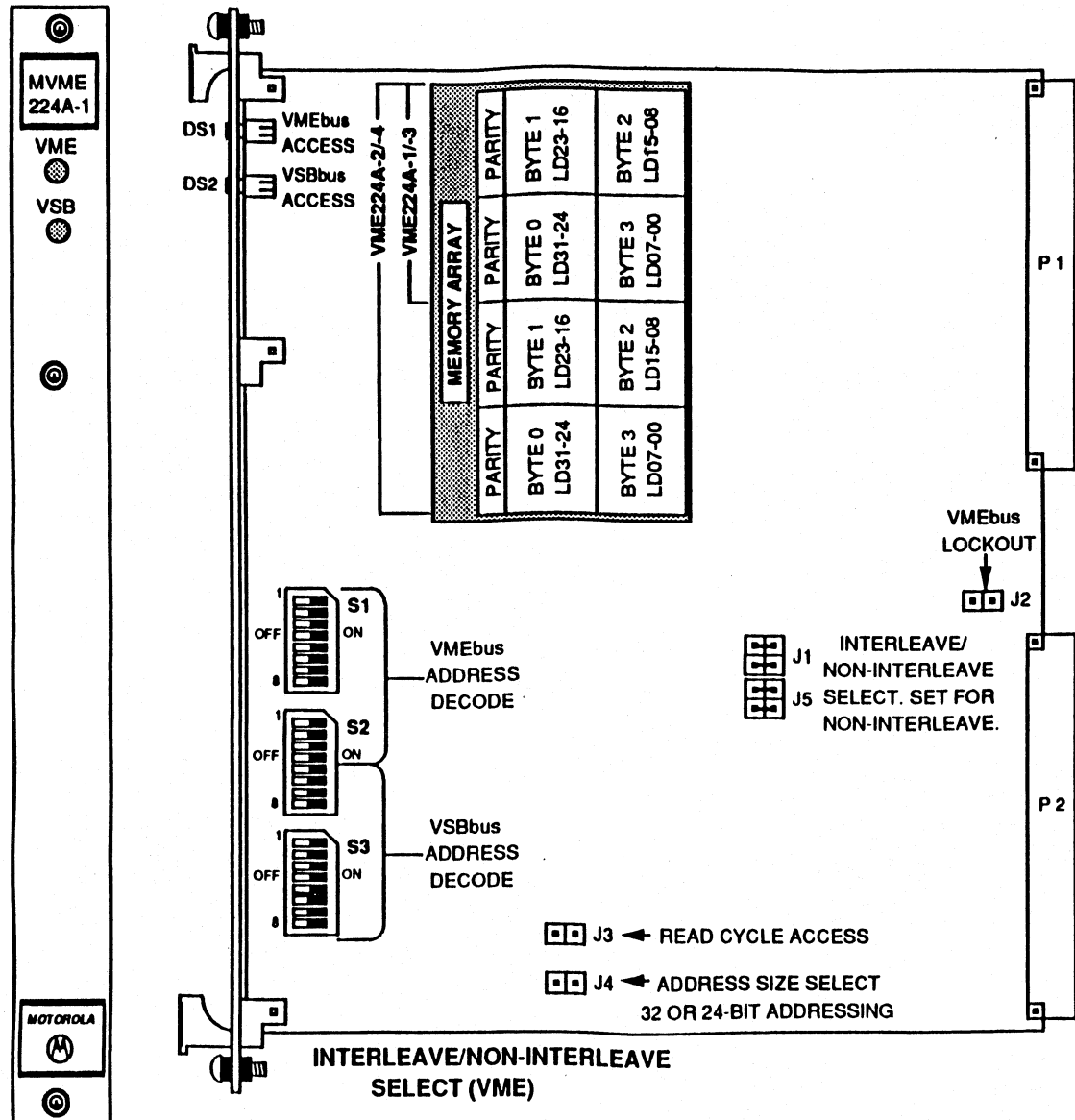


TABLE 2-4. MVME224-2 (8MB) USED WITH MVME134, MVME147 AND MVME147SA-1.



02/23/90



PART NUMBERS:

- MVME224A-1 4 MB DRAM 01-W3588B01 96011245
- MVME224A-2 8 MB DRAM 01-W3588B02 96011285
- MVME224A-3 16 MB DRAM 01-W3588B03 96011284
- MVME224A-4 32 MB DRAM 01-W3588B04 96011283

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION HISTORY.

- NOTE 1 : ACTIVE PART OF THE SWITCH IS DARKENED AREA.
- NOTE 2 : BASE ADDRESS IS SELECTABLE ON 1 MEG BOUNDRIES IN COMPLETE 32-BIT ADDRESS MAP.
- NOTE 3 : USED AT ADDRESS \$0800000 FOR SYS3708.

**VMEbus LOCKOUT
ENABLED**
 J2

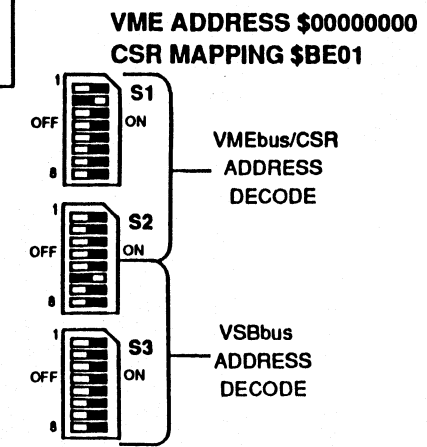
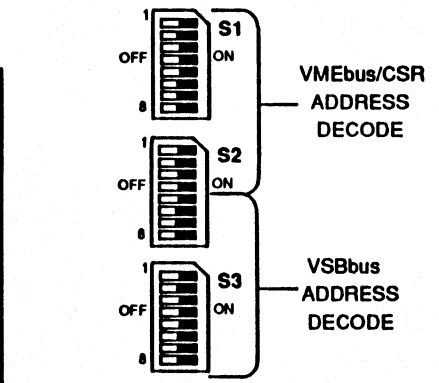
ADDRESS SIZE SELECT
 J4
 24-BIT ADDRESSING
 OFF

**INTERLEAVE/
NON-INTERLEAVE
SELECT (VME)**
 J1
 J5
 INTERLEAVE/
NON-INTERLEAVE
SELECT. SET FOR
INTERLEAVE.

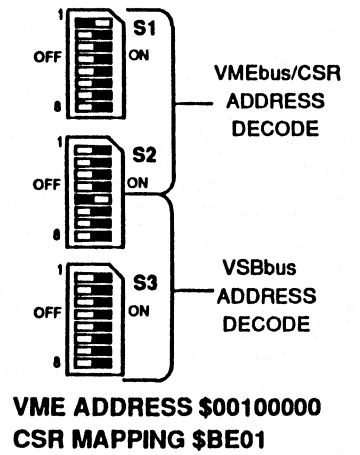
READ CYCLE ACCESS
 J3
 ASACK0* OR ASACK1*
DECODE FOR LESS
THAN 50 ns WAIT*
SIGNAL DURING READ.

**INTERLEAVE/
NON-INTERLEAVE
SELECT. SET FOR
NON-INTERLEAVE.**
 J1
 J5

J3 ← READ CYCLE ACCESS
 J4 ← ADDRESS SIZE SELECT
 32 OR 24-BIT ADDRESSING



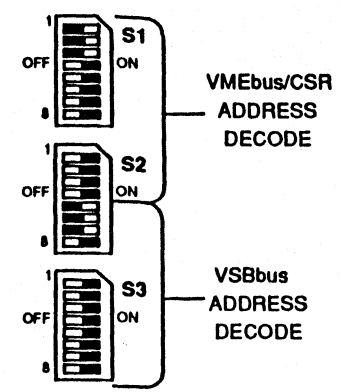
**VME ADDRESS \$00200000
CSR MA I G \$BE05**



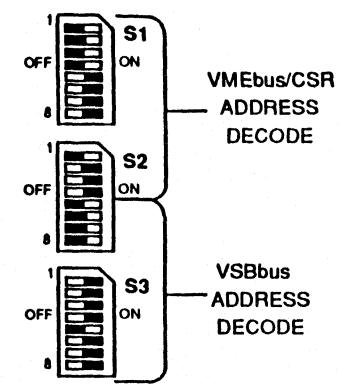
**MVME224A-1/-2/-3/-4
4, 8, 16, 32 MB DRAM
MEMORY VMEmodule
PAGE 1 3**

04/16/91

VME				VME								VME OR VSB BEGINNING ADDRESS (HEX)	VME CSR SHORT I/O (1 BYTE)	
S2-4	S2-3	S2-2	S2-1	S1-8	S1-7	S1-6	S1-5	S1-4	S1-3	S1-2	S1-1			
A31	A30	A29	A28	A27	A26	A25	A24	A23	A22	A21	A20			
0	0	0	0	0	0	0	0	0	0	0	0	0	00000000	BE01
0	0	0	0	0	0	0	0	0	0	0	0	1	00100000	BE01
0	0	0	0	0	0	0	0	0	0	0	1	0	00200000	BE05
0	0	0	0	0	0	0	0	0	0	0	1	1	00300000	BE05
0	0	0	0	0	0	0	0	0	0	1	0	0	00400000	BE09
0	0	0	0	0	0	0	0	0	0	1	0	1	00500000	BE09
0	0	0	0	0	0	0	0	0	0	1	1	0	00600000	BE0D
0	0	0	0	0	0	0	0	0	0	1	1	1	00700000	BE0D
0	0	0	0	0	0	0	0	0	1	0	0	0	00800000	BE11
0	0	0	0	0	0	0	0	0	1	0	0	1	00900000	BE11
0	0	0	0	0	0	0	0	0	1	0	1	0	00A00000	BE15
0	0	0	0	0	0	0	0	0	1	0	1	1	00B00000	BE15
0	0	0	0	0	0	0	0	0	1	1	0	0	00C00000	BE19
0	0	0	0	0	0	0	0	0	1	1	0	1	00D00000	BE19
0	0	0	0	0	0	0	0	0	1	1	1	0	00E00000	BE1D
0	0	0	0	0	0	0	0	0	1	1	1	1	00F00000	BE1D
0	0	0	0	0	0	0	0	1	0	0	0	0	01000000	BE21
0	0	0	0	0	0	1	0	0	0	0	0	0	02000000	BE41
0	0	0	0	0	1	0	0	0	0	0	0	0	04000000	BE81
0	0	0	0	1	0	0	0	0	0	0	0	0	08000000	BE01
0	0	0	1	0	0	0	0	0	0	0	0	0	10000000	BE01
0	0	1	0	0	0	0	0	0	0	0	0	0	20000000	BE01
0	1	0	0	0	0	0	0	0	0	0	0	0	40000000	BE01
1	0	0	0	0	0	0	0	0	0	0	0	0	80000000	BE01
AD31	AD30	AD29	AD28	AD27	AD26	AD25	AD24	AD23	AD22	AD21	AD20			
S3-8	S3-7	S3-6	S3-5	S3-4	S3-3	S3-2	S3-1	S2-8	S2-7	S2-6	S2-5			
2GB	1GB	512M	256M	128M	64M	32M	16M	8M	4M	2M	1M			
VSB				VSB								VME OR VSB BEGINNING ADDRESS (HEX)	VME CSR ADDRESS SHORT I/O (1 BYTE)	



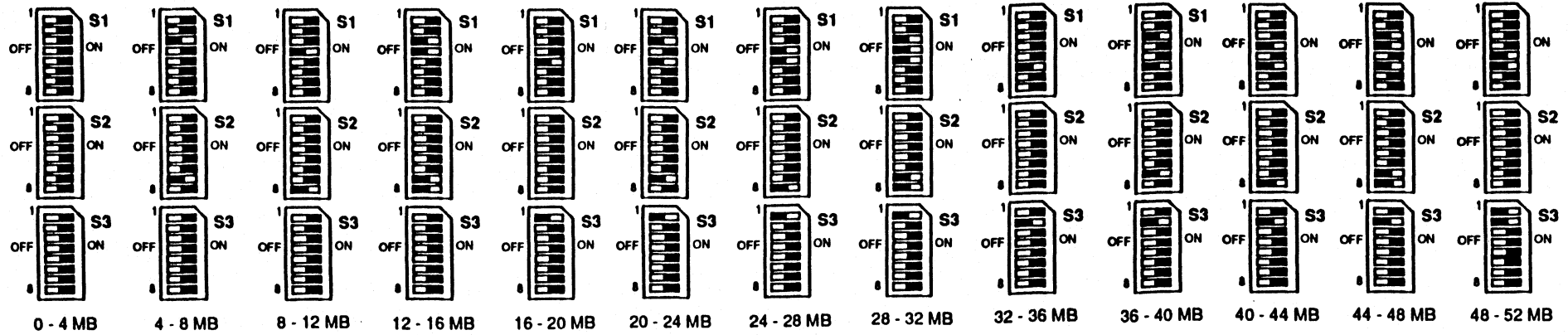
VME ADDRESS \$00700000
CSR MAPPING \$BE0D



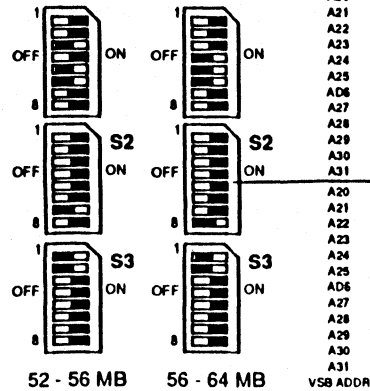
VME ADDRESS \$10F00000
CSR MAPPING \$BE1D

09/10/90

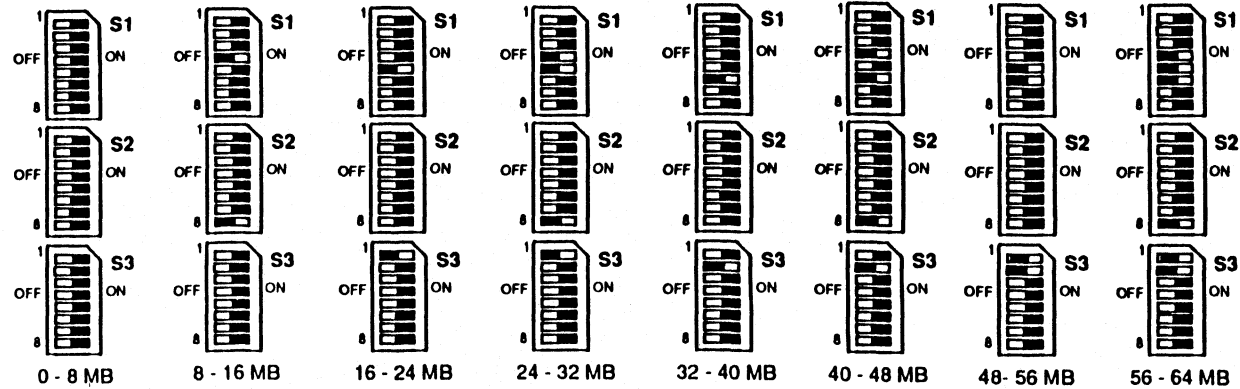
MVME224A-1 (4MB) ADDRESS SWITCH SETTINGS.



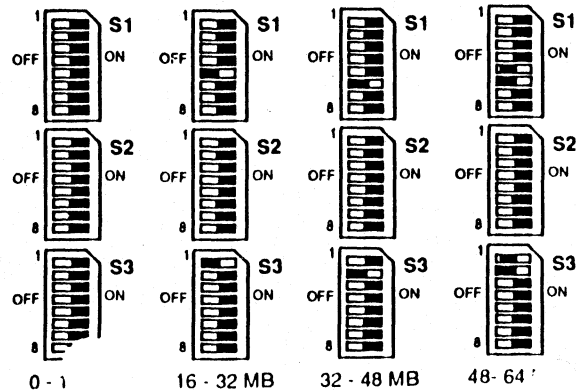
MVME224A-1 (CONT.)



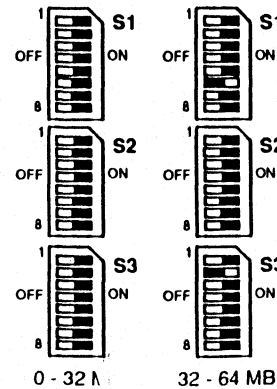
MVME224A-2 (8MB) ADDRESS SWITCH SETTINGS.

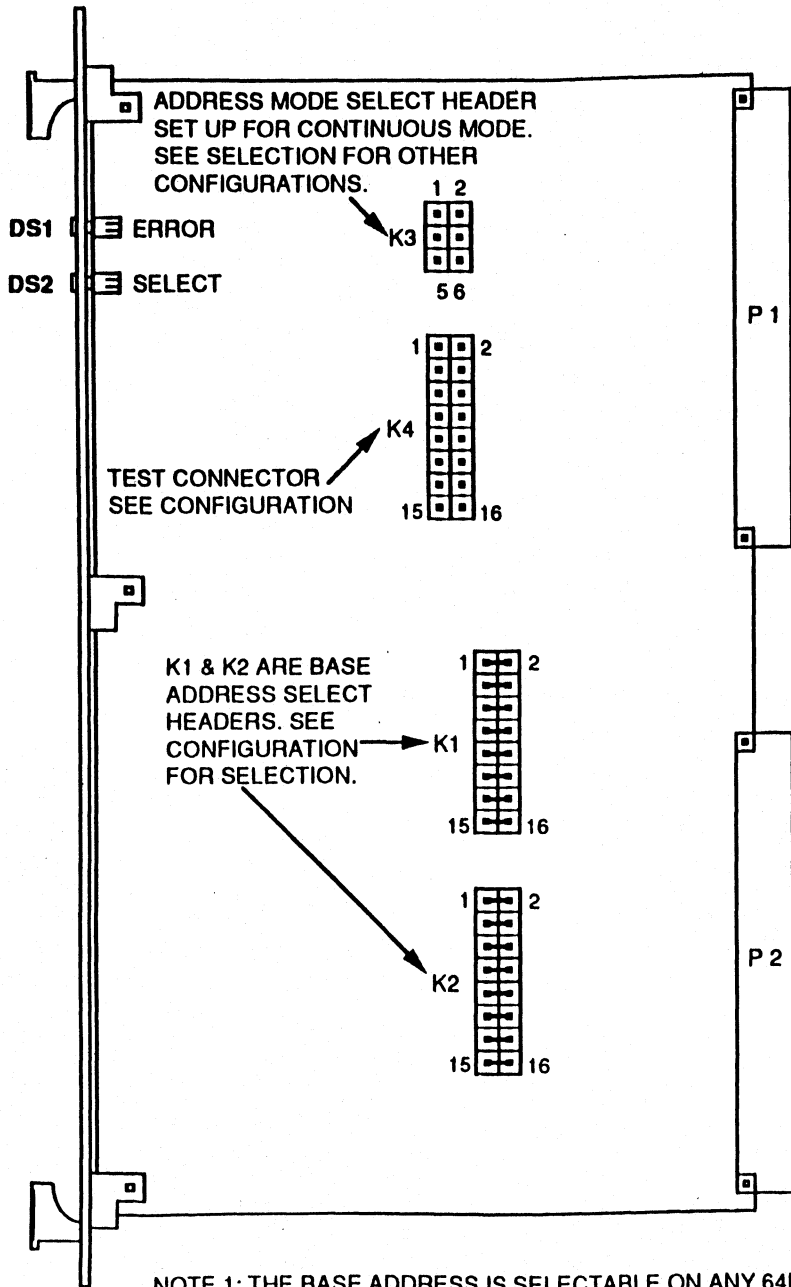
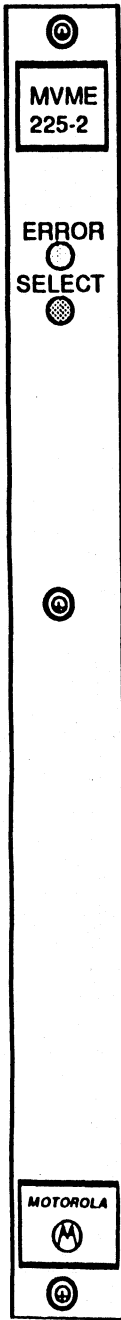


MVME224A-3 (16MB) ADDRESS SWITCH SETTINGS.



MVME224A-4 (32MB) ADDRESS SWITCH SETTINGS.





NOTE 1: THE BASE ADDRESS IS SELECTABLE ON ANY 64K BOUNDARY IN THE COMPLETE "STANDARD" OR "EXTENDED" ADDRESS MAP.

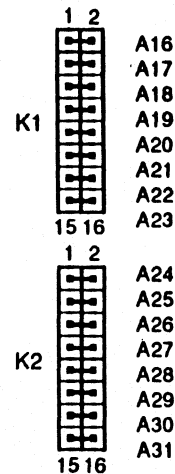
PART NUMBERS:

MVME225-1 01-W3509B01 76435370

MVME225-2 01-W3509B02 76435371

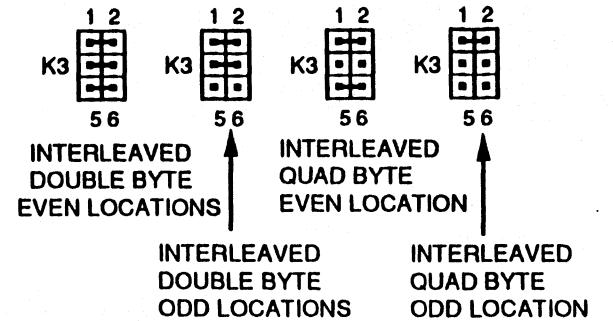
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

BASE ADDRESS SELECT

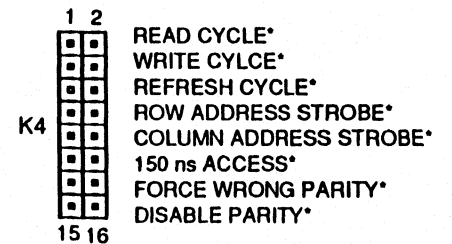


REMOVE JUMPERS FOR TRUE SIGNAL.
(i.e. \$700000, REMOVE A20, A21 A22.)
STANDARD = A16 - A23
EXTENDED = A16 - A31
SEE PAGE 2 FOR DETAILS.

ADDRESS MODE SELECT HEADER

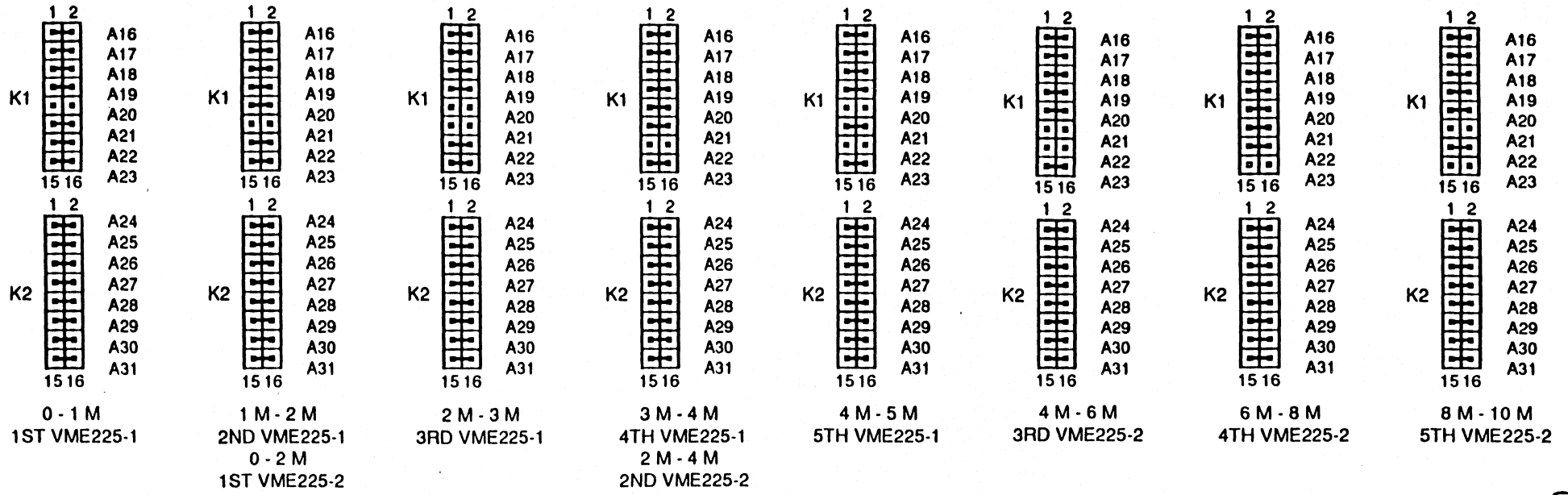


TEST CONNECTOR

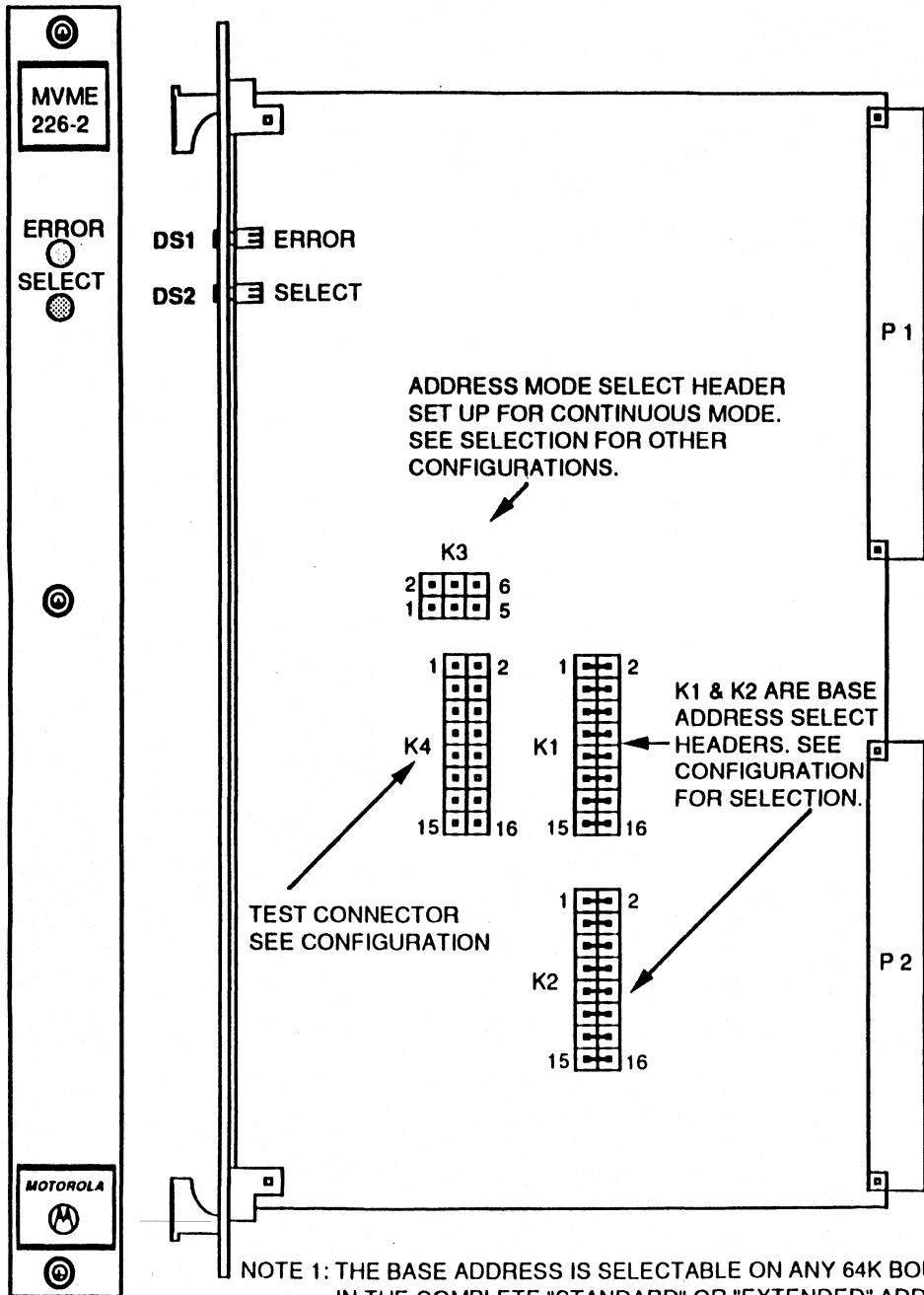


09/12/89

BASE ADDRESS SELECTFOR VME225-1 AND VNE225-2



09/12/89



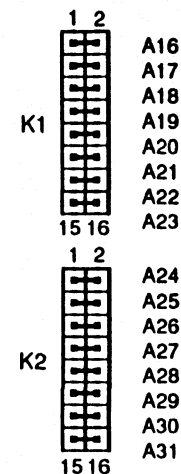
NOTE 1: THE BASE ADDRESS IS SELECTABLE ON ANY 64K BOUNDARY IN THE COMPLETE "STANDARD" OR "EXTENDED" ADDRESS MAP.

PART NUMBERS:

- MVME226-1 01-G3045M01 76435388 EURO BUILD
- MVME226-1 01-W3510B01 96011127 US BUILD
- MVME226-2 01-G3045M02 76435387 EURO BUILD
- MVME226-2 01-W3510B02 96015699 US BUILD

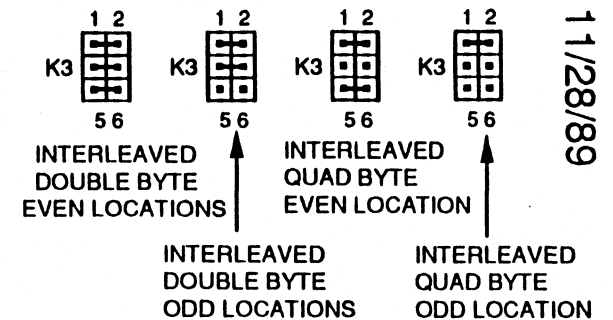
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

BASE ADDRESS SELECT



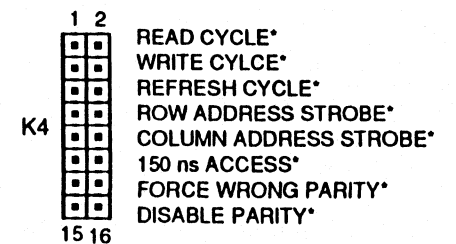
REMOVE JUMPERS FOR TRUE SIGNAL.
(i.e. \$700000, REMOVE A20, A21 A22.)
STANDARD = A16 - A23
EXTENDED = A16 - A31
SEE PAGE 2 FOR DETAILS.

ADDRESS MODE SELECT HEADER

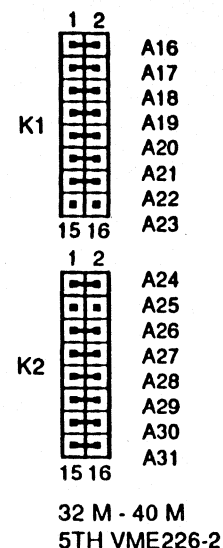
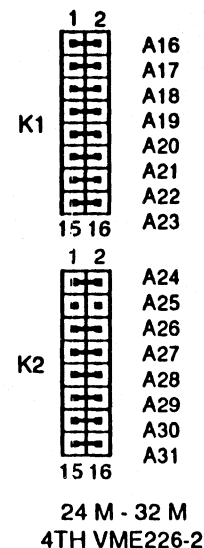
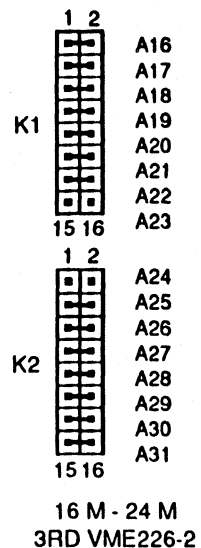
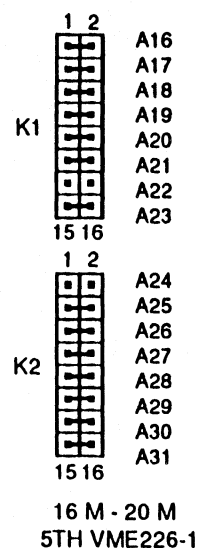
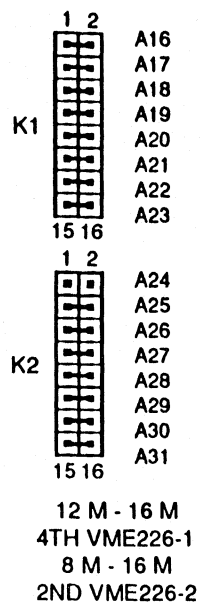
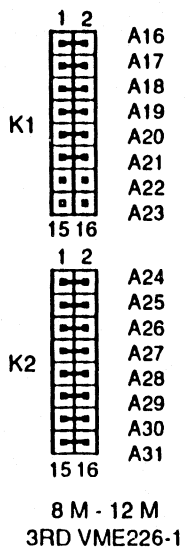
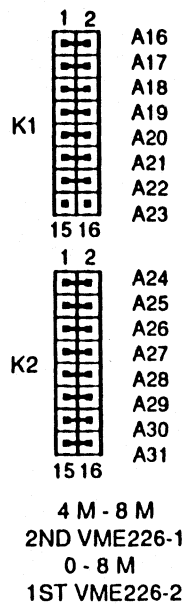
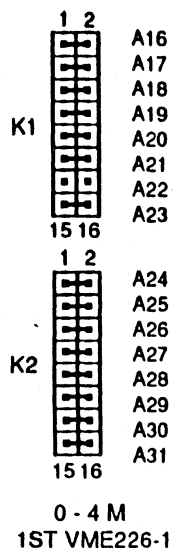


1/1/28/89

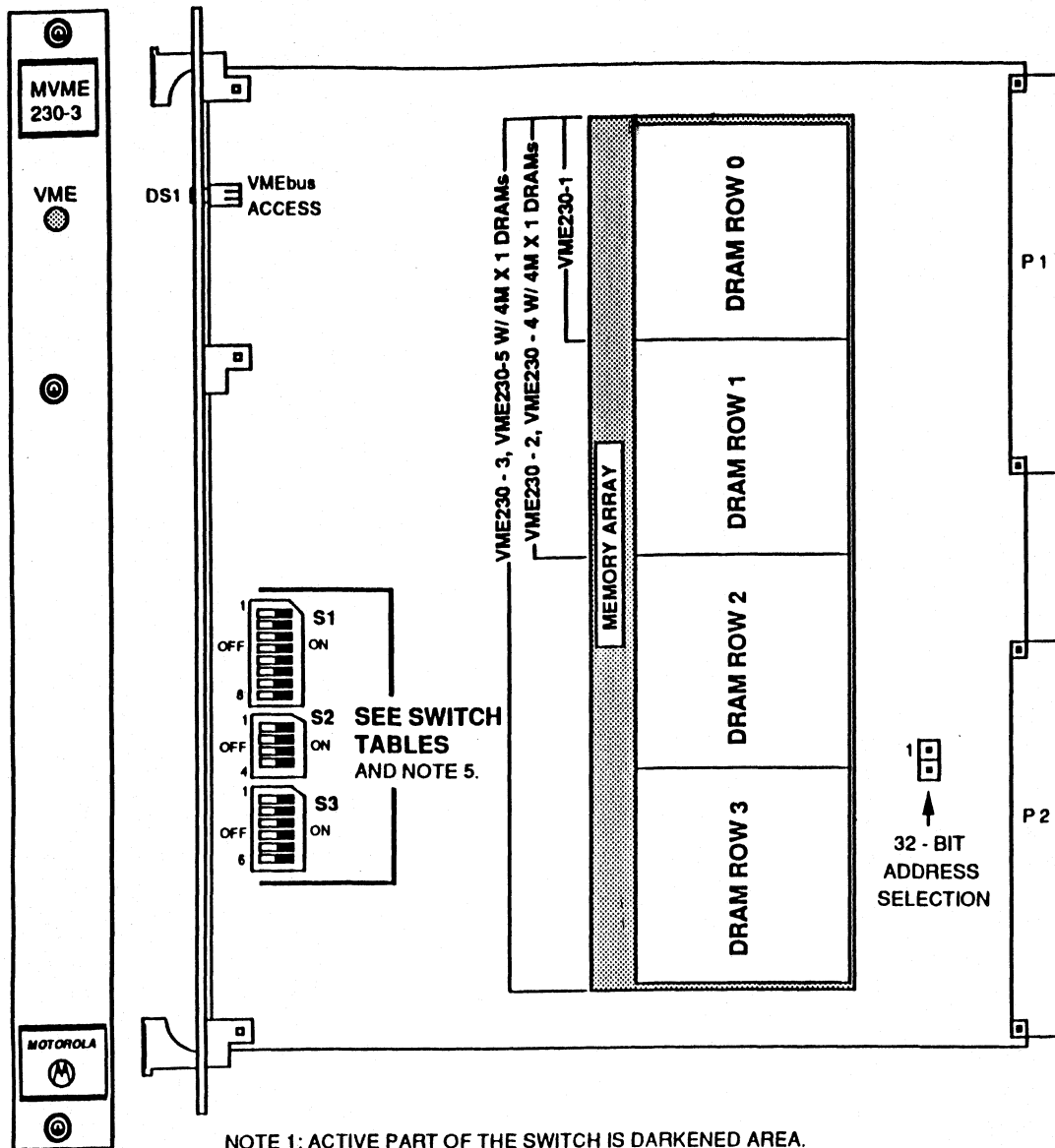
TEST CONNECTOR



BASE ADDRESS SELECT FOR VME226-1 AND VME226-2



09/12/89



NOTE 1: ACTIVE PART OF THE SWITCH IS DARKENED AREA.

NOTE 2: J1, NO JUMPER INSTALLED IS 24 OR 32-BIT ADDRESSING. WITH J1 PIN 1 TO 2 INSTALLED, THE MASTER WILL COMMUNICATE WITH A 24-BIT ADDRESS BOARD WITHOUT INTERFERENCE BY OR WITH THE VME230 BOARD.

NOTE 5: S1 AND S2 ARE VMEbus ADDRESS DECODE SET FOR ADDRESS \$00000000. S3 IS THE CONTROL STATUS REGISTER (CSR) ADDRESS MAPPING SWITCH SET FOR \$BF00. THE BASE ADDRESS IS SELECTABLE ON ANY 1 MEG BOUNDARY IN THE COMPLETE 32-BIT ADDRESS MAP.

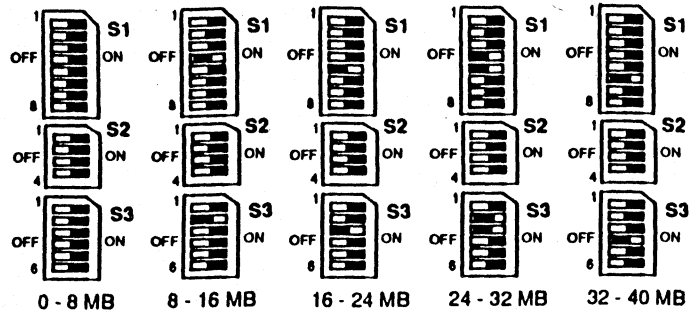
PART NUMBERS:

MVME230-1	01-W3501B01	96011046
MVME230-2	01-W3501B02	96011045
MVME230-3	01-W3501B03	96011354
MVME230-4	01-W3501B04	TBD
MVME230-5	01-W3501B05	TBD

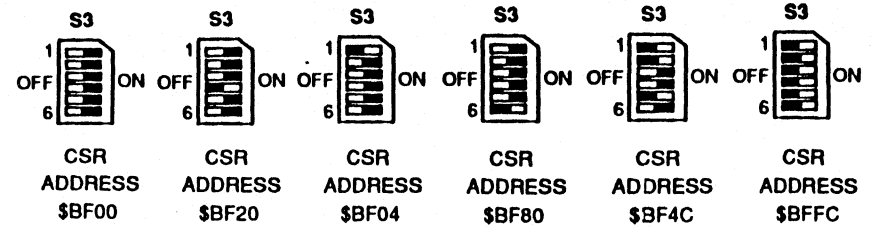
SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION HISTORY.

11/13/91

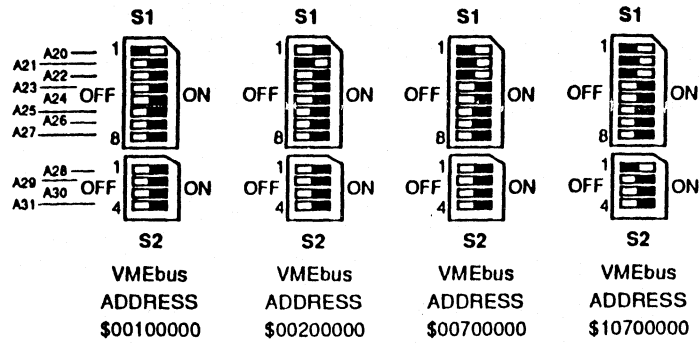
MVME230-1/-2/-3/-4/-5
4, 8, 16, 32, 64 MB DRAM
MEMORY VMFmodule
PAGE 1 (



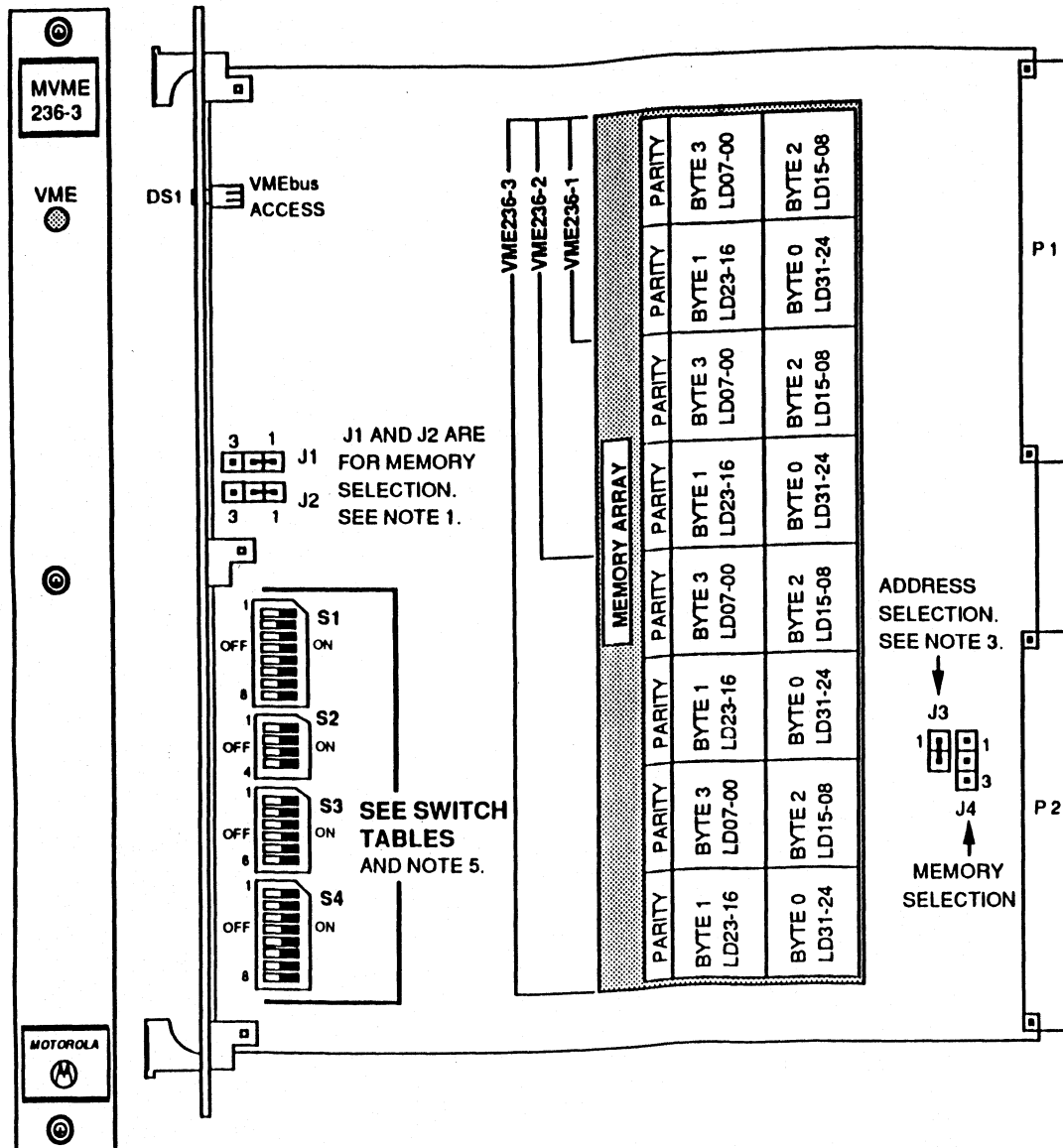
CONTROL STATUS REGISTER (CSR) ADDRESS DECODE



VMEbus ADDRESS DECODE



11/13/91



PART NUMBERS:

MVME236-1 01-W3561B01 TBD

MVME236-2 01-W3561B02 96011178

MVME236-3 01-W3561B03 96011173

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION HISTORY.

NOTE 1: J1 AND J2 ARE FACTORY HARD WIRE SET JUMPERS AND ARE BETWEEN PINS 1 AND 2 ON BOTH.

NOTE 2: ACTIVE PART OF THE SWITCH IS DARKENED AREA.

NOTE 3: J3, NO JUMER INSTALLED IS 24 OR 32-BIT ADDRESSING. WITH J2 PIN 1 TO 2 INSTALLED, 24-BIT ADDRESS MODIFIERS WILL NOT BE DECODED AND THE MODULE WILL NOT RESPOND TO 24-BIT ADDRESSES.

NOTE 4: J4 IS A FACTORY JUMPER AND HAS NO JUMPERS.

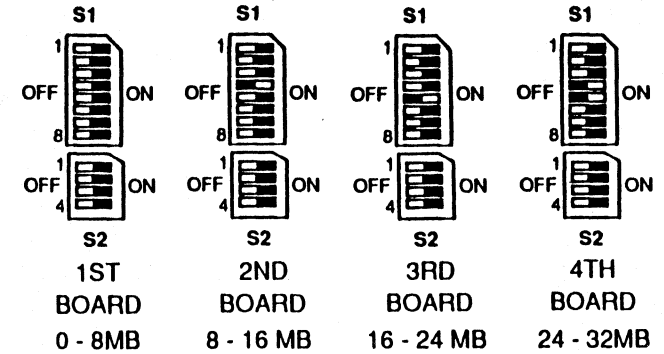
NOTE 5: S1 AND S2 ARE VMEbus ADDRESS DECODE SET FOR ADDRESS \$00000000. S3 AND S4 ARE CONTROL STATUS REGISTER (CSR) ADDRESS MAPPING SWITCHES SET FOR \$0001. THE BASE ADDRESS IS SELECTABLE ON ANY 1 MEG BOUNDARY IN THE COMPLETE 32-BIT ADDRESS MAP.

NOTE 6: THIS CONFIGURATION IS ALSO USED IN SYS8400, 8608's.

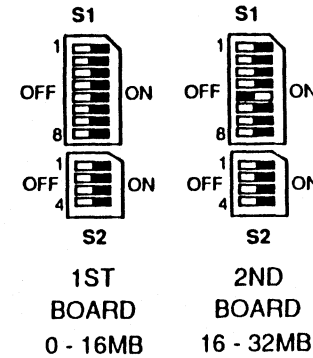
03/14/91

VME ADDRESS MAPPING SWITCHES												RAM ADDRESS (HEXIDECIMAL)
S2-4	S2-3	S2-2	S2-1	S1-8	S1-7	S1-6	S1-5	S1-4	S1-3	S1-2	S1-1	
A31	A30	A29	A28	A27	A26	A25	A24	A23	A22	A21	A20	
0	0	0	0	0	0	0	0	0	0	0	0	00000000
0	0	0	0	0	0	0	0	0	0	0	1	00100000
0	0	0	0	0	0	0	0	0	0	1	0	00200000
0	0	0	0	0	0	0	0	0	0	1	1	00300000
0	0	0	0	0	0	0	0	0	1	0	0	00400000
0	0	0	0	0	0	0	0	0	1	0	1	00500000
0	0	0	0	0	0	0	0	0	1	1	0	00600000
0	0	0	0	0	0	0	0	0	1	1	1	00700000
0	0	0	0	0	0	0	0	1	0	0	0	00800000
0	0	0	0	0	0	0	0	1	0	0	1	00900000
0	0	0	0	0	0	0	0	1	0	1	0	00A00000
0	0	0	0	0	0	0	0	1	0	1	1	00B00000
0	0	0	0	0	0	0	0	1	1	0	0	00C00000
0	0	0	0	0	0	0	0	1	1	0	1	00D00000
0	0	0	0	0	0	0	0	1	1	1	0	00E00000
0	0	0	0	0	0	0	0	1	1	1	1	00F00000
0	0	0	0	0	0	0	1	0	0	0	0	01000000
0	0	0	0	0	0	1	0	0	0	0	0	02000000
0	0	0	0	0	1	0	0	0	0	0	0	04000000
0	0	0	0	1	0	0	0	0	0	0	0	08000000
0	0	0	1	0	0	0	0	0	0	0	0	10000000
0	0	1	0	0	0	0	0	0	0	0	0	20000000
0	1	0	0	0	0	0	0	0	0	0	0	40000000
1	0	0	0	0	0	0	0	0	0	0	0	80000000
2GB	1GB	512M	256M	128M	64M	32M	16M	8M	4M	2M	1M	RAM ADDRESS (HEX)

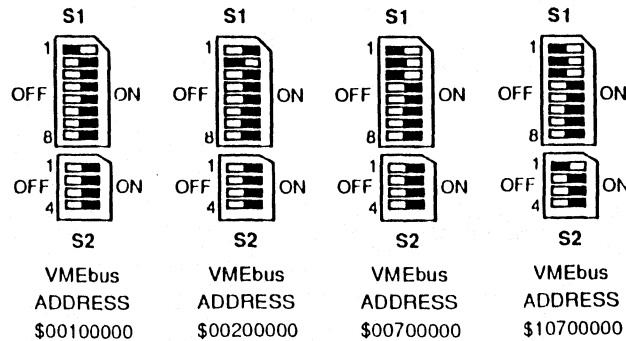
SWITCH SETTINGS FOR VME236-2



SWITCH SETTINGS FOR VME236-3



VMEbus ADDRESS DECODE EXAMPLES

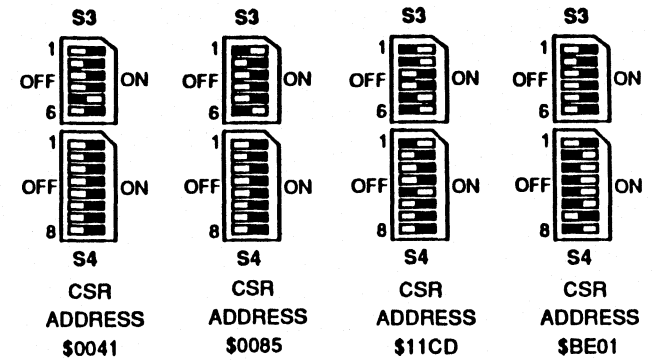


MVME236-1/-2/-3
4, 8, 16 MB DRAM
MEMORY VMEmodule
PAGE 2 OF 3

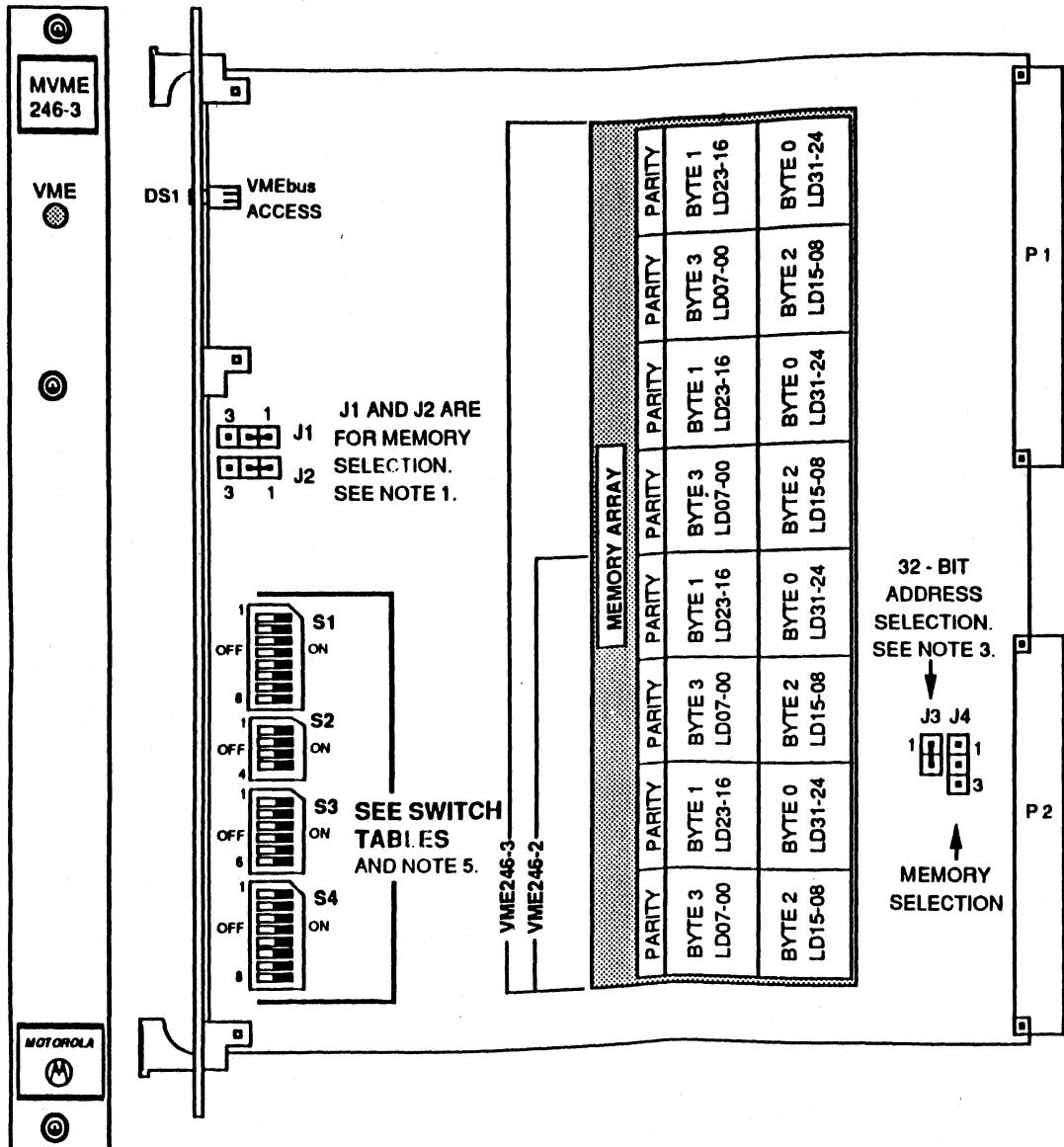
04/01/91

CSR ADDRESS MAPPING SWITCHES													CSR ADDRESS (HEXIDECIMAL)	
S4-8	S4-7	S4-6	S4-5	S4-4	S4-3	S4-2	S4-1	S3-6	S3-5	S3-4	S3-3	S3-2		S3-1
0	0	0	0	0	0	0	0	0	0	0	0	0	0	\$ 0 0 0 1
0	0	0	0	0	0	0	0	0	0	0	0	0	1	\$ 0 0 0 5
0	0	0	0	0	0	0	0	0	0	0	0	1	0	\$ 0 0 0 9
0	0	0	0	0	0	0	0	0	0	0	0	1	1	\$ 0 0 0 D
0	0	0	0	0	0	0	0	0	0	0	1	0	0	\$ 0 0 1 1
0	0	0	0	0	0	0	0	0	0	0	1	0	1	\$ 0 0 1 5
0	0	0	0	0	0	0	0	0	0	0	1	1	0	\$ 0 0 1 9
0	0	0	0	0	0	0	0	0	0	0	1	1	1	\$ 0 0 1 D
0	0	0	0	0	0	0	0	0	0	1	0	0	0	\$ 0 0 2 1
0	0	0	0	0	0	0	0	0	0	1	0	0	1	\$ 0 0 2 5
0	0	0	0	0	0	0	0	0	0	1	0	1	0	\$ 0 0 2 9
0	0	0	0	0	0	0	0	0	0	1	0	1	1	\$ 0 0 2 D
0	0	0	0	0	0	0	0	0	0	1	1	0	0	\$ 0 0 3 1
0	0	0	0	0	0	0	0	0	0	1	1	0	1	\$ 0 0 3 5
0	0	0	0	0	0	0	0	0	0	1	1	1	0	\$ 0 0 3 9
0	0	0	0	0	0	0	0	0	0	1	1	1	1	\$ 0 0 3 D
0	0	0	0	0	0	0	0	0	1	0	0	0	0	\$ 0 0 4 1
0	0	0	0	0	0	0	0	1	0	0	0	0	0	\$ 0 0 8 1
0	0	0	0	0	0	0	1	0	0	0	0	0	0	\$ 0 1 0 1
0	0	0	0	0	0	1	0	0	0	0	0	0	0	\$ 0 2 0 1
0	0	0	0	0	1	0	0	0	0	0	0	0	0	\$ 0 4 0 1
0	0	0	0	1	0	0	0	0	0	0	0	0	0	\$ 0 8 0 1
0	0	0	1	0	0	0	0	0	0	0	0	0	0	\$ 1 0 0 1
0	0	1	0	0	0	0	0	0	0	0	0	0	0	\$ 2 0 0 1
0	1	0	0	0	0	0	0	0	0	0	0	0	0	\$ 4 0 0 1
1	0	0	0	0	0	0	0	0	0	0	0	0	0	\$ 8 0 0 1

CONTROL STATUS REGISTER (CSR) ADDRESS DECODE
(EXAMPLE ONLY. SEE CHART TO THE LEFT.)



04/05/91



PART NUMBERS:

MVME246-2 01-W3561B05 96012106

MVME246-3 01-W3561B06 96012105

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION HISTORY.

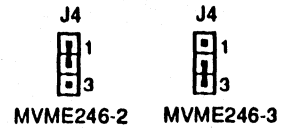
NOTE 1: J1 AND J2 ARE FACTORY SET JUMPERS AND ARE BETWEEN PINS 1 AND 2 ON BOTH.

NOTE 2: ACTIVE PART OF THE SWITCH IS DARKENED AREA.

NOTE 3: J3, NO JUMER INSTALLED IS 24 OR 32-BIT ADDRESSING. WITH J2 PIN 1 TO 2 INSTALLED, 24-BIT ADDRESS MODIFIERS WILL NOT BE DECODED AND THE MODULE WILL NOT RESPOND TO 24-BIT ADDRESSES.

NOTE 4: J4 IS A FACTORY JUMPER AND HAS NO JUMPERS.

NOTE 5: S1 AND S2 ARE VMEbus ADDRESS DECODE SET FOR ADDRESS \$00000000. S3 AND S4 ARE CONTROL STATUS REGISTER (CSR) ADDRESS MAPPING SWITCHES SET FOR \$0001. THE BASE ADDRESS IS SELECTABLE ON ANY 1 MEG BOUNDARY IN THE COMPLETE 32-BIT ADDRESS MAP.

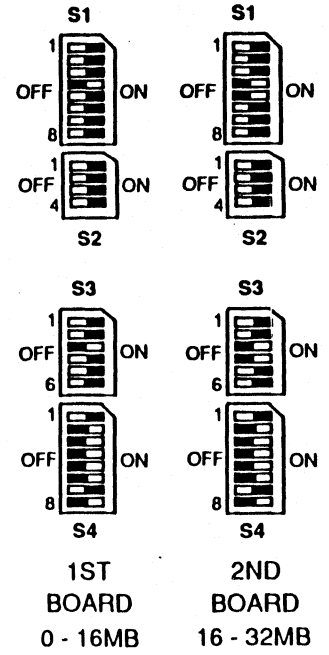


11/13/91

VME ADDRESS MAPPING SWITCHES												RAM ADDRESS (HEXIDECIMAL)
S2-4	S2-3	S2-2	S2-1	S1-8	S1-7	S1-6	S1-5	S1-4	S1-3	S1-2	S1-1	
A31	A30	A29	A28	A27	A26	A25	A24	A23	A22	A21	A20	
0	0	0	0	0	0	0	0	0	0	0	0	00000000
0	0	0	0	0	0	0	0	0	0	0	1	00100000
0	0	0	0	0	0	0	0	0	0	1	0	00200000
0	0	0	0	0	0	0	0	0	0	1	1	00300000
0	0	0	0	0	0	0	0	0	1	0	0	00400000
0	0	0	0	0	0	0	0	0	1	0	1	00500000
0	0	0	0	0	0	0	0	0	1	1	0	00600000
0	0	0	0	0	0	0	0	0	1	1	1	00700000
0	0	0	0	0	0	0	0	1	0	0	0	00800000
0	0	0	0	0	0	0	0	1	0	0	1	00900000
0	0	0	0	0	0	0	0	1	0	1	0	00A00000
0	0	0	0	0	0	0	0	1	0	1	1	00B00000
0	0	0	0	0	0	0	0	1	1	0	0	00C00000
0	0	0	0	0	0	0	0	1	1	0	1	00D00000
0	0	0	0	0	0	0	0	1	1	1	0	00E00000
0	0	0	0	0	0	0	0	1	1	1	1	00F00000
0	0	0	0	0	0	0	1	0	0	0	0	01000000
0	0	0	0	0	0	1	0	0	0	0	0	02000000
0	0	0	0	0	1	0	0	0	0	0	0	04000000
0	0	0	0	1	0	0	0	0	0	0	0	08000000
0	0	0	1	0	0	0	0	0	0	0	0	10000000
0	0	1	0	0	0	0	0	0	0	0	0	20000000
0	1	0	0	0	0	0	0	0	0	0	0	40000000
1	0	0	0	0	0	0	0	0	0	0	0	80000000
2GB	1GB	512M	256M	128M	64M	32M	16M	8M	4M	2M	1M	RAM ADDRESS (HEX)

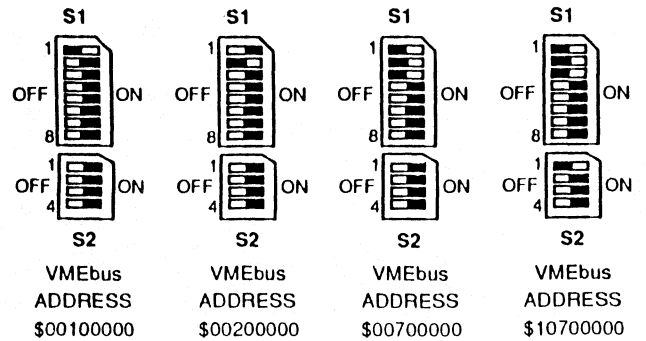
SWITCH SETTINGS : 0 = CLOSED (ON); 1 = OPEN (OFF)

SWITCH SETTINGS FOR VME246
(EXAMPLE ONLY SEE CHART TO THE LEFT)



11/13/91

VMEbus ADDRESS DECODE

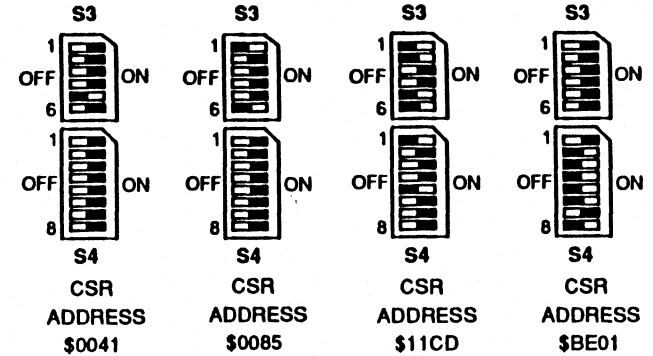


MVME246-2/-3
32, 64 MB DRAM
MEMORY VMEmodule
PAGE 2 OF 3

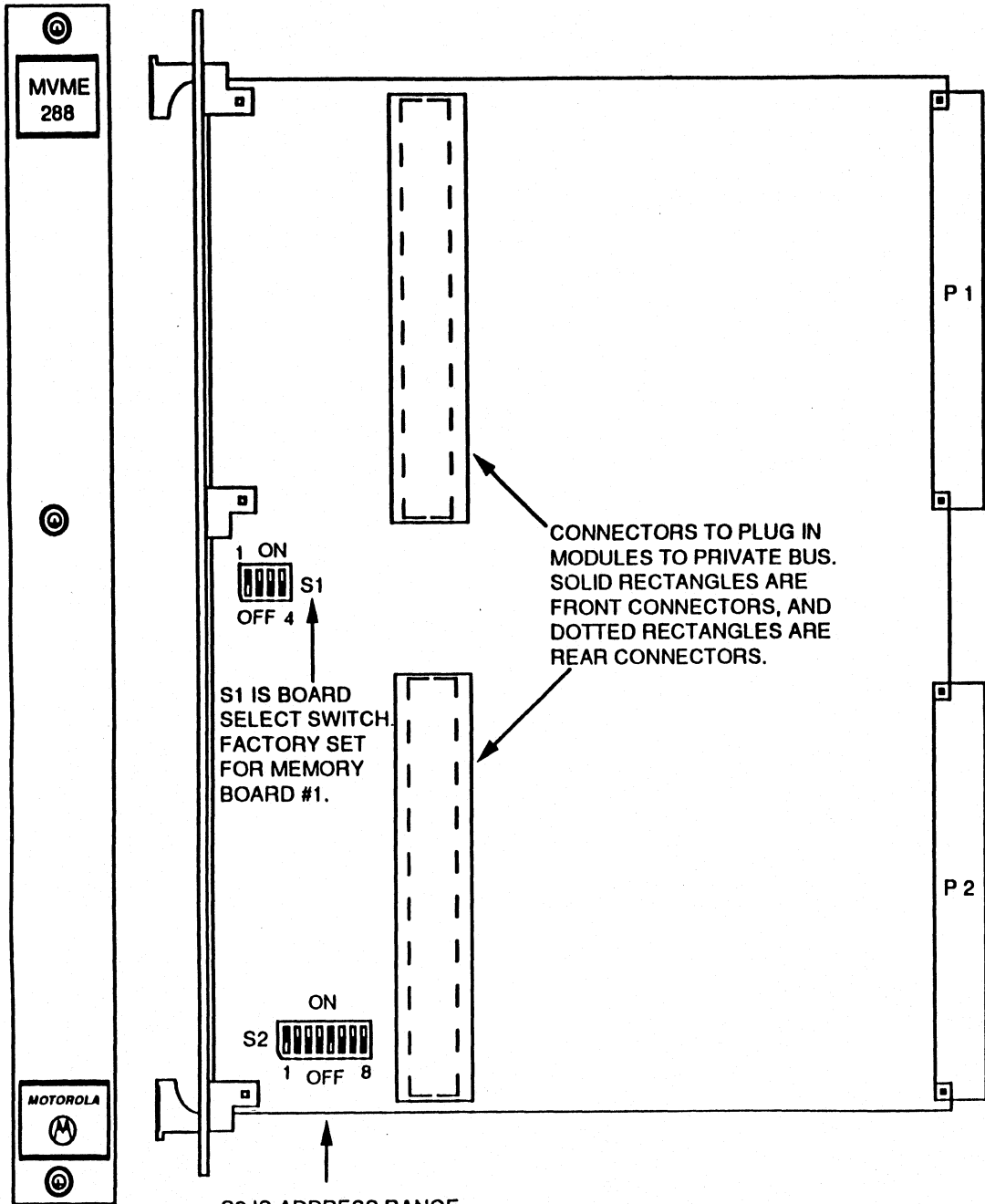
CSR ADDRESS MAPPING SWITCHES													CSR ADDRESS (HEXIDECIMAL)	
S4-8	S4-7	S4-6	S4-5	S4-4	S4-3	S4-2	S4-1	S3-6	S3-5	S3-4	S3-3	S3-2		S3-1
0	0	0	0	0	0	0	0	0	0	0	0	0	0	\$ 0 0 0 1
0	0	0	0	0	0	0	0	0	0	0	0	0	1	\$ 0 0 0 5
0	0	0	0	0	0	0	0	0	0	0	0	1	0	\$ 0 0 0 9
0	0	0	0	0	0	0	0	0	0	0	0	1	1	\$ 0 0 0 D
0	0	0	0	0	0	0	0	0	0	0	1	0	0	\$ 0 0 1 1
0	0	0	0	0	0	0	0	0	0	0	1	1	0	\$ 0 0 1 5
0	0	0	0	0	0	0	0	0	0	0	1	1	1	\$ 0 0 1 9
0	0	0	0	0	0	0	0	0	0	0	1	1	1	\$ 0 0 1 D
0	0	0	0	0	0	0	0	0	0	1	0	0	0	\$ 0 0 2 1
0	0	0	0	0	0	0	0	0	0	1	0	0	1	\$ 0 0 2 5
0	0	0	0	0	0	0	0	0	0	1	0	1	0	\$ 0 0 2 9
0	0	0	0	0	0	0	0	0	0	1	0	1	1	\$ 0 0 2 D
0	0	0	0	0	0	0	0	0	0	1	1	0	0	\$ 0 0 3 1
0	0	0	0	0	0	0	0	0	0	1	1	0	1	\$ 0 0 3 5
0	0	0	0	0	0	0	0	0	0	1	1	1	0	\$ 0 0 3 9
0	0	0	0	0	0	0	0	0	0	1	1	1	1	\$ 0 0 3 D
0	0	0	0	0	0	0	0	0	1	0	0	0	0	\$ 0 0 4 1
0	0	0	0	0	0	0	0	1	0	0	0	0	0	\$ 0 0 8 1
0	0	0	0	0	0	0	1	0	0	0	0	0	0	\$ 0 1 0 1
0	0	0	0	0	0	1	0	0	0	0	0	0	0	\$ 0 2 0 1
0	0	0	0	0	1	0	0	0	0	0	0	0	0	\$ 0 4 0 1
0	0	0	0	1	0	0	0	0	0	0	0	0	0	\$ 0 8 0 1
0	0	0	1	0	0	0	0	0	0	0	0	0	0	\$ 1 0 0 1
0	0	1	0	0	0	0	0	0	0	0	0	0	0	\$ 2 0 0 1
0	1	0	0	0	0	0	0	0	0	0	0	0	0	\$ 4 0 0 1
1	0	0	0	0	0	0	0	0	0	0	0	0	0	\$ 8 0 0 1

SWITCH SETTINGS : 0 = CLOSED (ON); 1 = OPEN (OFF). CSR ADDRESSES ARE IN SHORT I/O ADDRESS SPACE.

CONTROL STATUS REGISTER (CSR) ADDRESS DECODE
(EXAMPLE ONLY SEE CHART TO THE LEFT.)



11/13/91



CONNECTORS TO PLUG IN
MODULES TO PRIVATE BUS.
SOLID RECTANGLES ARE
FRONT CONNECTORS, AND
DOTTED RECTANGLES ARE
REAR CONNECTORS.

S1 IS BOARD
SELECT SWITCH.
FACTORY SET
FOR MEMORY
BOARD #1.

S2 IS ADDRESS RANGE
SELECT SWITCH. FACTORY
SET FOR \$00000000-00FFFFFF.

PART NUMBERS:

IN KIT # 01-W2306C01, 02, 03, 04 & 06

MVME288DF 01-W3541B01 96011189

MVME288SF 01-W3560B01 96011199
(SURFACE MOUNT)

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

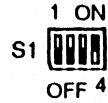
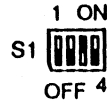
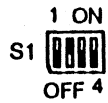
NOTE 1: SWITCHES S1/S2 =
OPEN = OFF, CLOSED = ON.

NOTE 2: ACTIVE PART OF SWITCH IS
DARKENED AREA.

05/13/91

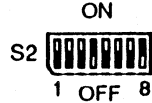
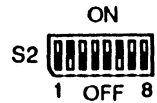
**MVME288DF
16 MB DRAM
W/ PRIVATE BUS
PAGE 1 OF 2**

BOARD SELECT SWITCH



2ND MEMORY BOARD 3RD MEMORY BOARD 4TH MEMORY BOARD

ADDRESS RANGE SELECT SWITCH



SET FOR 01000000-01FFFFFF
(FIRST ADDITIONAL BOARD).

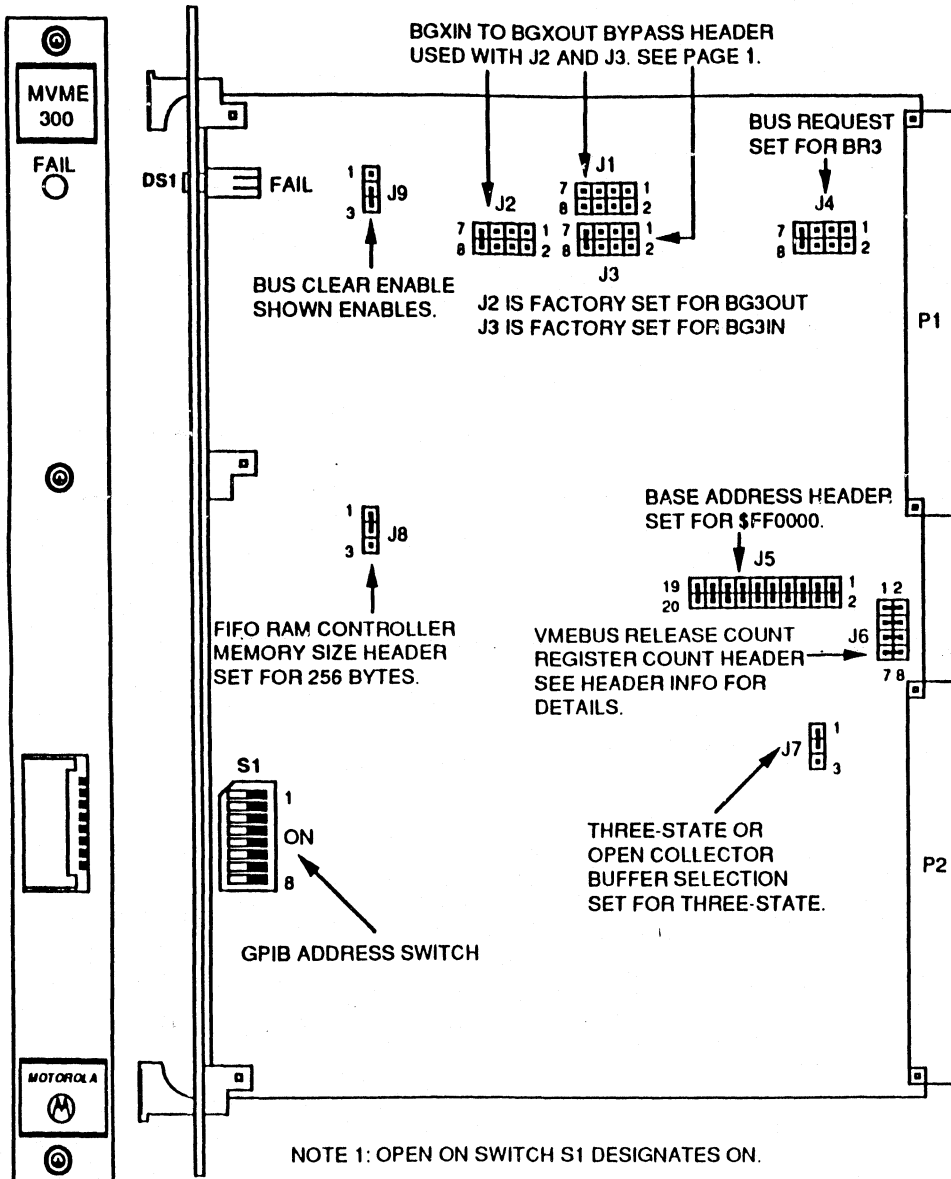
SET FOR 02000000-02FFFFFF
(SECOND ADDITIONAL BOARD).

SET FOR 03000000-03FFFFFF
(THIRD ADDITIONAL BOARD).

05/13/91

BOARD #	BOARD TYPE	S1 SELECT				S2 REFRESH/GRANT/BUSY								BEGINNING ADDRESS (HEX)
		S1-1	S1-2	S1-3	S1-4	S2-1	S2-2	S2-3	S2-4	S2-5	S2-6	S2-7	S2-8	
0	16M DRAM	1	0	0	0	1	0	0	0	1	0	0	0	00000000
1	16M DRAM	0	1	0	0	0	1	0	0	0	1	0	0	00100000
2	16M DRAM	0	0	1	0	0	0	1	0	0	0	1	0	00200000
3	16M DRAM	0	0	0	1	0	0	0	1	0	0	0	1	00300000
0	64M DRAM	1	0	0	0	1	0	0	0	1	0	0	0	00000000
1	64M DRAM	0	1	0	0	0	1	0	0	0	1	0	0	00400000
2	64M DRAM	0	0	1	0	0	0	1	0	0	0	1	0	00800000
3	64M DRAM	0	0	0	1	0	0	0	1	0	0	0	1	00C00000

SECTION 3

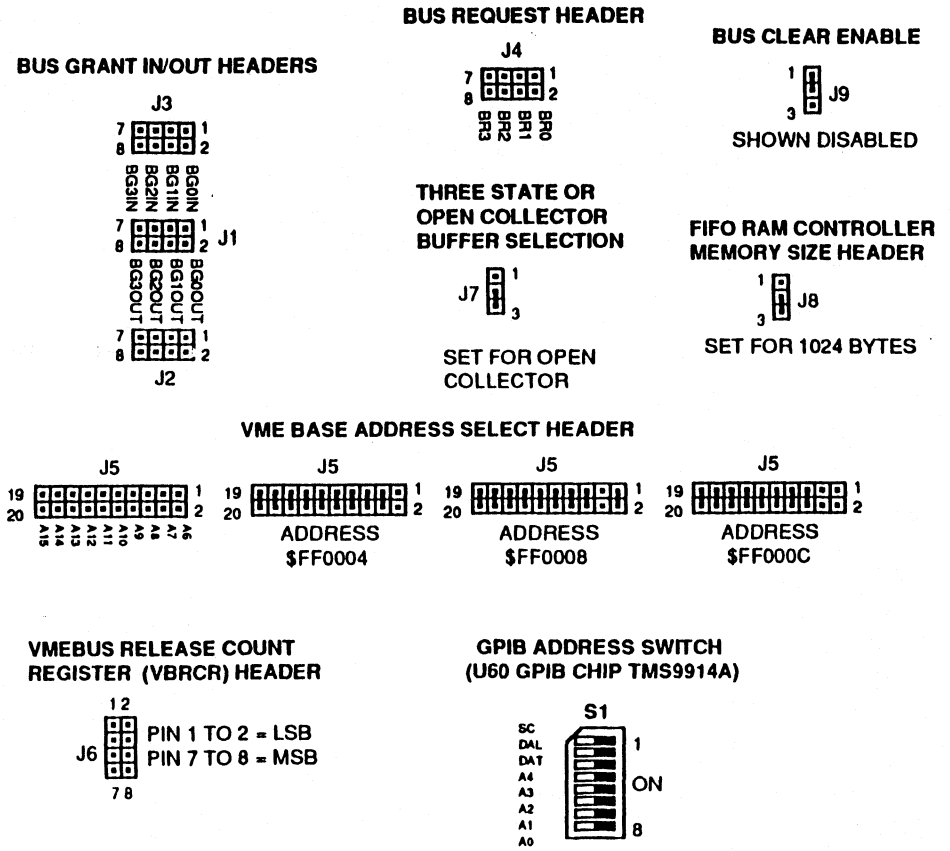


NOTE 1: OPEN ON SWITCH S1 DESIGNATES ON.
 NOTE 2: ACTIVE PART OF SWITCH IS DARKENED AREA.

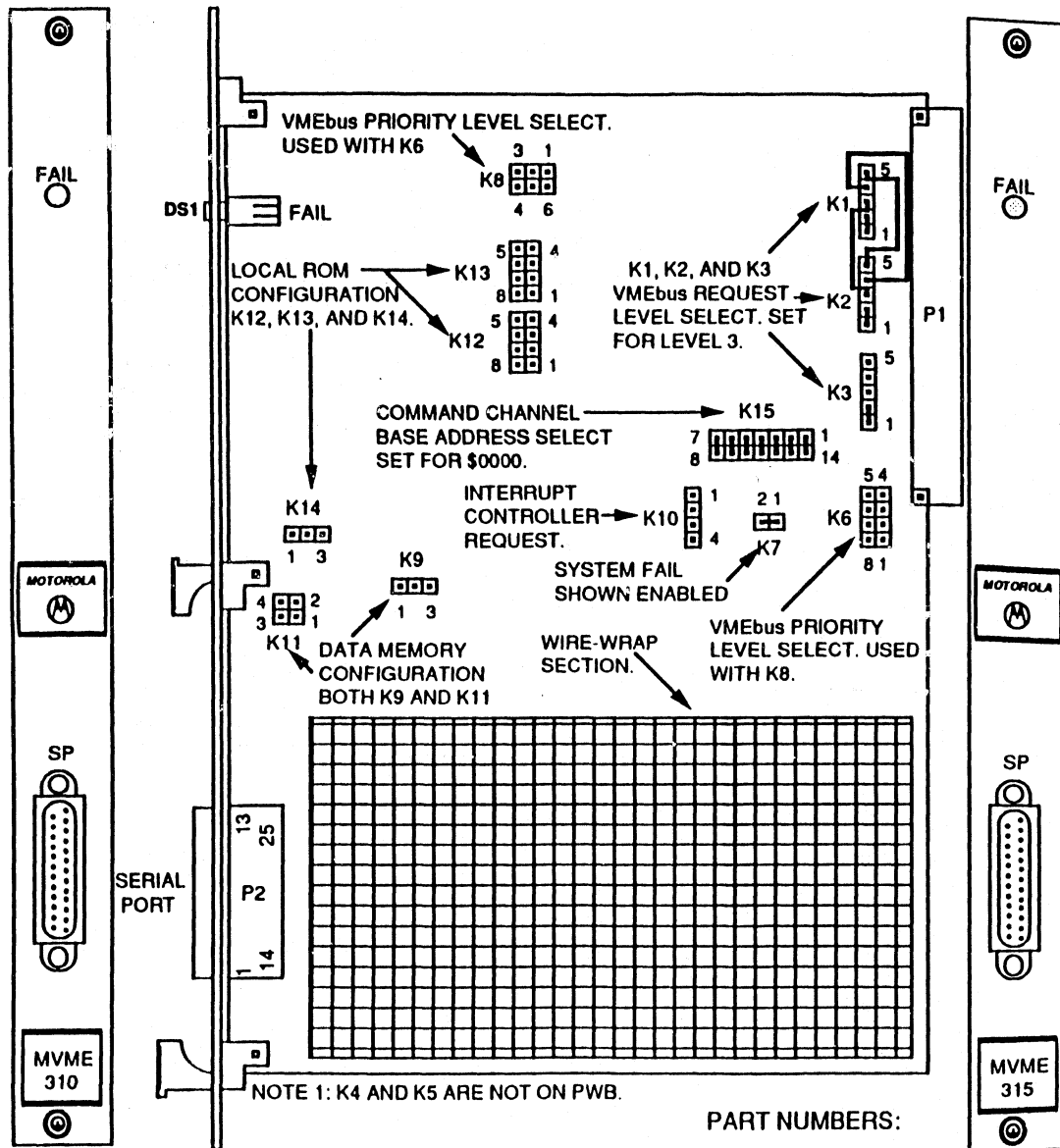
PART NUMBERS:

MVME300 01-W3148B01 76431494

SEE CURRENT REVISION LEVEL (CRL)
 FOR CURRENT REVISION INFORMATION.



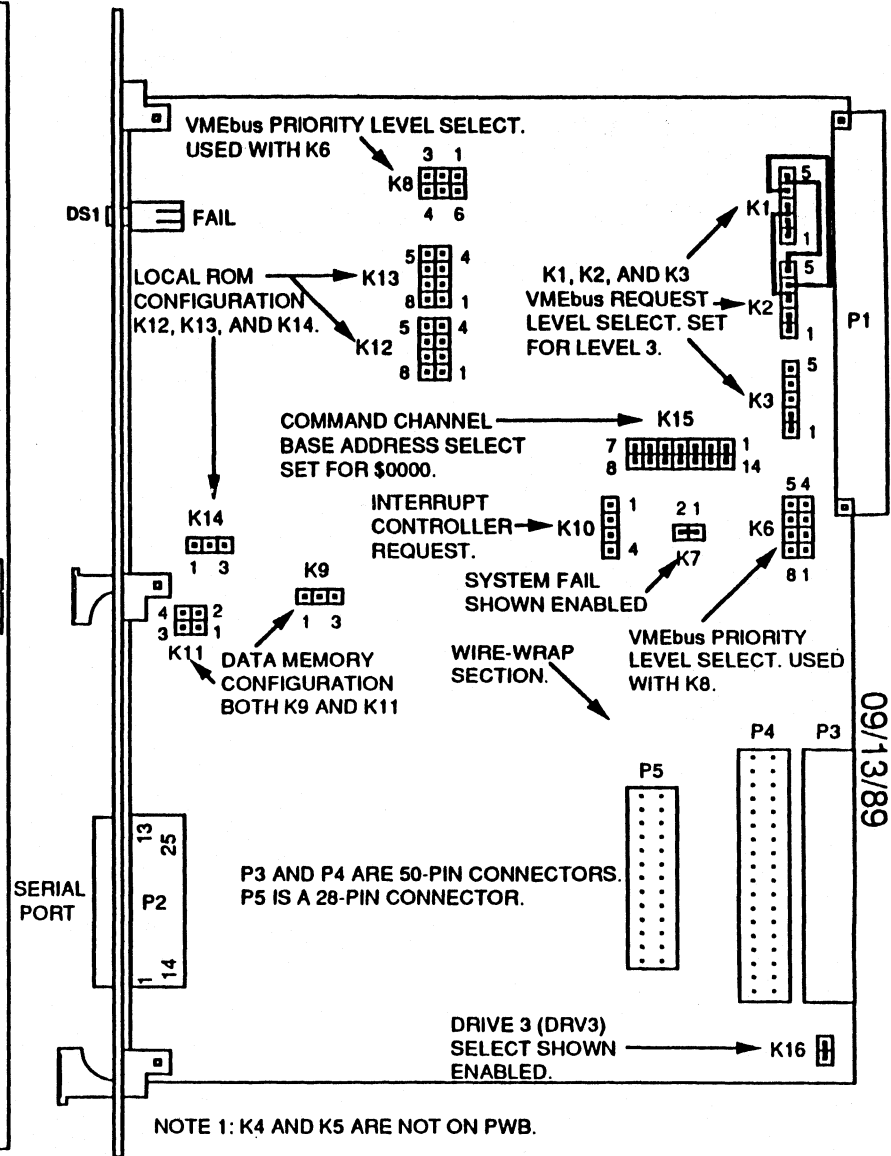
03/13/91



PART NUMBERS:

MVME310	01-G3015M01	76432690
MVME315	01-G3013M01	76432691 96010962
MVME319	01-G3013M02	76433071
	01-W3532B01	76433071

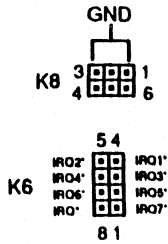
SEE CURRENT REVISION LEVEL (L) FOR CURRENT REVISION HISTORY.



MVME310/15/19
(UICP)
UNIVERSAL
INTELLIGENT
PERIPHERAL
CONTROLLER
PAGE 1 OF 4

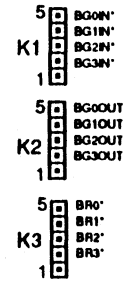
09/13/89

VMEBUS INTERRUPT PRIORITY LEVEL SELECT



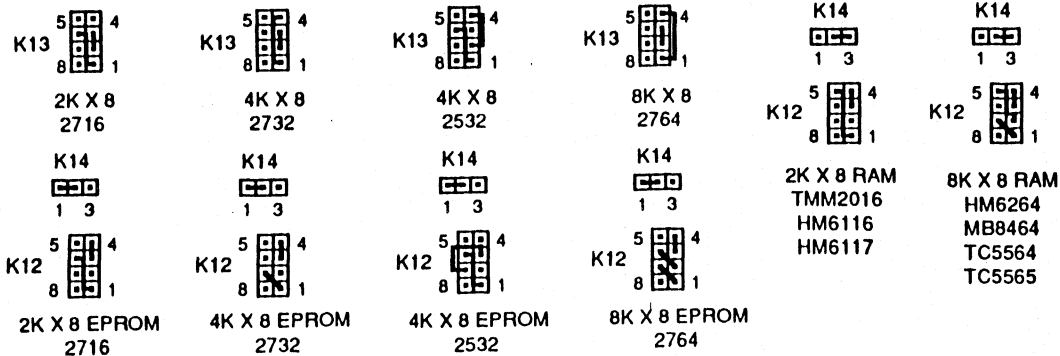
K6, AND K3 CONNECTIONS	VMEBUS INTERRUPTER PRIORITY LEVEL SELECT
K6: 4-8; K8: 1-6, 2-5	VMEBUS PRIORITY LEVEL 1
K6: 5-8; K8: 1-6, 3-4	VMEBUS PRIORITY LEVEL 2
K6: 3-8; K8: 1-6	VMEBUS PRIORITY LEVEL 3
K6: 6-8; K8: 2-5, 3-4	VMEBUS PRIORITY LEVEL 4
K6: 2-8; K8: 2-5	VMEBUS PRIORITY LEVEL 5
K6: 7-8; K8: 3-4	VMEBUS PRIORITY LEVEL 6
K6: 1-8; K8: NONE	VMEBUS PRIORITY LEVEL 7

VMEbus REQUEST LEVEL SELECT

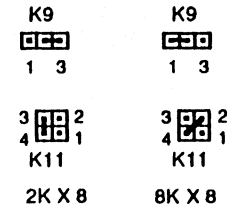


K1, K2, AND K3 CONNECTION	VMEBUS REQUEST LEVEL
K1: 1-5; K2: 1-5; K3: 1-5	BUS REQUEST LEVEL 0
K1: 1-4; K2: 1-4; K3: 1-4	BUS REQUEST LEVEL 1
K1: 1-3; K2: 1-3; K3: 1-3	BUS REQUEST LEVEL 2
K1: 1-2; K2: 1-2; K3: 1-2	BUS REQUEST LEVEL 3

PROGRAMMABLE MEMORY CONFIGURATIONS



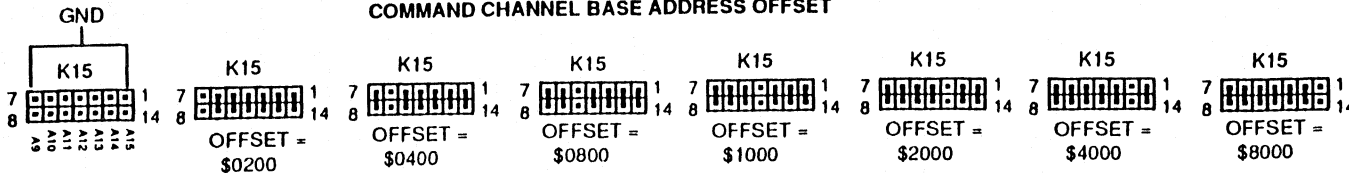
LOCAL RAM DATA MEMORY CONFIGURATION



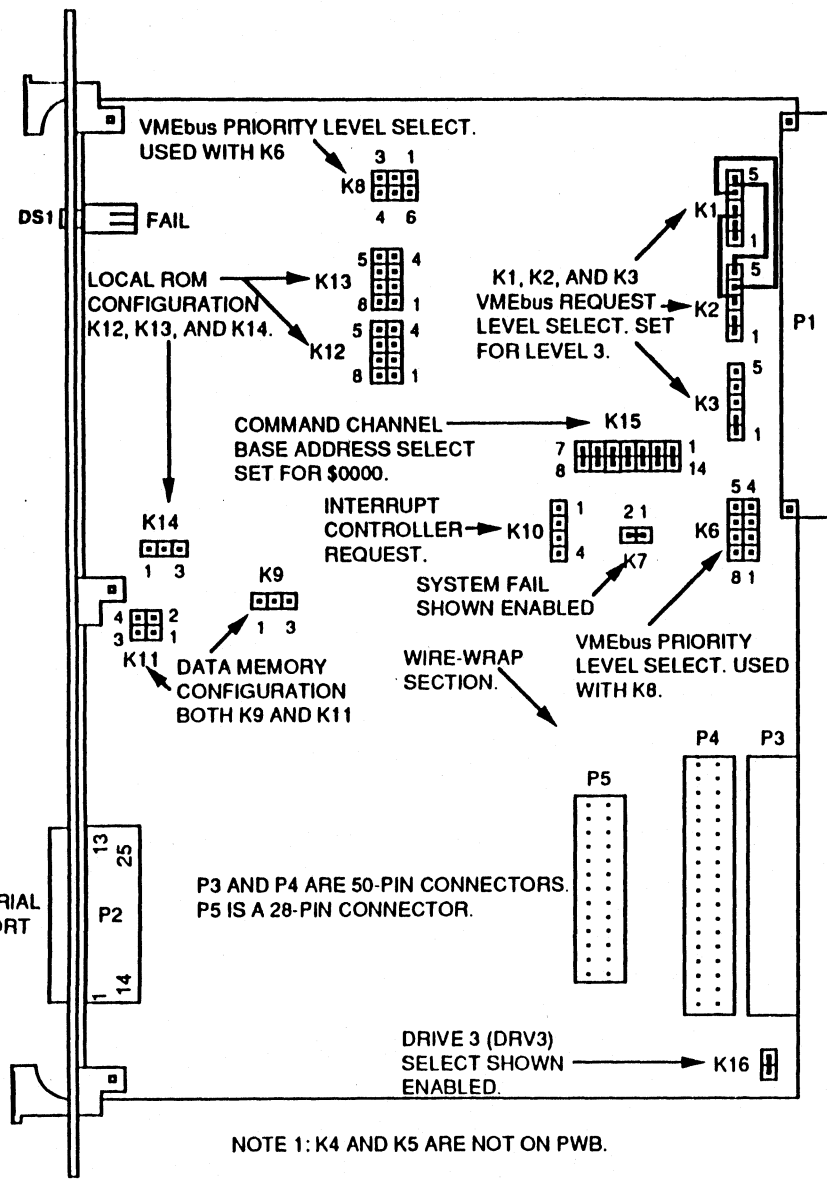
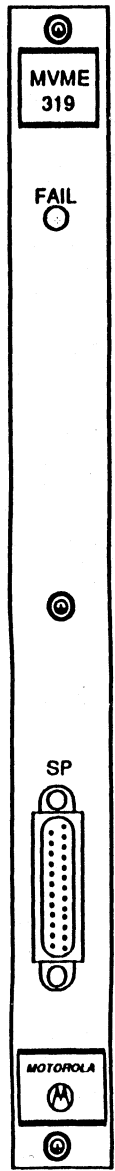
INTERRUPT CONTROLLER REQUEST SELECT HEADER



COMMAND CHANNEL BASE ADDRESS OFFSET



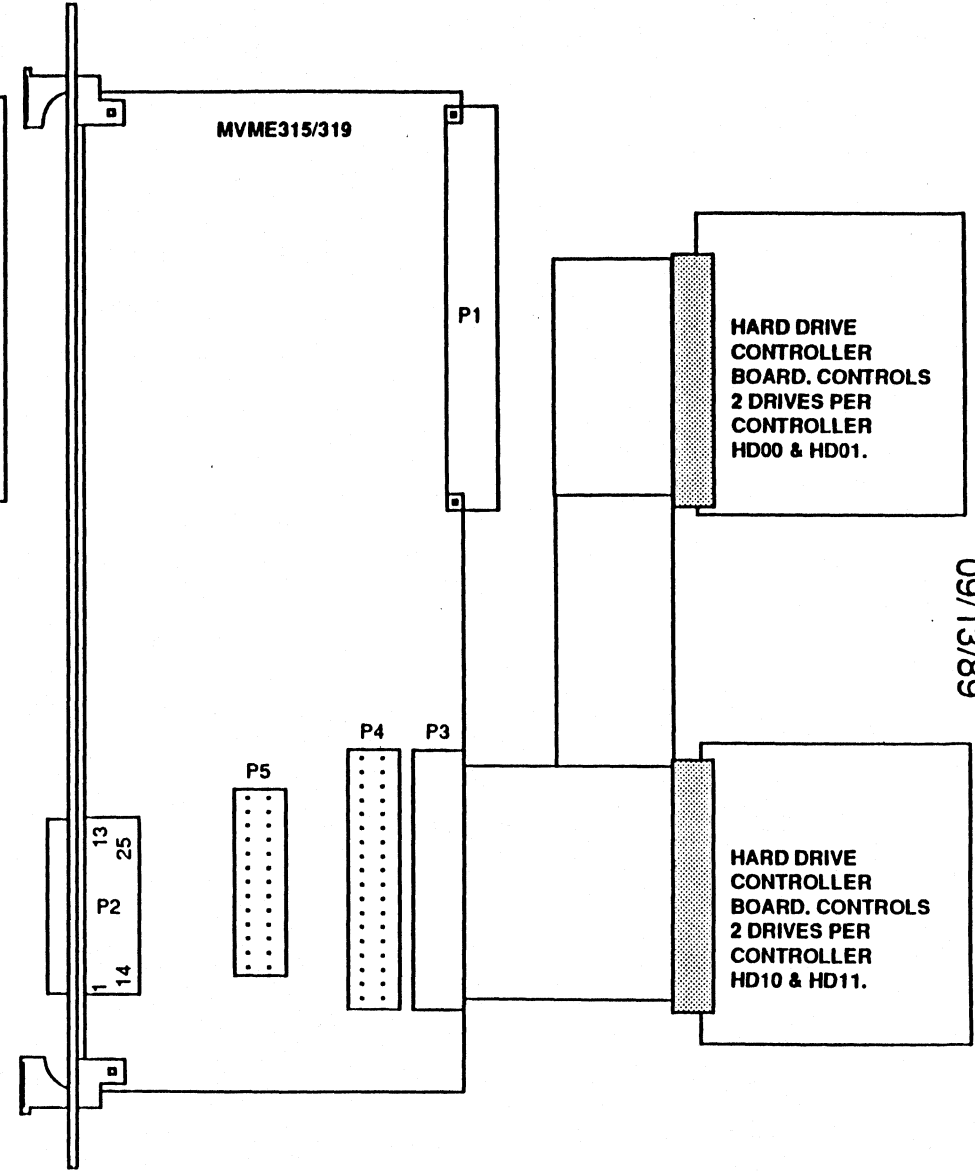
09/13/89



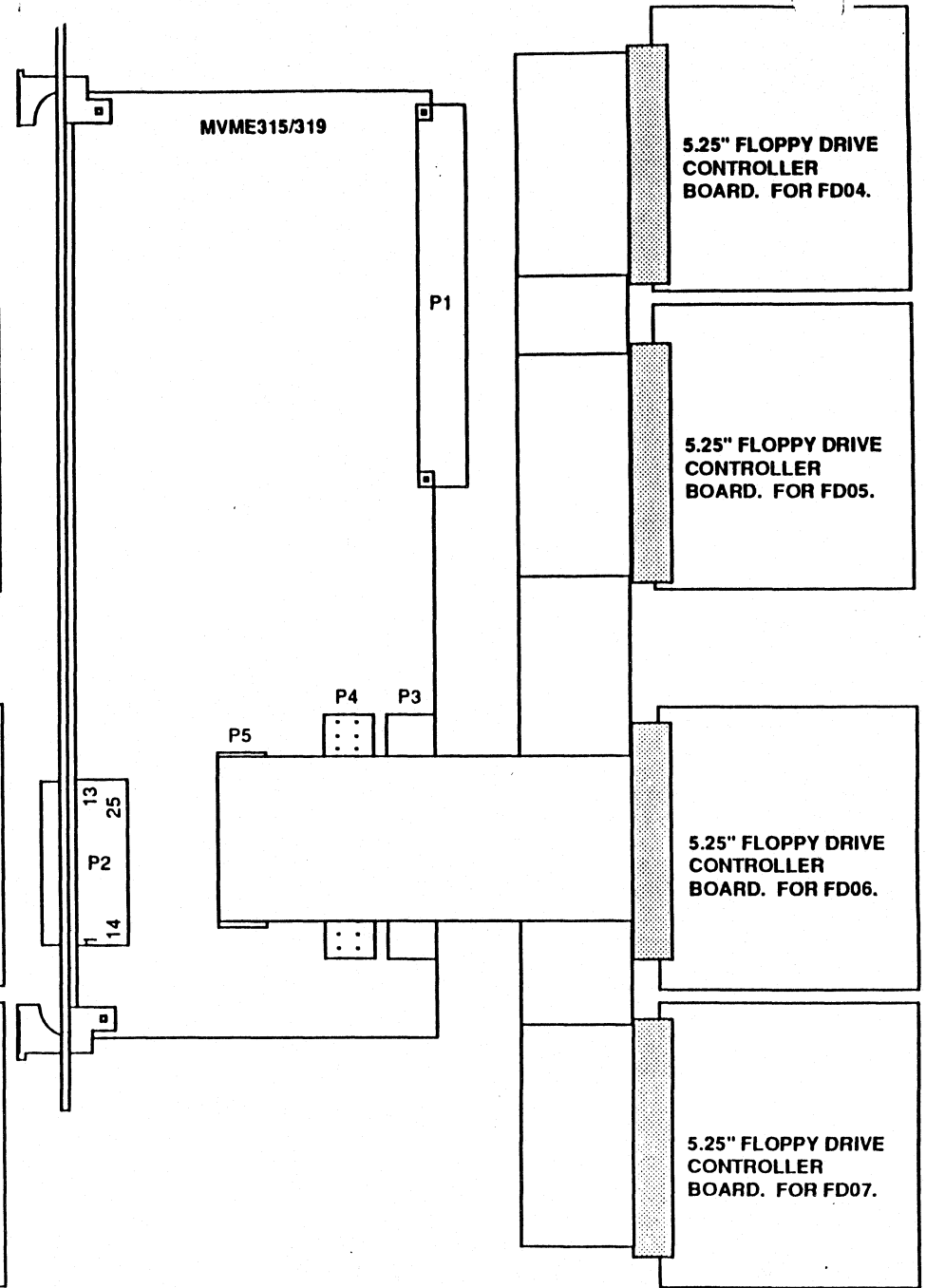
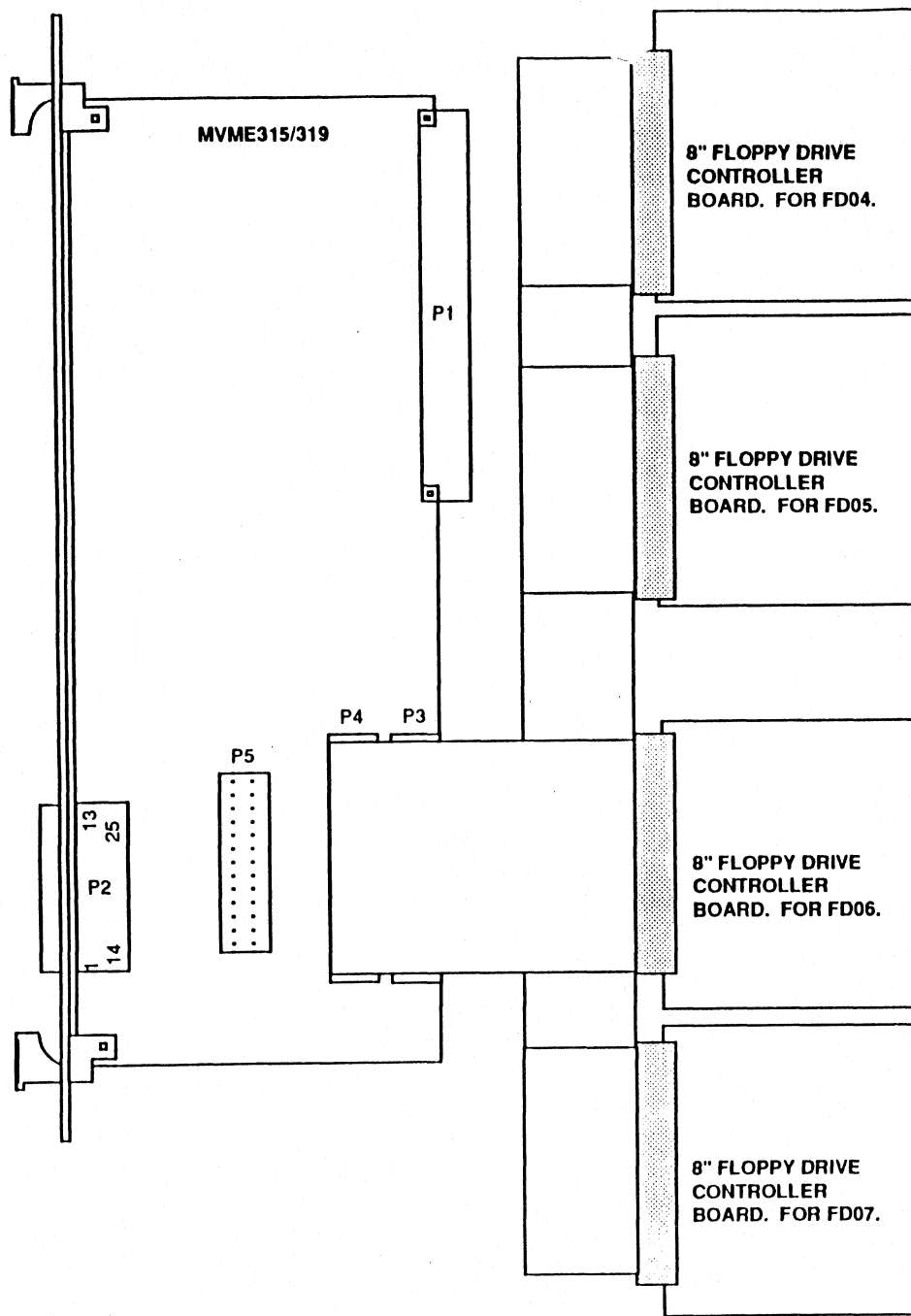
NOTE 1: K4 AND K5 ARE NOT ON PWB.

NOTE 2: K16 IS NOT ON VME310.

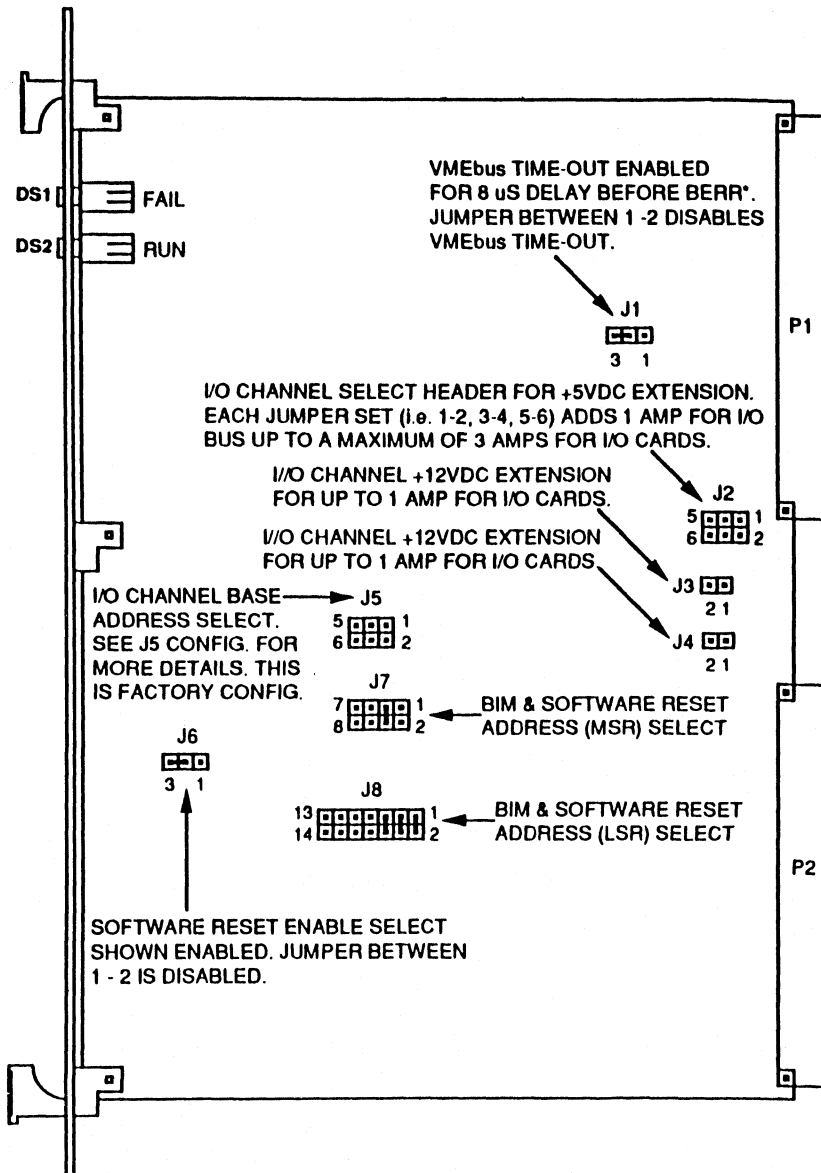
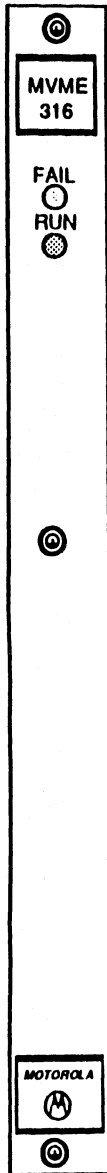
NOTE 3: P3 IS USED TO CONNECT HARD DISKS, P4 IS FOR 8 IN. FLOPPIES, AND P5 IS FOR 5.25 IN. FLOPPIES. ALL MAY BE USED FOR DAISY CHAINING DRIVES.



09/13/89



09/13/89



PART NUMBERS:

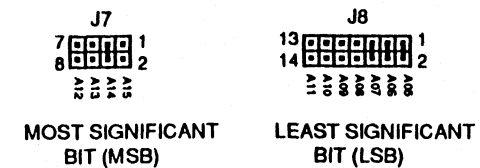
MVME316 01-W3336B01 76433000
& 76435172

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

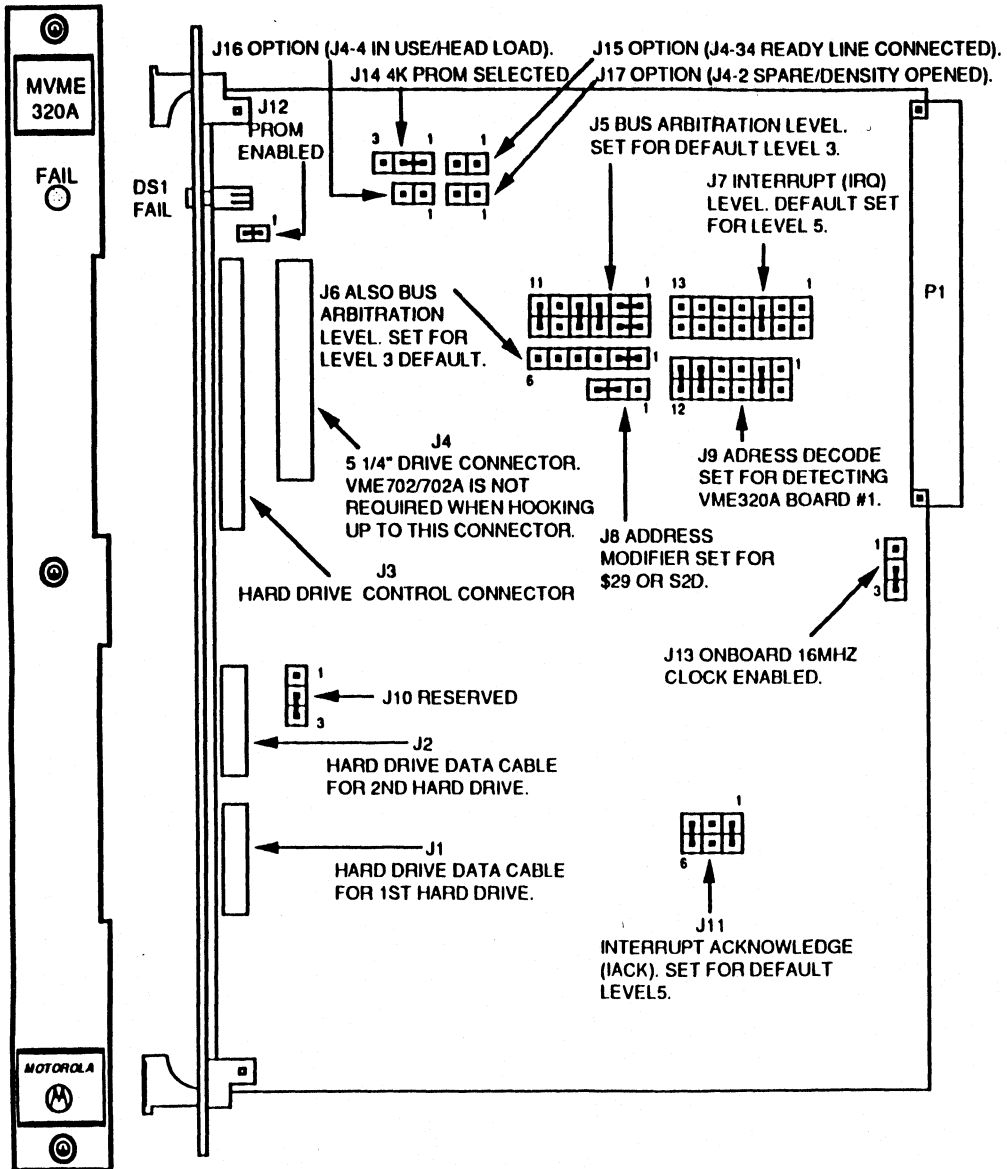
I/O CHANNEL BASE ADDRESS A13 - A15 SELECT HEADER

J5		A15	A14	A13	VME ADDRESS RANGE	
5	6	1-2	3-4	5-6	LOWEST	HIGHEST
ON	ON	ON	ON	ON	FF0000	FF1FFF
ON	ON	OFF	OFF	OFF	FF2000	FF3FFF
ON	OFF	ON	ON	ON	FF4000	FF5FFF
ON	OFF	OFF	OFF	OFF	FF6000	FF7FFF
OFF	ON	ON	ON	ON	FF8000	FF9FFF
OFF	ON	OFF	OFF	OFF	FFA000	FFBFFF
OFF	OFF	ON	ON	ON	FFC000	FFDFFF
OFF	OFF	OFF	OFF	OFF	FFE000	FFFFF

BIM & SOFTWARE RESET ADDRESS SELECT HEADERS



02/26/90



PART NUMBERS:

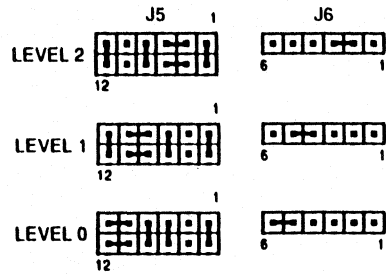
MVME320A 01-W3429B03 96010816

SEE CURRENT REVISION LEVEL (CTL) FOR CURRENT REVISION INFORMATION.

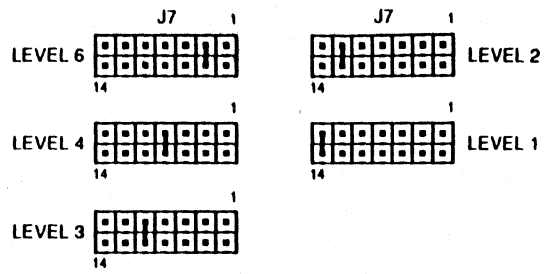
NOTE 1: THE CABLING DIAGRAMS ARE THE SAME FOR THE MVME320B BOARDS.

02/26/90

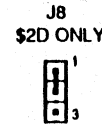
BUS ARBITRATION LEVELS.



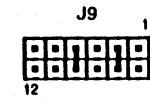
INTERRUPT LEVEL (IRQ)



ADDRESS DECODER

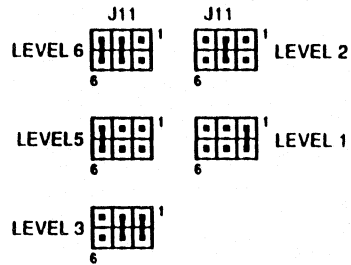


ADDRESS DECODE



SET FOR DETECTING
 VME320A AS BOARD #2.

INTERRUPT LEVEL (IACK)



CLOCK SELECT



USES SYSCLK

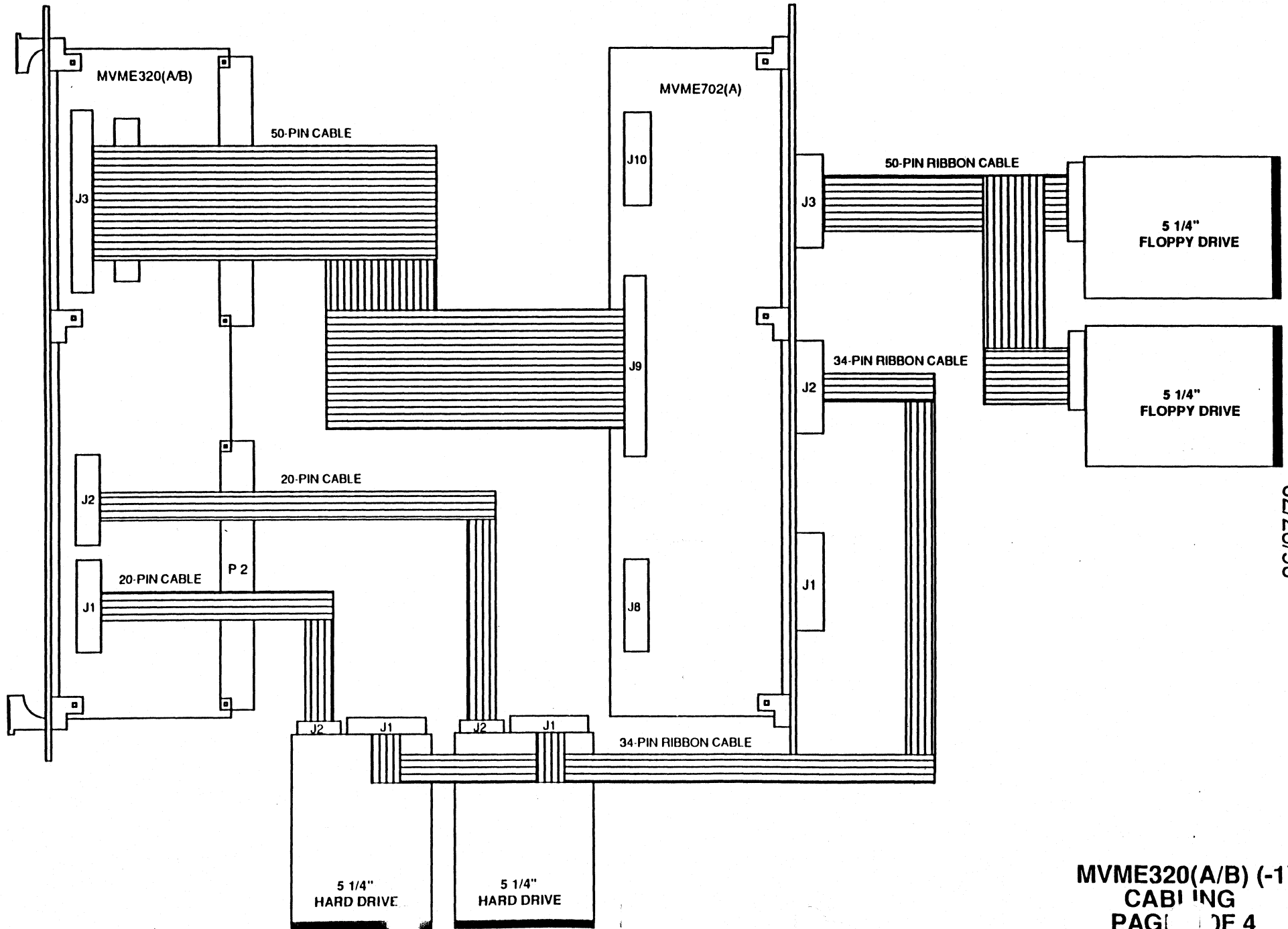
PROM SELECT SIZE



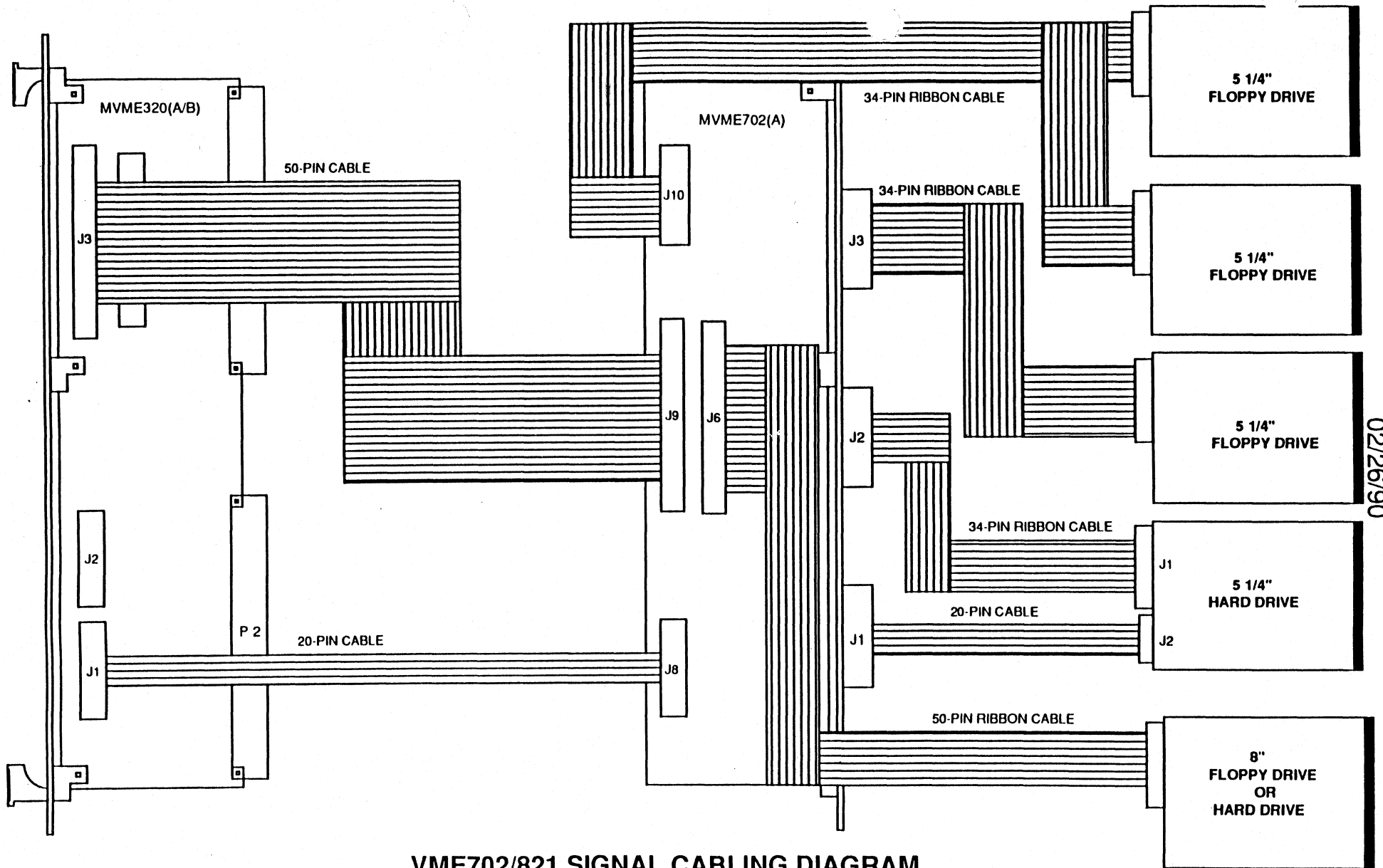
8K PROM

11/14/91

VME702 SIGNAL CABLING DIAGRAM

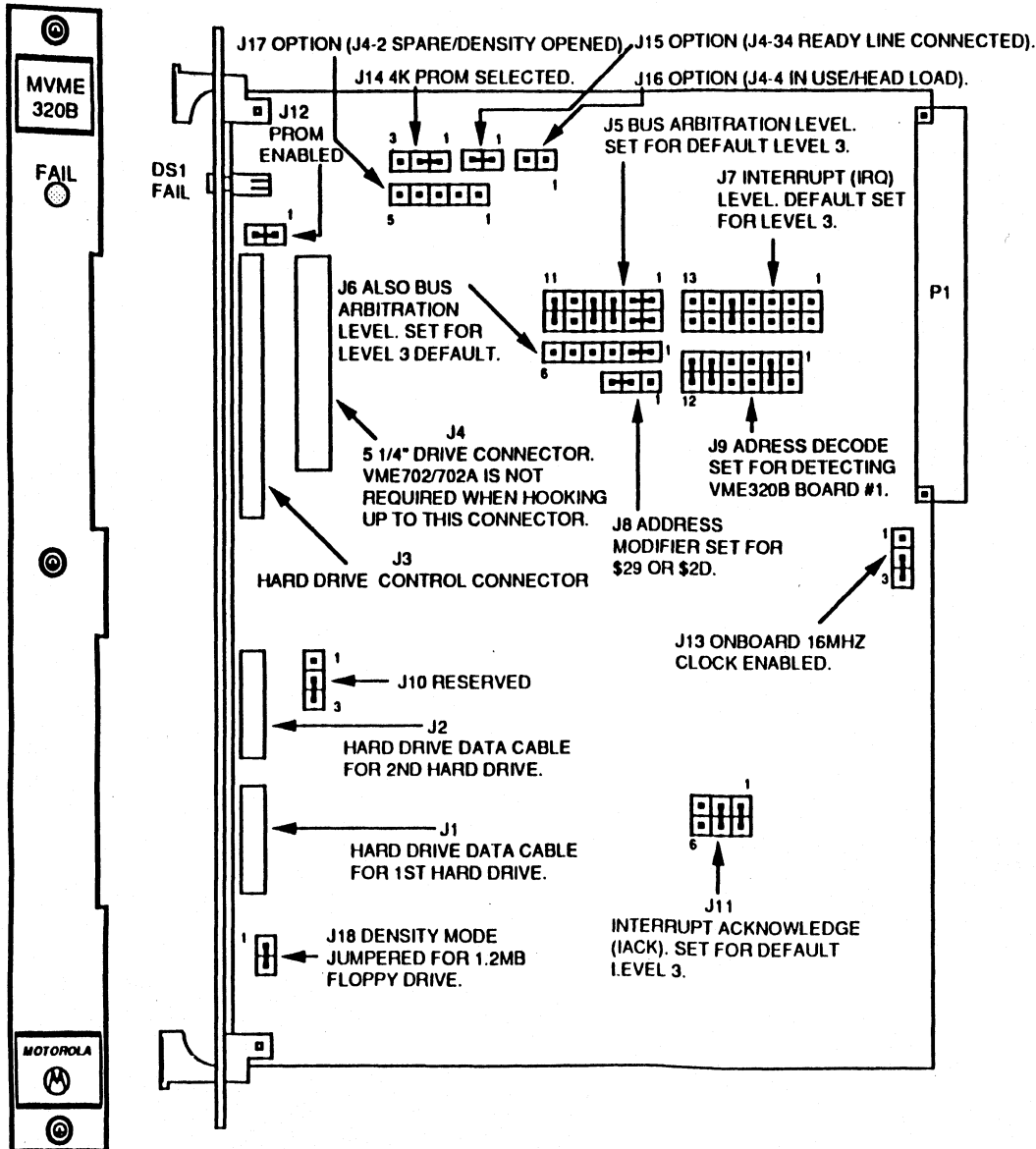


02/26/90



02/26/90

VME702/821 SIGNAL CABLING DIAGRAM



NOTE 1 : THE CABLING DIAGRAMS ARE THE SAME FOR THE VME320A BOARDS.

NOTE 2 : WHEN USING A TEAC FD-235HF SET J7 & J11 FOR LEVEL 3.

PART NUMBERS:

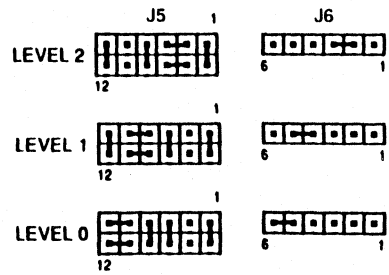
MVME320B 01-W3483B01 76435373

MVME320B-1 01-W3483B02 76435389

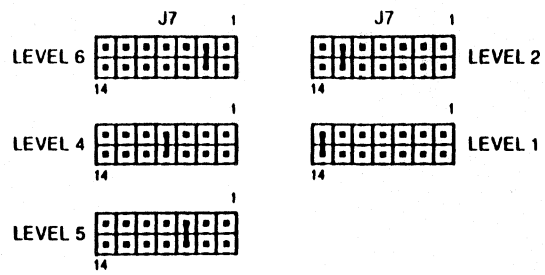
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

11/14/91

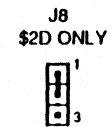
BUS ARBITRATION LEVELS.



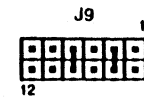
INTERRUPT LEVEL (IRQ)



ADDRESS MOD

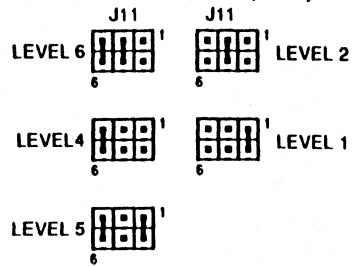


ADDRESS DECODE



SET FOR DETECTING
VME320A AS BOARD #2.

INTERRUPT LEVEL (IACK)



CLOCK SELECT



USES SYSCLK

PROM SELECT SIZE



8K PROM

OPTION (SPARE/DENSITY)



NORMAL DEFAULT
BUT NOT ON DELTA'S.



TIED TO 1K OHM
PULLUP RESISTOR.



TIED TO GROUND.

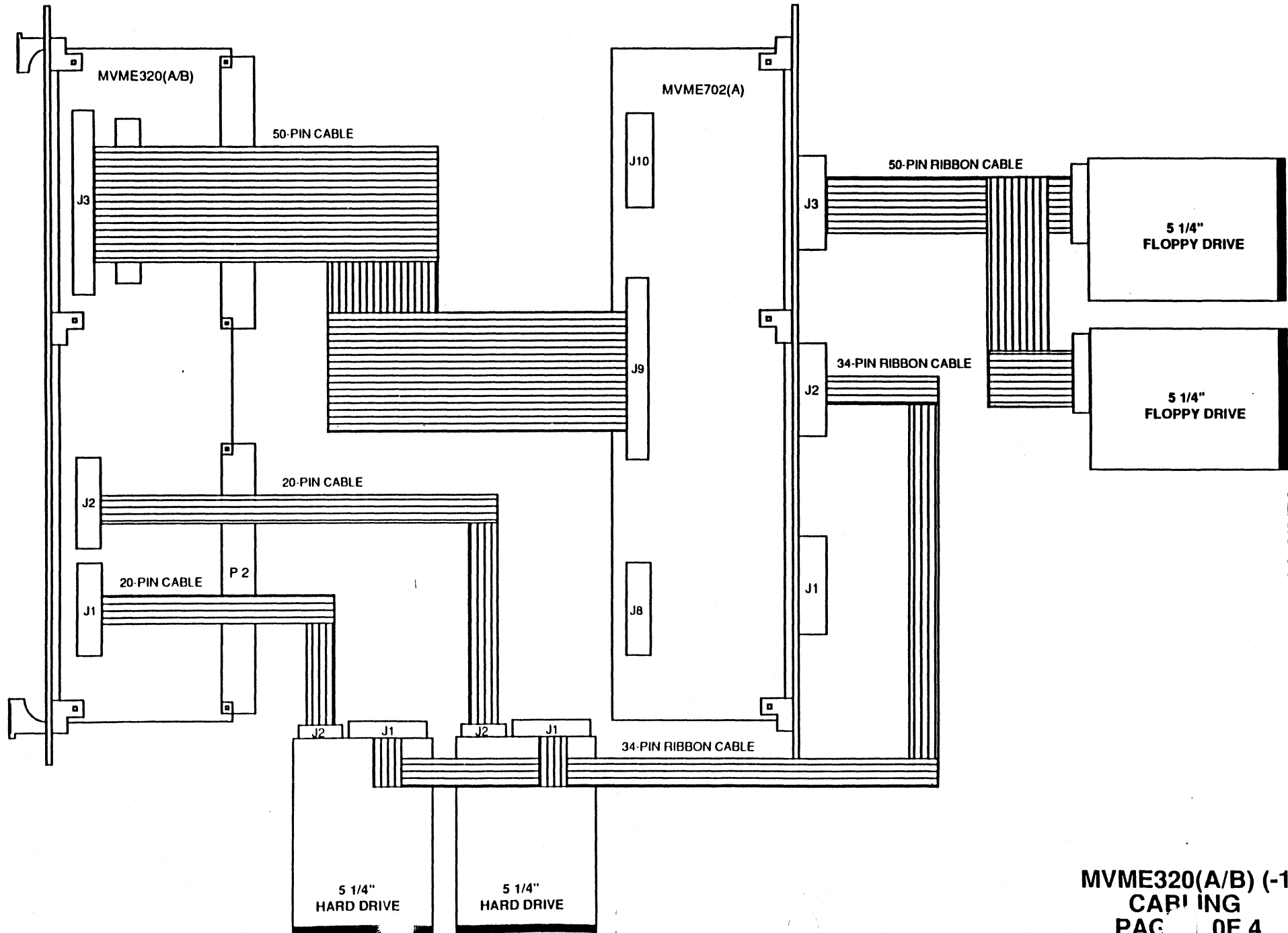
DENSITY MODE



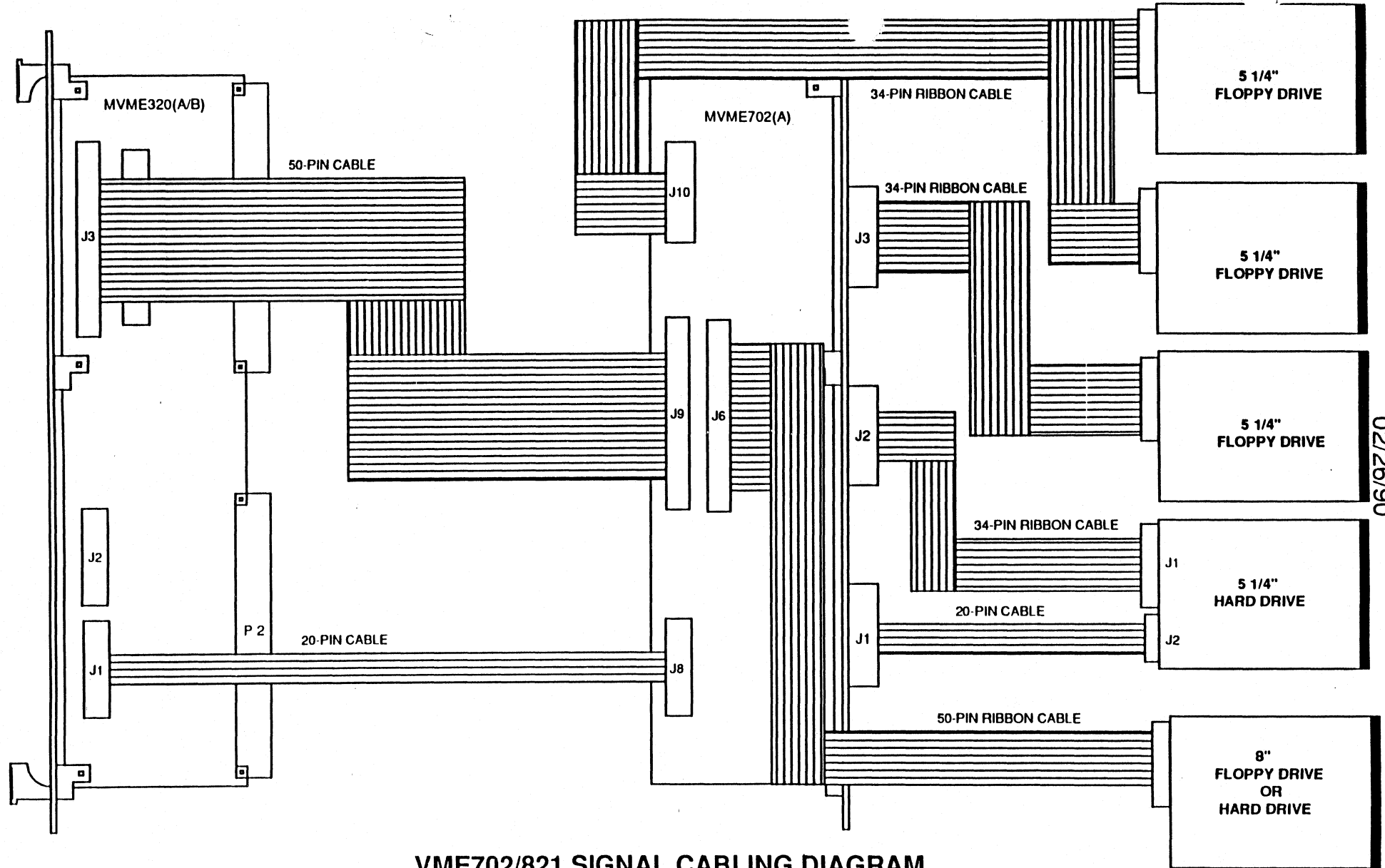
SET FOR 655KB
FLOPPY DRIVE.

11/14/91

VME702 SIGNAL CABLING DIAGRAM

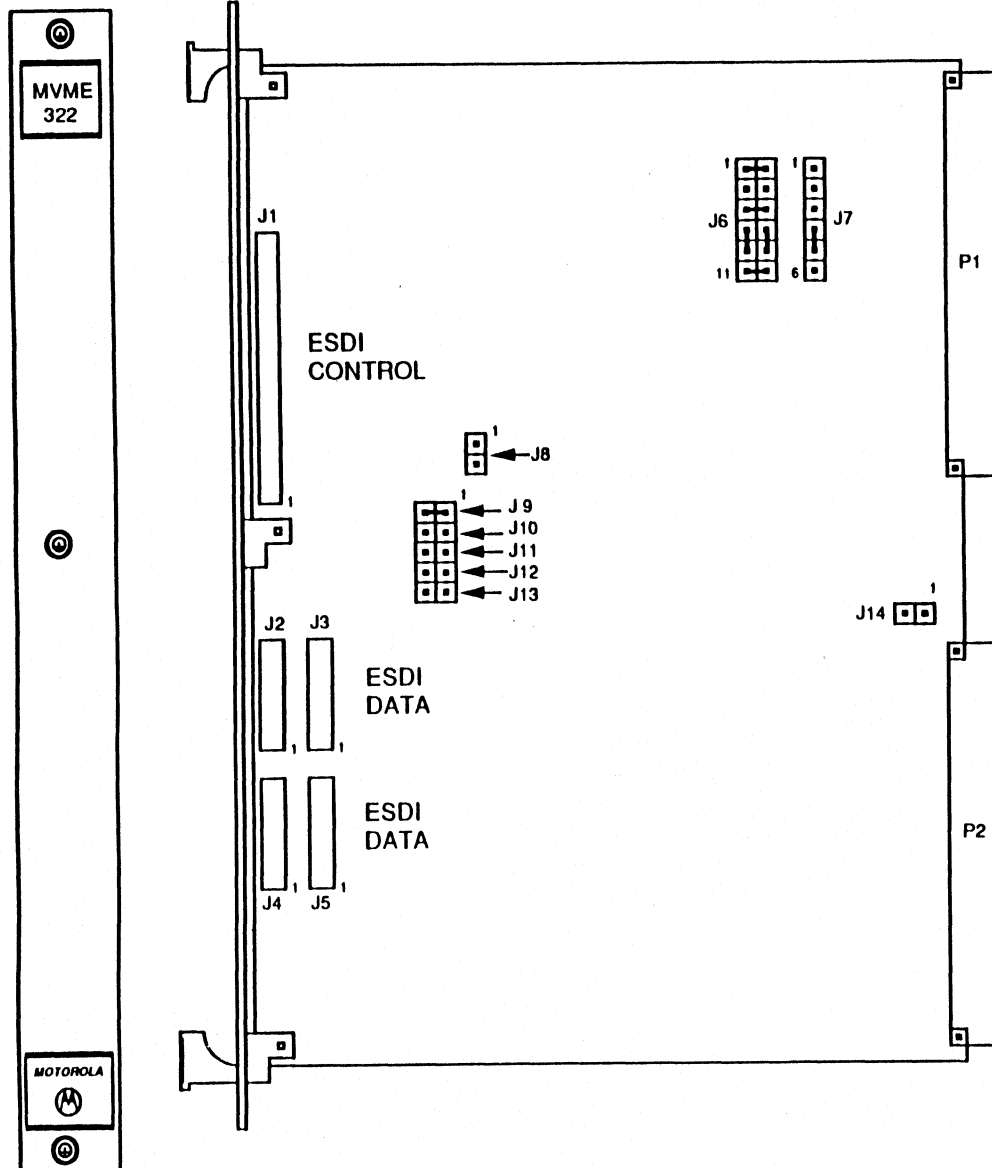


02/26/90



02/26/90

VME702/821 SIGNAL CABLING DIAGRAM



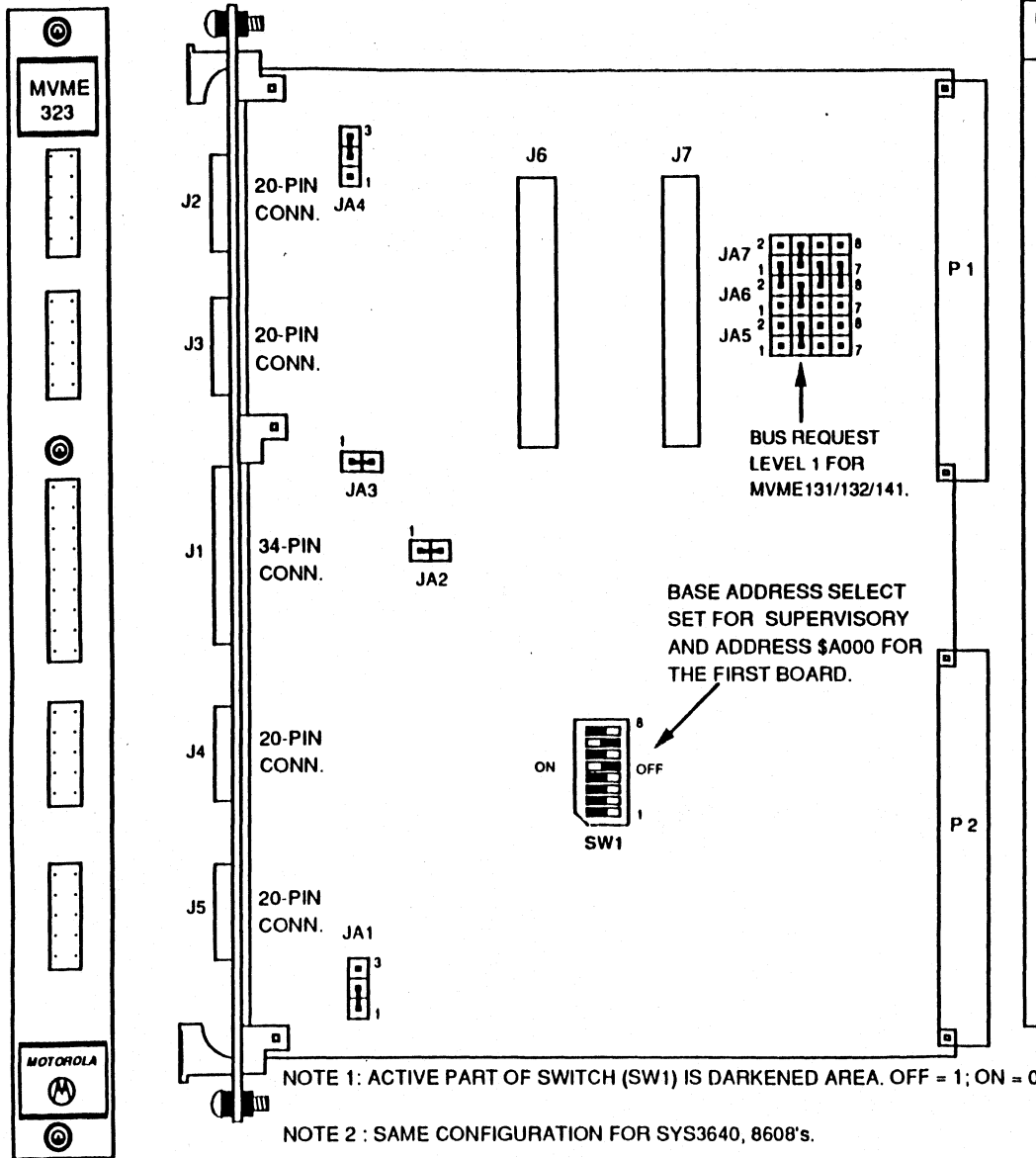
PART NUMBERS:

MVME322 01-W3516B01 96011022

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

02/26/90

**MVME322
ESDI DISK
CONTROLLER
PAGE 1 OF 1**



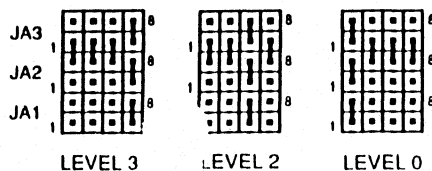
ERROR CODES FOR COMMANDS COMPLETED WITH ERROR			
ERROR CODE	DESCRIPTION	ERROR CODE	DESCRIPTION
10	DISK NOT READY	42	GAP SPECIFICATION ERROR
12	SEEK ERROR	4B	SEEK ERROR
13	ECC ERROR-DATA FIELD	50	SECTORS/TRACK ERROR
14	INVALID COMMAND CODE	51	BYTES SECTOR SPEC. ERROR
15	ILLEGAL FETCH AND EXECUTE	52	INTERLEAVE SPEC. FACTOR
16	INVALID SECTOR IN COMMAND	53	INVALID HEAD ADDRESS
17	ILLEGAL MEMORY TYPE	54	INVALID CYLINDER ADDRESS
18	BUS TIMEOUT	55	ESDI COMMAND COMPLETE TIMEOUT
19	HEADER CHECKSUM ERROR	56	ZERO SECTOR COUNT
1A	DISK WRITE PROTECTED	5D	INVALID DMA TRANSFER COUNT
1B	UNIT NOT SELECTED	60	IOPB FAILED
1C	SEEK ERROR TIMEOUT	61	DMA FAILED
1D	FAULT TIMEOUT	62	ILLEGAL VME ADDRESS
1E	DRIVE FAULTED	6A	UNRECOGNIZED HEAD FIELD
1F	READY TIMEOUT	6B	MAPPED HEADER ERROR
20	END OF MEDIUM	6E	SPARE SECTOR SPEC. ERROR
21	TRANSLATION FAULT	6F	NO SPARE SECTOR ENABLED
22	INVALID HEADER PAD	77	COMMAND ABORTED
23	UNCORRECTABLE ERROR	78	ACFAIL DETECTED
24	TRANSLATION ERROR, CYLINDER	80	XFER ASSERTION TIMEOUT
25	TRANSLATION ERROR, HEAD	81	XFER RELEASE TIMEOUT
26	TRANSLATION ERROR, SECTOR	82	STATUS ASSERTION TIMEOUT
27	DATA OVERRUN	83	STATUS RELEASE TIMEOUT
28	NO INDEX PULSE ON FORMAT	A0	S/G LIST TOO LARGE
29	SECTOR NOT FOUND	A1	ILLEGAL ELEMENT BYTE COUNT
2A	ID FIELD ERROR, WRONG HEAD	AB	ILLEGAL ELEMENT SIZE
2B	INVALID SYNC IN DATA FIELD	AC	ILLEGAL LIST BYTE COUNT
2C	NO VALID HEADER FOUND	AD	ILLEGAL IOPB SECTOR BYTE COUNT
2D	SEEK TIMEOUT ERROR	AE	ILLEGAL ELEMENT COUNT
2F	NOT ON CYLINDER	C0	BOTH BITS SET
30	RTZ TIMEOUT	C1	MSE WITHOUT INITIALIZE LONG
31	INVALID SYNC IN HEADER	F0	MAPPED HEADER
3E	UIB SKEW FACTOR	F1	SECTOR NOT FLAGGED
3F	NO HEADS SPECIFIED	FC	NO WRITE LIST
40	UNIT NOT INITIALIZED	FD	NO WRITE BUFFERS
FF	COMMAND NOT IMPLEMENTED	FE	OUT OF BUFFERS
41	NOT USED		

11/08/91

PART NUMBERS:

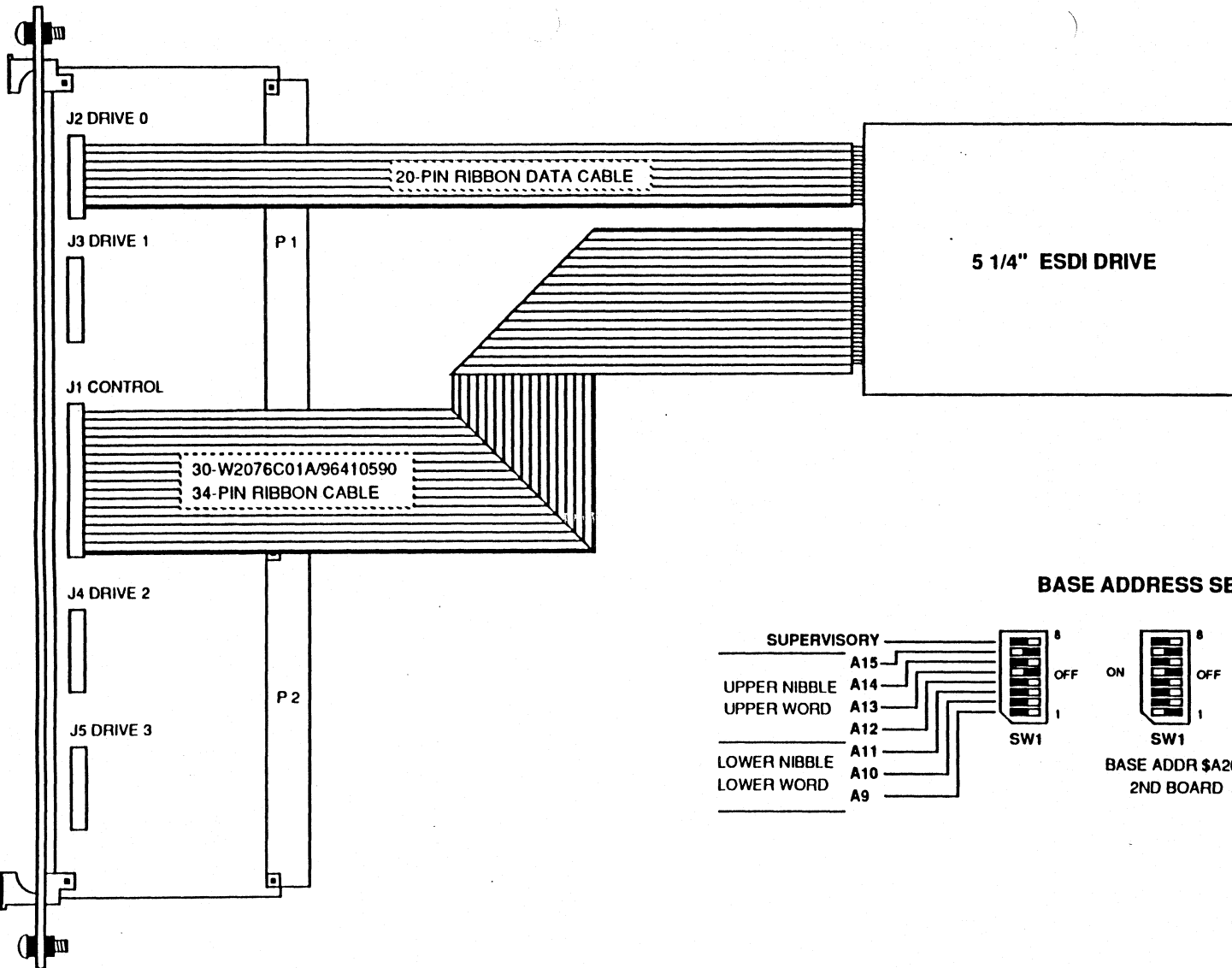
- MVME323 01-W2944B01 96010888 INTERPHASE
MODEL # V/ESDI 4201; PART# CC4201-0105
FW REV. 020, PAL & I/O CONNECTOR
- MVME323-1 01-W2944B02 96010888 INTERPHASE
MODEL # V/ESDI 4201; PART# CC4201-0106
FW 020 & PAL
- MVME323-2 01-W2944B03 96011040 INTERPHASE
MODEL # V/ESDI 4201; PART# CC4201-0010
FW 060 PAL

BUS REQUEST LEVELS

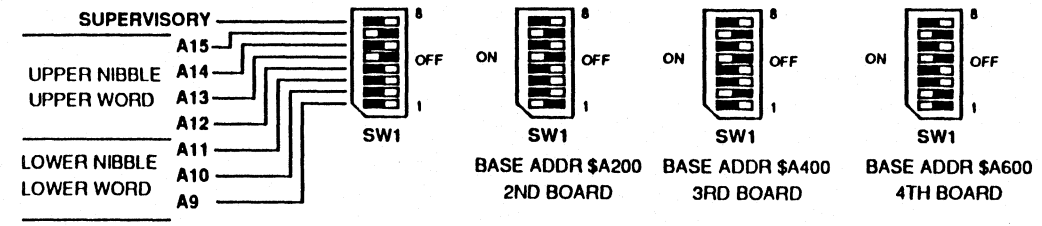


- MVME323-2 01-W2944B04 96011257 INTERPHASE
MODEL # V/ESDI 4201; PART# CC4201-0150
FW 060, PAL & SCSI DAUGHTER CARD
- MVME323-2 01-W2944B05 96011226 INTERPHASE
MODEL # V/ESDI 4201; PART# CC4201-0157
FW 060, PAL & I/O CONNECTOR
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

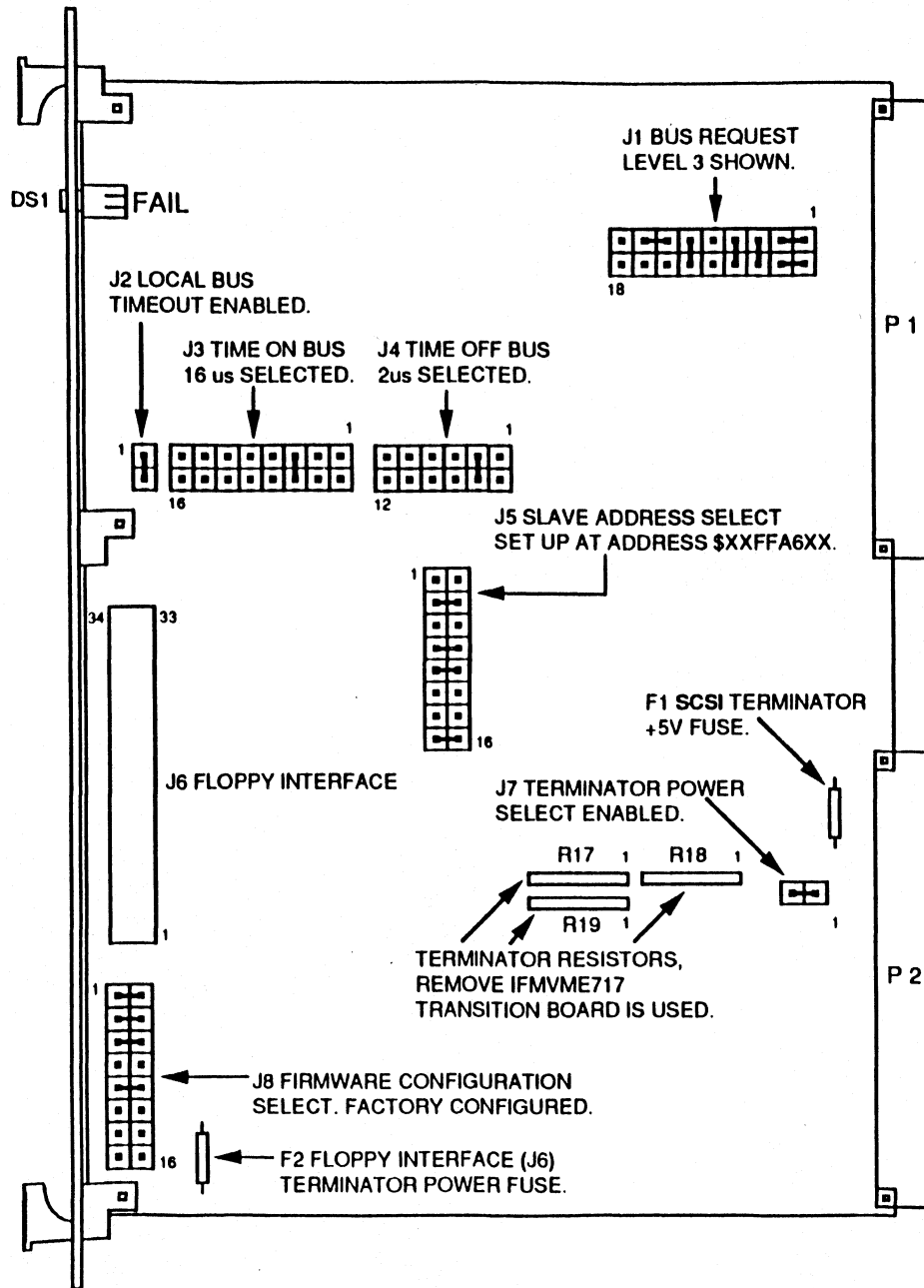
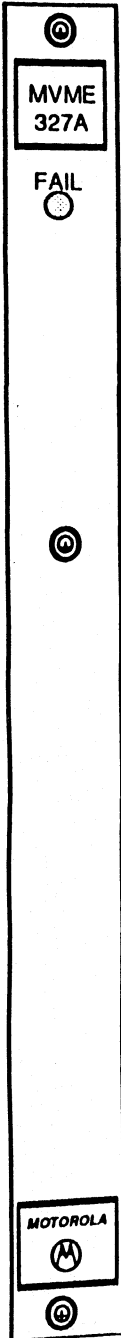
**MVME323 (-1/-2)
ESDI DISK
CONTROLLER
PAGE 1 OF 2**



BASE ADDRESS SELECT



04/12/91



PART NUMBERS:

MVME327A 01-W3550B01 96011025

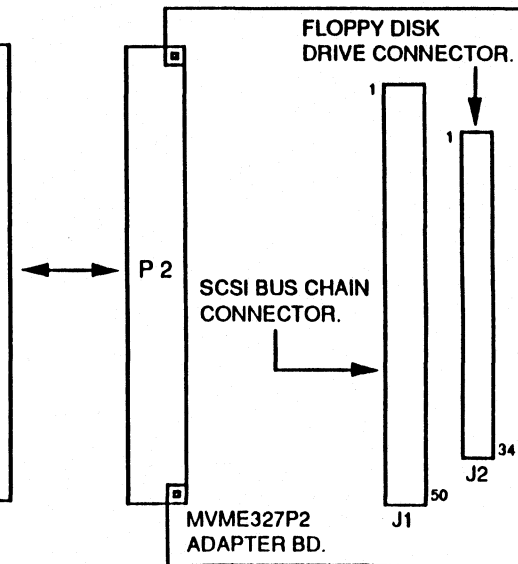
MVME327P2 01-W3544B01 96011059

MVME327A F/W REV. 2.3 RELEASED W/PWB.

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

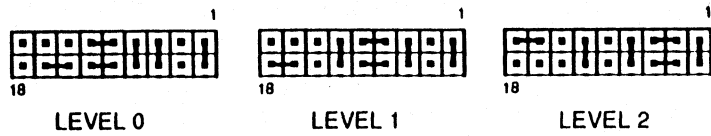
NOTE 1 : SAME CONFIGURATION FOR SYS3400, 3640, 8400 & 8608's.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

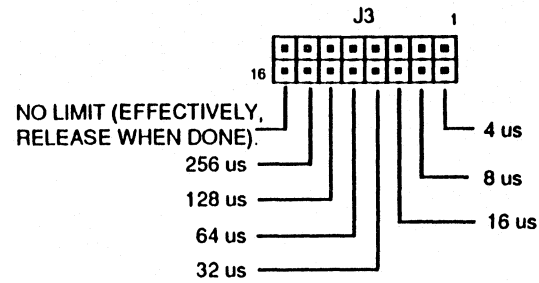


03/14/91

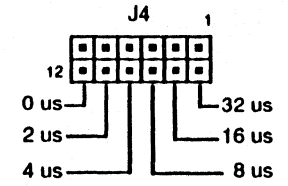
BUS REQUEST LEVELS



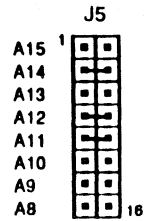
TIME ON BUS SELECT



TIME OFF BUS SELECT

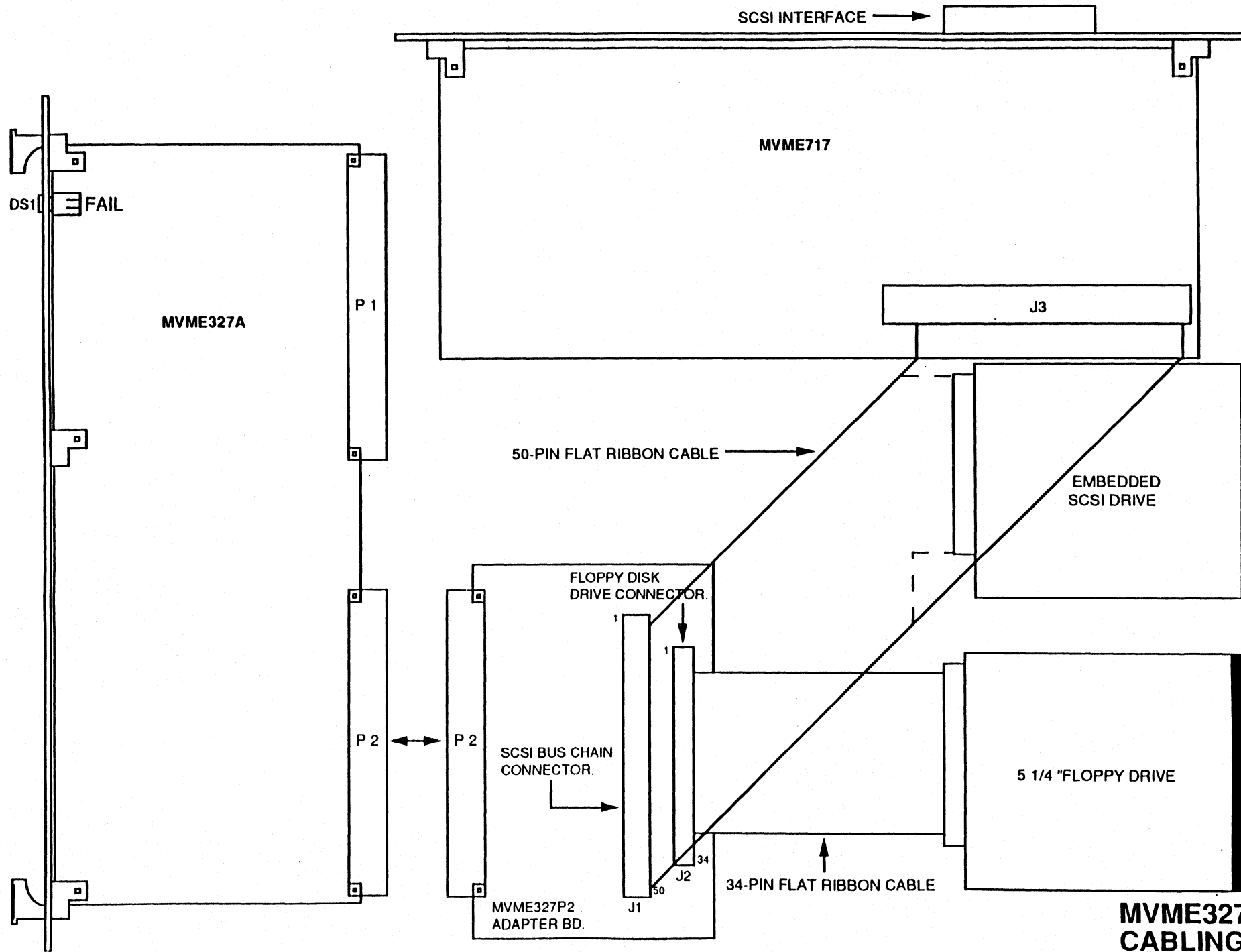


SLAVE ADDRESS SELECT

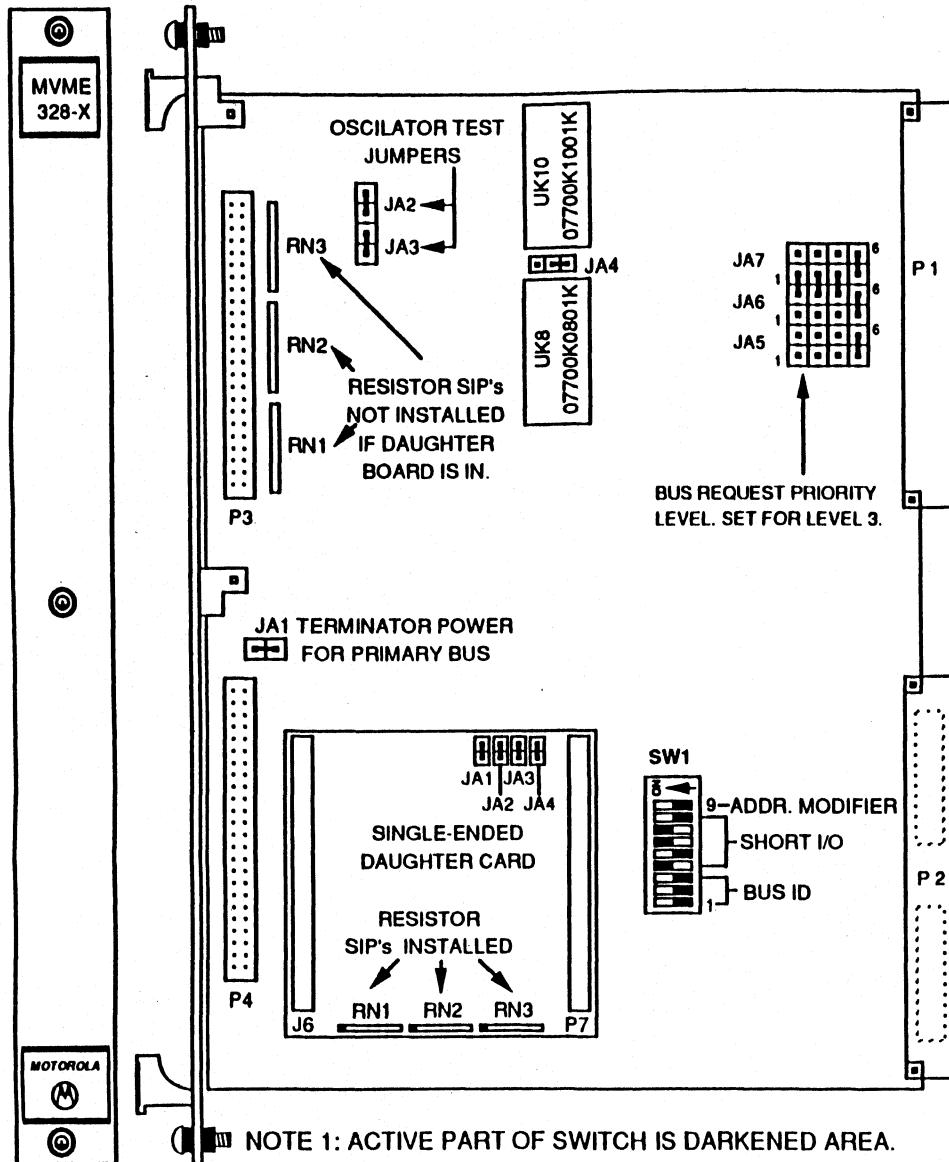


2ND MVME327A SHORT I/O
 ADDRESSSPACE IS \$XXFFA7XX.

03/14/91



09/13/89



NOTE 1: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 2: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 3: IN THE 6-SLOT (SYS8440) CHASSIS, THE SINGLE ENDED DAUGHTER BOARD ON A VME328-2 CAN ONLY BE USED FOR EXTERNAL DEVICES, SO IT MUST BE TERMINATED.

PART NUMBERS:

MVME328-1 01-W2625C01 96011439
INTERPHASE P/N CC04210-0108 Rev. 2

MVME328-2 01-W2825C02 96011438
WITH SEDC INSTALLED
INTERPHASE P/N CC4210-0151 Rev. 2

MVME328-1 DIFFERENTIAL DAUGHTER CARD
01-W2626C01 TBD

MVME328-1 SINGLE ENDED DAUGHTER CARD
01-W2626C02 TBD

MVME328P2 PADDLE BOARD FOR P2 BACKPLANE
01-W2627C01 TBD
INTERPHASE P/N 0870-00A-000

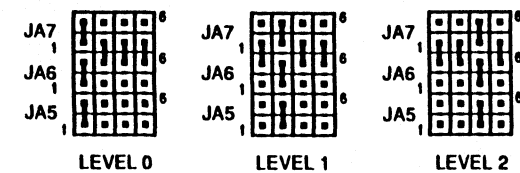
F/W REVISION: UK8 = 07700K0801K
UK10 = 07700K1001K

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

MOTOROLA P/N & REVISION

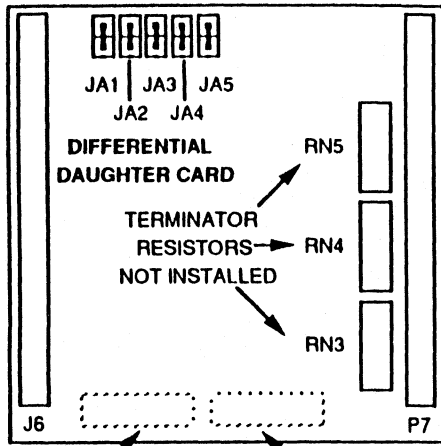
MANUFACTURER'S P/N & REVISION

VMEBUS PRIORITY INTERRUPT SELECT



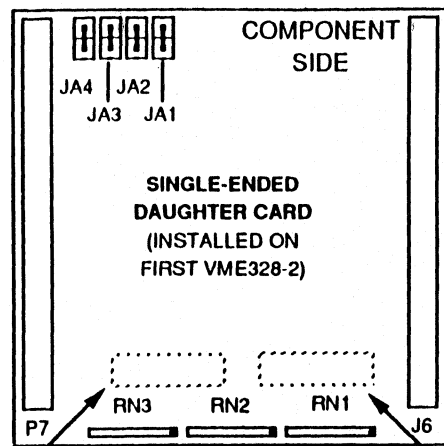
11/04/91

JA1, JA2, JA3 ARE FOR SECONDARY BUS ID
 JA4 IS SECONDARY BUS TERMINATOR POWER
 JA5 IS OSCILLATOR TEST JUMPER



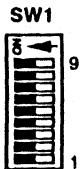
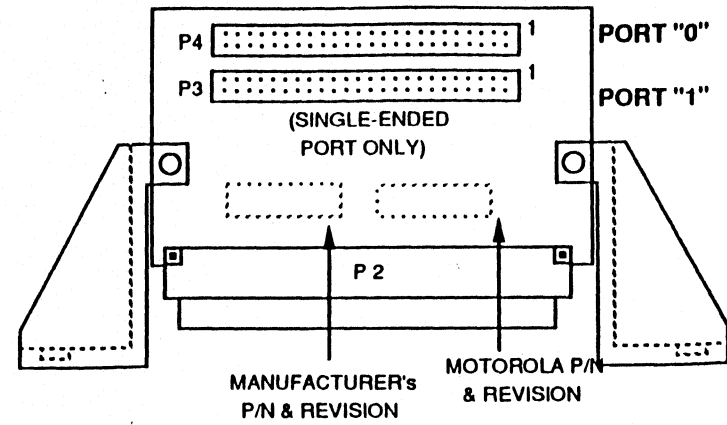
MANUFACTURER'S P/N & REVISION
 MOTOROLA P/N & REVISION

JA1, JA2, JA3 ARE FOR SECONDARY BUS ID
 JA4 IS SECONDARY BUS TERMINATOR POWER

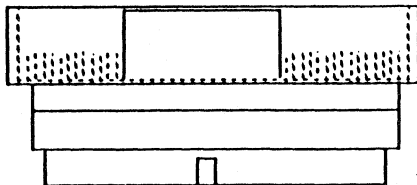


MOTOROLA P/N & REVISION
 RESISTOR SIP'S NOT INSTALLED
 MANUFACTURER'S P/N & REVISION

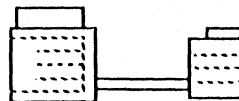
P2 ADAPTER FOR VME328-1/-2



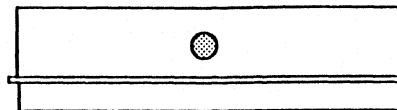
FUNCTION	PRIMARY SCSI BUS ID 7			SHORT I/O ADDR (9000)					ADDR. MOD.
	1	2	3	A11	A12	A13	A14	A15	
SWITCH #	1	2	3	4	5	6	7	8	9
SETTING	OFF	OFF	OFF	ON	OFF	ON	ON	OFF	OFF



INTERNAL ACTIVE TERMINATOR
 TOP VIEW
 P/N 58NW9419A08/66432314
 OR 66432311



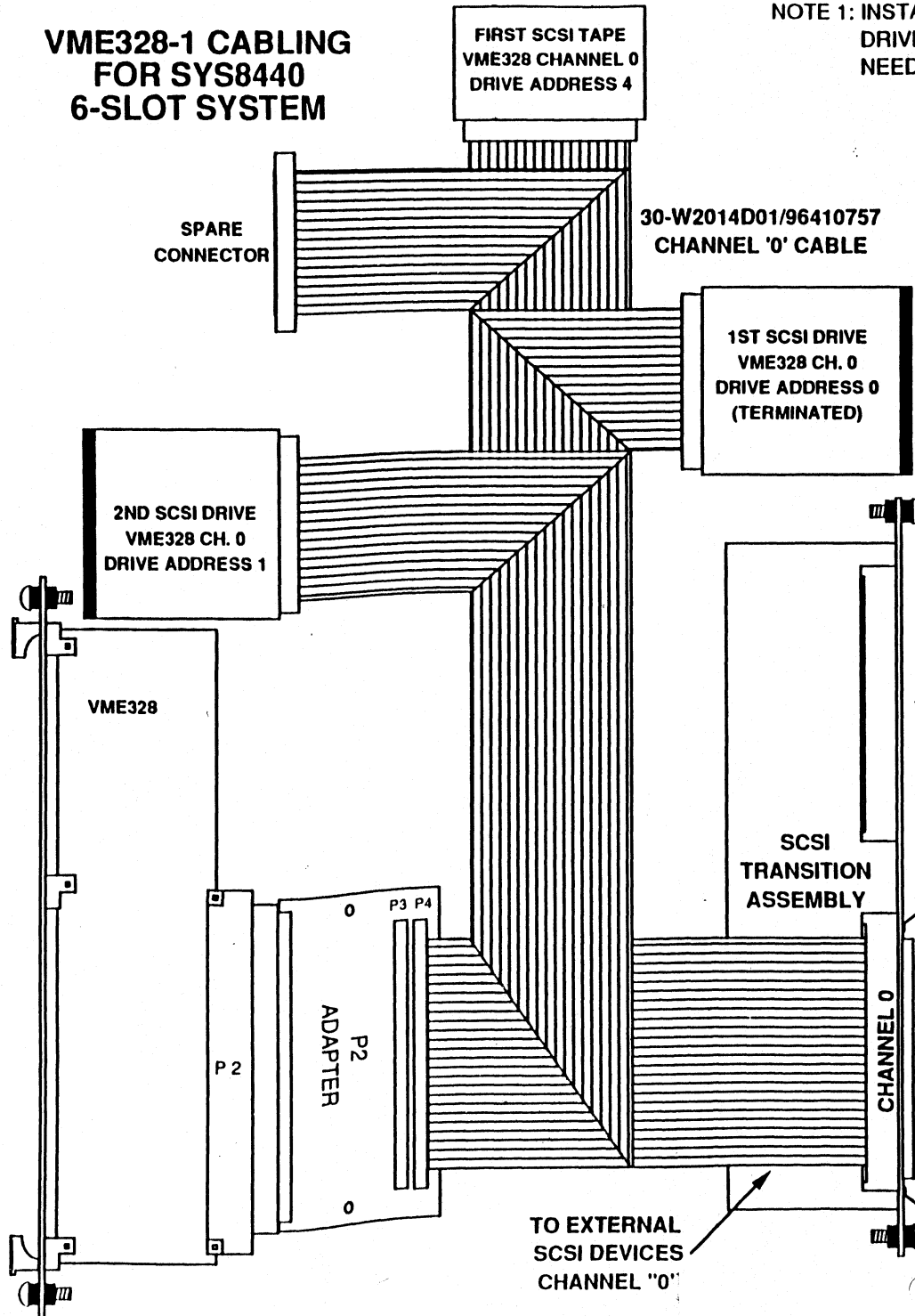
INTERNAL ACTIVE TERMINATOR
 SIDE VIEW



EXTERNAL TERMINATOR W/ GREEN LED FOR TERMINAL POWER
 P/N 58NW9419A07/66432310

**VME328-1 CABLING
FOR SYS8440
6-SLOT SYSTEM**

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL
DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS
NEEDED USE 58NW9419A08/66432314 OR 66432311.

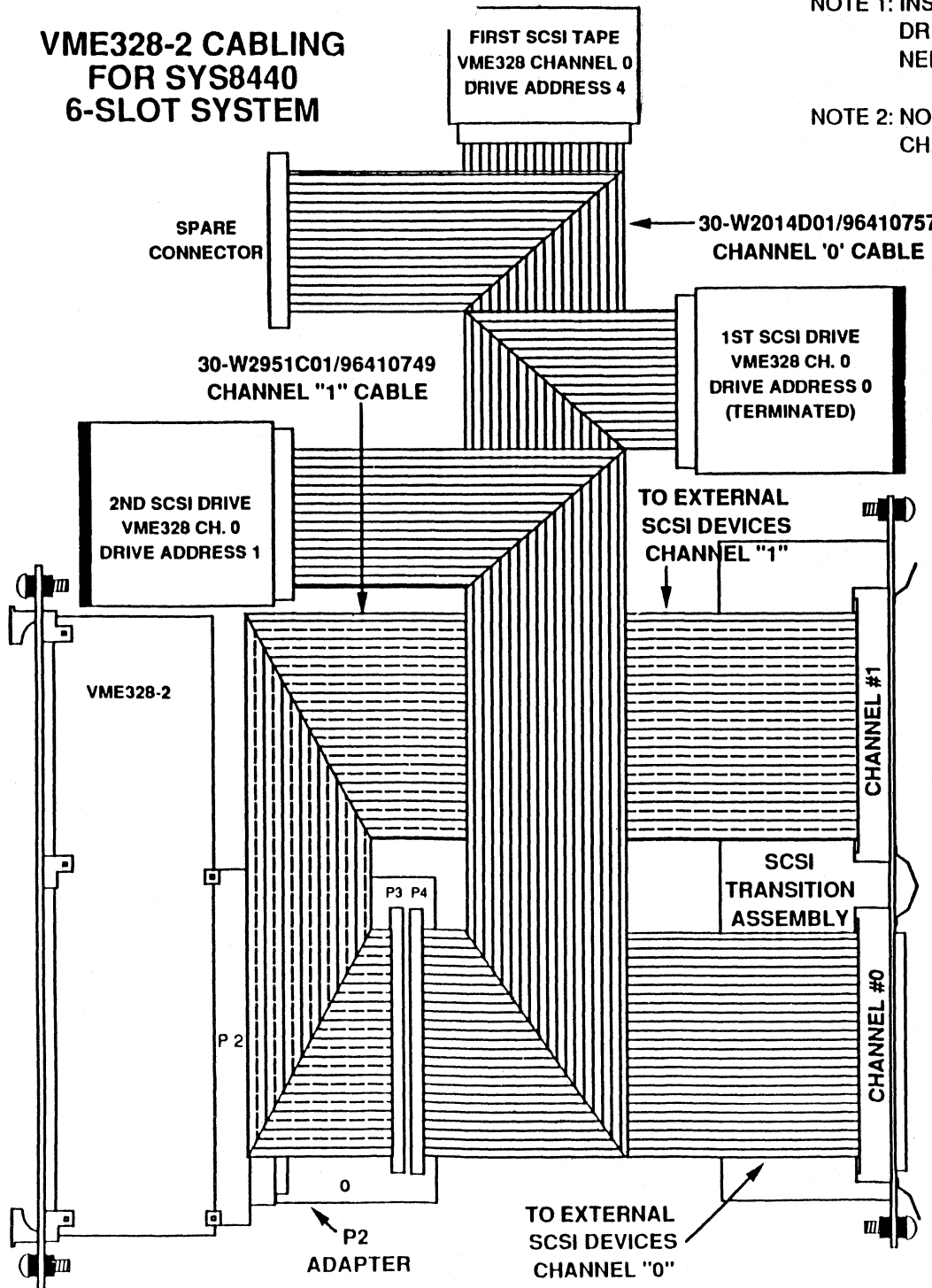


04/01/91

**VME328-2 CABLING
FOR SYS8440
6-SLOT SYSTEM**

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: NO INTERNAL CHANNEL "1" DRIVES ARE ALLOWED IN THIS CHASSIS.



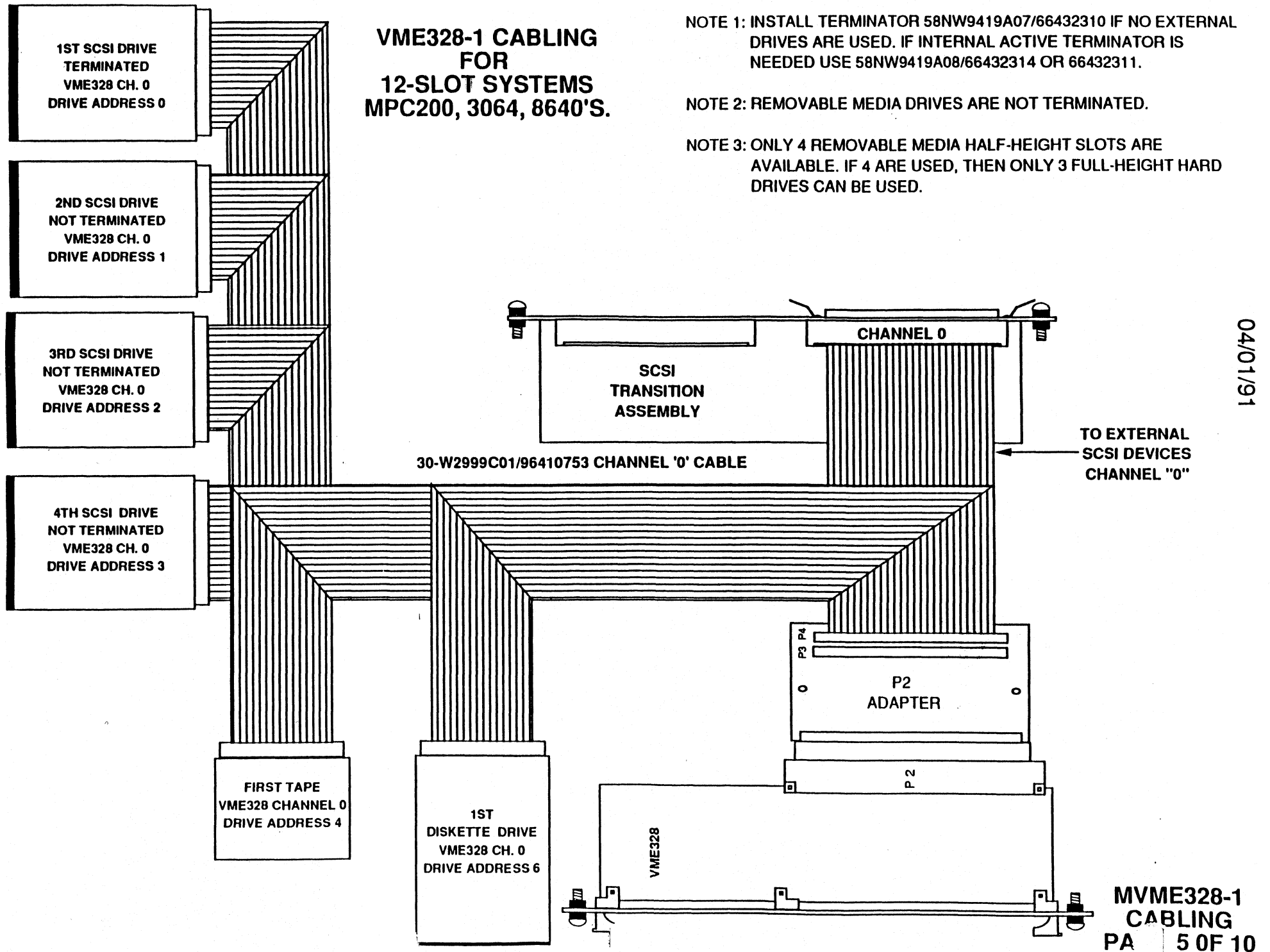
04/01/91

**VME328-1 CABLING
FOR
12-SLOT SYSTEMS
MPC200, 3064, 8640'S.**

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.



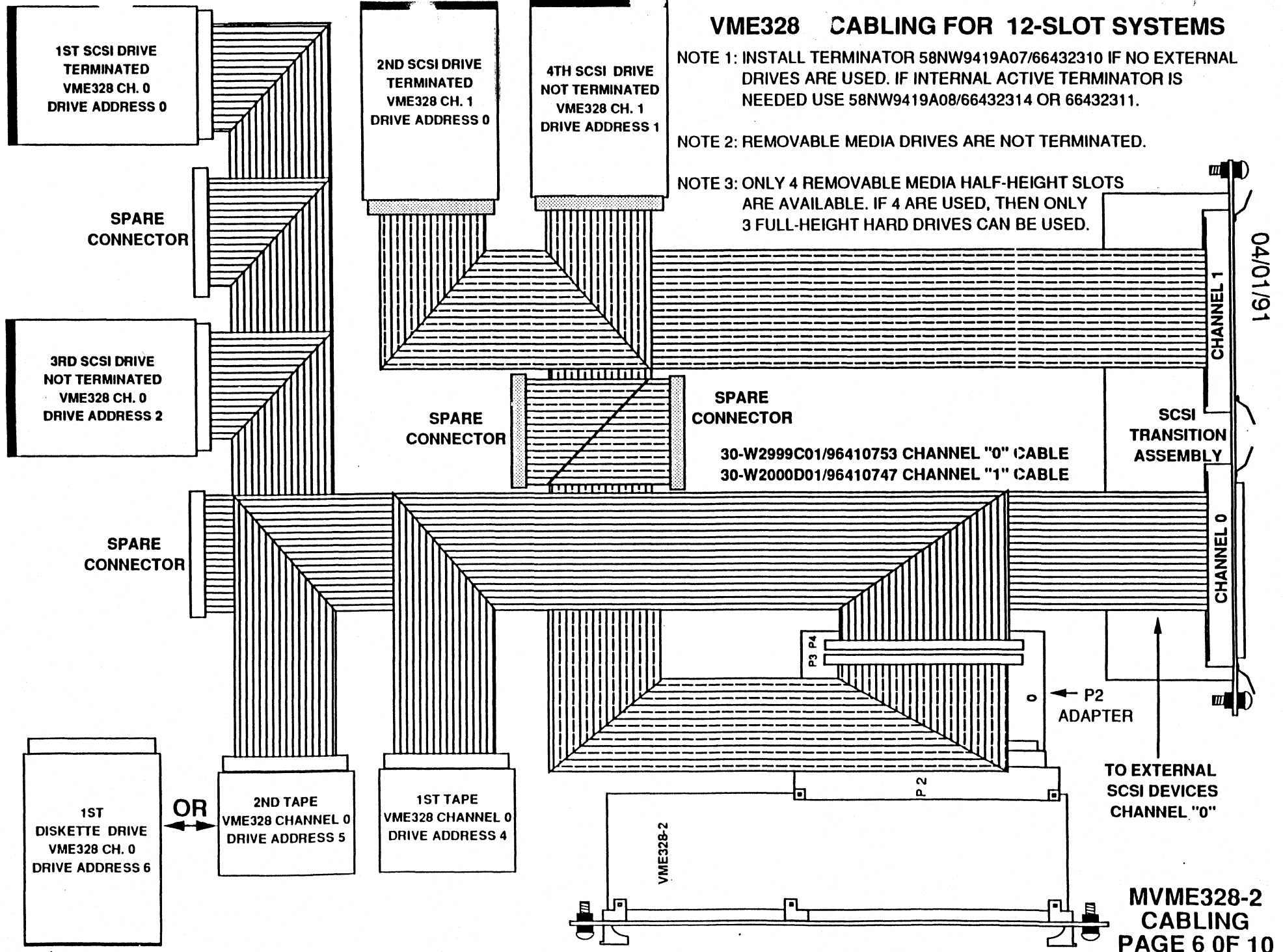
04/01/91

VME328 CABLING FOR 12-SLOT SYSTEMS

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.

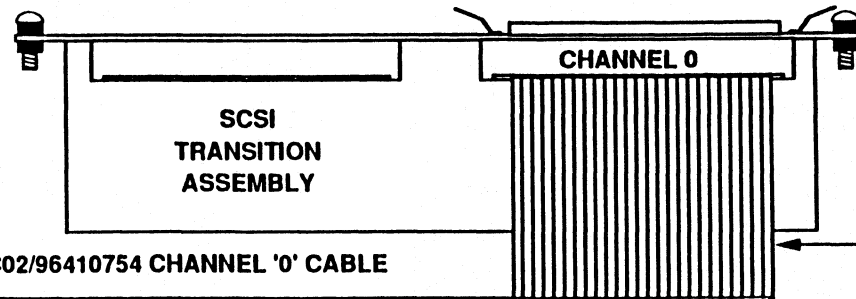
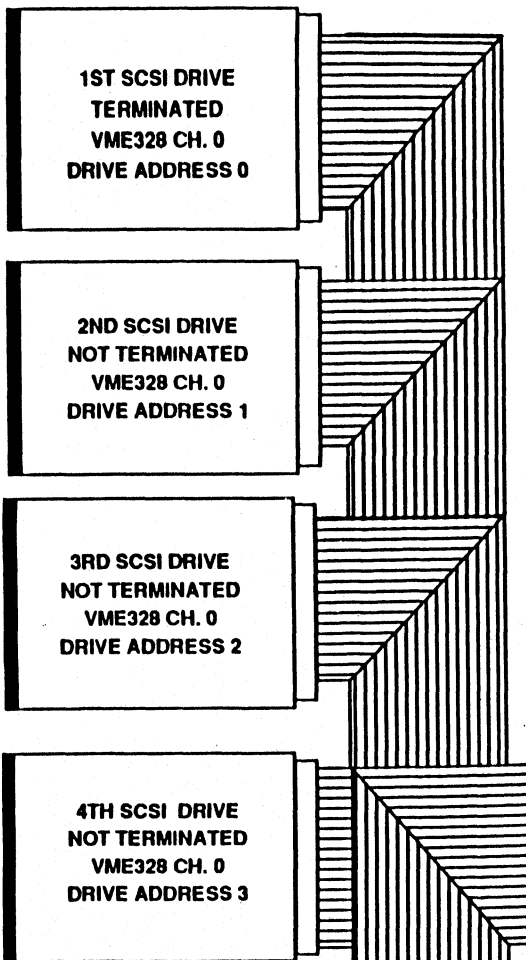


VME328-1 CABLING FOR 20-SLOT SYSTEMS W/ 2.3 GB. 8mm OPTION

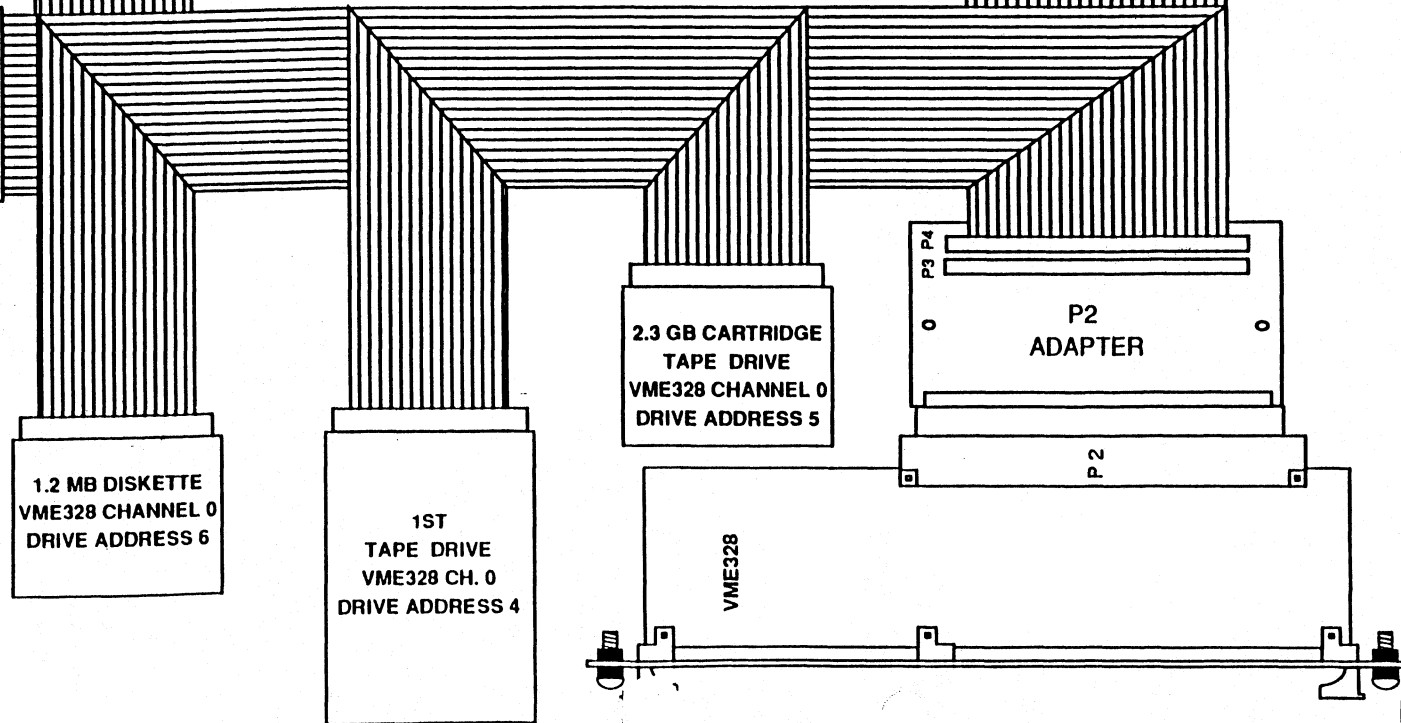
NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.



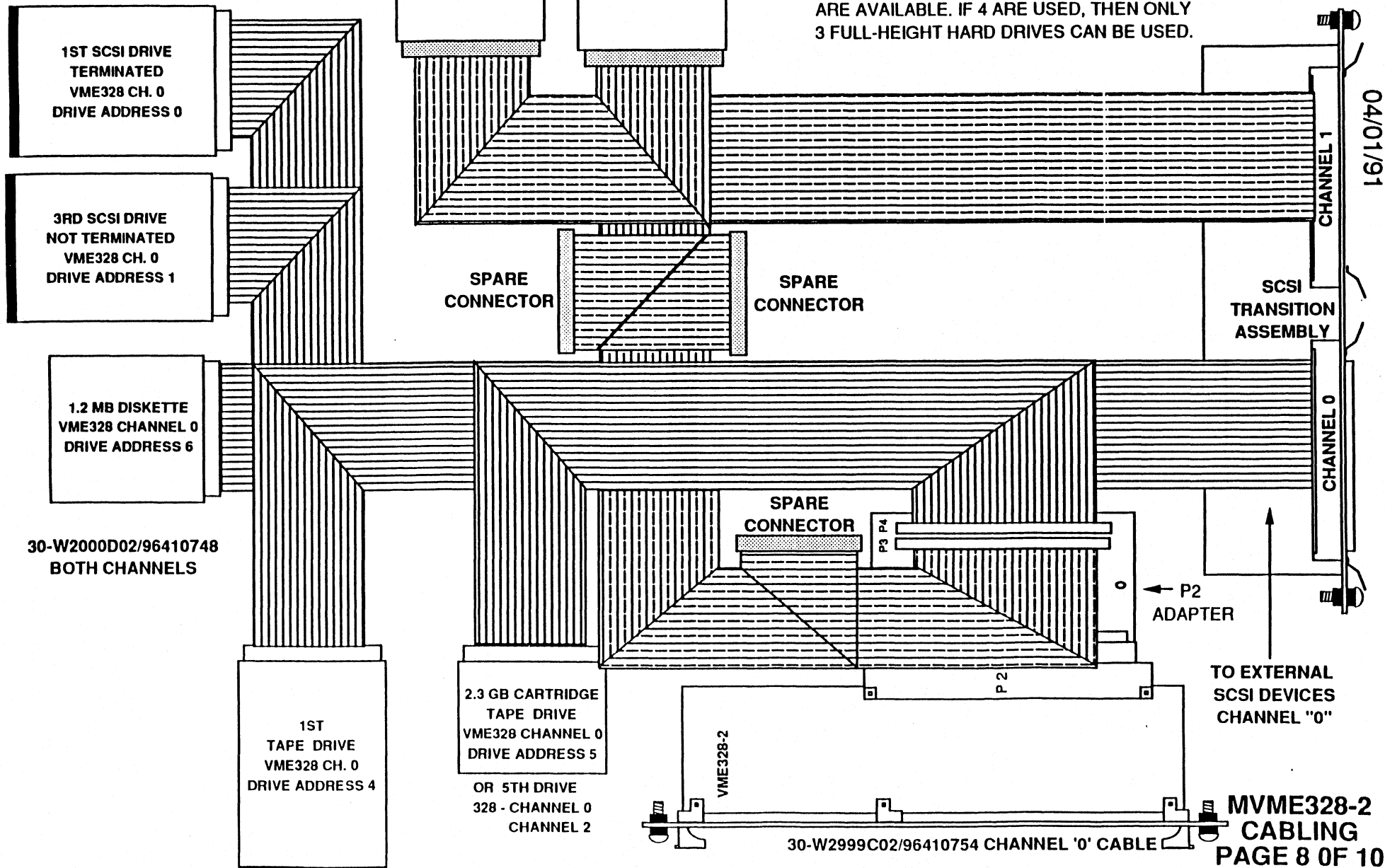
30-W2999C02/96410754 CHANNEL '0' CABLE



MVME328-1
CABLING
P/ 7 OF 10

04/01/91

**VME328-2 CABLING
FOR
20-SLOT SYSTEMS
W/ 2.3 GB. 8mm
OPTION**



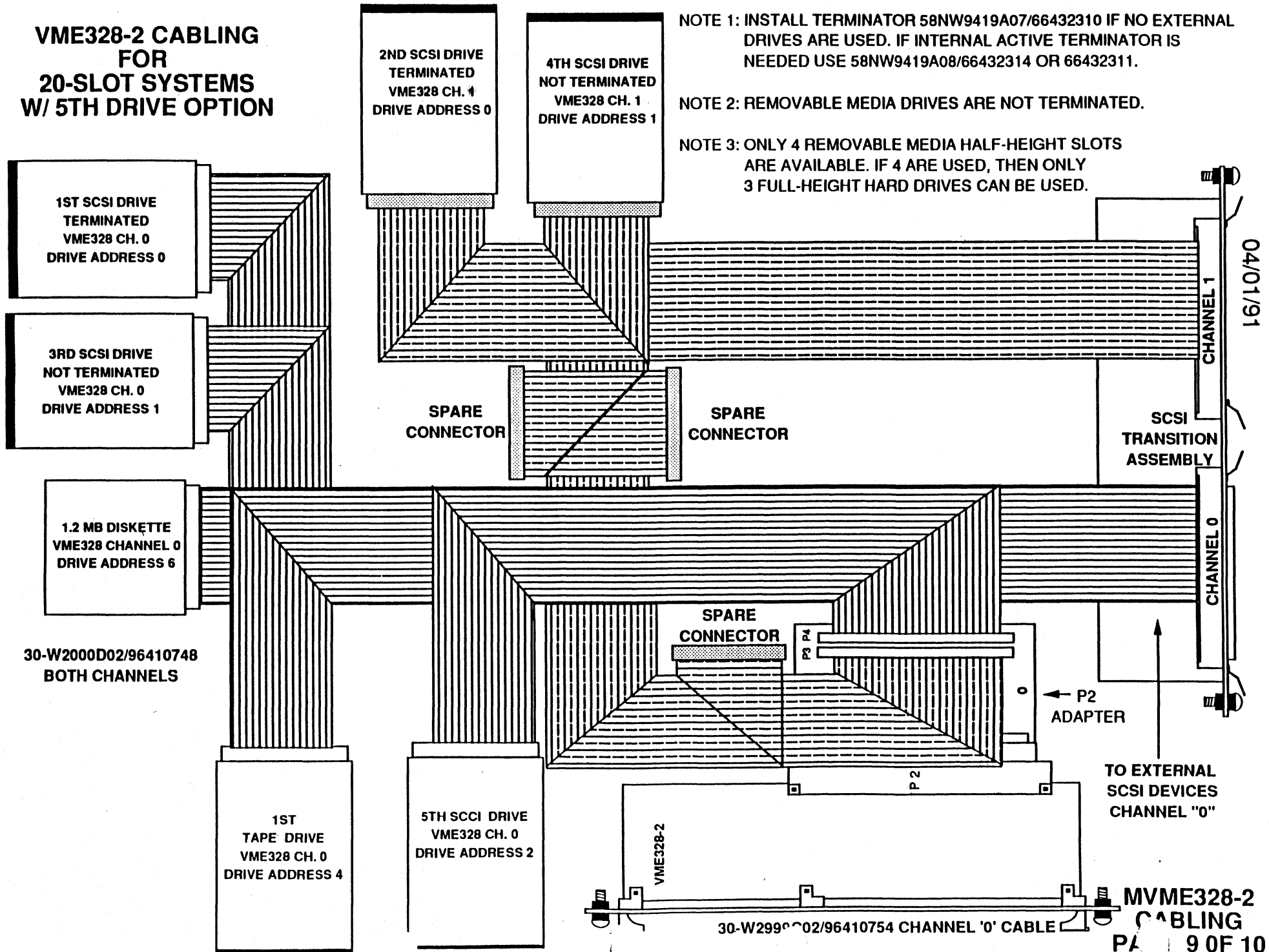
NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.

04/01/91

VME328-2 CABLING FOR 20-SLOT SYSTEMS W/ 5TH DRIVE OPTION



1ST SCSI DRIVE
TERMINATED
VME328 CH. 0
DRIVE ADDRESS 0

3RD SCSI DRIVE
NOT TERMINATED
VME328 CH. 0
DRIVE ADDRESS 1

1.2 MB DISKETTE
VME328 CHANNEL 0
DRIVE ADDRESS 6

30-W2000D02/96410748
BOTH CHANNELS

1ST
TAPE DRIVE
VME328 CH. 0
DRIVE ADDRESS 4

2ND SCSI DRIVE
TERMINATED
VME328 CH. 1
DRIVE ADDRESS 0

4TH SCSI DRIVE
NOT TERMINATED
VME328 CH. 1
DRIVE ADDRESS 1

SPARE
CONNECTOR

SPARE
CONNECTOR

SPARE
CONNECTOR

5TH SCSI DRIVE
VME328 CH. 0
DRIVE ADDRESS 2

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.

CHANNEL 1
CHANNEL 0

04/01/91

SCSI
TRANSITION
ASSEMBLY

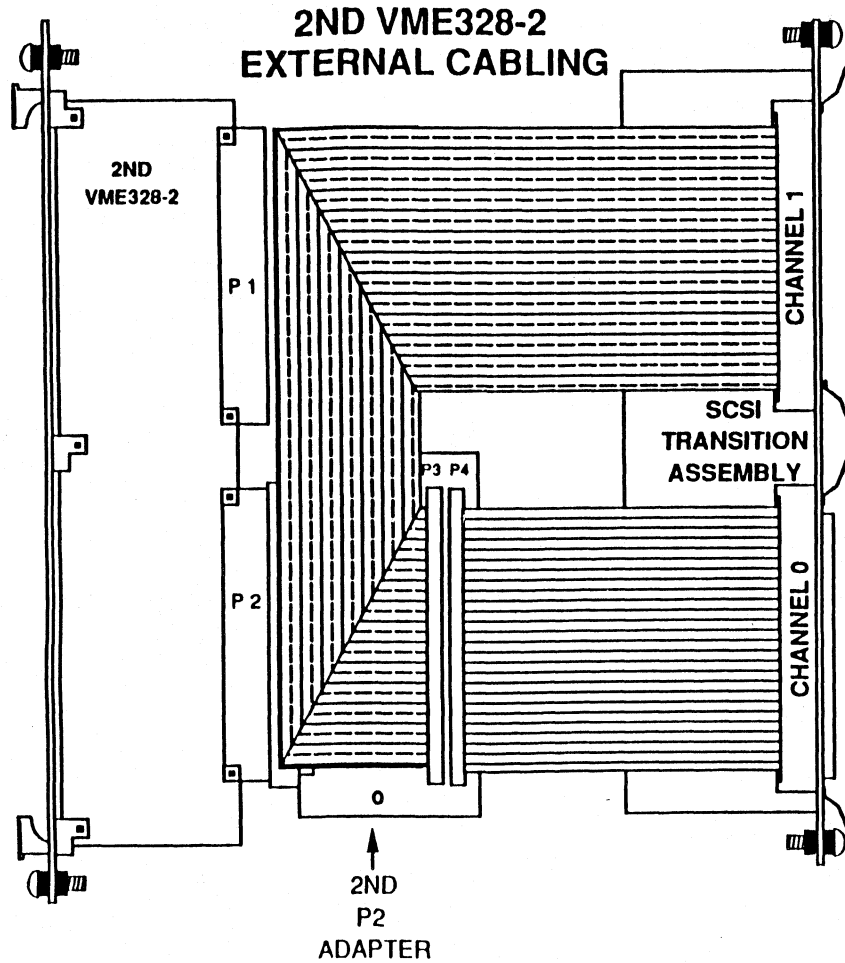
P2
ADAPTER

TO EXTERNAL
SCSI DEVICES
CHANNEL "0"

VME328-2

30-W299002/96410754 CHANNEL '0' CABLE

6-SLOT (8440); 12-SLOT (8640); 20-SLOT (8840)

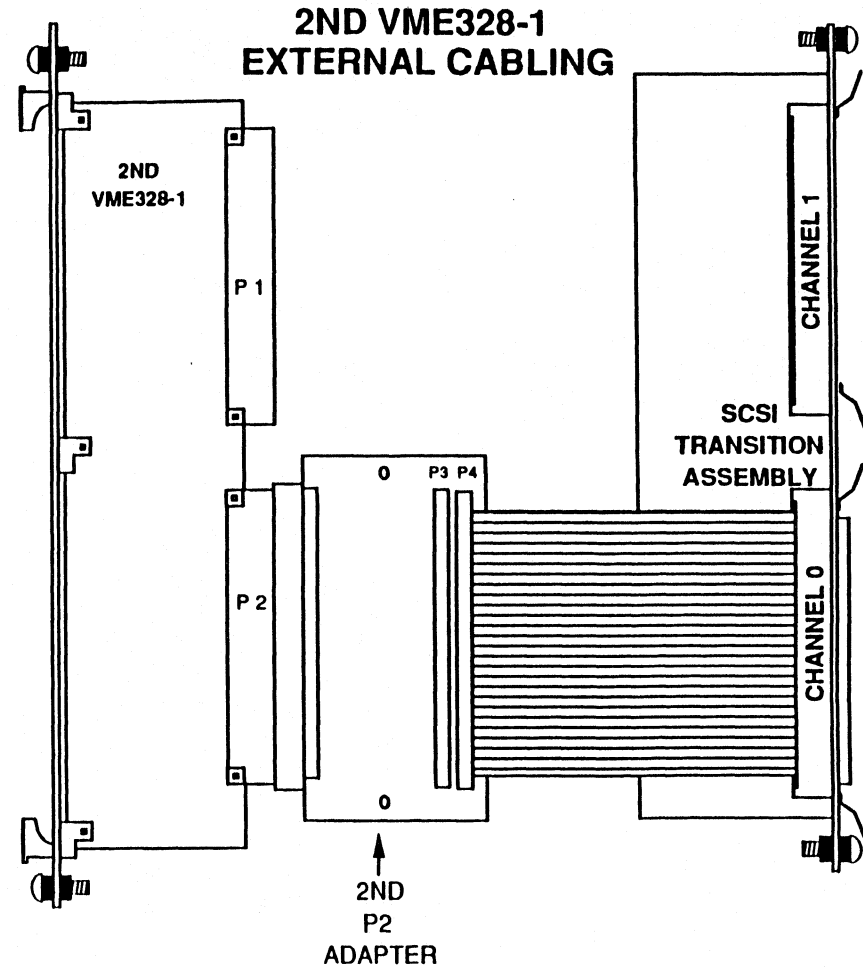


NOTE 1: SCSI CABLE IS FROM P2 ADAPTER "P3" CONNECTOR FOR CHANNEL "1". CABLE 30-W2951C01 OR C02 IS CHASSIS DEPENDENT. SEE ADDITIONAL DRAWINGS.

NOTE 2: SCSI CABLE IS FROM P2 ADAPTER "P4" CONNECTOR FOR CHANNEL "0". CABLE 30-W2951C01 OR C02 IS CHASSIS DEPENDENT. SEE ADDITIONAL DRAWINGS.

NOTE 3: TERMINATE 2ND VME328-2.

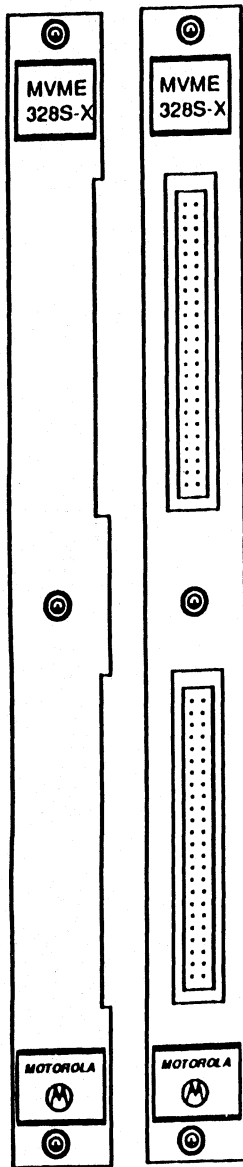
6-SLOT (8440); 12-SLOT (8640); 20-SLOT (8840)



NOTE 1: SCSI CABLE IS FROM P2 ADAPTER "P4" CONNECTOR FOR CHANNEL "0". CABLE 30-W2951C01 OR C02 IS CHASSIS DEPENDENT. SEE ADDITIONAL DRAWINGS.

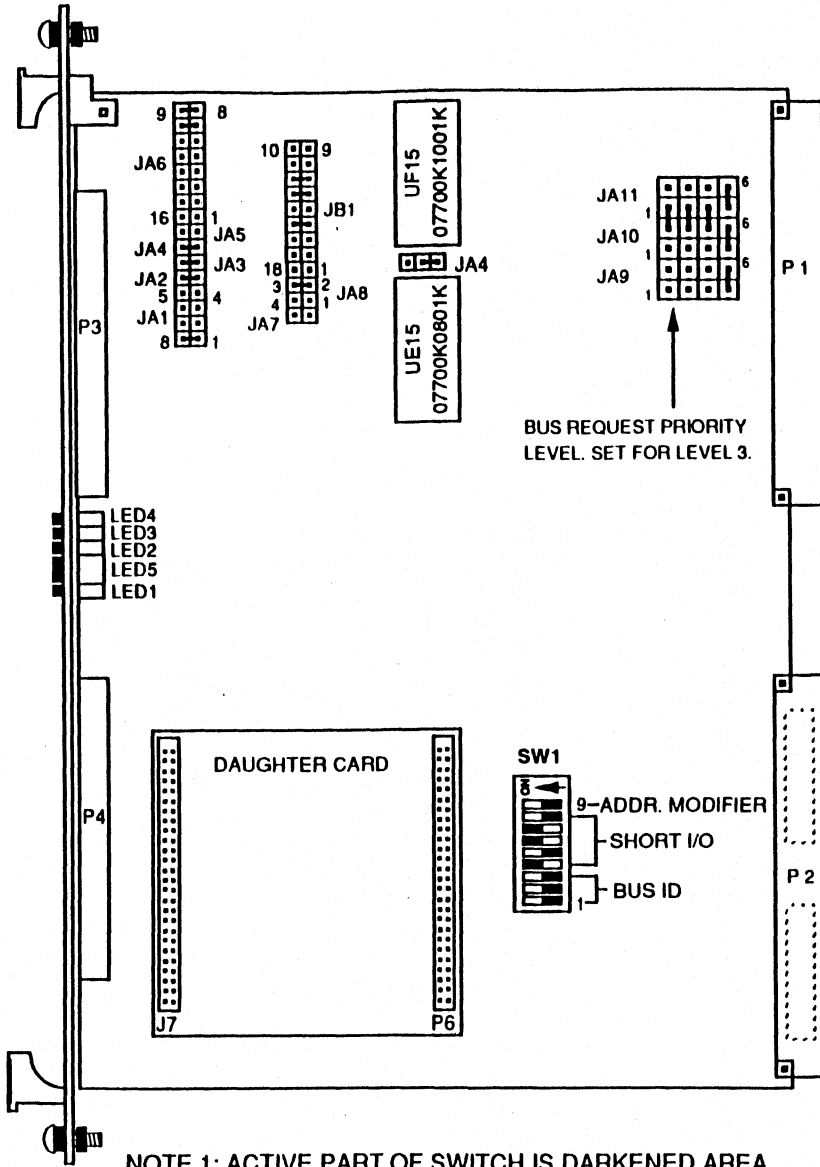
NOTE 2: TERMINATE 2ND VME328-1.

11/14/91



MVME328S-1
MVME328S-2
HANDLE

MVME328S-4
MVME328S-5
HANDLE



NOTE 1: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 2: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 3: IN THE 6-SLOT (SYS8440) CHASSIS, THE SINGLE ENDED DAUGHTER BOARD ON A VME328-2 CAN ONLY BE USED FOR EXTERNAL DEVICES, SO IT MUST BE TERMINATED.

PART NUMBERS:

MVME328S-1 01-W2220D01 TBD SINGLE CHANNEL
INTERPHASE P/N CC04210-0327

MVME328S-2 01-W2220D02 TBD DUAL CHANNEL
INTERPHASE P/N CC4210-0328

MVME328S-4 01-W220D03 96011762 DUAL CH. W/ DIFF.
INTERPHASE P/N CC04210-0358

MVME328S-5 01-W2220D04 96011763 DUAL CH. W/ DIFF.
DAUGHTER BD. = SINGLE-ENDED
INTERPHASE P/N CC04210-0357

F/W REVISION: UK8 = 07700K081K
UK10 = 07700K1001K

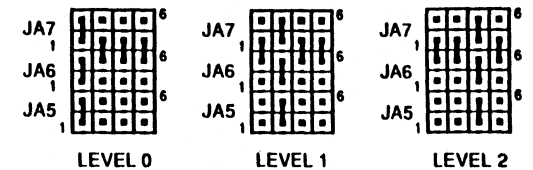
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT
REVISION INFORMATION.

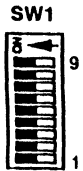
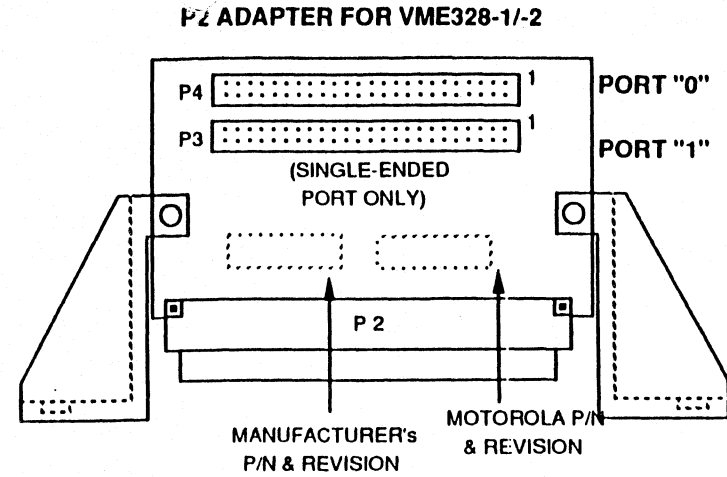
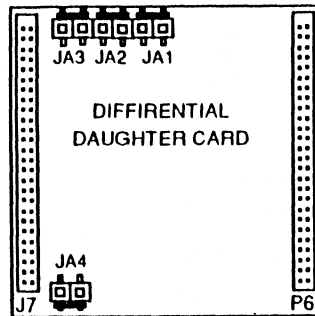
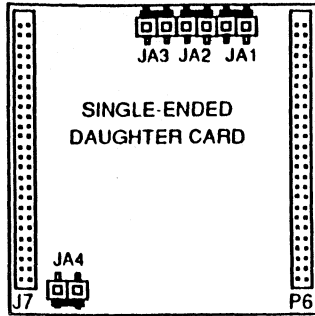
1/1/15/91

MOTOROLA P/N
& REVISION

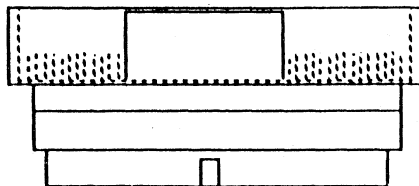
MANUFACTURER'S
P/N & REVISION

VMEBUS PRIORITY INTERRUPT SELECT

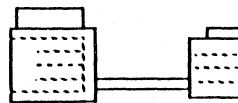




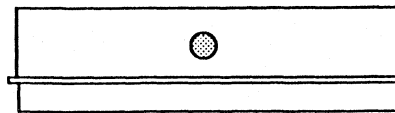
FUNCTION	PRIMARY SCSI BUS ID 7			SHORT I/O ADDR (9000)					ADDR. MOD.
	1	2	3	A11	A12	A13	A14	A15	
SWITCH #	1	2	3	4	5	6	7	8	9
SETTING	OFF	OFF	OFF	ON	OFF	ON	ON	OFF	OFF



INTERNAL ACTIVE TERMINATOR
TOP VIEW
P/N 58NW9419A08/66432314
OR 66432311



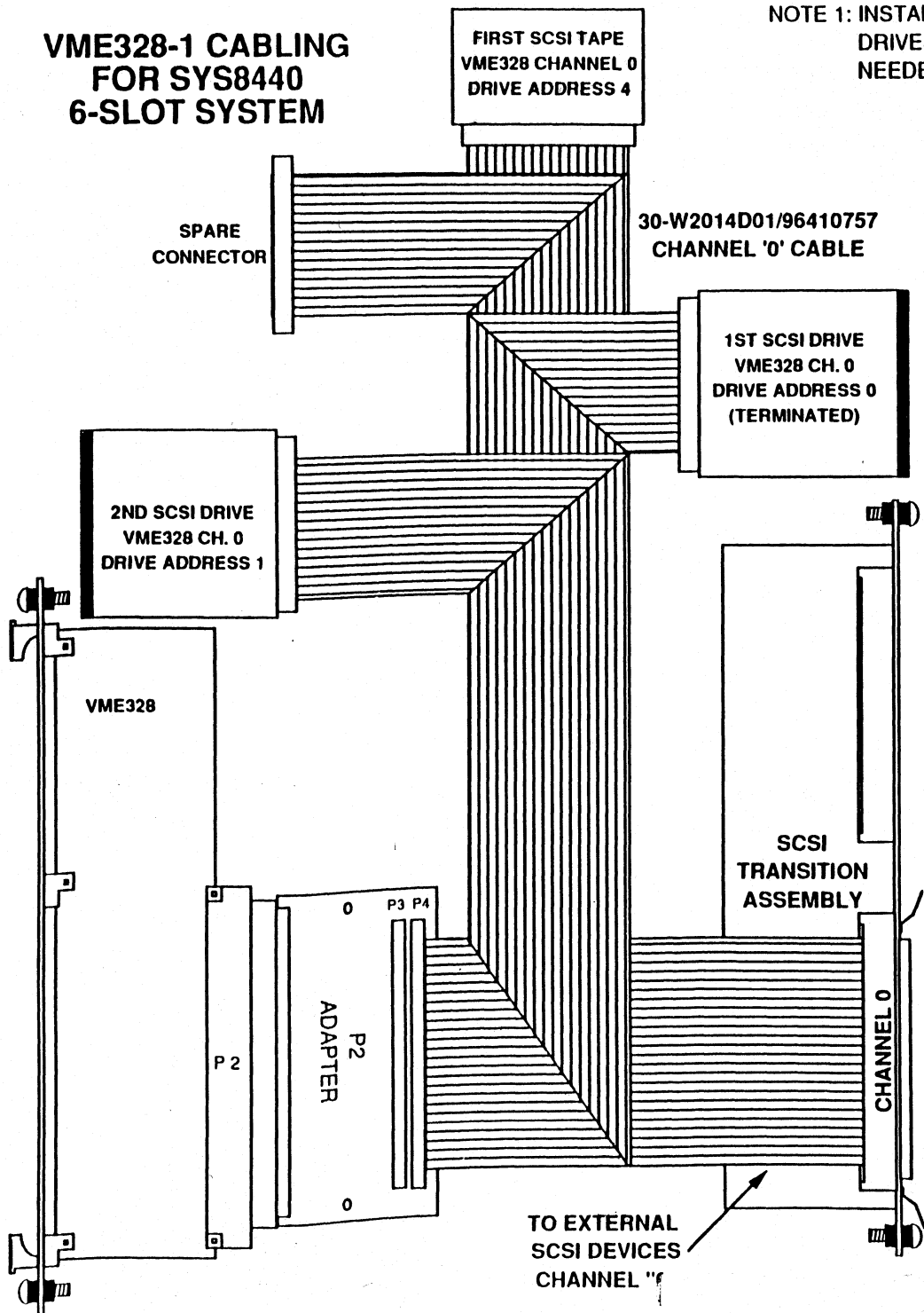
INTERNAL ACTIVE TERMINATOR
SIDE VIEW



EXTERNAL TERMINATOR W/ GREEN LED FOR TERMINAL POWER
P/N 58NW9419A07/66432310

**VME328-1 CABLING
FOR SYS8440
6-SLOT SYSTEM**

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL
DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS
NEEDED USE 58NW9419A08/66432314 OR 66432311.

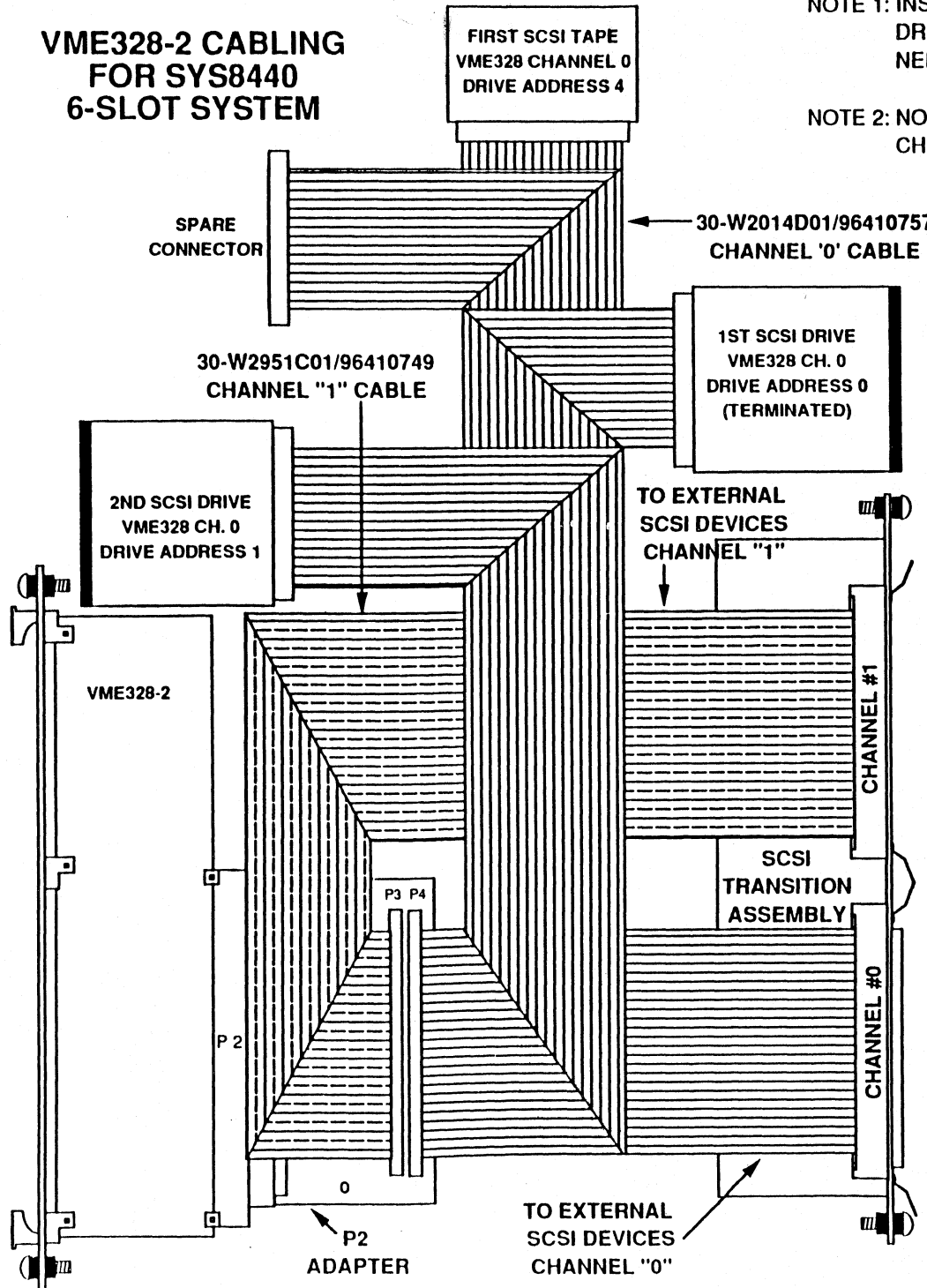


11/15/91

**VME328-2 CABLING
FOR SYS8440
6-SLOT SYSTEM**

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: NO INTERNAL CHANNEL "1" DRIVES ARE ALLOWED IN THIS CHASSIS.



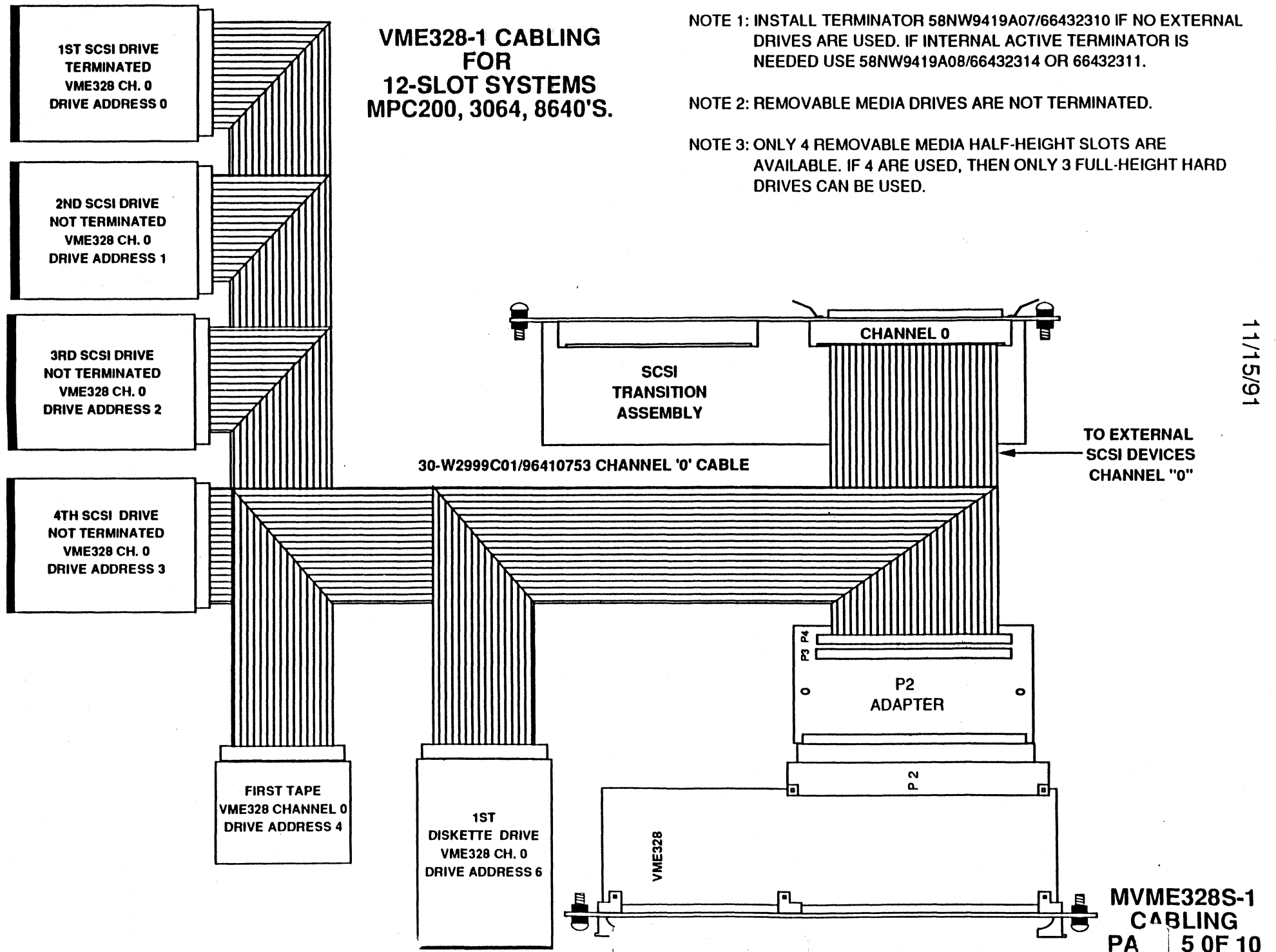
11/15/91

VME328-1 CABLING FOR 12-SLOT SYSTEMS MPC200, 3064, 8640'S.

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.



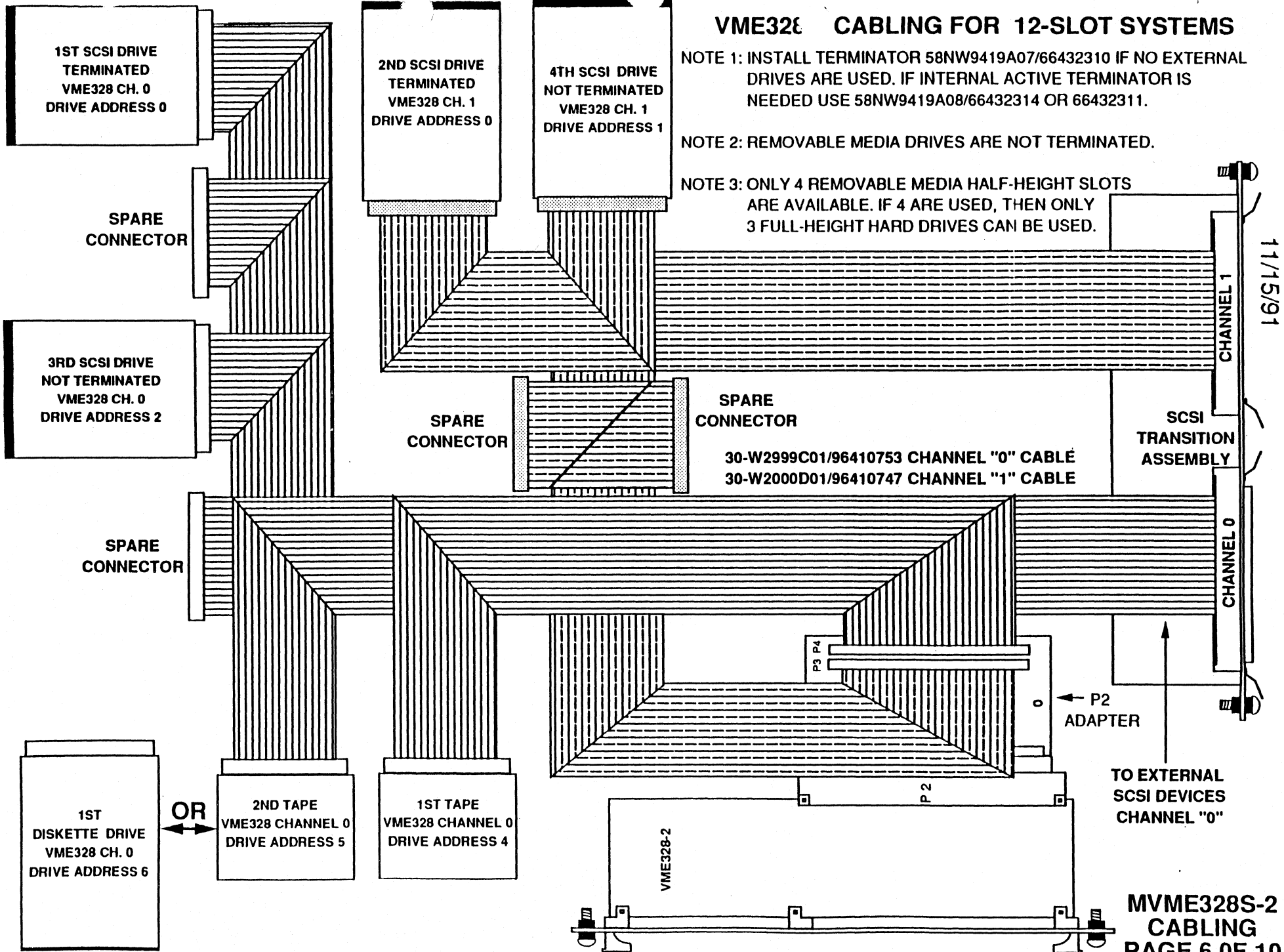
11/15/91

VME32E CABLING FOR 12-SLOT SYSTEMS

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.



11/15/91

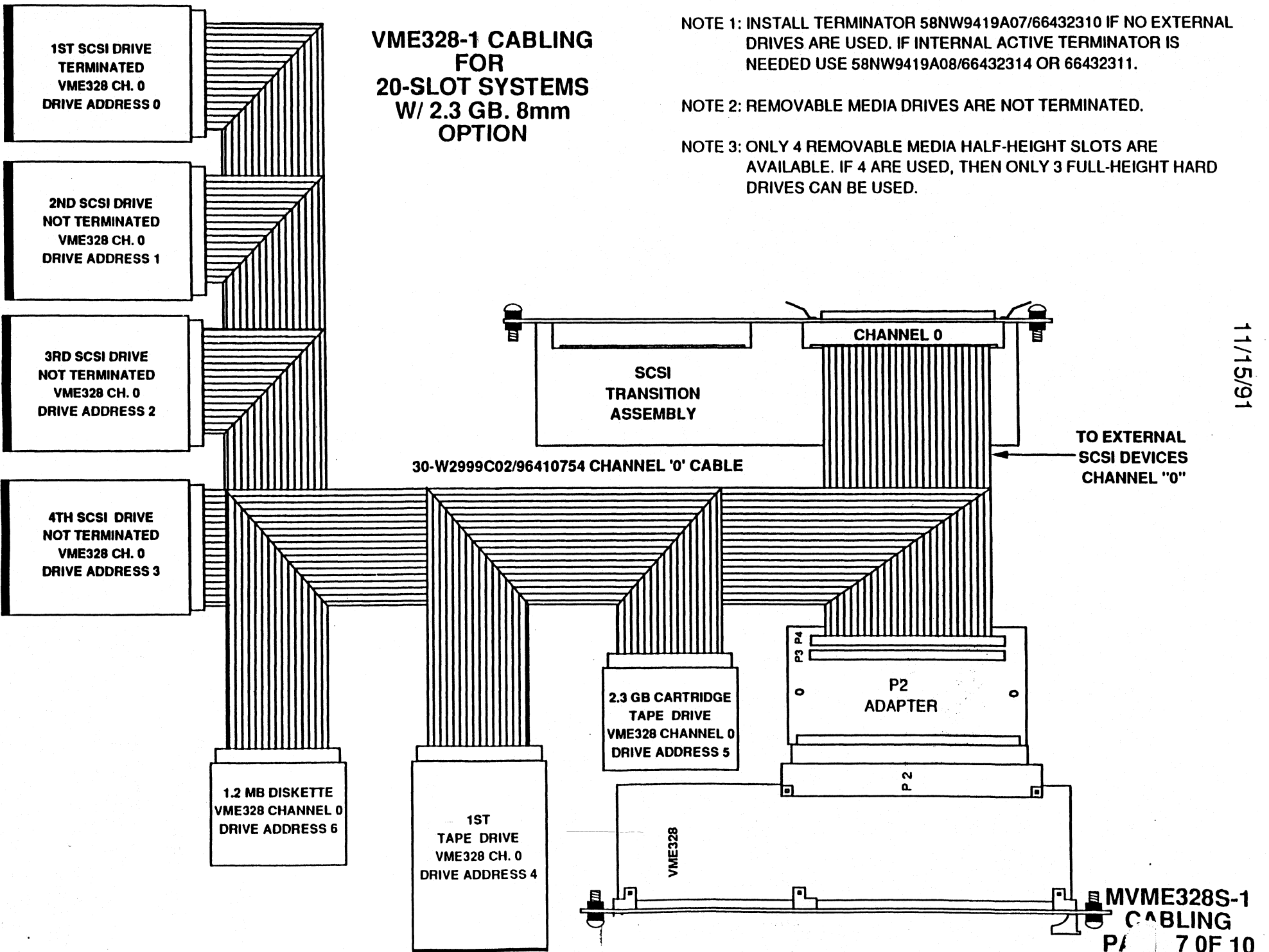
TO EXTERNAL SCSI DEVICES CHANNEL "0"

**VME328-1 CABLING
FOR
20-SLOT SYSTEMS
W/ 2.3 GB. 8mm
OPTION**

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

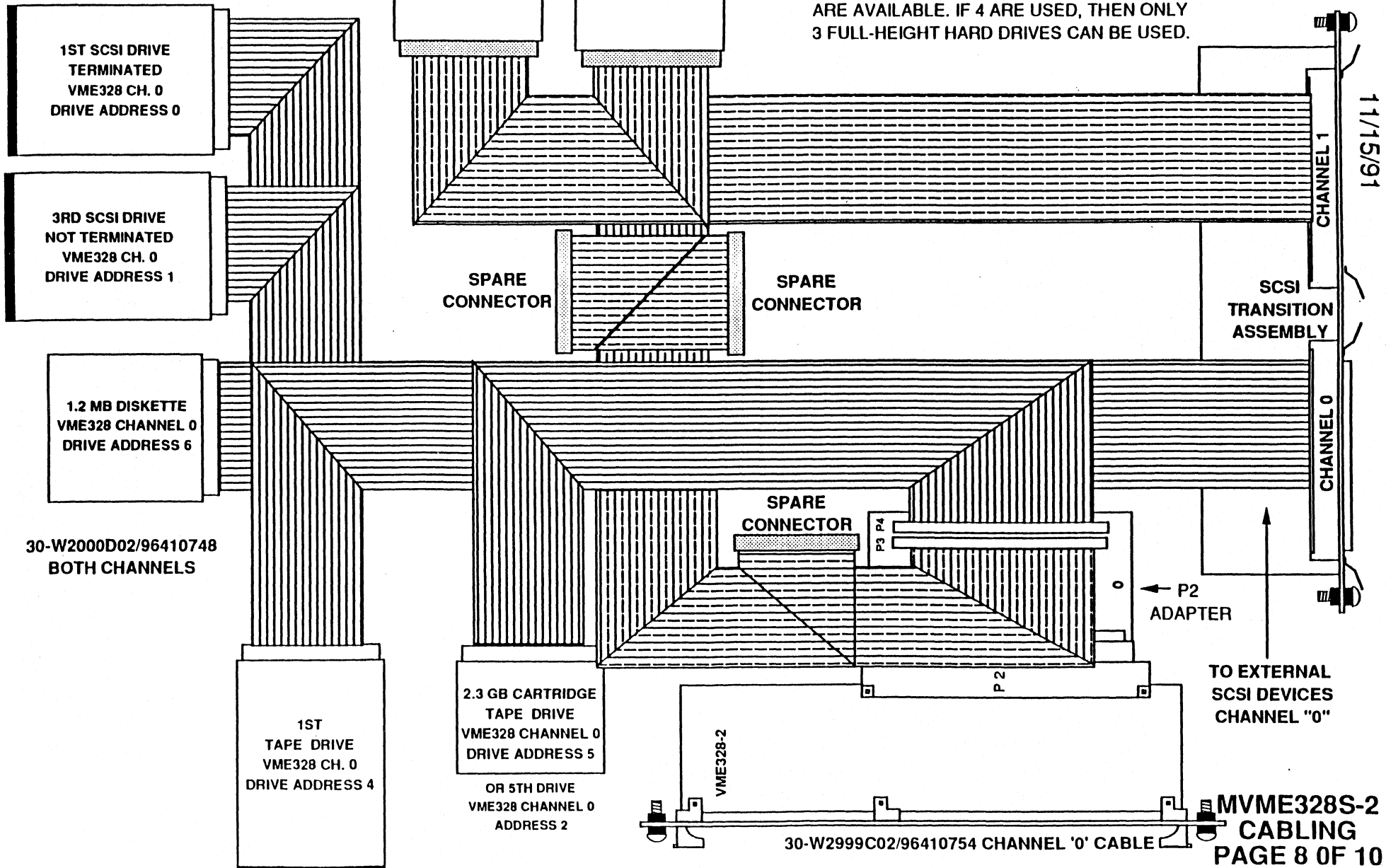
NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.

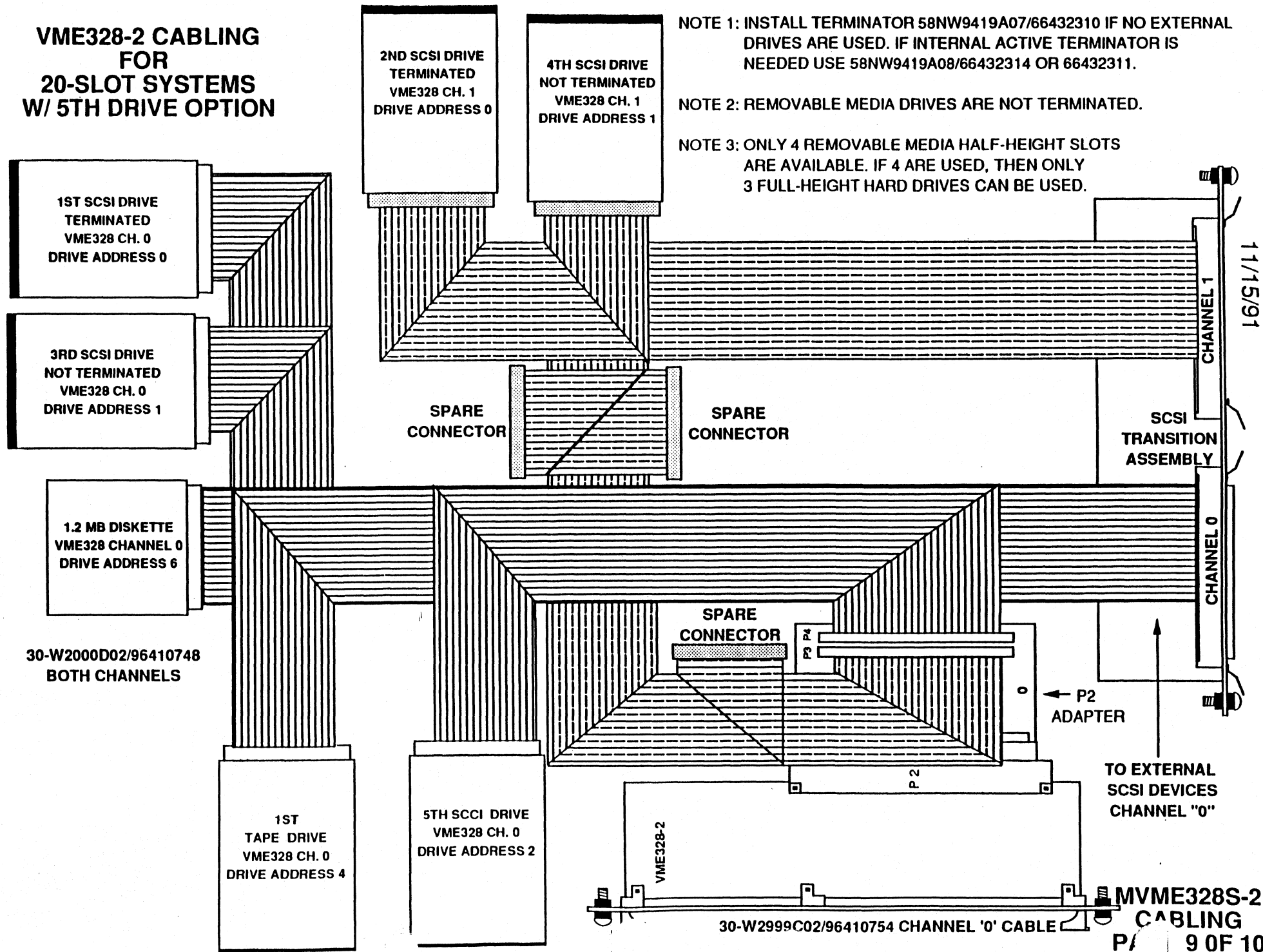


11/15/91

VME328-2 CABLING FOR 20-SLOT SYSTEMS W/ 2.3 GB. 8mm OPTION



VME328-2 CABLING FOR 20-SLOT SYSTEMS W/ 5TH DRIVE OPTION



1ST SCSI DRIVE
TERMINATED
VME328 CH. 0
DRIVE ADDRESS 0

3RD SCSI DRIVE
NOT TERMINATED
VME328 CH. 0
DRIVE ADDRESS 1

1.2 MB DISKETTE
VME328 CHANNEL 0
DRIVE ADDRESS 6

30-W2000D02/96410748
BOTH CHANNELS

1ST
TAPE DRIVE
VME328 CH. 0
DRIVE ADDRESS 4

5TH SCSI DRIVE
VME328 CH. 0
DRIVE ADDRESS 2

2ND SCSI DRIVE
TERMINATED
VME328 CH. 1
DRIVE ADDRESS 0

4TH SCSI DRIVE
NOT TERMINATED
VME328 CH. 1
DRIVE ADDRESS 1

SPARE
CONNECTOR

SPARE
CONNECTOR

SPARE
CONNECTOR

VME328-2

30-W2999C02/96410754 CHANNEL '0' CABLE

NOTE 1: INSTALL TERMINATOR 58NW9419A07/66432310 IF NO EXTERNAL DRIVES ARE USED. IF INTERNAL ACTIVE TERMINATOR IS NEEDED USE 58NW9419A08/66432314 OR 66432311.

NOTE 2: REMOVABLE MEDIA DRIVES ARE NOT TERMINATED.

NOTE 3: ONLY 4 REMOVABLE MEDIA HALF-HEIGHT SLOTS ARE AVAILABLE. IF 4 ARE USED, THEN ONLY 3 FULL-HEIGHT HARD DRIVES CAN BE USED.

CHANNEL 1

CHANNEL 0

SCSI
TRANSITION
ASSEMBLY

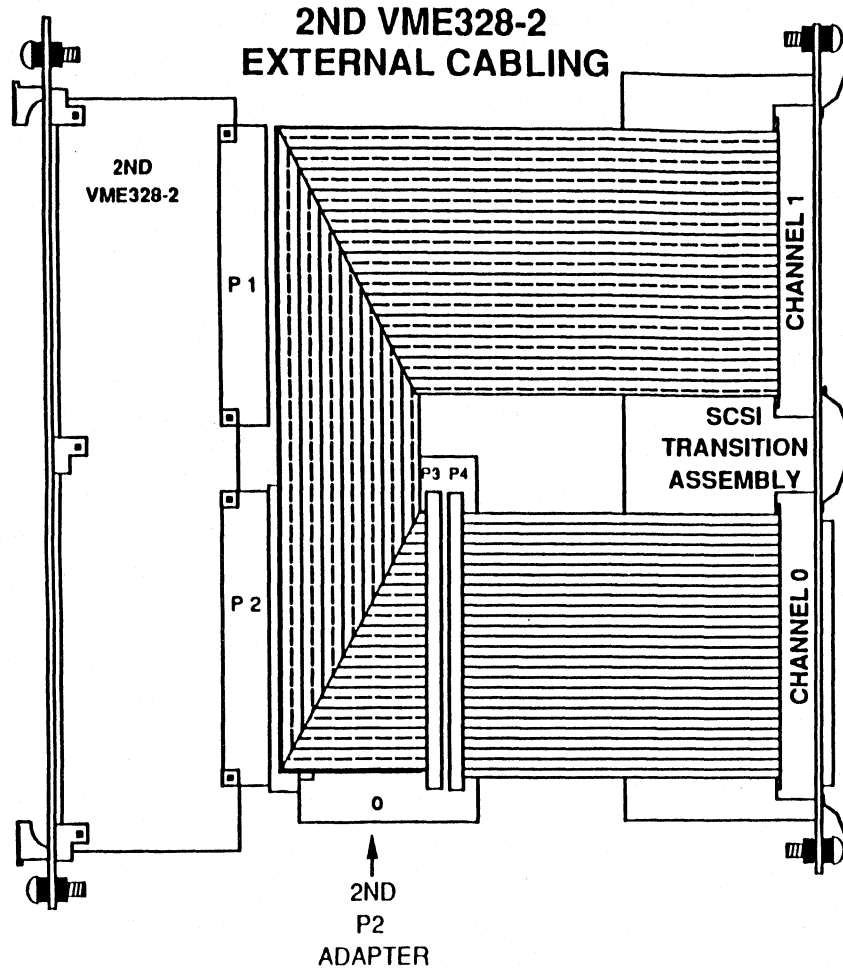
P2
ADAPTER

TO EXTERNAL
SCSI DEVICES
CHANNEL "0"

MVME328S-2
CABLING
P/ 9 OF 10

11/15/91

6-SLOT (8440); 12-SLOT (8640); 20-SLOT (8840)

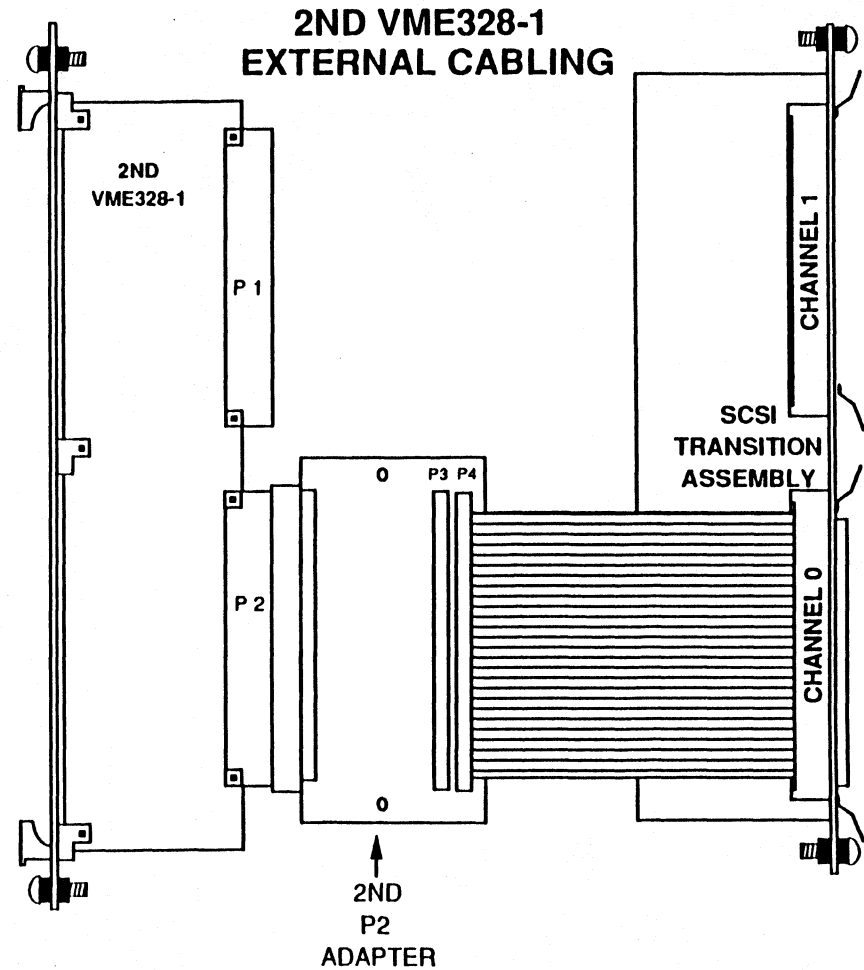


NOTE 1: SCSI CABLE IS FROM P2 ADAPTER "P3" CONNECTOR FOR CHANNEL "1". CABLE 30-W2951C01 OR C02 IS CHASSIS DEPENDENT. SEE ADDITIONAL DRAWINGS.

NOTE 2: SCSI CABLE IS FROM P2 ADAPTER "P4" CONNECTOR FOR CHANNEL "0". CABLE 30-W2951C01 OR C02 IS CHASSIS DEPENDENT. SEE ADDITIONAL DRAWINGS.

NOTE 3: TERMINATE 2ND VME328-2.

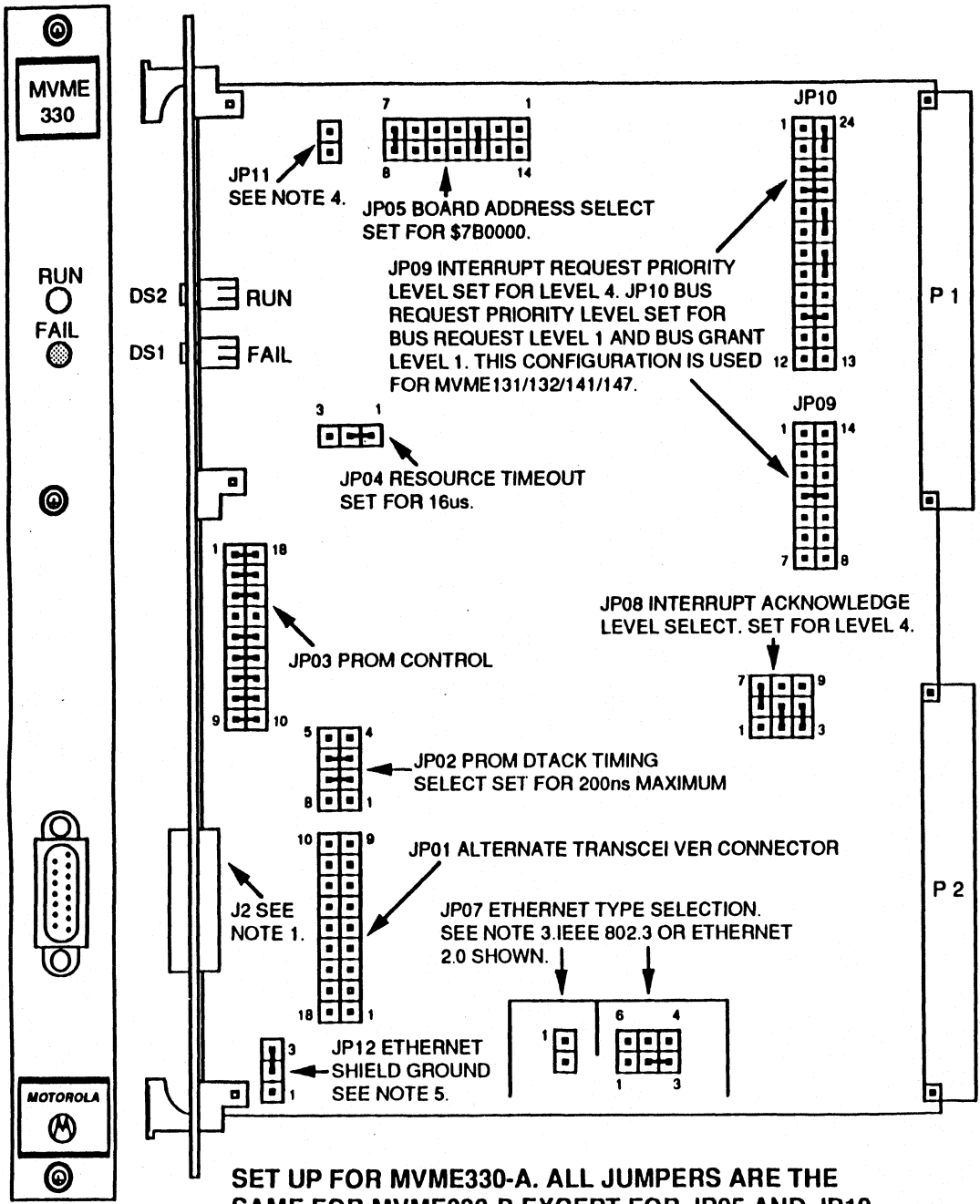
6-SLOT (8440); 12-SLOT (8640); 20-SLOT (8840)



NOTE 1: SCSI CABLE IS FROM P2 ADAPTER "P4" CONNECTOR FOR CHANNEL "0". CABLE 30-W2951C01 OR C02 IS CHASSIS DEPENDENT. SEE ADDITIONAL DRAWINGS.

NOTE 2: TERMINATE 2ND VME328-1.

11/15/91



SET UP FOR MVME330-A. ALL JUMPERS ARE THE SAME FOR MVME330-B EXCEPT FOR JP05 AND JP10. SEE PAGE 2 FOR JUMPER SETTINGS.

PART NUMBERS:

MVME330K-A 01-W2821B01 96010826

MVME330K-B 01-W3321B03 96010891

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: DO NOT USE J2 IF JP01 IS USED.

NOTE 2: JP06 IS NOT USED. NO MORE INFORMATION IS AVAILABLE.

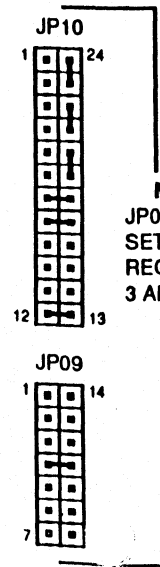
NOTE 3: FOR JP07 ARTWORK REV. C OR EARLIER HAS A 6 POSITION JUMPER. ARTWORK D OR LATER HAS ONLY A 2 POSITION JUMPER.

NOTE 4: JP11 HAS BEEN REPLACED BY A HARD WIRE.

NOTE 5: FOR JP12, J2 PIN 1 SETTING FOR IEEE 802.3, ETHERNET 2.0 SHOWN. THIS JUMPER IS PRESENT FOR ARTWORK REV. "D" OR LATER. THERE IS NO HEADER FOR AW "C" OR OLDER.

NOTE 6 : SAME CONFIGURATION FOR SYS3400, 3604/08, 3640 & 8400's.

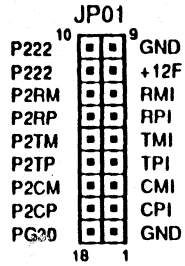
NOTE 7 : VME330-B's JP5 AND JP10 ARE DIFFERENT. SEE PAGE 2.



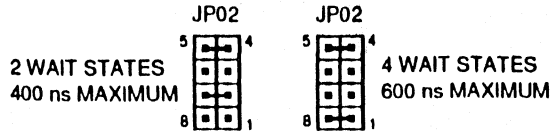
MVME134 CONFIGURATION
 JP09 INTERRUPT PRIORITY LEVEL SET FOR LEVEL 4. JP10 BUS REQUEST LEVEL SET FOR LEVEL 3 AND BUS GRANT ALSO LEVEL 3.

02/26/90

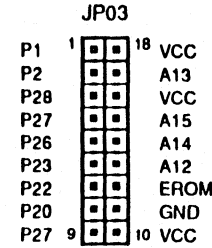
ALTERNATE TRANSCEIVER CONNECTOR



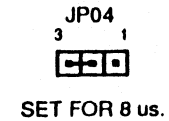
PROM DTAC...MING SELECT



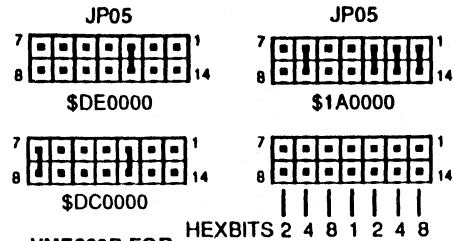
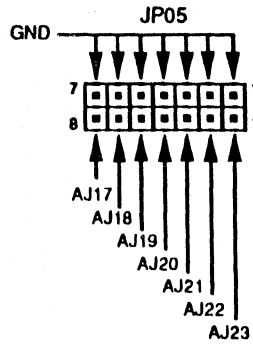
PROM CONTR



RESOURCE TIMEOUT



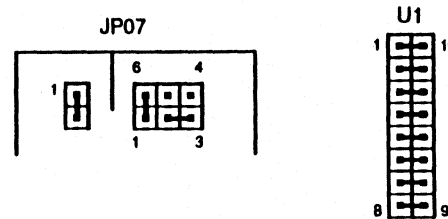
BOARD ADDRESS SELECT



VME330B FOR SYS3604/08's

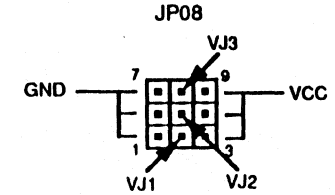
JUMPER INSTALLED DESELECTS THE LINE.

ETHERNET TYPE SELECTION

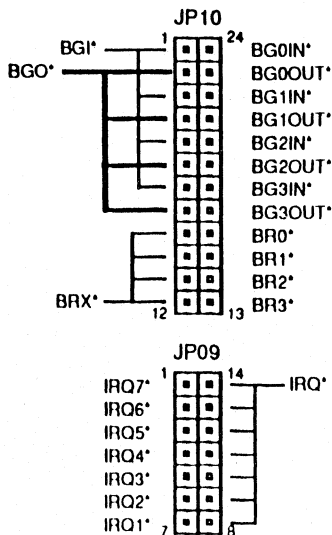


ARTWORK REV. C OR EARLIER HAS 6 POSITION JUMPER. ARTWORK D OR LATER HAS A 2 POSITION JUMPER. THIS IS CONFIGURED FOR ETHERNET 1.0. U1 SHOULD HAVE THE TRANSFORMER REMOVED AND SHOULD BE CONFIGURED AS ABOVE.

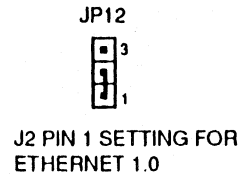
INTERRUPT ACKNOWLEDGE LEVEL



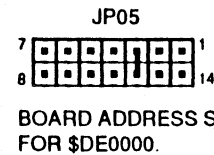
**J9 INTERRUPT REQUEST PRIORITY LEVEL
J10 BUS REQUEST PRIORITY LEVEL**



ETHERNET SHIELD GROUND



BOARD ADDRESS SELECT



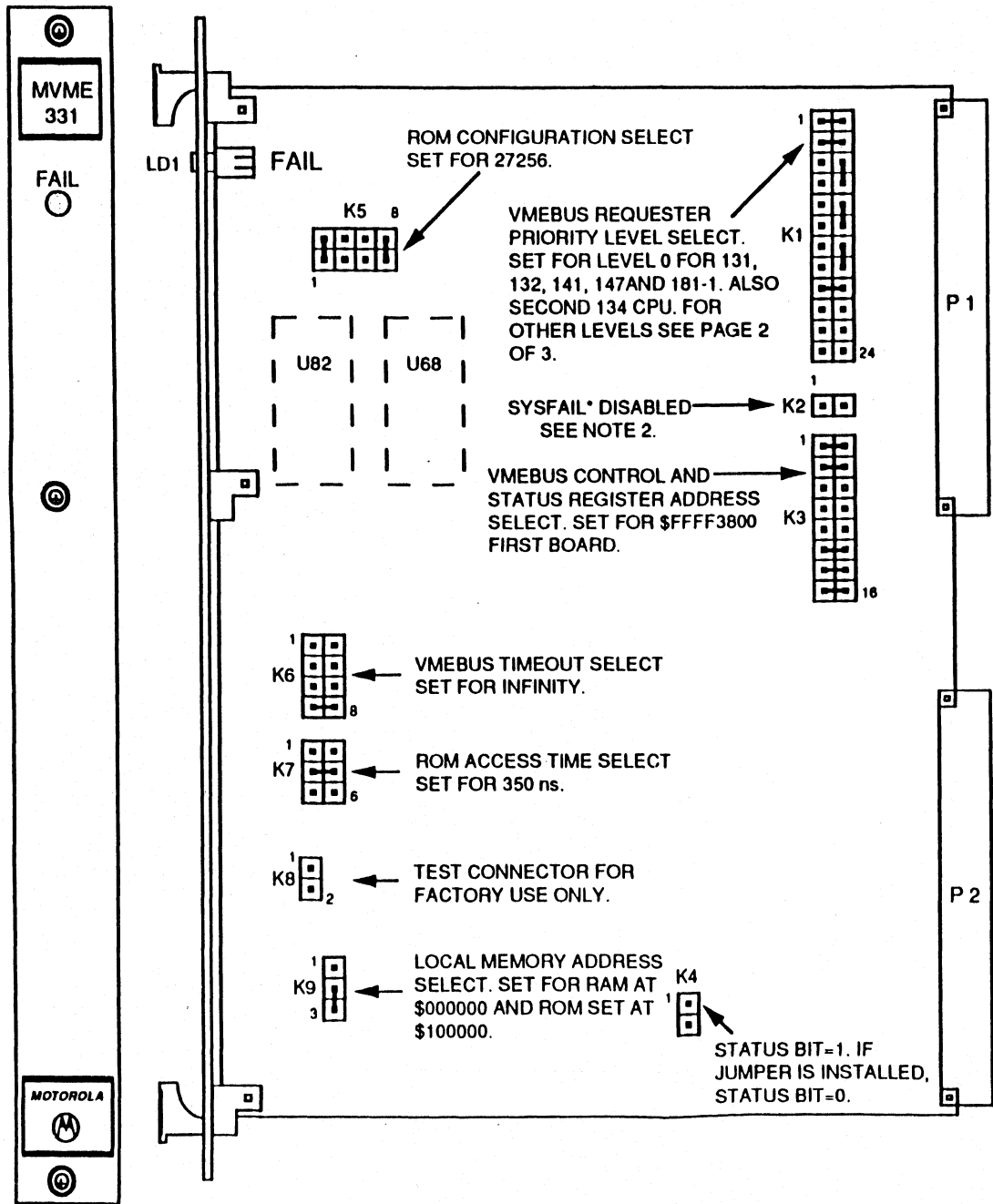
MVME330-B JUMPER DIFFERENCES.

BUS REQUEST PRIORITY LEVEL



SET FOR PRIORITY LEVEL 3. FOR SYS3604/08, & 3640's.

11/28/89



PART NUMBERS:

MVME331 01-G3027M01 76433077 (MUC. PWB)
 01-W3503B01 96010957/0861 (US PWB)

MVME331-2 01-G3027M03 NONE (MUC. PWB)

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: FIRMWARE INSTALLED DEFINES BOARD TYPE AND USE. (i.e. MVME331 .)

MVME331 REV. 1.4

U68 * 51AW4815B40 (44473G01)
 U82 * 51AW4815B39 (44472G01)

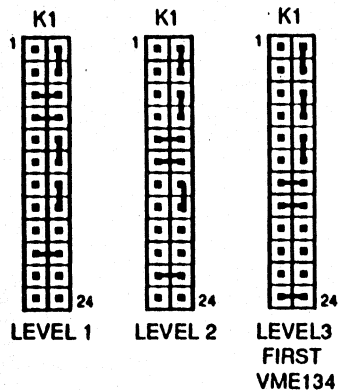
* MAY HAVE HUNTER/READY CHIPS.

NOTE 2: K2 IS OUT FOR THE FOLLOWING SYSTEMS: SYS2616, SYS330X, SYS36X0 AND SYS38X0.

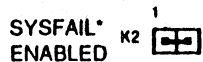
NOTE 3: SAME CONFIGURATION FOR SYS1147, 3200, 3400, 3604/08, 3640, 8400 & 8608's.

04/08/91

VMEBUS REQUEST LEVEL SELECT



SYSTEM FAIL OUTPUT SELECT



VME CONNECTION AND STATUS REGISTER SELECT

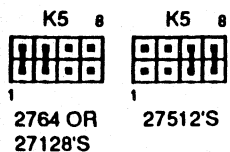
K3 CONNECTION BASE ADDRESS
OFFSET

A15	1-2	REMOVED	\$8000	1ST BOARD
A14	3-4	REMOVED	\$4000	2ND BOARD
A13	5-6	REMOVED	\$2000	3RD BOARD
A12	7-8	REMOVED	\$1000	4TH BOARD
A11	9-10	REMOVED	\$0800	5TH BOARD
A10	11-12	REMOVED	\$0400	6TH BOARD
A09	13-14	REMOVED	\$0200	7TH BOARD
A08	15-16	REMOVED	\$0100	8TH BOARD

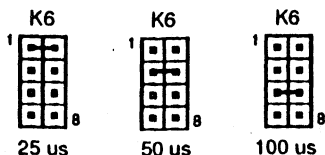
AXX=0, IF JUMPER IS INSTALLED
AXX=1, IF JUMPER IS REMOVED

2ND BD. SYS3604/08
\$3900 2ND BOARD

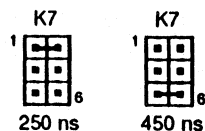
ROM CONFIGURATION SELECT



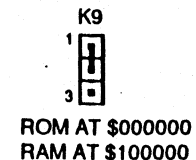
VMEBUS TIMEOUT SELECT



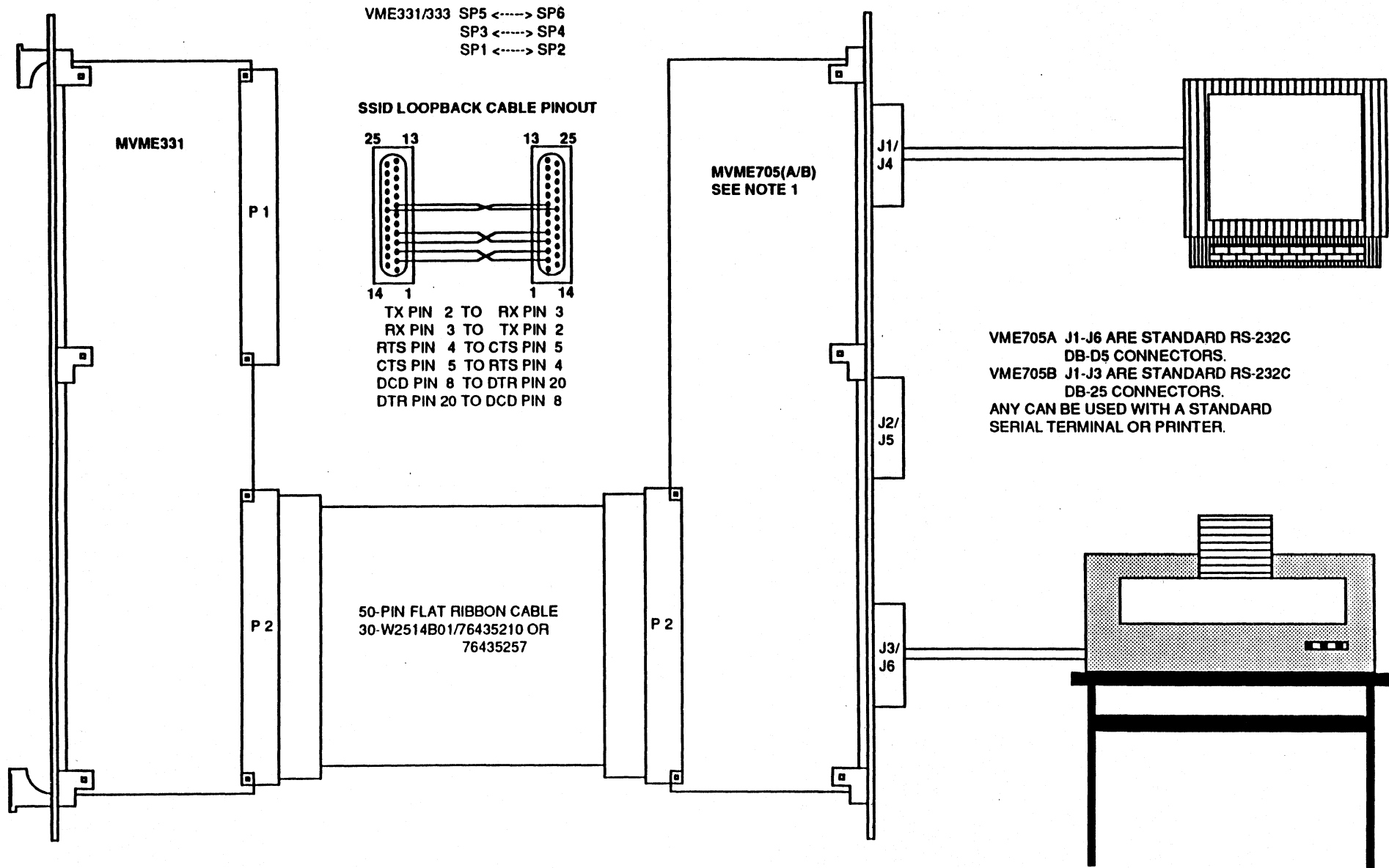
ROM ACCESS TIME SELECT



LOCAL MEMORY ADDRESS SELECT

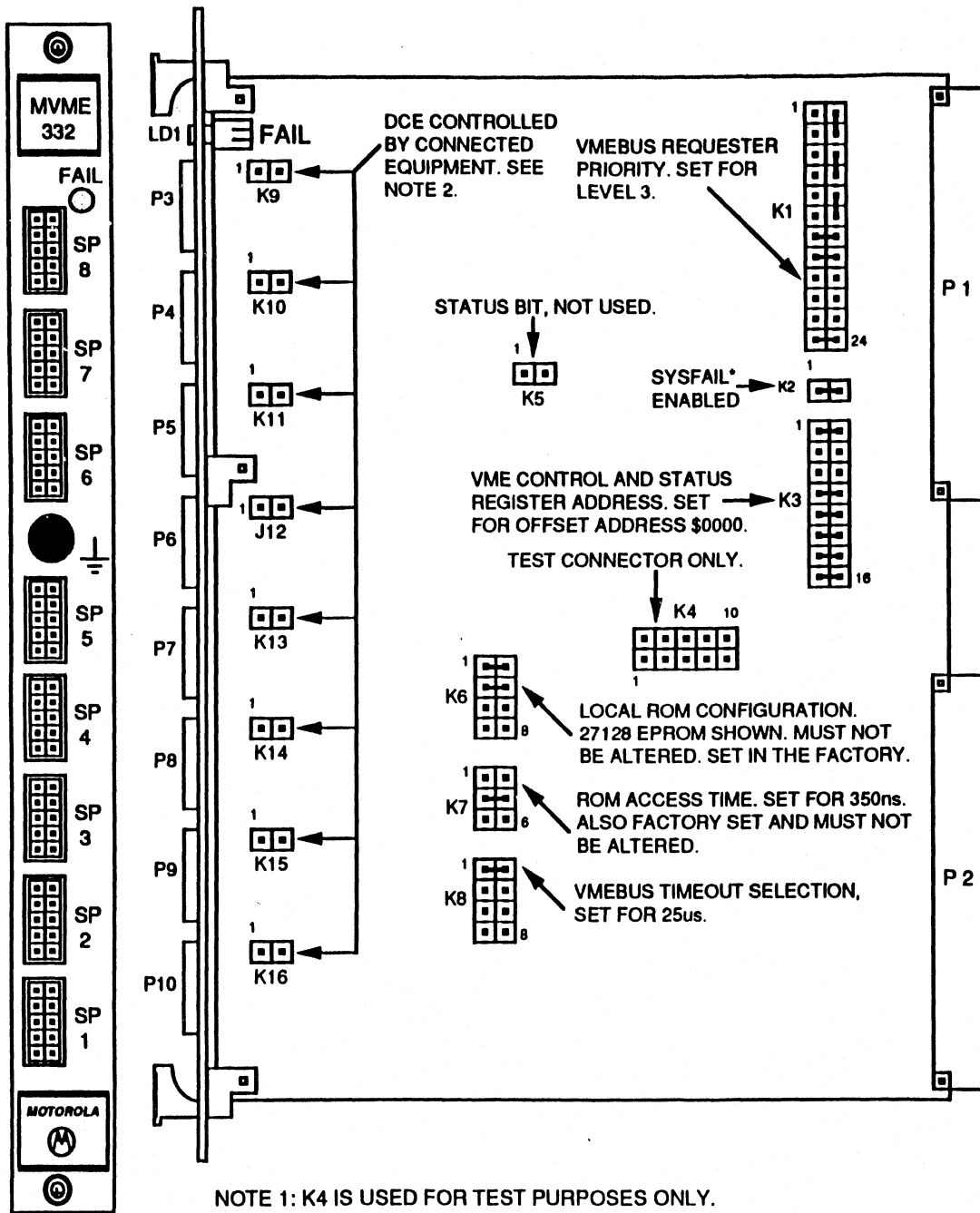


04/08/91



NOTE 1: VME705A HAS 6 SERIAL PORTS AND VME705B HAS ONLY 3 SERIAL PORTS. BOTH ARE CONSTRUCTED FROM THE SAME PCB BUT THE VME705B IS ONLY HALF POPULATED.

04/08/91



NOTE 1: K4 IS USED FOR TEST PURPOSES ONLY.

NOTE 2: JUMPER INSTALLED = DCD ISED AS A CONSTANT HIGH I/O.
 JUMPER REMOVED = DCD CONTROLLED BY CONNECTED EQUIPMENT.

PART NUMBERS:

MVME332 01-C3011A01 96010824
 MUNICH PCB

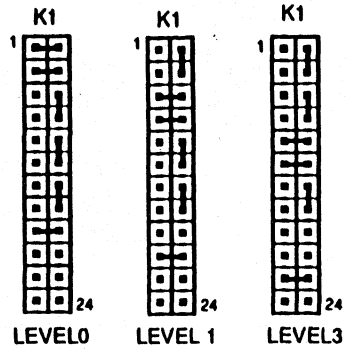
MVME332 01-W3504B01 XXXXXXXX

MVME332-2 01-W3504B02 96010824
 US BUILD PCB

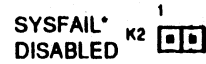
SEE CURRENT REVISION LEVEL (CRL) FOR
 CURRENT REVISION INFORMATION.

11/14/91

VMEBUS REQUEST LEVEL SELECT



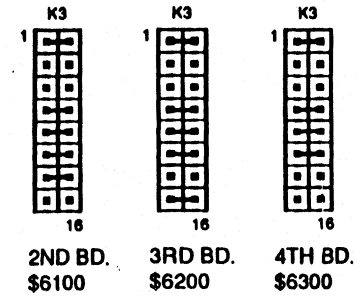
SYSTEM FAIL OUTPUT SELECT



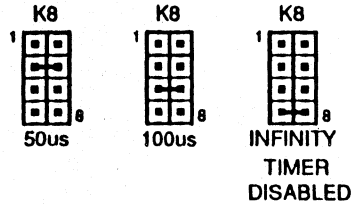
VME CONTROL AND STATUS REGISTER SELECT

K3 CONNECTION		BASE ADDRESS
		OFFSET
A15	1-2	REMOVED \$8000
A14	3-4	REMOVED \$4000
A13	5-6	REMOVED \$2000
A12	7-8	REMOVED \$1000
A11	9-10	REMOVED \$0800
A10	11-12	REMOVED \$0400
A09	13-14	REMOVED \$0200
A08	15-16	REMOVED \$0100

AXX-0, IF JUMPER IS INSTALLED
AXX-1, IF JUMPER IS REMOVED

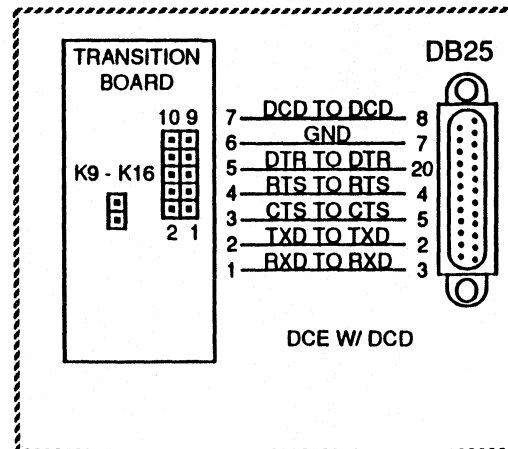
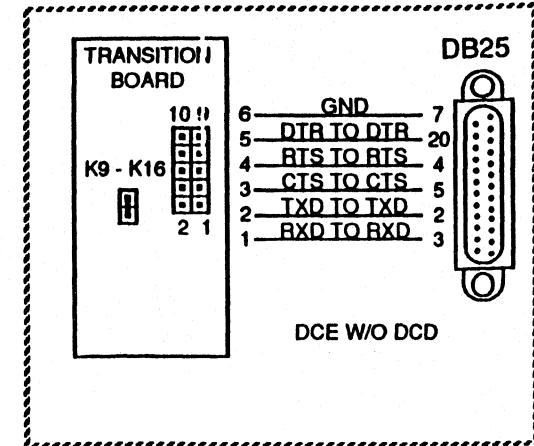
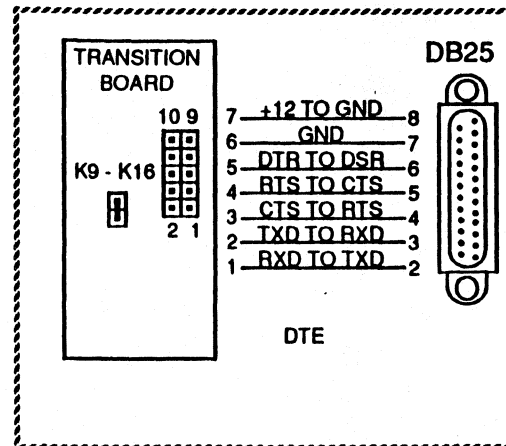


VMEBUS TIMEOUT SELECTION



**SERIAL PORT DCD
LINE FUNCTION**

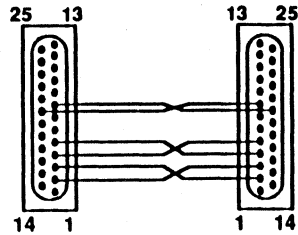
K9 - K16
DCD USED AS A CONSTANT
HIGH INPUT/OUTPUT.



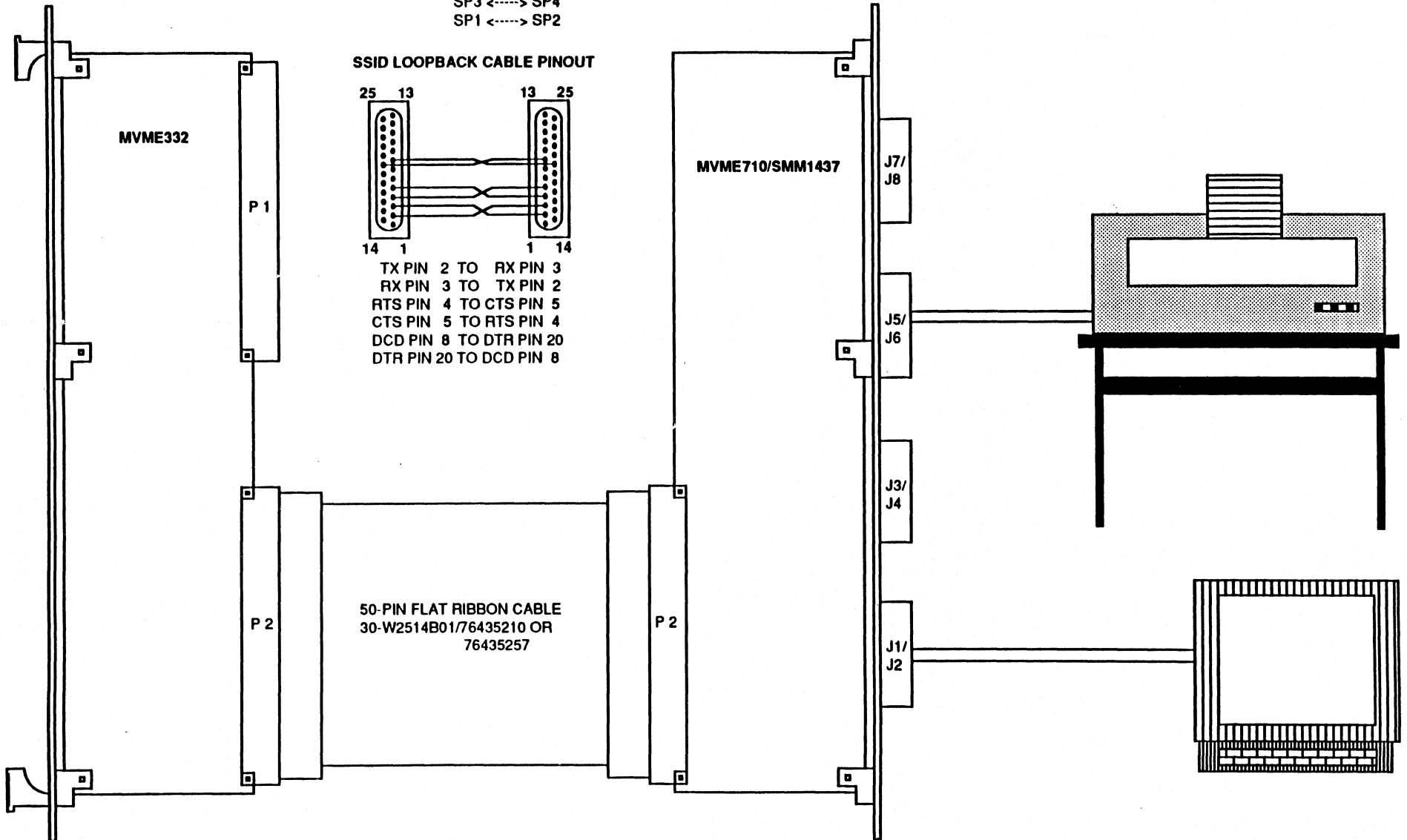
11/14/91

VME332(XT) SP7 <-----> SP8
 SP5 <-----> SP6
 SP3 <-----> SP4
 SP1 <-----> SP2

SSID LOOPBACK CABLE PINOUT



TX PIN 2 TO RX PIN 3
 RX PIN 3 TO TX PIN 2
 RTS PIN 4 TO CTS PIN 5
 CTS PIN 5 TO RTS PIN 4
 DCD PIN 8 TO DTR PIN 20
 DTR PIN 20 TO DCD PIN 8

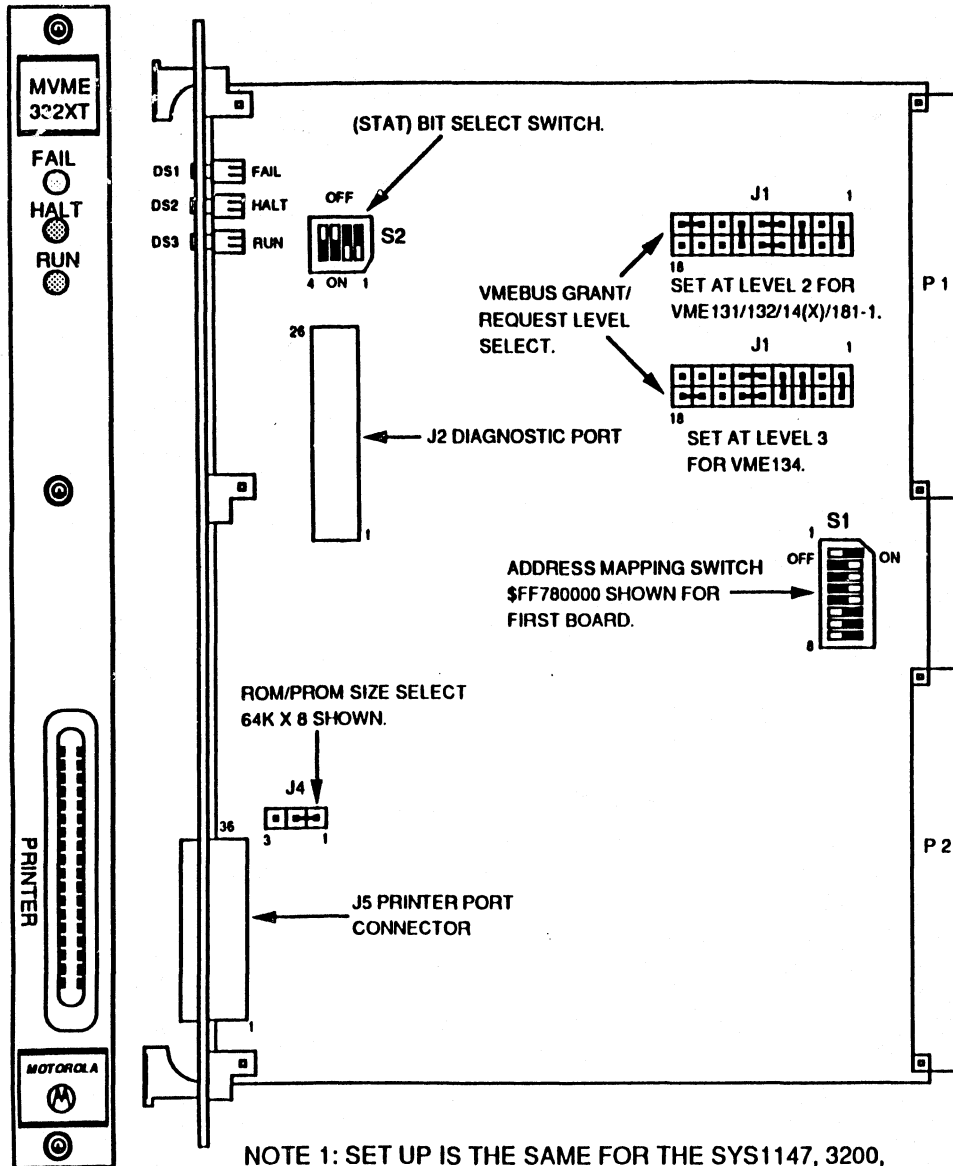


NOTE 1: ALL 8 PORTS ARE RS-232C COMPATIBLE. ANY STANDARD SERIAL PERIPHERAL CAN BE TIED TO THE PORTS. A SERIAL TERMINAL AND PRINTER ARE ATTACHED AS AN EXAMPLE. DB-25 ARE THE STANDARD CONNECTORS USED.

PART NUMBERS:

MVME332XT 01-W3475B01 76435358

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.



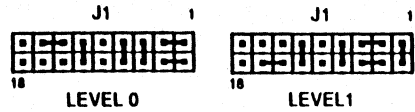
NOTE 1: SET UP IS THE SAME FOR THE SYS1147, 3200,
3400, 3604/08, 3640, 8400 & 8608's.

NOTE 2: ACTIVE PART OF SWITCH IS DARKENED AREA.

02/26/90

**MVME332XT
8-CHANNEL SERIAL
1-CHANNEL PARALLEL
INTELLIGENT
COMMUNICATIONS
CONTROLLER
PAGE 1 O**

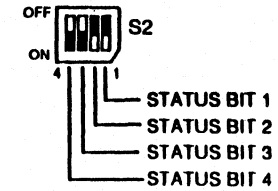
VMEBUS GRANT/REQUEST LEVEL SELECT



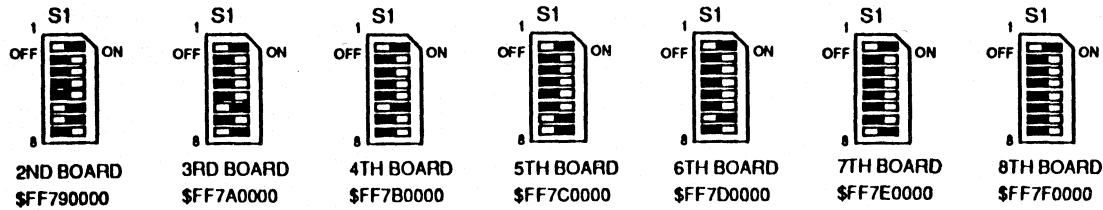
RAM/EPROM SIZE



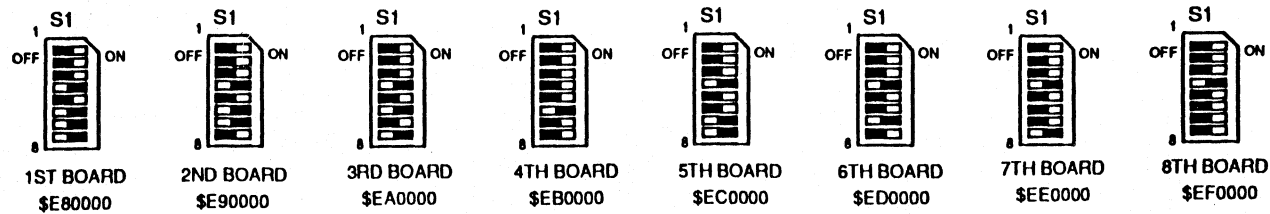
(STAT) BIT SELECT SWITCH



ADDRESS MAPPING SWITCH SET UP FOR 32 -BIT ADDRESSING



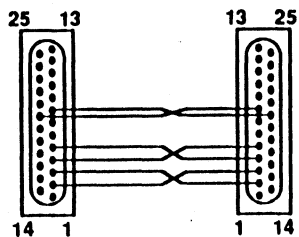
ADDRESS MAPPING SWITCH SET UP FOR 24 -BIT ADDRESSING



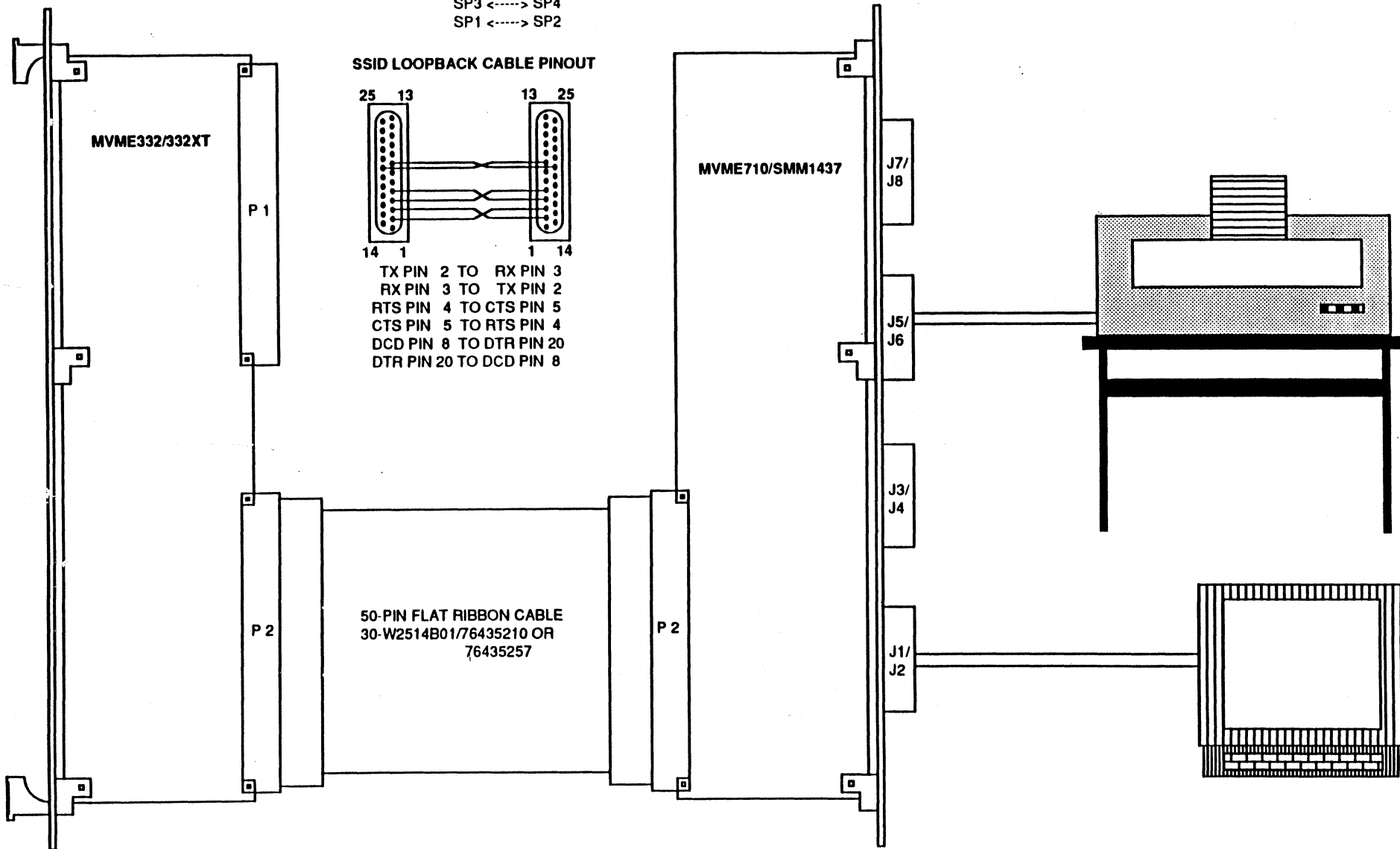
09/13/89

VME332(XT) SP7 <-----> SP8
 SP5 <-----> SP6
 SP3 <-----> SP4
 SP1 <-----> SP2

SSID LOOPBACK CABLE PINOUT

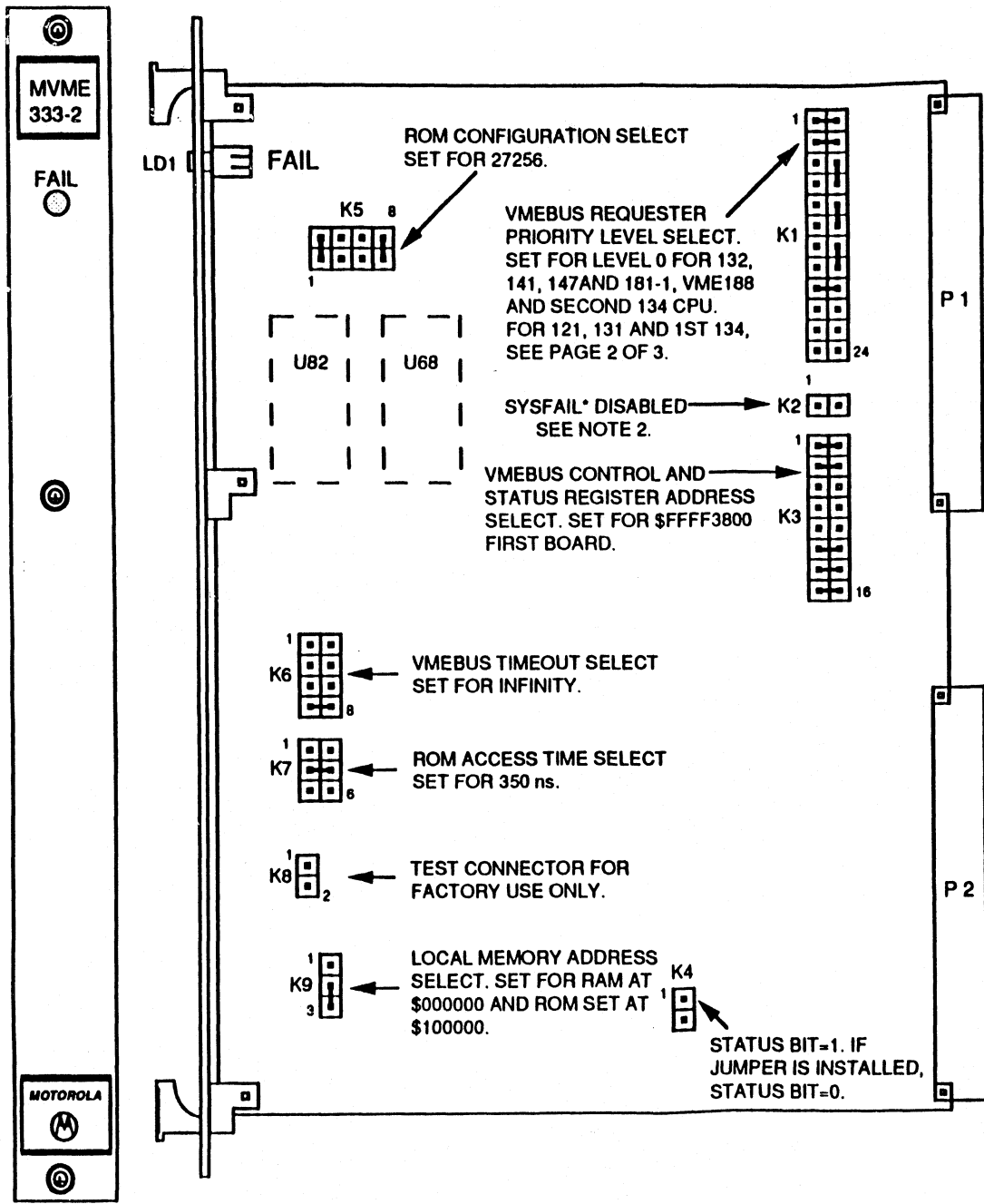


TX PIN 2 TO RX PIN 3
 RX PIN 3 TO TX PIN 2
 RTS PIN 4 TO CTS PIN 5
 CTS PIN 5 TO RTS PIN 4
 DCD PIN 8 TO DTR PIN 20
 DTR PIN 20 TO DCD PIN 8



09/13/89

NOTE 1: ALL 8 PORTS ARE RS-232C COMPATIBLE. ANY STANDARD SERIAL PERIPHERAL CAN BE TIED TO THE PORTS. A SERIAL TERMINAL AND PRINTER ARE ATTACHED AS AN EXAMPLE. DB-25 ARE THE STANDARD CONNECTORS USED.



PART NUMBERS:

- MVME333 01-G3027M02 76433076 (MUC. PWB)
- MVME333-2 01-C3012A01 96010863 (DELTA PWB)
- MVME333-2 01-G3027M04 76435173 (MUC. PWB)
- 01-W3503B04 96010917/1124 (US PWB)
- 01-W3503B05 96010982 (US PWB)

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: FIRMWARE INSTALLED DEFINES BOARD TYPE AND USE. (i.e. MVME333-2.)

MVME333-2 REV 1.4

- U68 * 51AW4815B40 (44473G01)
- U82 * 51AW4815B39 (44472G01)

* MAY HAVE HUNTER/READY CHIPS.

NOTE 2: K2 IS OUT FOR THE FOLLOWING SYSTEMS: SYS2616, SYS330X, SYS36X0 AND SYS38X0.

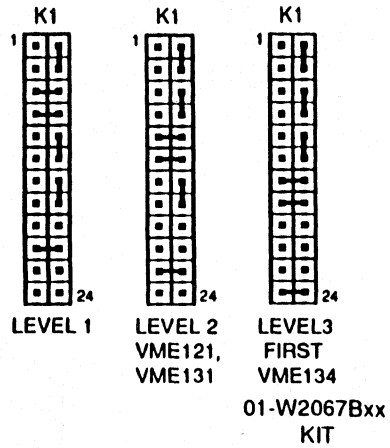
NOTE 3: SAME CONFIGURATION FOR SYS1147, 3200, 3400, 3604/08, 3640, 8400 & 8608's.

NOTE 4 : FOR B05'S ONLY, CHANGE K5 FROM 1 - 2, 7 - 8 TO 5 - 6, 7 - 8 FOR 512K EPROMS.

NOTE 5 : 01-W2067Bxx KIT, JUMPERS ARE ALL THE SAME EXCEPT FOR K5 AND K1. SEE PAGE 2.

1/1/5/91

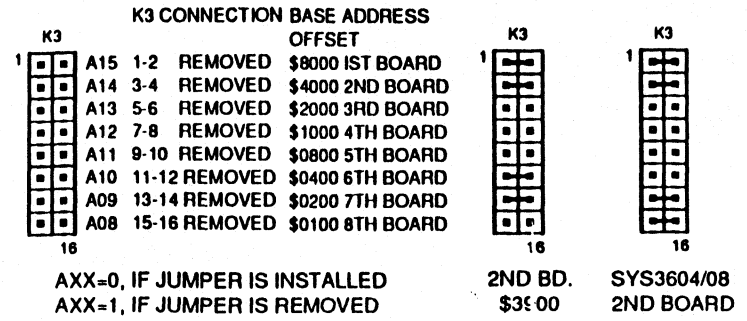
VMEBUS REQUEST LEVEL SELECT



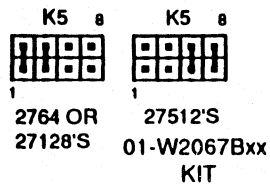
SYSTEM FAIL OUTPUT SELECT



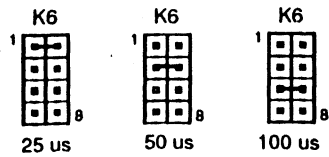
VME CONNECTION AND STATUS REGISTER SELECT



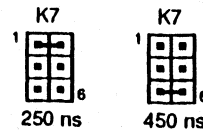
ROM CONFIGURATION SELECT



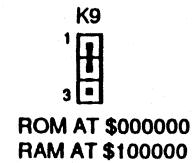
VMEBUS TIMEOUT SELECT



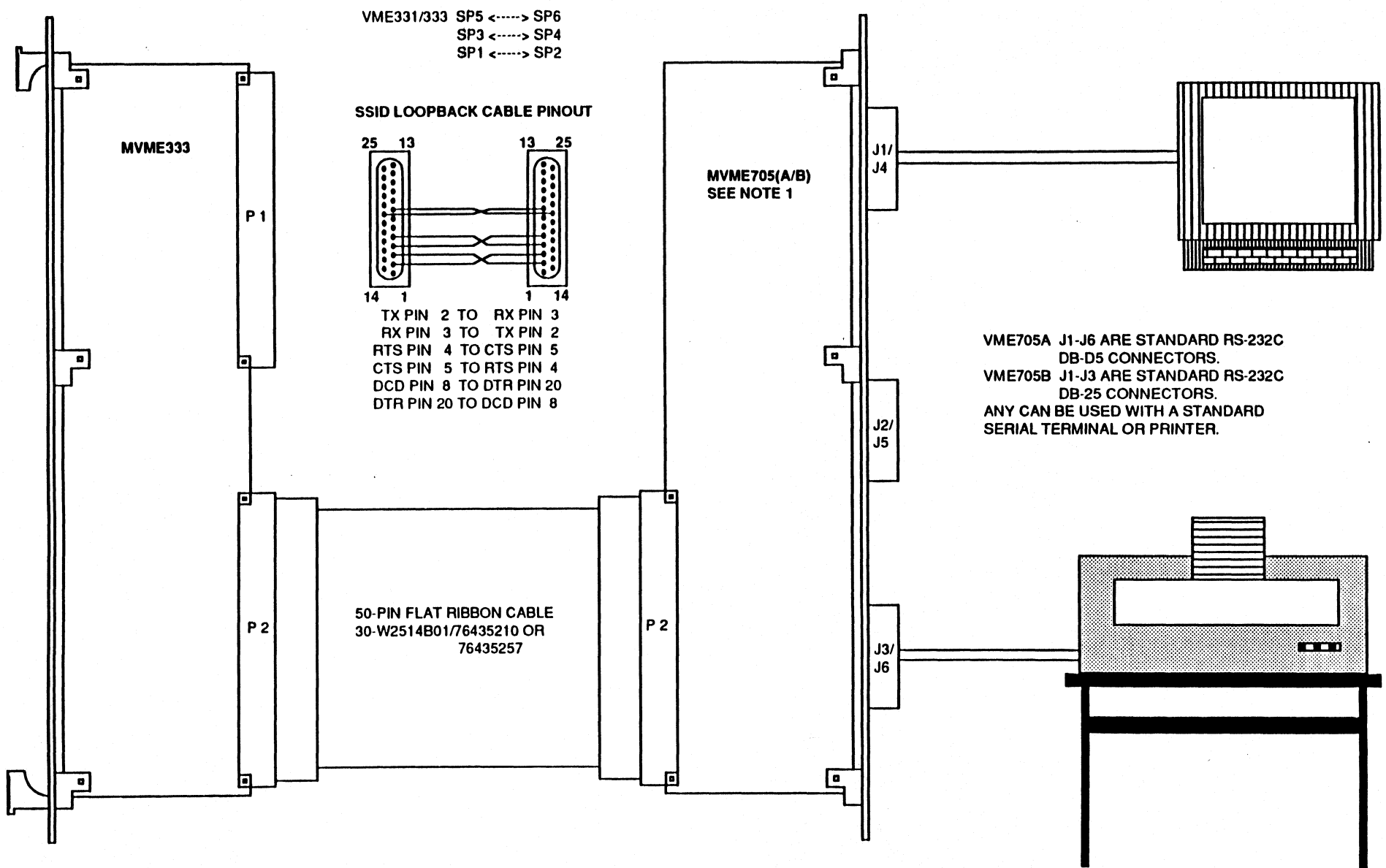
ROM ACCESS TIME SELECT



LOCAL MEMORY ADDRESS SELECT

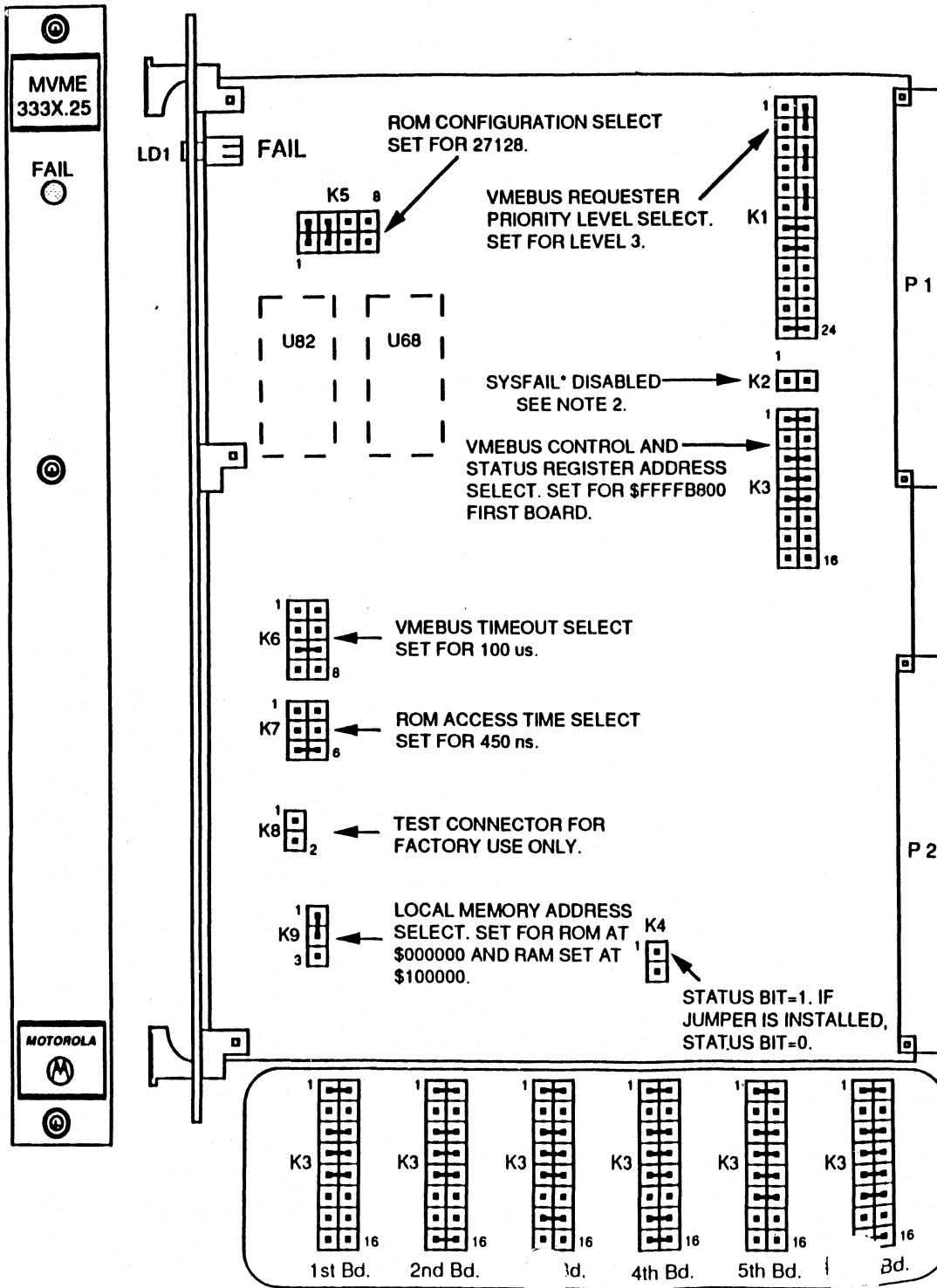


11/15/91



NOTE 1: VME705A HAS 6 SERIAL PORTS AND VME705B HAS ONLY 3 SERIAL PORTS. BOTH ARE CONSTRUCTED FROM THE SAME PCB BUT THE VME705B IS ONLY HALF POPULATED.

04/08/91



PART NUMBERS:

MVME333X25 01-W3503B06 96011010 (US PWB)

MVME333X25BUG 51-W5048B17 ODD
51-W5048B18 EVEN REV. 2.1

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: FIRMWARE INSTALLED DEFINES BOARD TYPE AND USE. (i.e. MVME333X.25.)

MVME333X.25 REV. 2.1
U68 51-W5048B17 (ODD)
U82 51-W5048B18 (EVEN)

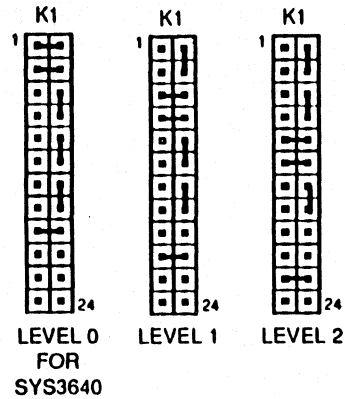
NOTE 2: K2 IS OUT FOR THE FOLLOWING SYSTEMS: SYS2616, SYS330X, SYS36X0 AND SYS38X0.

NOTE 3: SAME CONFIGURATION FOR SYS1147, 3200, 3400, 3604/08, 8400 & 8608's.

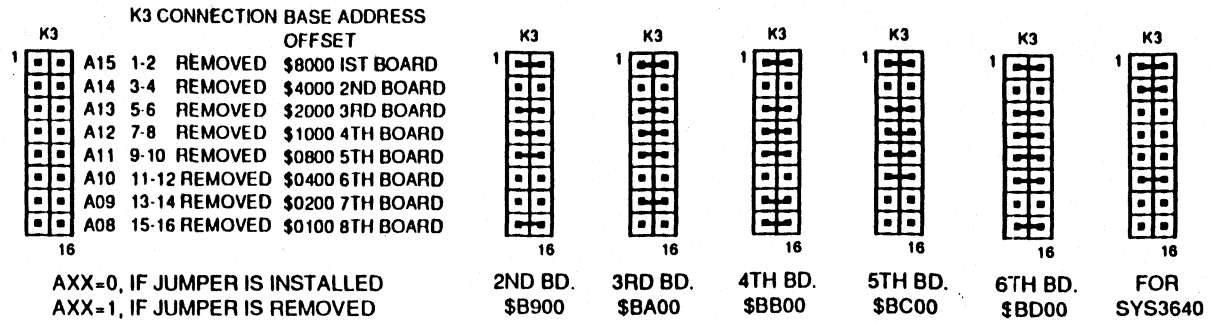
NOTE 4: SYS3640 JUMPERING ON K1 AND K3 ARE DIFFERENT. SEE PAGE 2 FOR DETAILS.

04/08/91

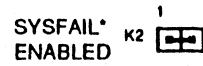
VMEBUS REQUEST LEVEL SELECT



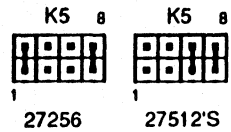
VME CONTROL AND STATUS REGISTER SELECT



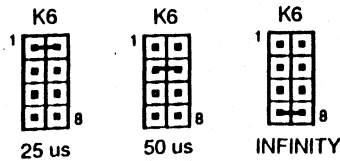
SYSTEM FAIL OUTPUT SELECT



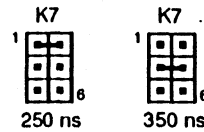
ROM CONFIGURATION SELECT



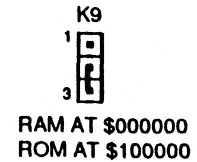
VMEBUS TIMEOUT SELECT



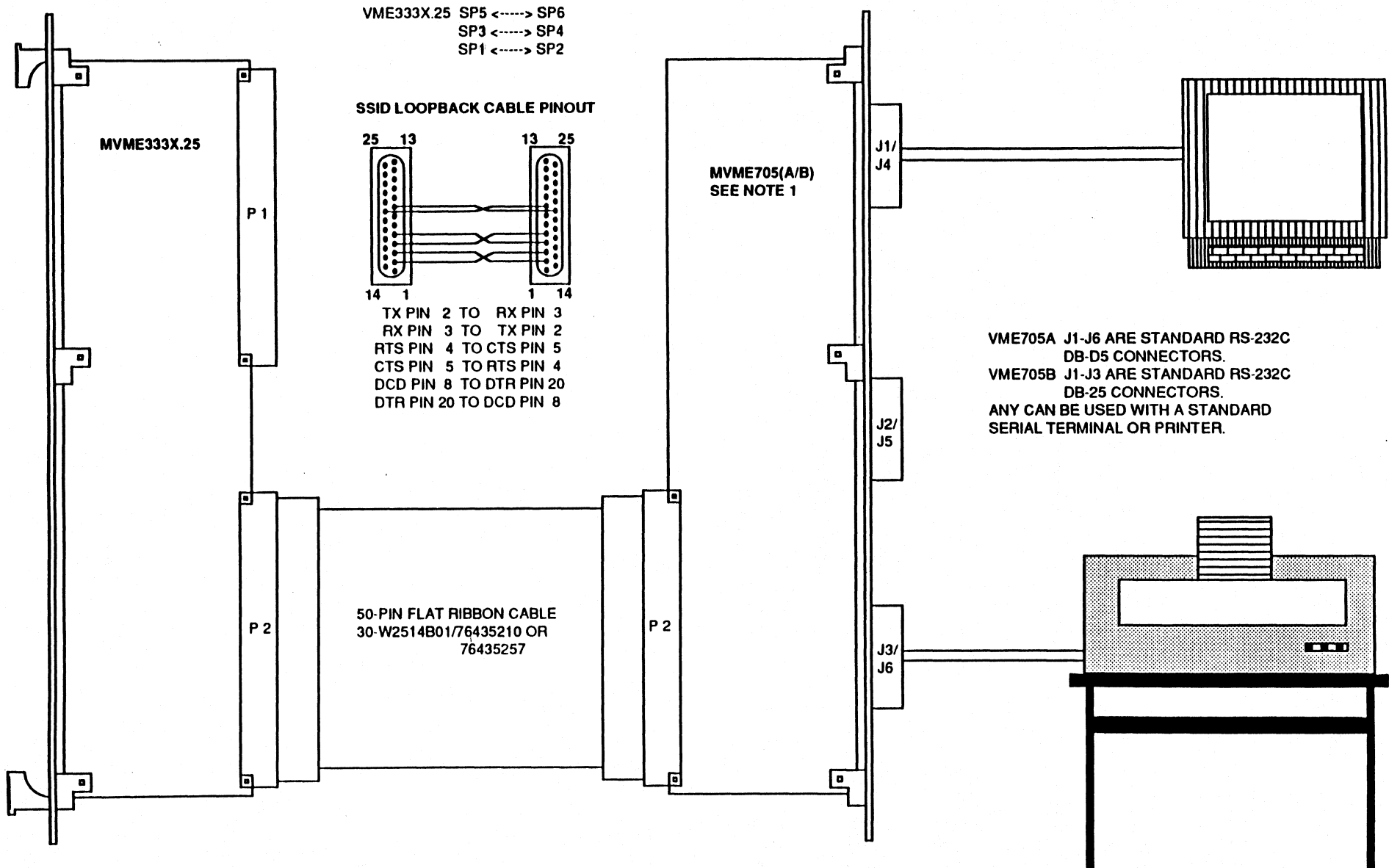
ROM ACCESS TIME SELECT



LOCAL MEMORY ADDRESS SELECT

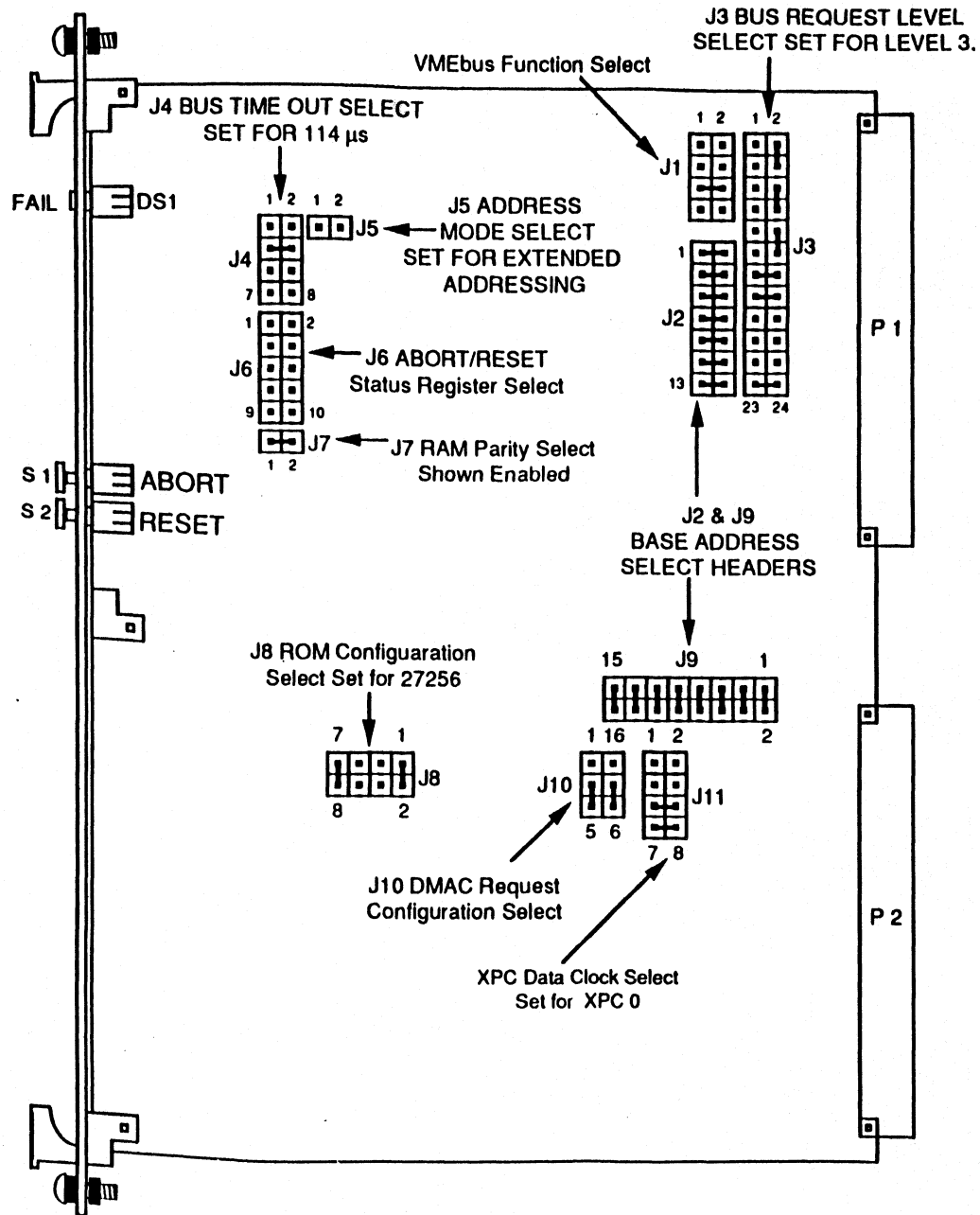
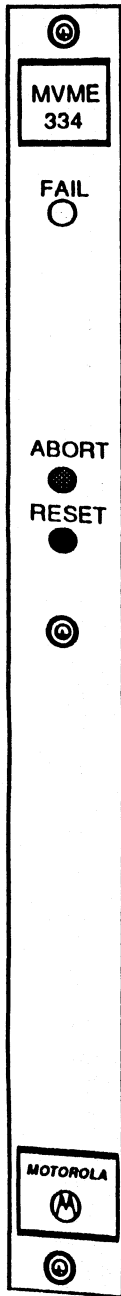


09/11/90



06/1/90

NOTE 1: VME705A HAS 6 SERIAL PORTS AND VME705B HAS ONLY 3 SERIAL PORTS. BOTH ARE CONSTRUCTED FROM THE SAME PCB BUT THE VME705B IS ONLY HALF POPULATED.



PART NUMBERS:

MVME334 01-G3034M01 76435641
MUNICH VERSION OBSOLETE

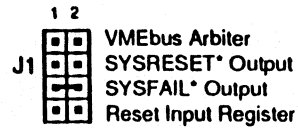
MVME334 01-W3592B01 96011338
U.S. BUILD OLD VERSION

MVME334 01-W3708B01 TBD
U.S. BUILD NEW VERSION

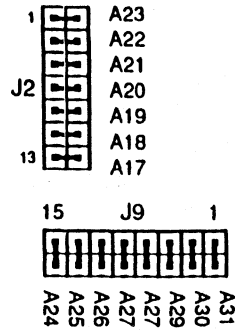
SEE CURRENT RESISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

03/04/91

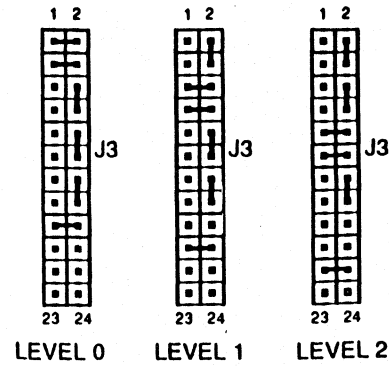
VMEbus Function Select



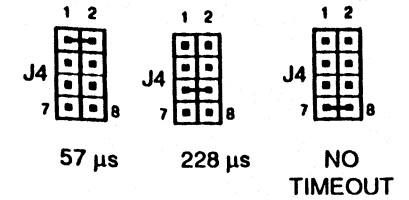
**J2 & J9
BASE ADDRESS
SELECT HEADERS**



**J3 BUS REQUEST LEVEL
SELECT SET FOR LEVEL 3.**



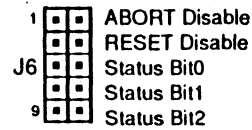
**J4 BUS TIME OUT
SELECT**



**J5 ADDRESS
MODE SELECT
SET FOR STANDARD
(A24) ADDRESSING**



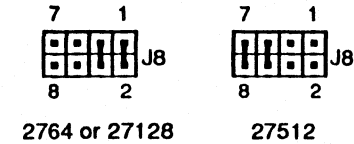
**ABORT/RESET
Status Register Select**



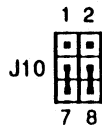
**J7 RAM Parity Select
Shown Disabled**



J8 ROM Configuration Select

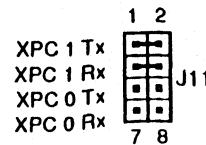


J10 DMAC Request Configuration Select

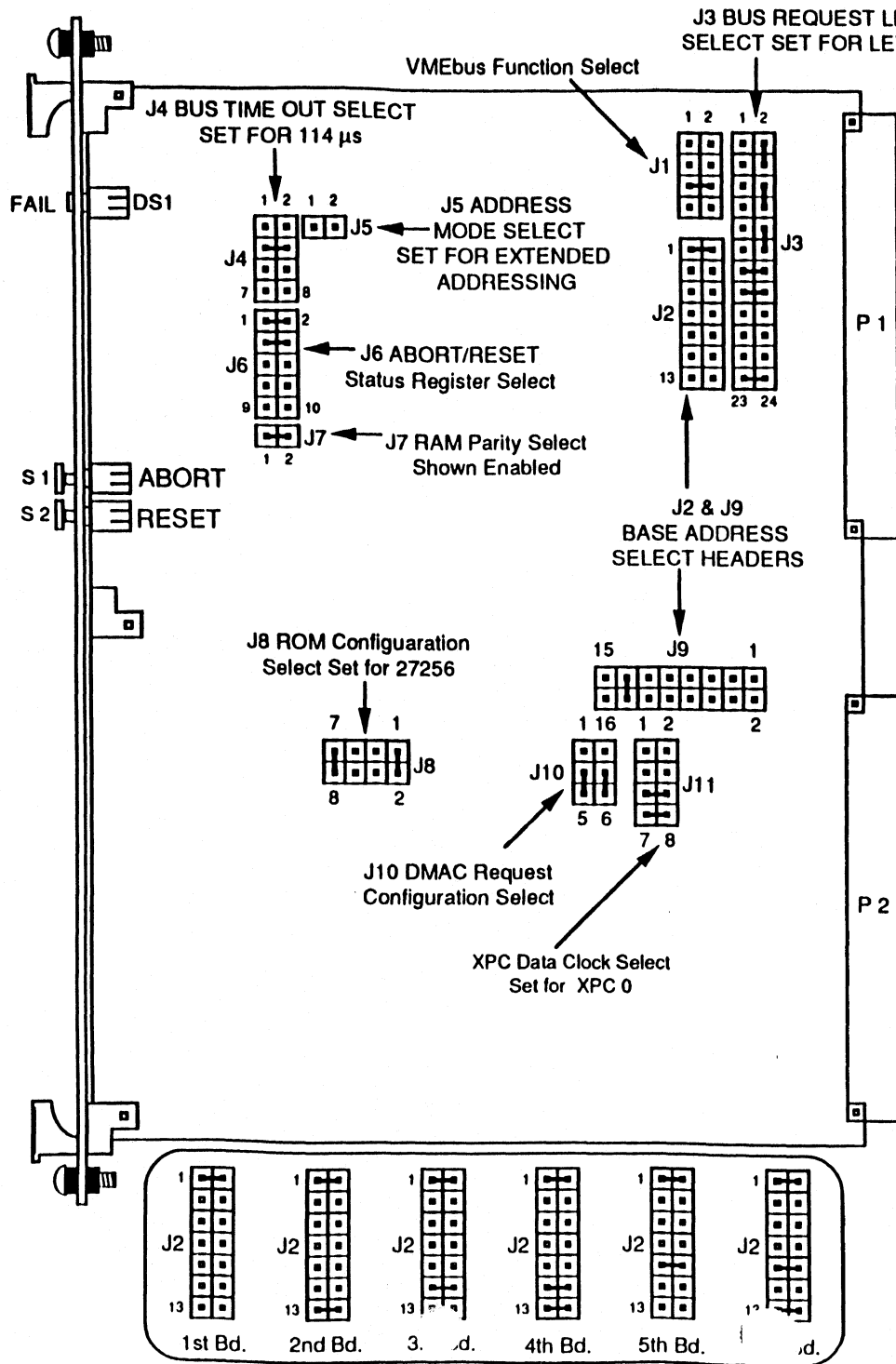
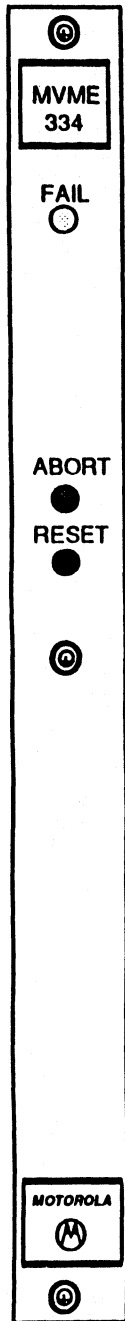


JUMPER	DMAC CONFIGURATION
1 - 3 In	DMAC Channel 2 Connected to DUSCC1 RTXDRA
3 - 5 In	DMAC Channel 2 Connected to DUSCC0 TXDRA
2 - 4 In	DMAC Channel 3 Connected to DUSCC1 RTXDQB
4 - 6 In	DMAC Channel 3 Connected to DUSCC0 TXDQB

XPC Data Clock Select



03/04/91



PART NUMBERS:

MVME334X25 01-W3708B01 TBD
U.S. BUILD NEW VERSION

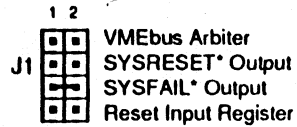
SEE CURRENT RESISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

NOTE 1: MVME709 (01-W3603B01/76435780) IS THE
TRANSITION BOARD. 30-W2514B01/76435210 IS
THE INTERCONNECTING CABLE.

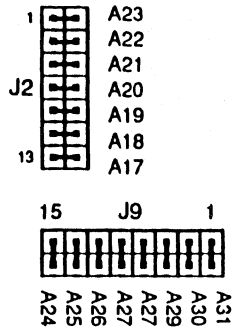
04/08/91

**MVME334X.25
MULTIPROTOCOL
COMMUNICATIONS
CONTROLLER
PAGE 1 OF 2**

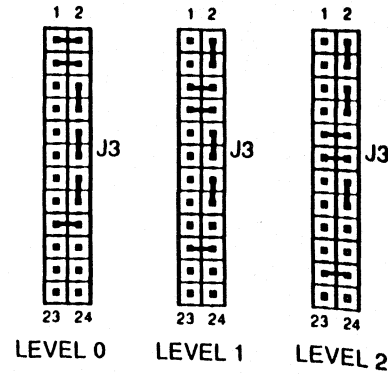
VMEbus Function Select



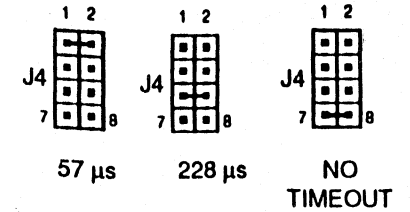
**J2 & J9
BASE ADDRESS
SELECT HEADERS**



**J3 BUS REQUEST LEVEL
SELECT SET FOR LEVEL 3.**



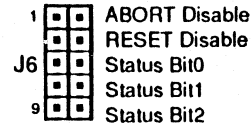
**J4 BUS TIME OUT
SELECT**



**J5 ADDRESS
MODE SELECT
SET FOR STANDARD
(A24) ADDRESSING**



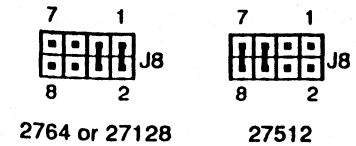
**ABORT/RESET
Status Register Select**



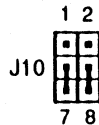
**J7 RAM Parity Select
Shown Disabled**



J8 ROM Configuration Select

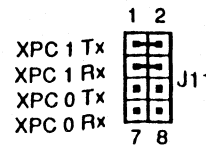


J10 DMAC Request Configuration Select

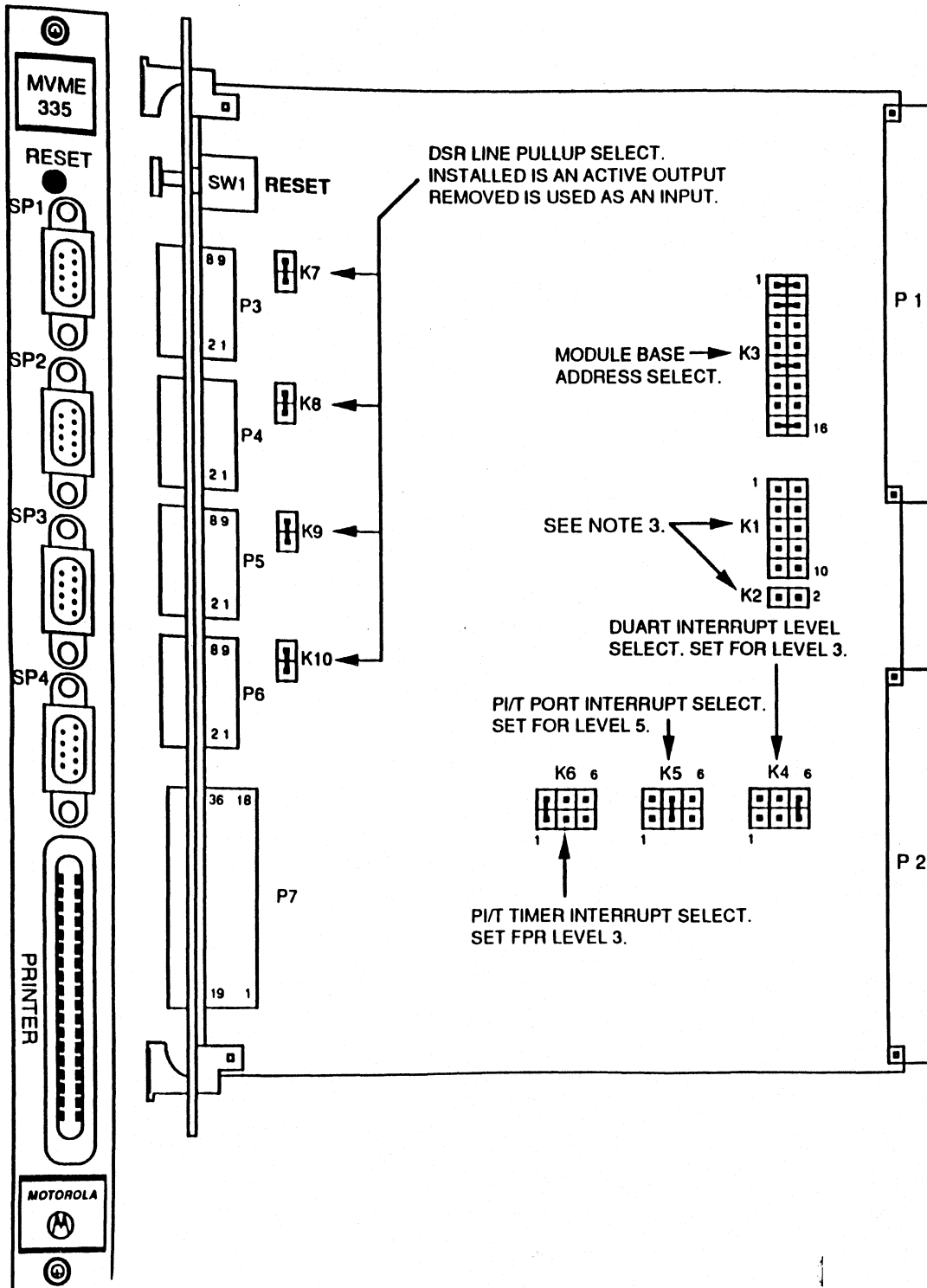


JUMPER	DMAC CONFIGURATION
1 - 3 In	DMAC Channel 2 Connected to DUSCC1 RTXDRQA
3 - 5 In	DMAC Channel 2 Connected to DUSCC0 TXDRQA
2 - 4 In	DMAC Channel 3 Connected to DUSCC1 RTXDRQB
4 - 6 In	DMAC Channel 3 Connected to DUSCC0 TXDRQB

XPC Data Clock Select



04/08/91



PART NUMBERS:

MVME335 01-C3014A01 96010907
MUNICH PWB

MVME335 01-W3530B02 96010907

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

NOTE 1: P3, P4, P5 AND P6 ARE 9-PIN DIN RS-232C
CONNECTORS FOR 4 SERIAL PORTS.

NOTE 2: P7 IS A 36-PIN CENTRONICS PRINTER PORT
CONNECTOR.

NOTE 3: K1 AND K2 ARE NOT USED.

NOTE 4: SETUP IS THE SAME FOR SYS3200, 3400,
8400, & 8608's.

02/26/90

**MVME335
SERIAL/
PARALLEL
I/O MODULE
PAGE 1 OF 3**

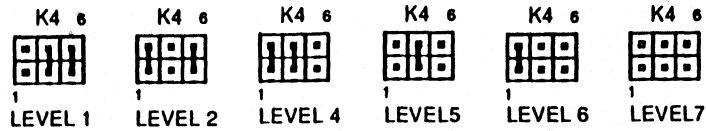
MODULE BASE ADDRESS SELECT

K3	K3 CONNECTION	BASE ADDRESS OFFSET
1	A15 1-2	REMOVED \$8000
	A14 3-4	REMOVED \$4000
	A13 5-6	REMOVED \$2000
	A12 7-8	REMOVED \$1000
	A11 9-10	REMOVED \$0800
	A10 11-12	REMOVED \$0400
	A09 13-14	REMOVED \$0200
	A08 15-16	REMOVED \$0100

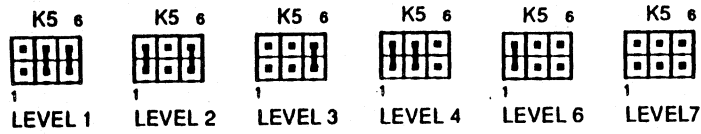
16

Axx=0, IF JUMPER IS INSTALLED
 Axx=1, IF JUMPER IS REMOVED

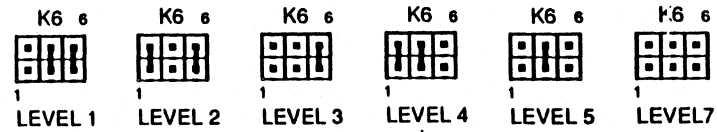
DUART INTERRUPT LEVEL SELECT



PI/T PORT INTERRUPT SELECT

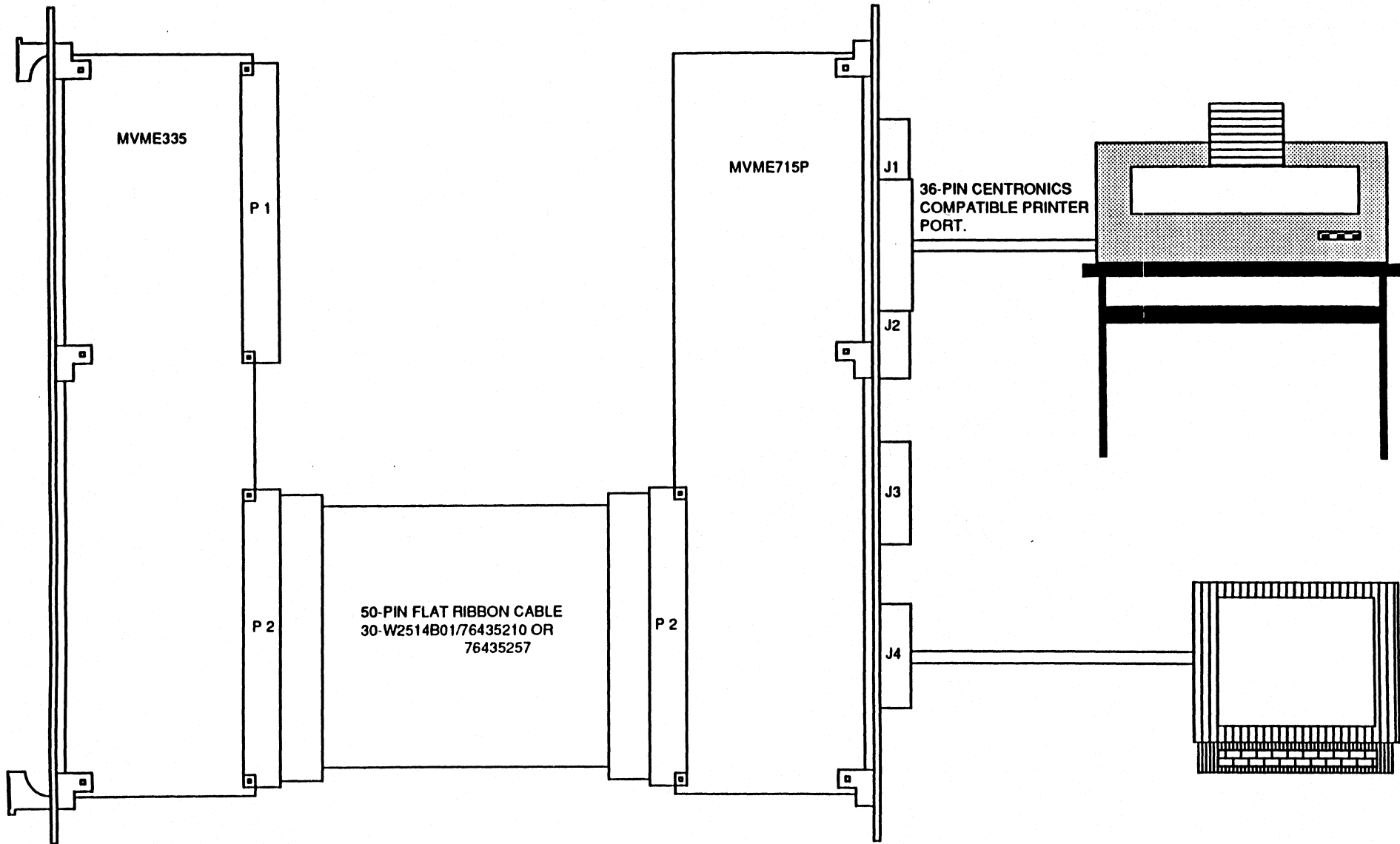


PI/T TIMER INTERRUPT SELECT

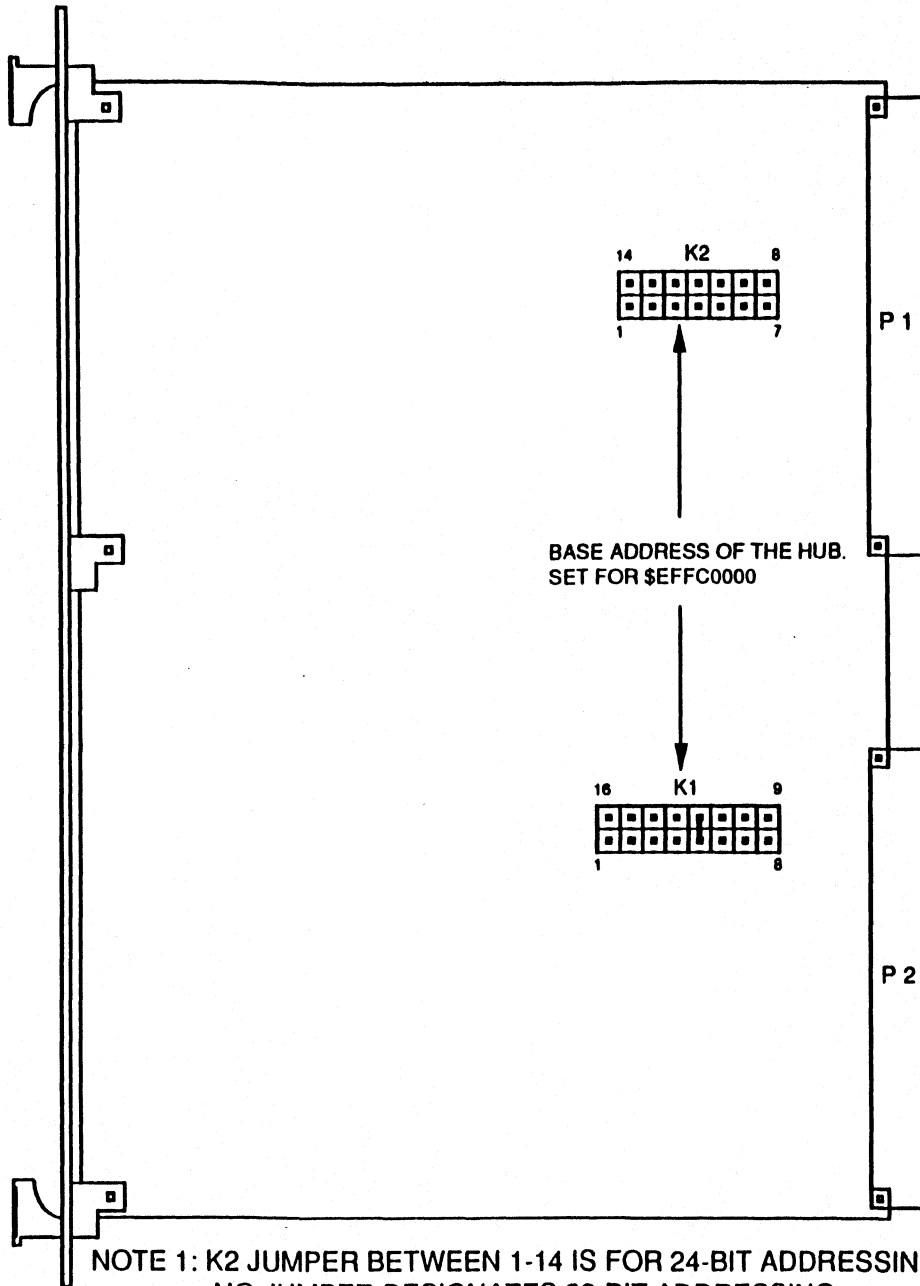
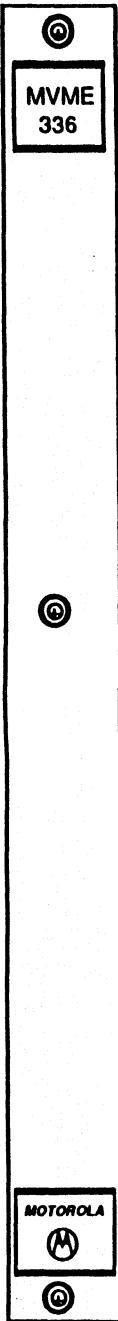


↑
 FOR UNISOFT SOFTWARE,
 SET AT THIS LEVEL.

11/29/89



NOTE 1: J1-J4 ARE RS-232C COMPATIBLE. ANY STANDARD SERIAL PERIPHERAL CAN BE TIED TO THE PORTS. A SERIAL TERMINAL IS ATTACHED AS AN EXAMPLE. DB-25 ARE THE STANDARD CONNECTORS USED.



NOTE 1: K2 JUMPER BETWEEN 1-14 IS FOR 24-BIT ADDRESSING.
NO JUMPER DESIGNATES 32-BIT ADDRESSING.

NOTE 2: SETUP IS THE SAME FOR SYS1147, 3200, 3400, 3604/08,
3640, 8400, & '608's.

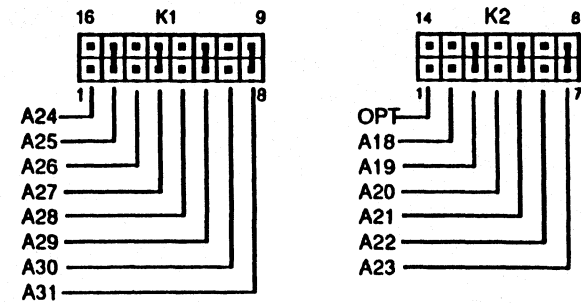
PART NUMBERS:

MVME336SRVR 01-W1347B01 96010944

MVME336HUB 01-W3487B01 96010942

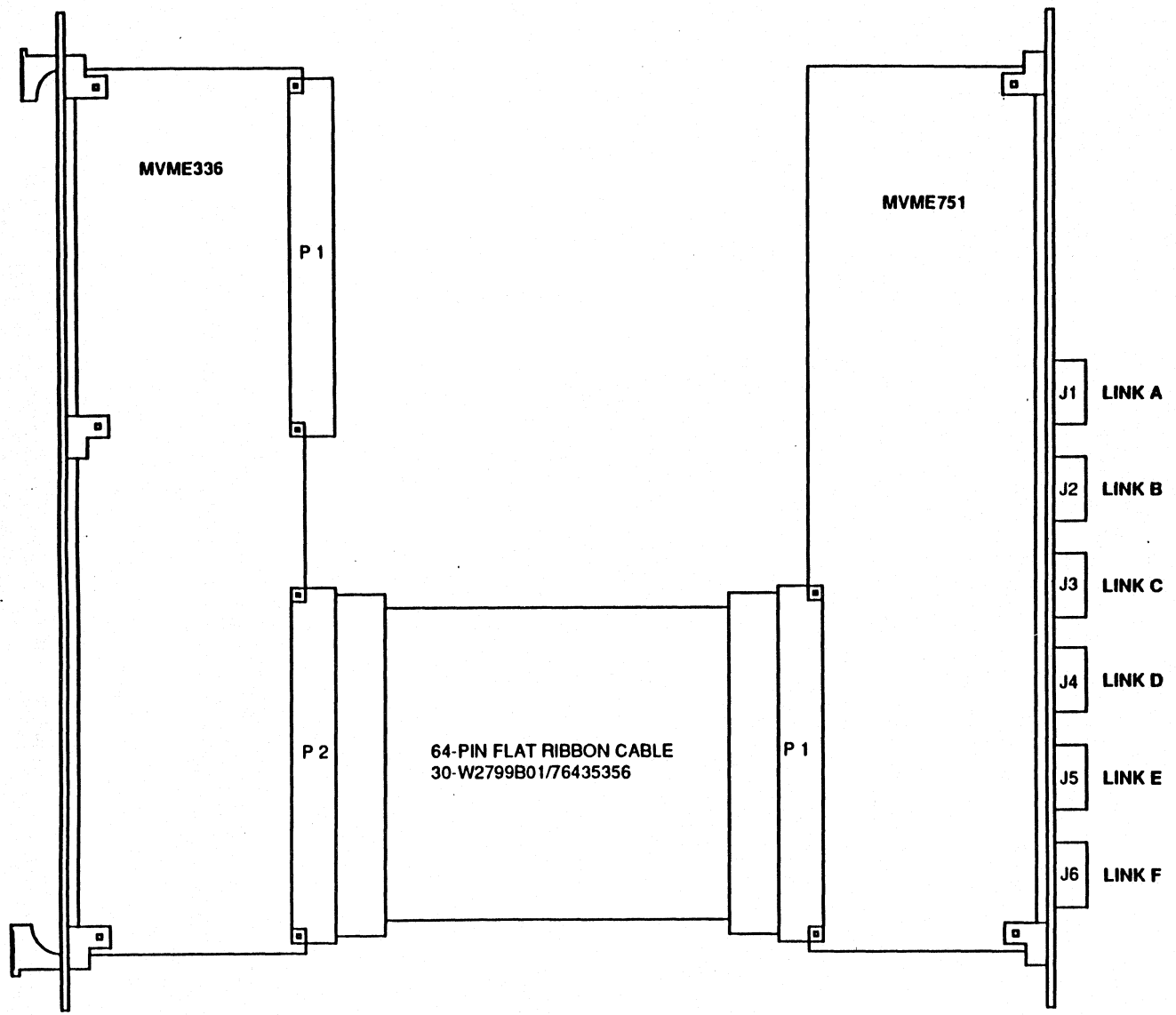
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

HUB BASE ADDRESS SELECT

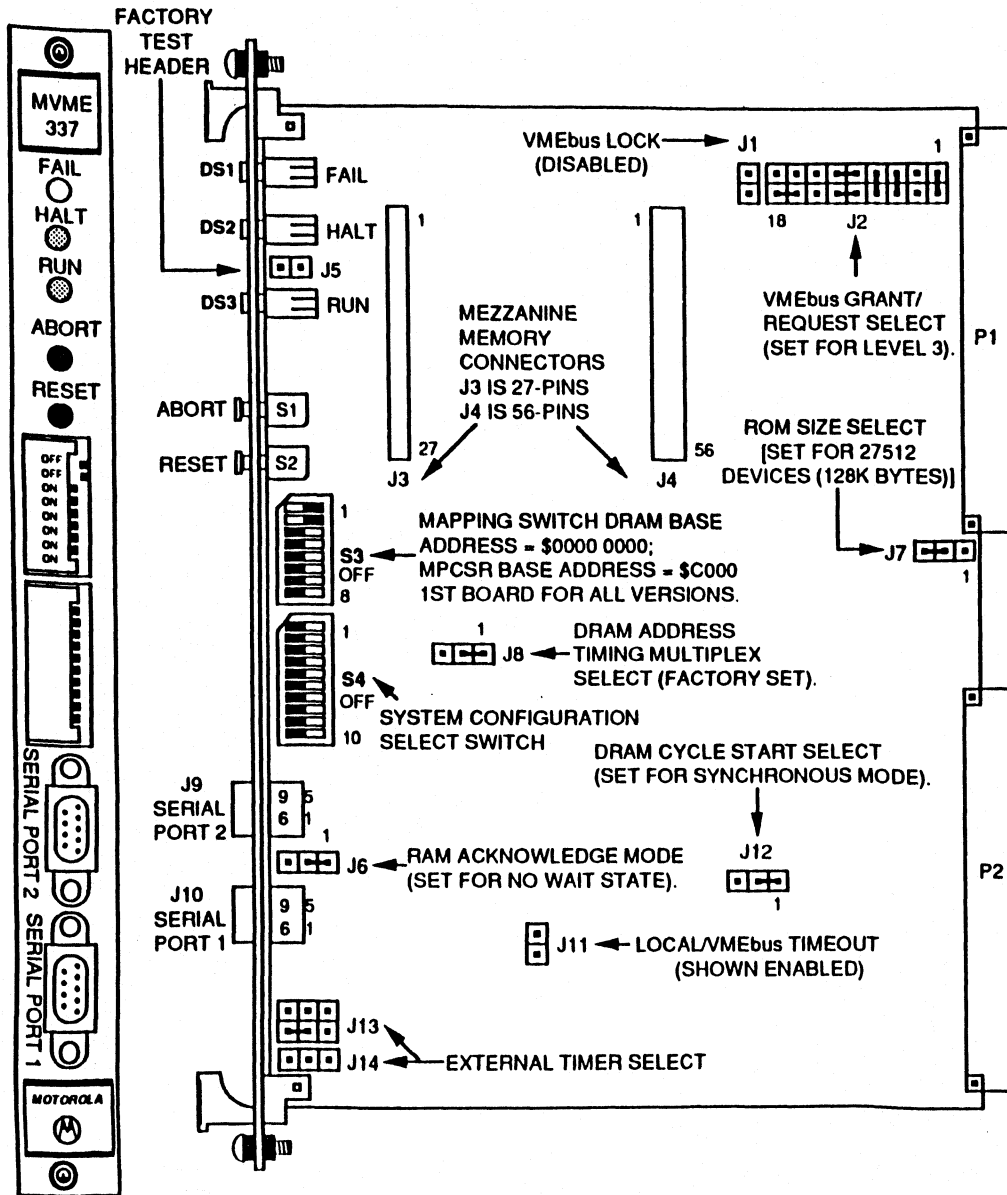


HUB BASE ADDRESS SET FOR \$55540000

02/26/90



09/13/89



PART NUMBERS:

MVME337-1 01-W3440B13 89000590
MVME337A 01-W3440B14 89000591

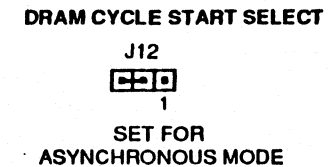
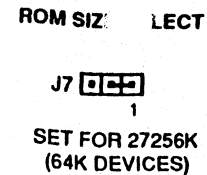
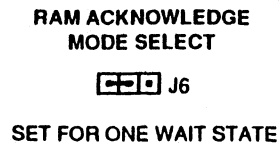
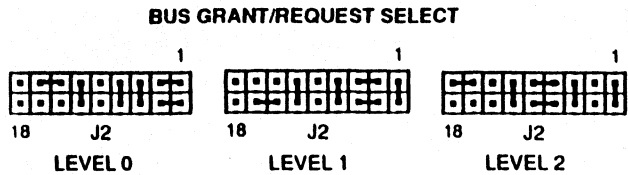
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: J5 IS A FACTORY TEST JUMPER AND IS NOT INSTALLED.

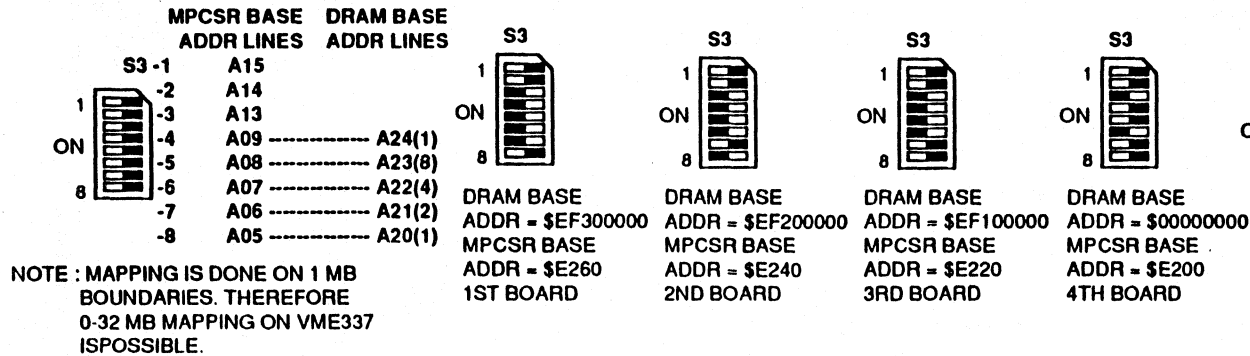
NOTE 2: J9 & J10 ARE BOTH DB-9 RS-232C CONNECTORS.

NOTE 3: ACTIVE PART OF SWITCH IS DARKENED AREA.

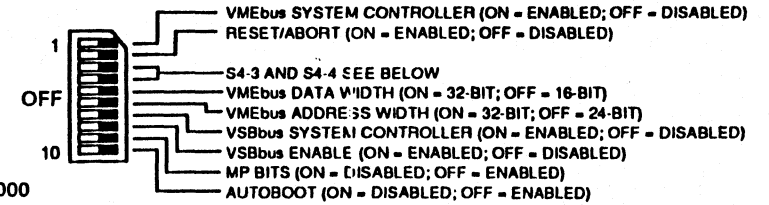
03/07/91



VMEBUS ADDRESS MAPPING SWITCH FOR VME337



SYSTEM CONFIGURATION SELECT SWITCH



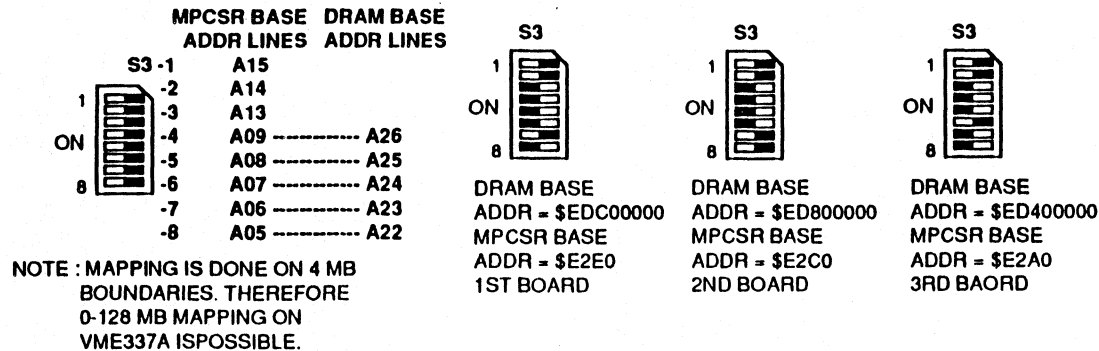
S4-3 OFF = OFF-BOARD VMEbus MEMORY
S4-4 OFF = 337bug EXECUTES IN FIRST

S4-3 OFF = VMEbus BASE
S4-4 ON = 337bug EXECUTES OVER

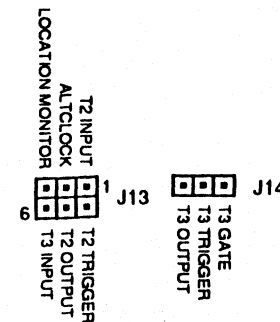
S4-3 ON = DRAM MAPPED AT \$FFX00000
S4-4 OFF = 337bug EXECUTES LOCALLY

S4-3 ON = DRAM MAPPED AT \$00000000
S4-4 ON = 337bug EXECUTES LOCALLY

VMEBUS ADDRESS MAPPING SWITCH FOR VME337A



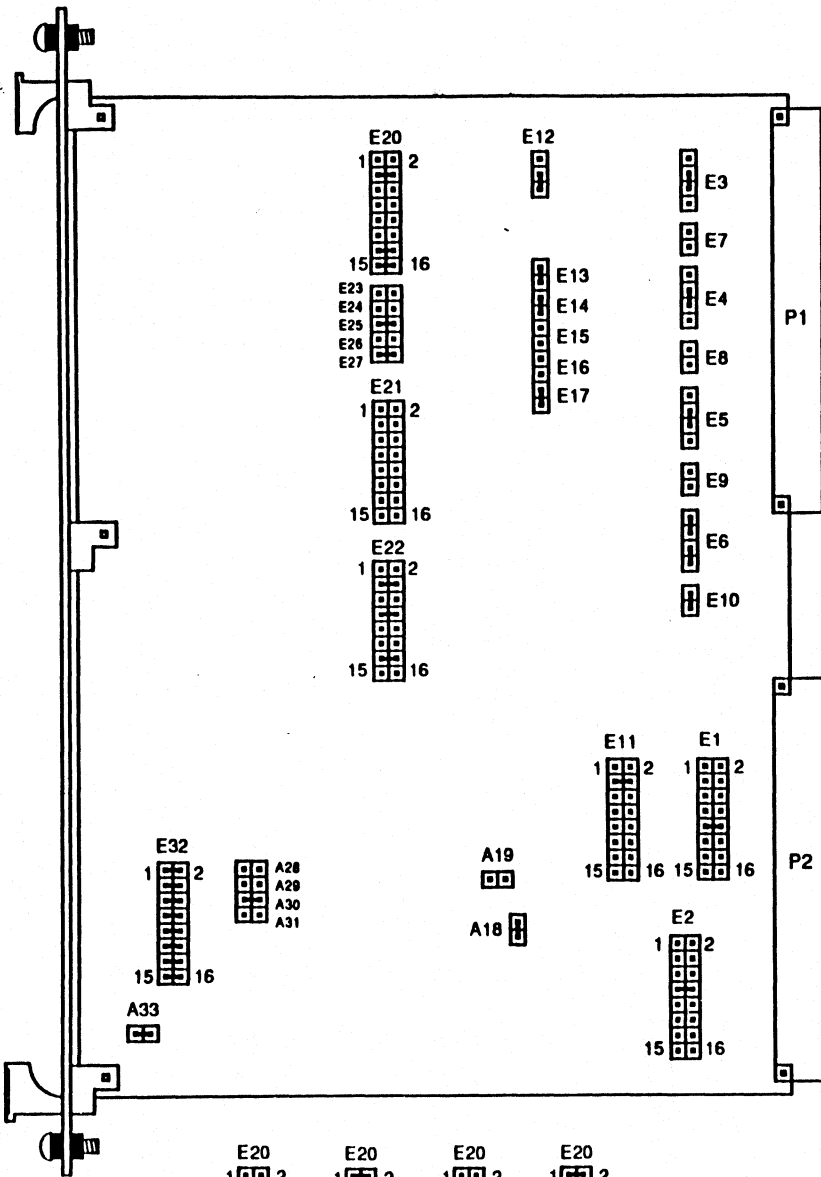
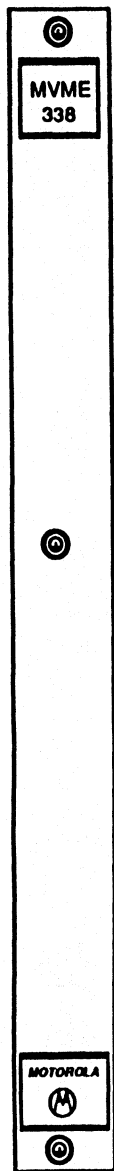
EXTERNAL TIMER SELECT



SLAVE RESOURCE MAPPING SELECT (S3)

MVME337-1/MVME337A SHARED RAM AND SHORT I/O ADDRESSES			
MODULE	A32 DEFAULT	SHORT I/O	SWITCH S3
MVME337 #1	\$EF300000	\$FFFFE260	5, 6 ON; rest OFF
MVME337 #2	\$EF200000	\$FFFFE240	5, 6, 8 ON; rest OFF
MVME337 #3	\$EF100000	\$FFFFE220	5, 6, 7 ON; rest OFF
MVME337 #4	\$EF000000	\$FFFFE200	5, 6, 7, 8 ON; rest OFF
MVME337A #1	\$EDC00000	\$FFFFE2E0	5 ON; rest OFF
MVME337A #2	\$ED800000	\$FFFFE2C0	5, 8 ON; rest OFF
MVME337A #3	\$ED400000	\$FFFFE2A0	5, 7 ON; rest OFF
MVME337A #4	\$ED000000	\$FFFFE280	5, 7, 8 ON; rest OFF

03/07/91



PART NUMBERS:

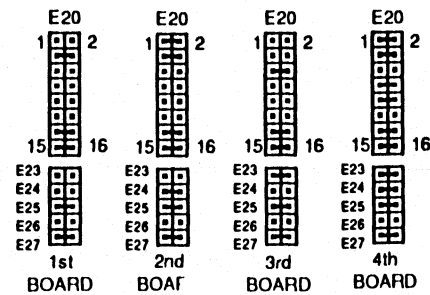
MVME338 01-W2840C01 96011254

SYSTECH MODEL# HPS-6245, SYSTECH P/N
65-800165-1-00 REV. G

F/W REV. (U57) HPS-6245-02B (22-300102-0)

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

MOTOROLA'S P/N
AND REV. LEVEL
SHALL BE LOCATED
HERE ON THE P2
CONNECTOR



11/04/91

**MVME338
TERMINAL
I/O CHANNEL
CONTROLLER
PAGE 1 OF 1**

PART NUMBERS:

MVME340 01-G3026M01 76435307
MUNICH OBSOLETE PWB

MVME340A 01-G3026M02 76435394
MUNICH PWB

MVME340A 01-W3531B02 76435394
NEWEST US PWB

MEZZANINE 01-G4169M01 66430088
MUNICH PWB

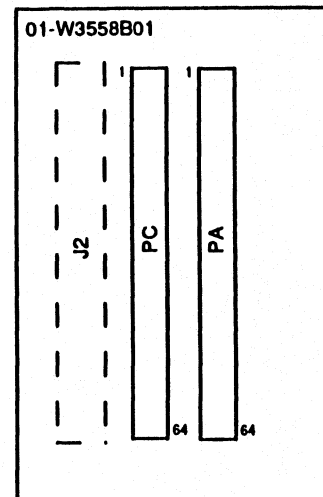
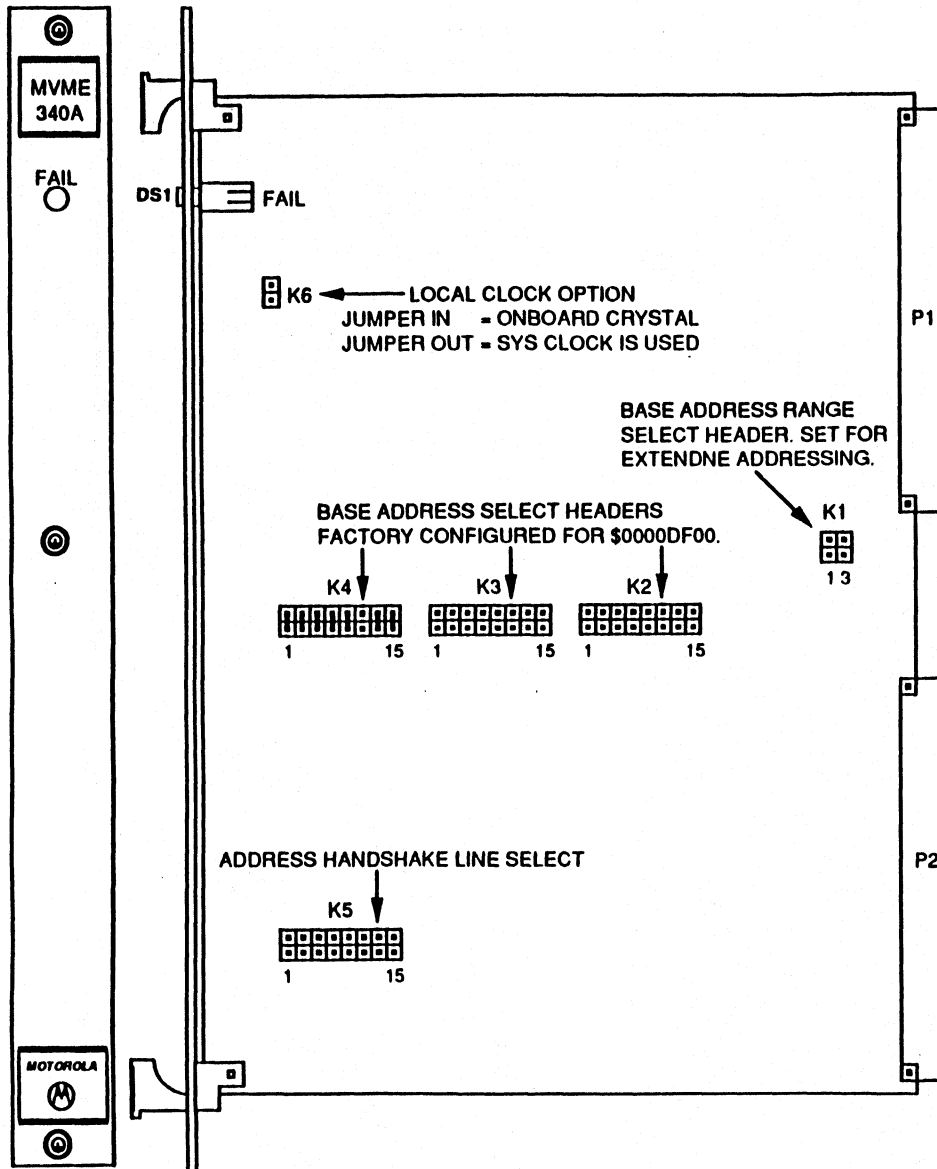
MEZZANINE 01-W3558B01 66431169
NEWEST US PWB.

BOTH US PWB'S REPLACE MUNICH PWB'S
AND ARE THE PREFERRED CHOICE.

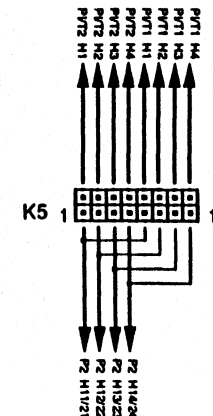
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

NOTE 1: SUB-ASSY. HAS A 96-PIN CONNECTOR ON
THE BACK SIDE THAT PLUGS INTO P2 ON
THE VME340(A). PA AND PC CONNECT
TO TWO 64-PIN RIBBON CABLES THAT
CONNECT TO OTHER INTERFACE MODULES.

09/13/89



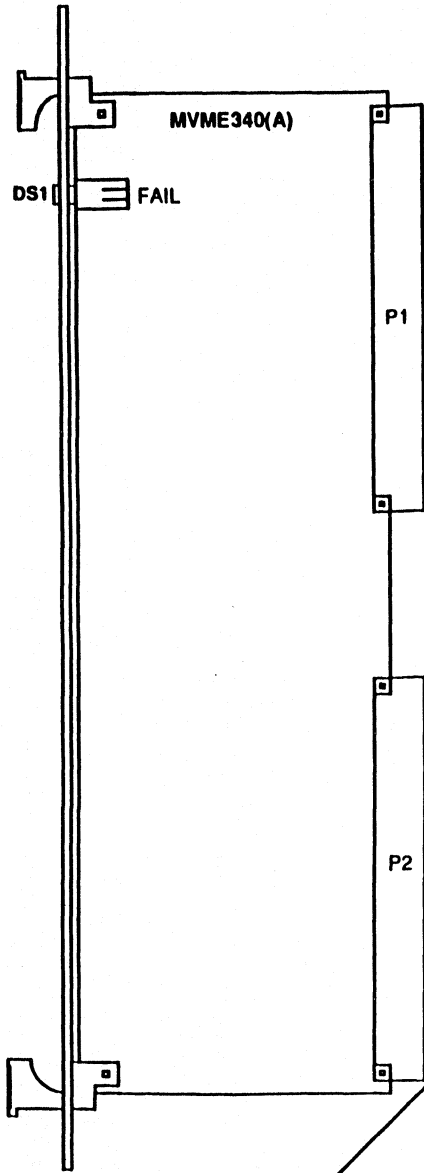
ADDRESS HANDSHAKE
LINE SELECT HEADER



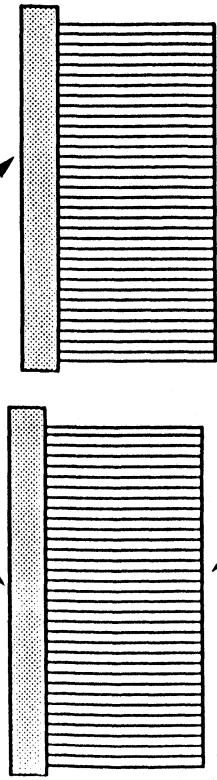
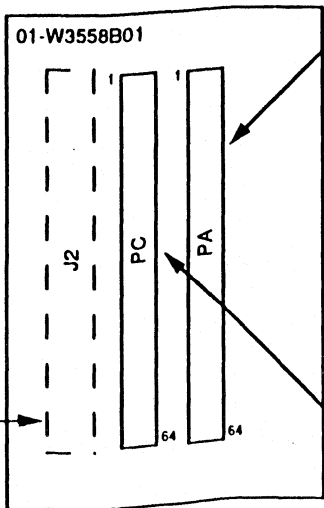
BASE ADDRESS RANGE SELECT HEADER

K1 JUMPERS	AM CODES	ADDRESS RANGE	DECODED ADDRESS LINES	RELEVANT BASE ADDRESS HEADERS
1 - 2, 3 - 4 K1	\$29, \$2D	SHORT I/O	A08 THRU A15	K4
3 - 4	\$39, \$3D	STANDARD	A08 THRU A23	K3 + K4
NONE 13	\$09, \$0D	EXTENDED	A08 THRU A31	K2 + K3 + K4

MVME340A
PARALLEL
INTERFACE/TIMER
MODULE
PAGE 2

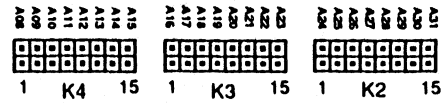


P2 ON VME340(A) CONNECTS TO J2 ON SUB-ASSY. BOARD.



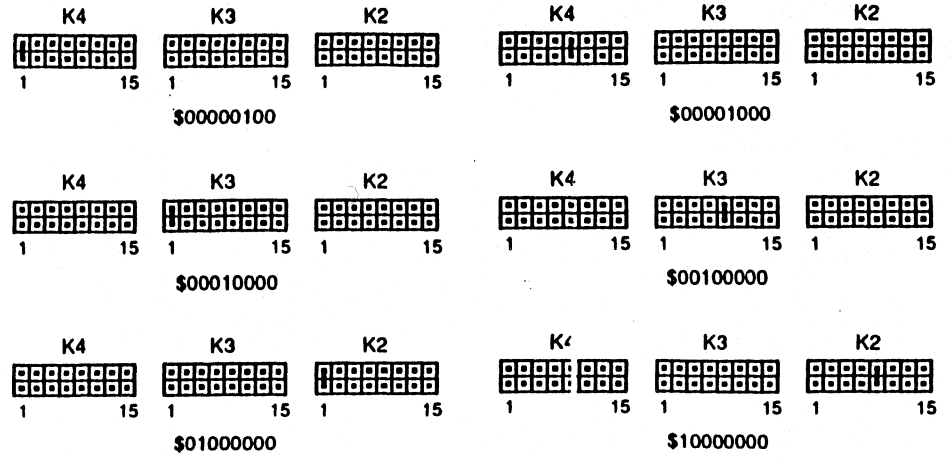
ONE 64-PINFLAT RIBBON CABLE GOES TO ONE PARALLEL INTERFACE PERIPHERAL AND THE OTHER GOES TO ANOTHER.

BASE ADDRESS SELE UMPERS

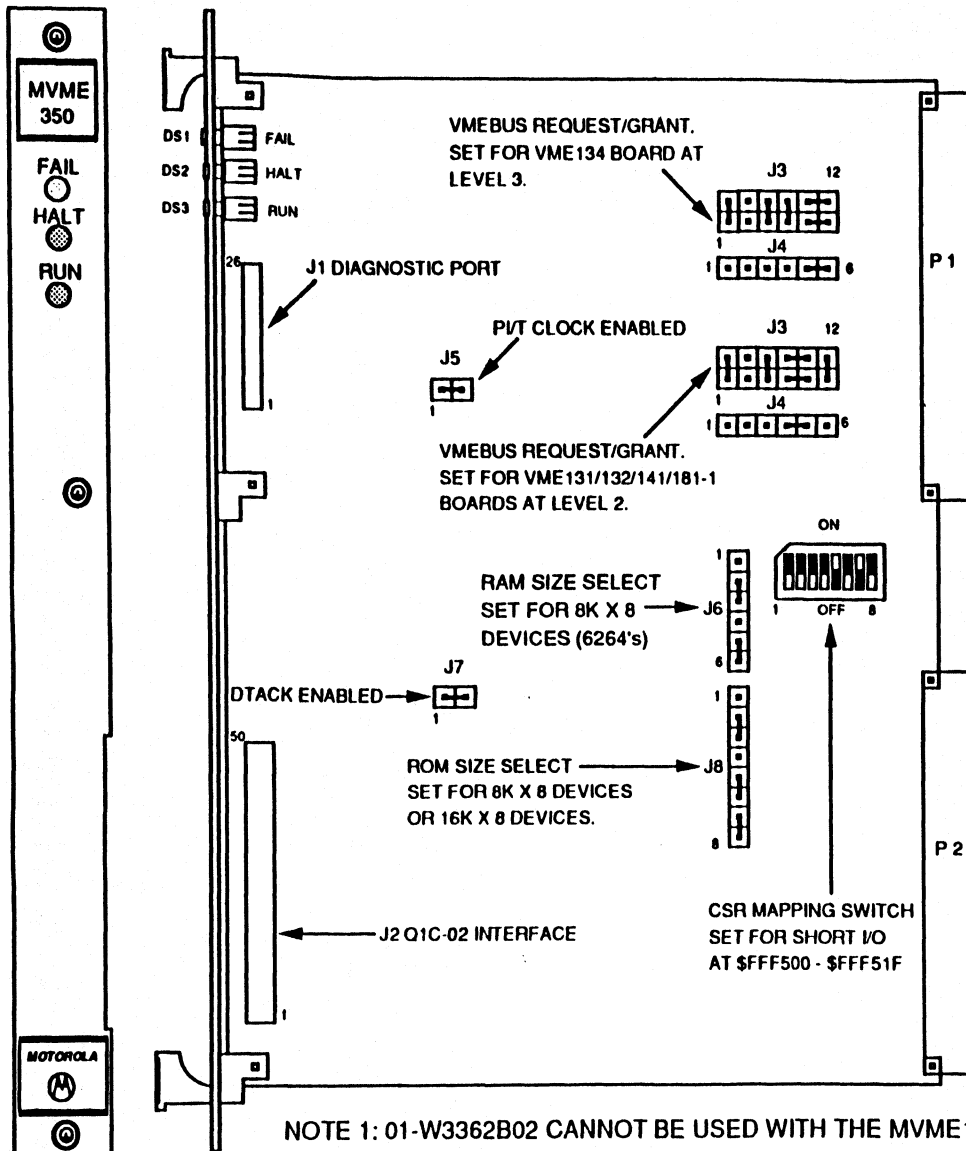


A_{xx} = 0 IF JUMPER IS INSTALLED
 A_{xx} = 1 IF JUMPER IS REMOVED

NOTE: SET FOR EXAMPLES ONLY AT SINGLE BIT INCREMENTS, BUT IS SELECTABLE IN 256-BYTE INCREMENTS. BASE ADDRESS SELECT RANGE (K1, K2 & K3) ALSO WORK IN CONJUNCTION WITH K1, BASE ADDRESS RANGE.



09/13/89



NOTE 1: 01-W3362B02 CANNOT BE USED WITH THE MVME134 CPU.

NOTE 2: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 3: SAME CONFIGURATION USED IN SYS3640, & 8608's.

PART NUMBERS:

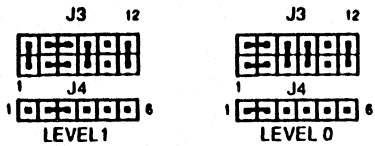
MVME350 01-W3362B01 96010817
60MB STREAMING TAPE CONTROLLER
AT "AA" REVISION IS EQUAL TO 150 MB
STREAMING TAPE CONTROLLER

MVME350 01-W3362B02 96010817
60/150MB STREAMING TAPE CONTROLLER

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT
REVISION INFORMATION.

09/11/90

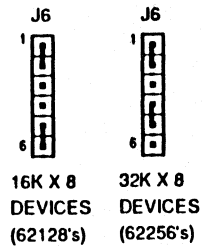
VMEBUS REQUEST/GRANT SELECT



P/I/T CLOCK DISABLED



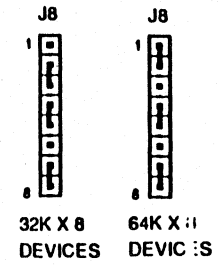
RAM SIZE SELECT



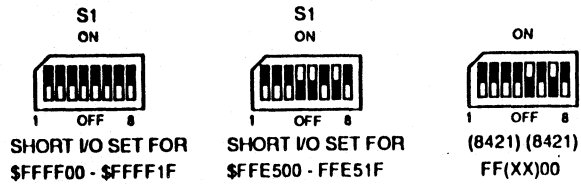
DTACK DISABLED



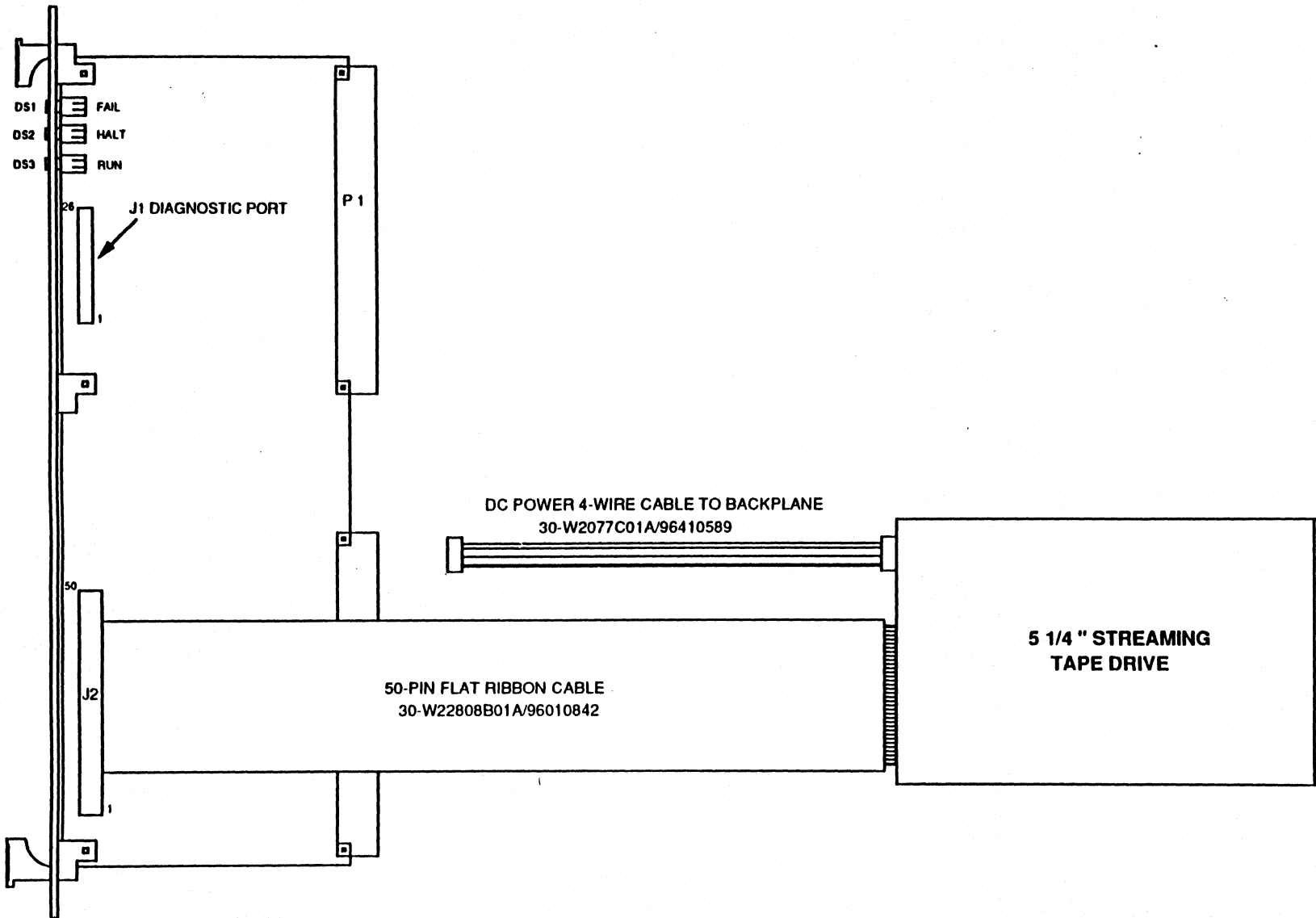
ROM SIZE SELECT



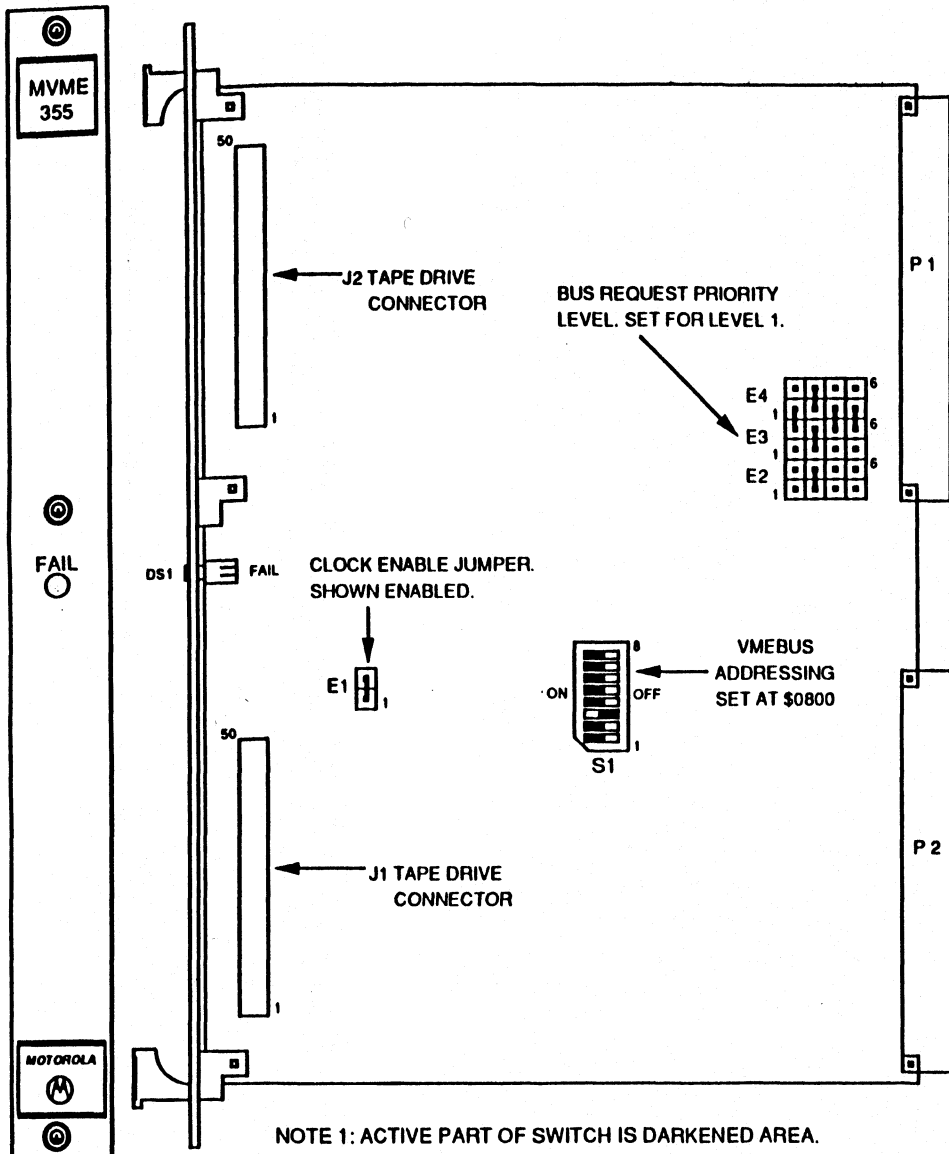
CSR MAPPING EXAMPLES



09/13/89



03/26/91



NOTE 1: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE2: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

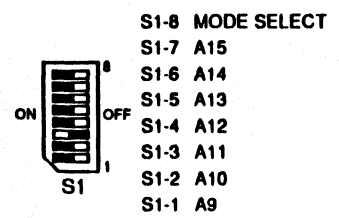
PART NUMBERS:

MVME355 01-W2852B01 96010820

MVME355P2 01-W3553B01 96011078

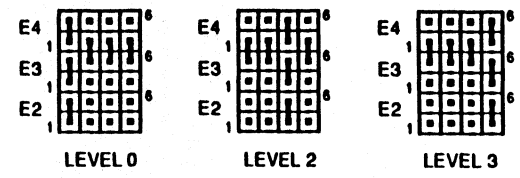
SEE CURRENT REVISION LEVE (CRL) FOR CURRENT REVISION INFORMATION.

VMEBUS ADDRESSING

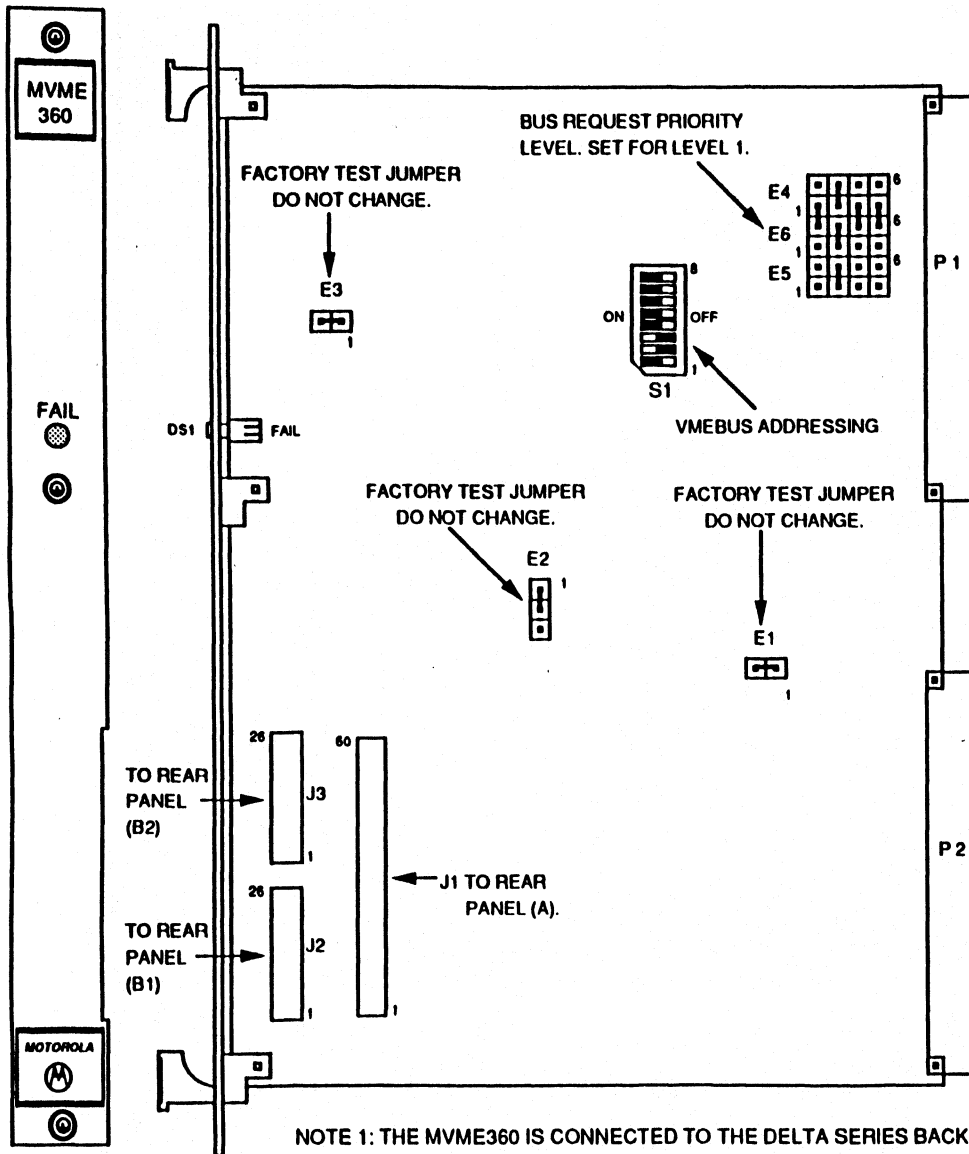


NOTE: MODE SELECT SET OFF = ADDRESS MODIFIER \$29
MODE SELECT SET ON = ADDRESS MODIFIER \$2D

VMEBUS PRIORITY INTERRUPT SELECT



03/15/91



NOTE 1: THE MVME360 IS CONNECTED TO THE DELTA SERIES BACKPLANE USING INTERNAL A (CONTROL) CABLES AND B (DATA) CABLES FROM THE FRONT PANEL OF THE BOARD TO A CONNECTOR PANEL ON THE BACKPLANE OF THE SYSTEM.

NOTE 2: ACTIVE PART OF SWITCH IS DARKENED AREA.

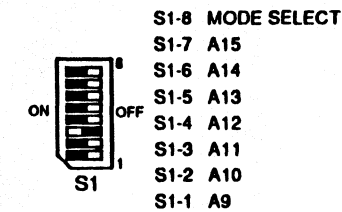
NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TERMINATION BOARD.)

PART NUMBERS:

MVME360 01-W2826B01 96010819
(01NW9804C96)

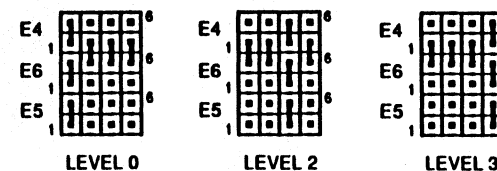
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

VMEBUS ADDRESSING



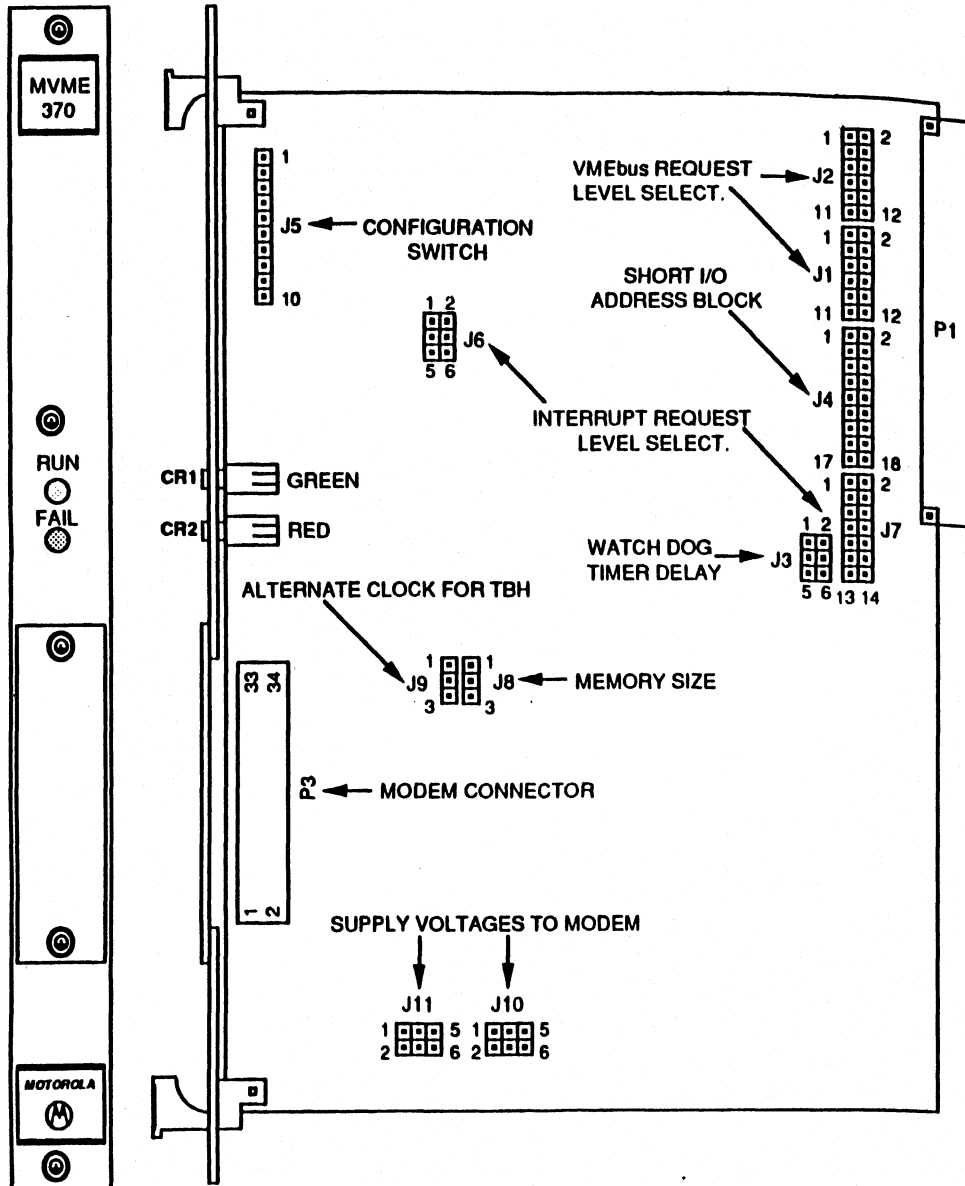
NOTE: MODE SELECT SET OFF = ADDRESS MODIFIER \$29
MODE SELECT SET ON = ADDRESS MODIFIER \$2D

VMEBUS PRIORITY INTERRUPT SELECT



03/15/91

**MVME360
SMD DISK
CONTROLLER
PAGE 1 OF 1**



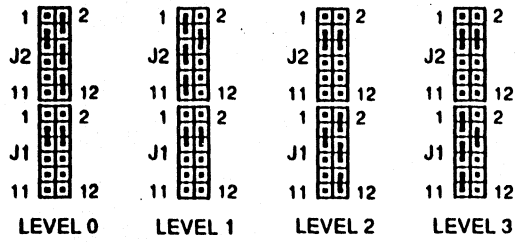
NOTE 1: MVME372 IS USED WITH THE MVME371FS-1.

PART NUMBERS:
 MVME370 01NW9804D13 76435142
 SEE CURRENT REVISION LEVEL (CRL)
 FOR CURRENT REVISION INFORMATION.

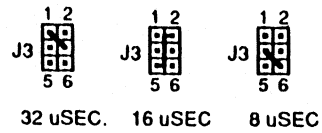
09/13/89

MVME370
VMEbus
TOKEN BUS
CONTROLLER
 PAC | OF 2

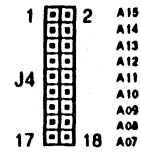
VMEbus REQUEST LEVEL SELECT



WATCH DOG TIMER DELAY



SHORT I/O ADDRESS BLOCK



JUMPER INSTALLED
SELECTS ADDRESS LINE.

MEMORY SIZE SELECT



1 - 2 = 512K
(FACTORY DEFAULT)
2 - 3 = 128K

ALTERNATE CLOCK FOR TBH

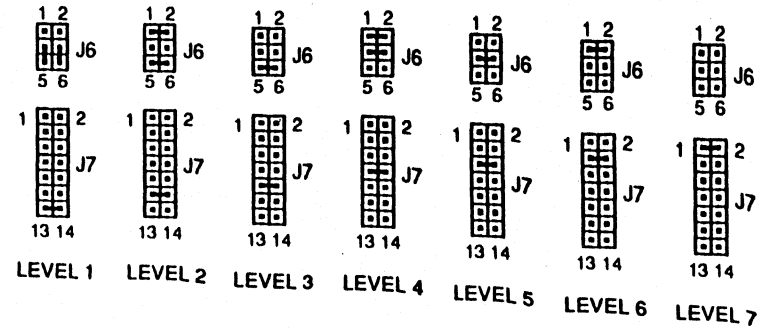


1 - 2 = USE MODEM CLOCK FOR TBH
(FACTORY DEFAULT)
2 - 3 = USE 80186 CLOCK FOR TBH

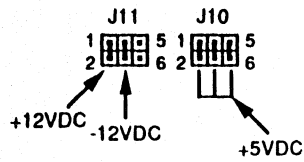
CONFIGURATION SWITCH

FUNCTION	TEST		RESERVED	BOOT	
	1-2	3-4	5-6	7-8	9-10
1 NORMAL	OUT	OUT	OUT	--	--
DEBUGGER	OUT	OUT	IN	--	--
J5 RESERVED	OUT	IN	OUT	--	--
RESERVED	OUT	IN	IN	--	--
MODEM XMIT ADJUST	IN	OUT	OUT	--	--
10 RESERVED	IN	OUT	IN	--	--
TEST MODE	IN	IN	OUT	--	--
SPECIAL MODEM TEST	IN	IN	IN	--	--
DOWNLOAD FROM NETWORK	--	--	--	--	--
DOWNLOAD FROM VMEbus	--	--	--	--	--

INTERRUPT REQUEST LEVEL SELECT

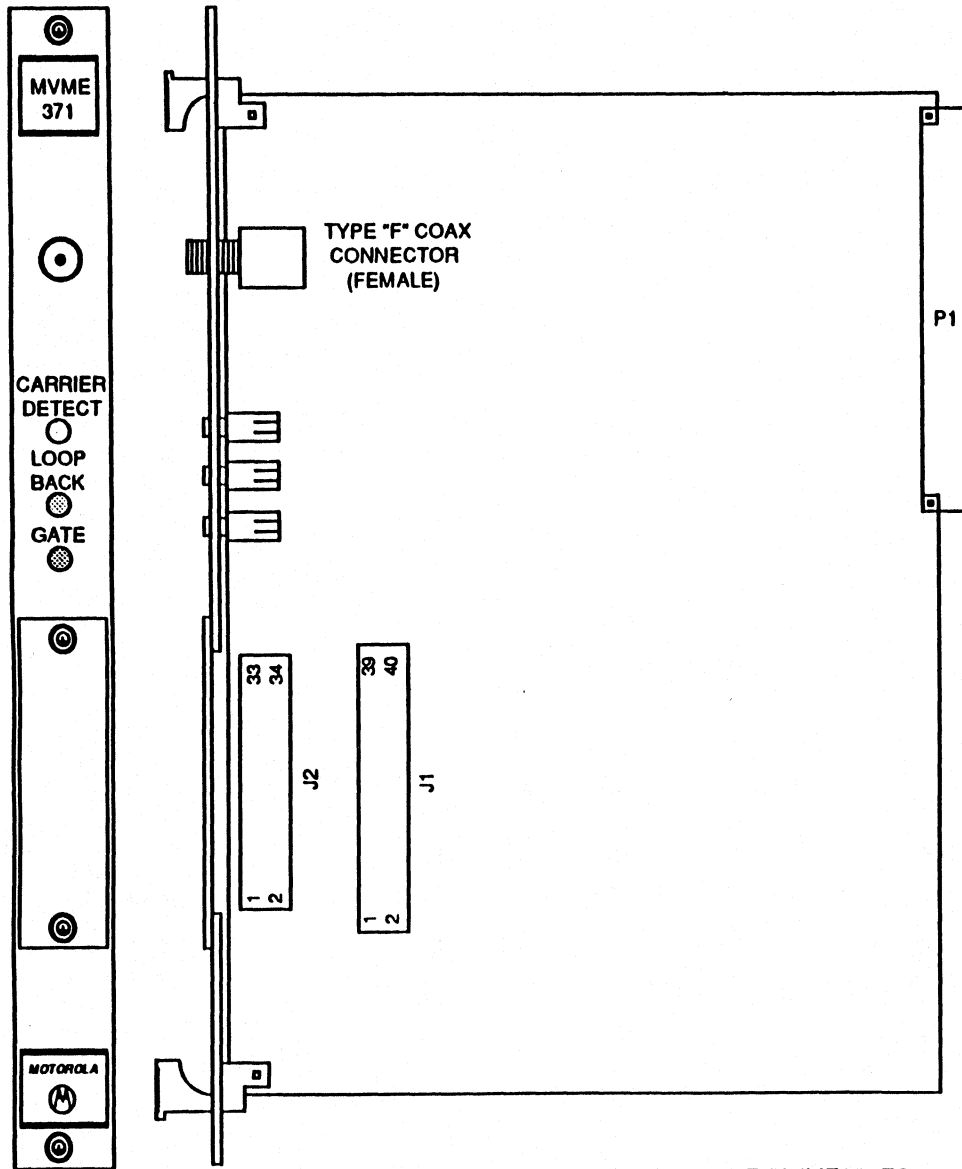


SUPPLY VOLTAGES TO MODEM



NO JUMPER MEANS POWER IS SUPPLIED
THRU THE BACKPLANE. JUMPER INSTALLED
MEANS POWER IS SUPPLIED BY THE TBC.

09/13/89



NOTE 1: MVME372 IS USED WITH THE MVME371FS-1.

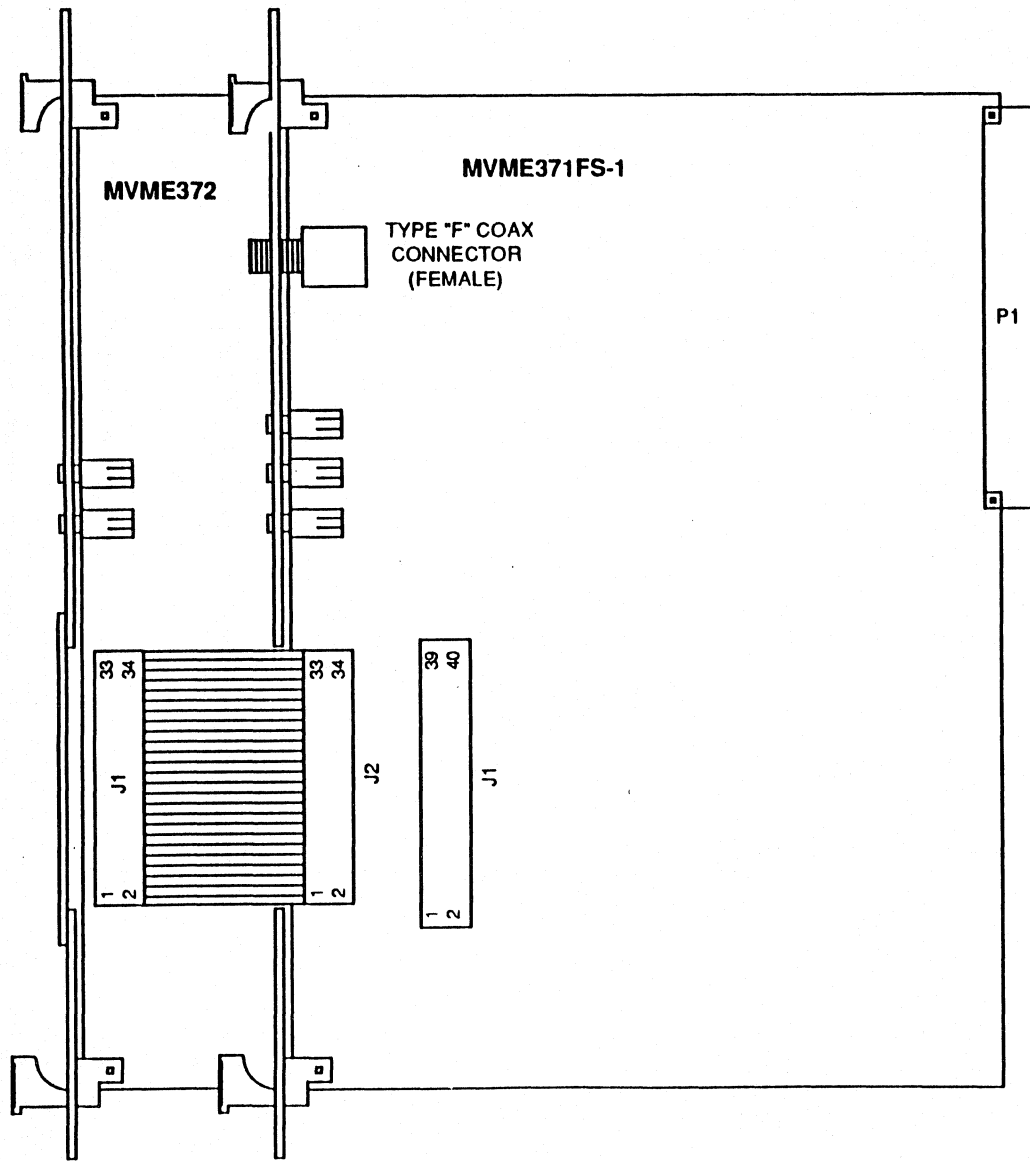
PART NUMBERS:

- | | | |
|----------------------|----------------------|------------|
| MVME371-1 | 01NW9804D11 | 76435337 |
| MVME371FA | 01-W2928B01 | NO FSD P/N |
| CABLE | 30-W2775B01 | 76435386 |
| MVME371FS-1 | 01NW9804D24 | 76435315 |
| HEAD END REMODULATOR | 01NW9804D20 | 76435383 |
| | CDS P/N L5-R051-21MO | |
| MVME371FS-2 | 01NW9804D26 | 76435380 |
| MVME371FS-3 | 01NW9804D26 | 76435314 |

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

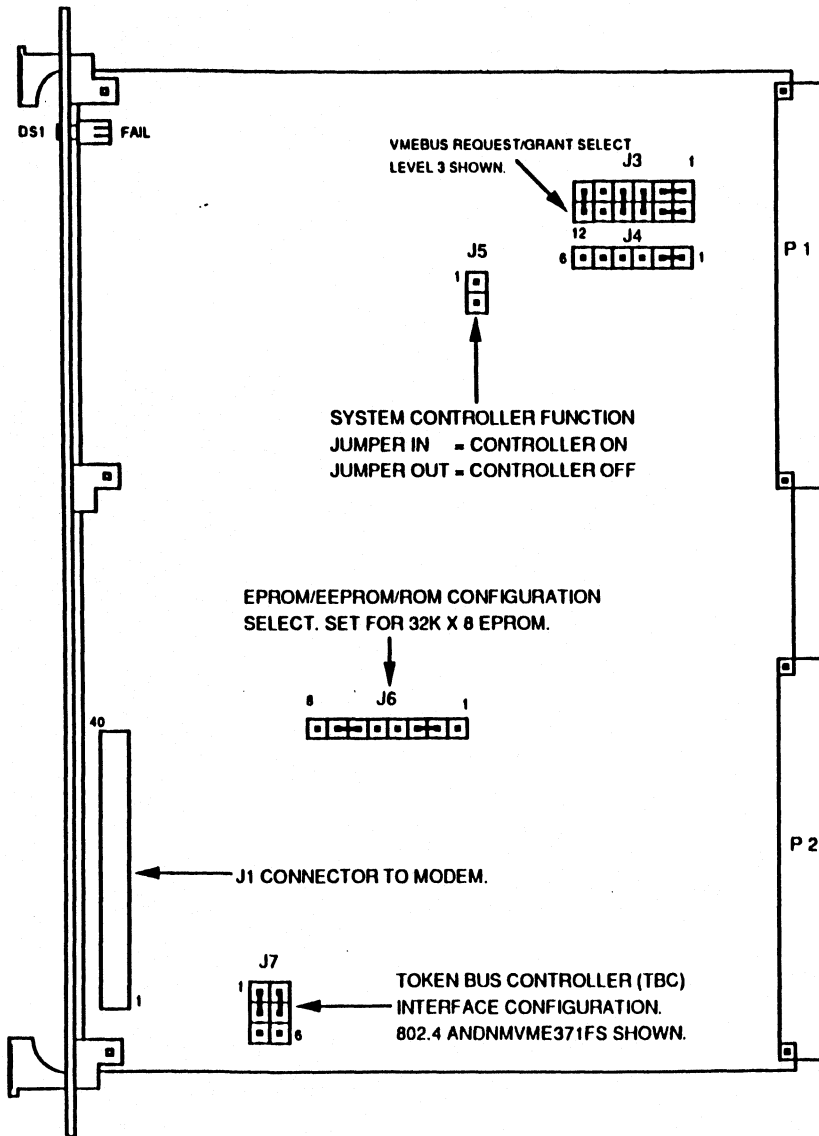
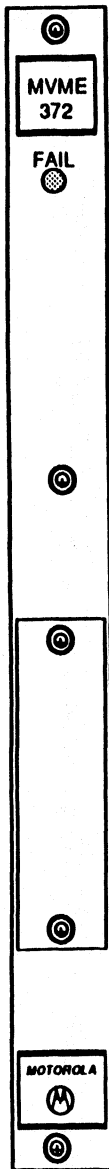
09/13/89

MVME371
10 MBITS/SEC
BROADBAND
MODEM
 PA () OF 2



NOTE 1: MVME372 AND MVME371FS-1 ARE CABLED TOGETHER THRU THE MODEM CONNECTOR BEHIND THE REMOVABLE FRONT PANEL.

09/13/89



PART NUMBERS:

MVME372 01-W3419B03 96011079

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

NOTE 1: FOR OTHER ASSEMBLIES OF THE MVME372, SEE THE
MCD CROSS-REFERENCE BIBLE.

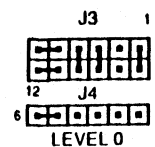
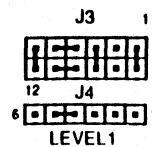
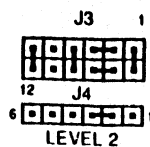
NOTE 2: FOR CABLING INFORMATION, SEE MVME371 CABLING.

TOKEN BUS CONTROLLER (TBC)

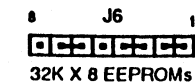
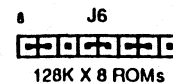
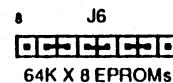
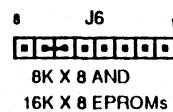


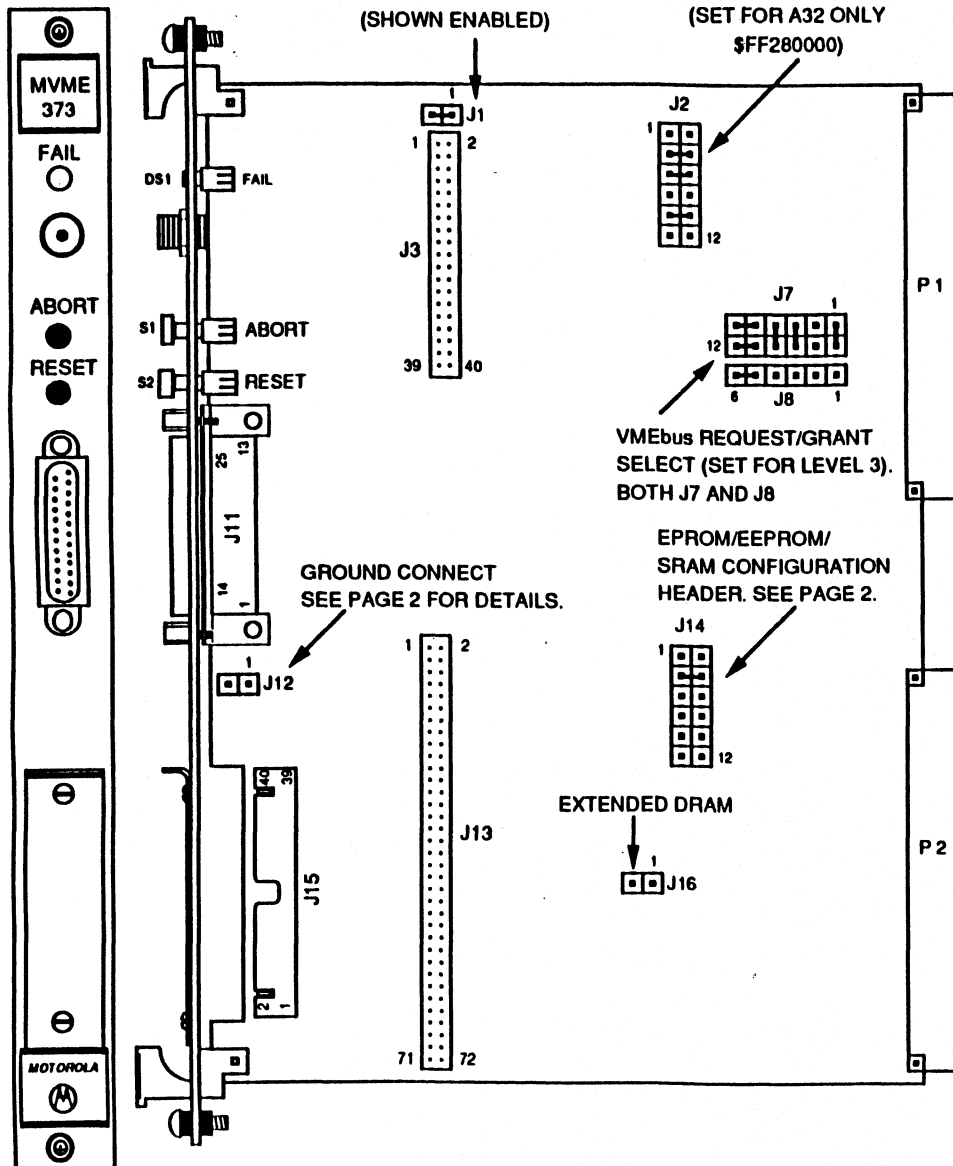
SET FOR MVME371

VMEBUS REQUEST/GRANT SELECT



EPROM/EEPROM/ROM SIZE SELECT





PART NUMBERS:

MVME373 01-W3564B01 96011080

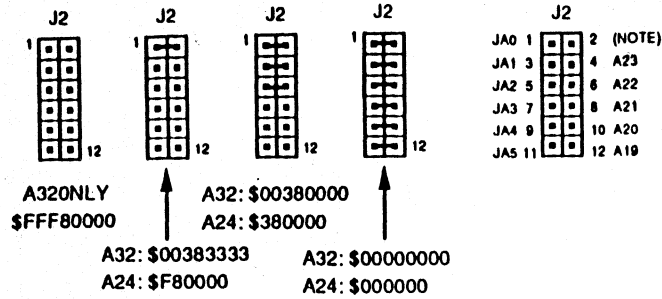
MVME373A 01-W3564B02 96011980

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

NOTE 1:

08/20/90

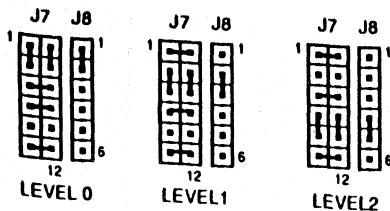
BASE ADDRESS JUMPERS JA0 THRU JA5



NOTE: WHEN JA0 (1-2) IS NOT INSTALLED, THE MVME373 RESPONDS TO A 32 ADDRESS MODIFIER CODE 09 AND 0D AT A BASE ADDRESS \$FFXX0000, WHERE XX IS THE ADDRESS SELECTED BY THE JUMPERS.

WHEN JA0 (1-2) IS INSTALLED, THE MVME373 RESPONDS TO ADDRESS MODIFIER CODE 09 AND 0D AT BASE ADDRESS \$00XX0000 ASND TO A24 ADDRESS MODIFIER CODE 39 AND 3D AT BASE ADDRESS \$XX0000, WHERE XX IS THE ADDRESS SELECTED BY THE JUMPERS.

VMEBUS REQUEST/GRANT SELECT

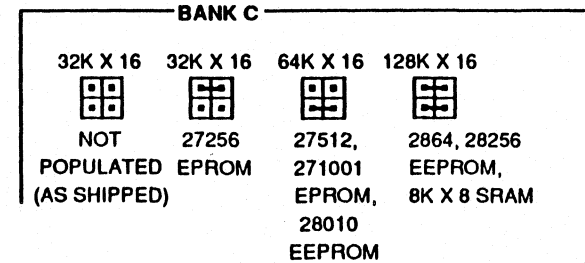
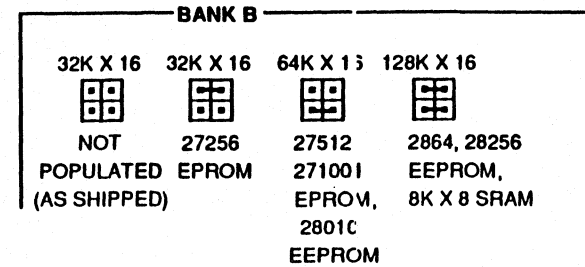
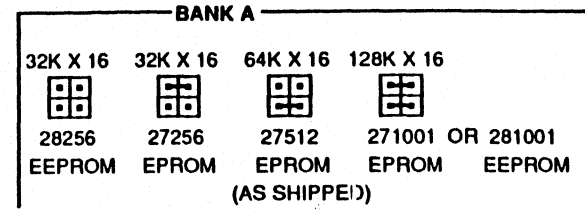
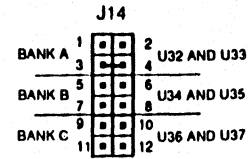


EXTENDED DRAM HEADER

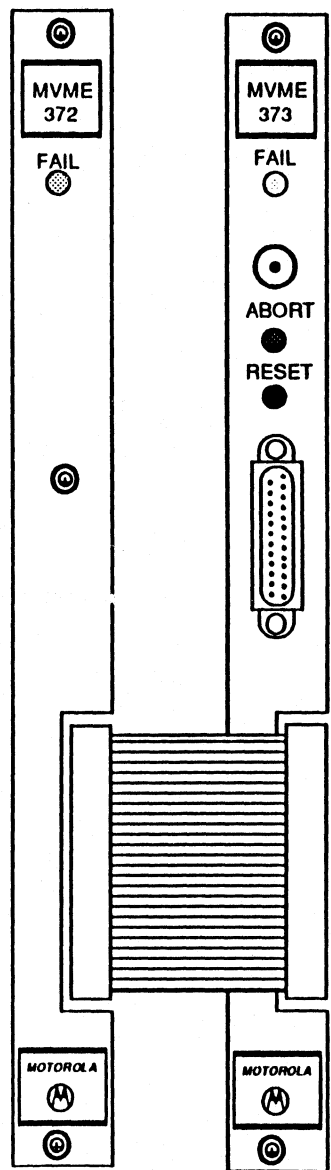
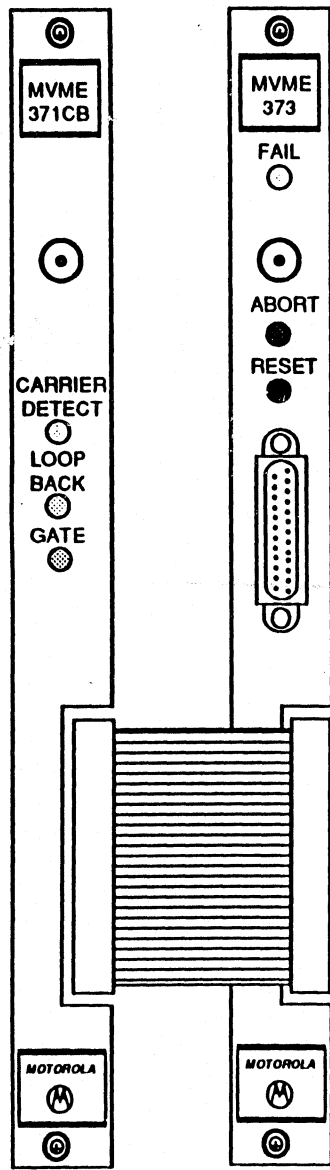


INSTALLED: EXTENDED DRAM DISABLED
 REMOVED: EXTENDED DRAM ENABLES
 (AS SHIPPED)

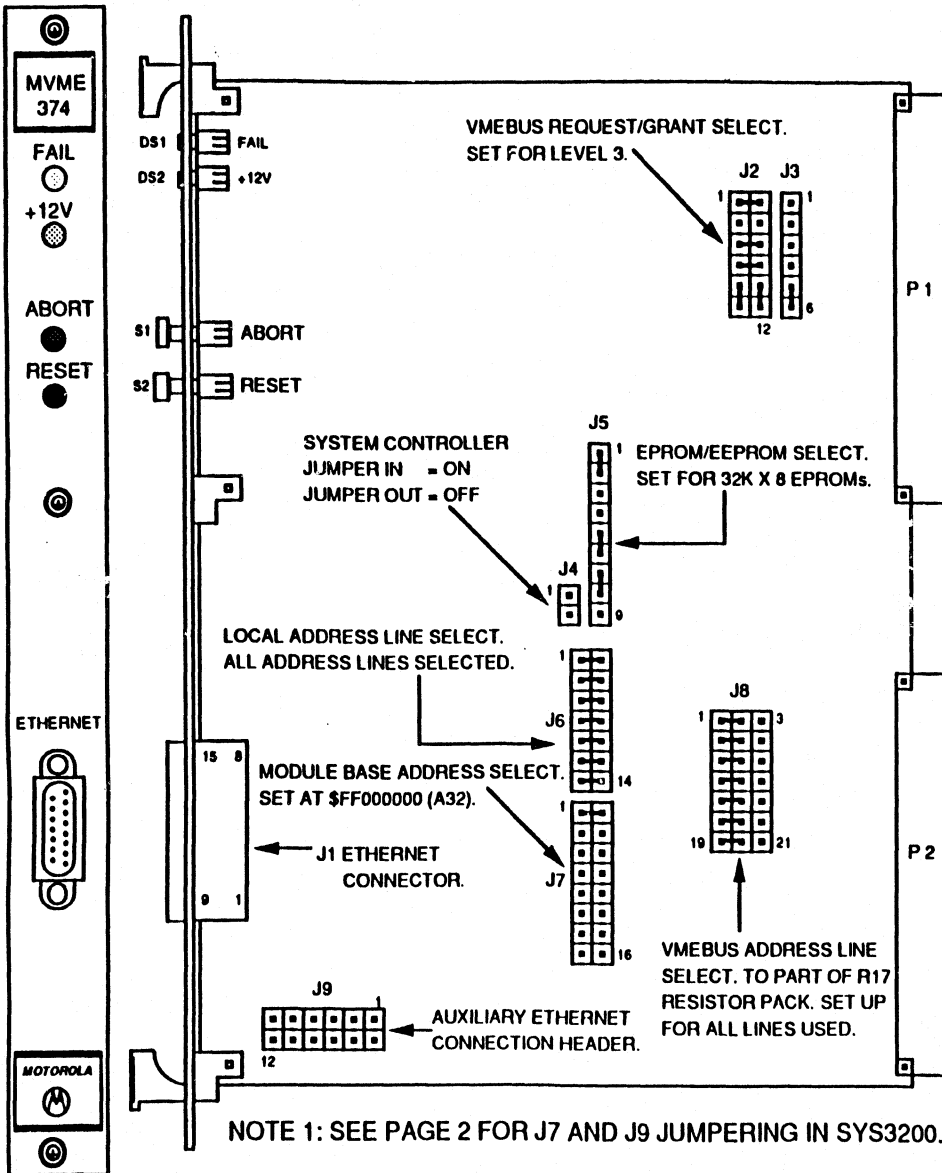
EPROM/EEPROM/SRAM CONFIGURATION HEADER



08/20/90



03/15/91



NOTE 1: SEE PAGE 2 FOR J7 AND J9 JUMPERING IN SYS3200.

NOTE 2: SEE PAGE 2 FOR J7 JUMPERING IN SYS3604/08, 3640, & 8608's. ALL OTHER JUMPERS ARE THE SAME.

NOTE 3: SYS3400/8400 SERIES, ALL JUMPERS ARE THE SAME EXCEPT J7. SEE PAGE 2.

PART NUMBERS:

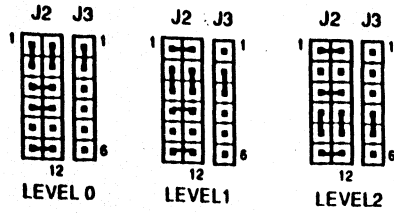
MVME374 01-W3517B01 96010996

CABLE, INTERNAL 30-W2822B02 96410598

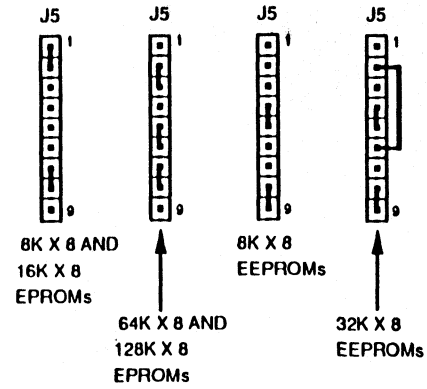
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

02/26/90

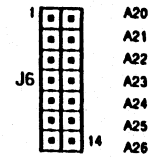
VMEBUS REQUEST/GRANT SELECT



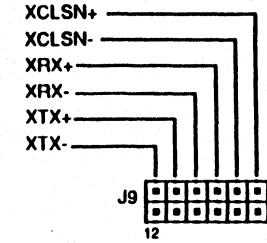
EPROM/EEPROM SIZE SELECT



LOCAL ADDRESS LINE SELECT

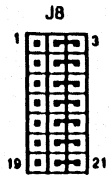


AUXILIARY ETHERNET CONNECTOR



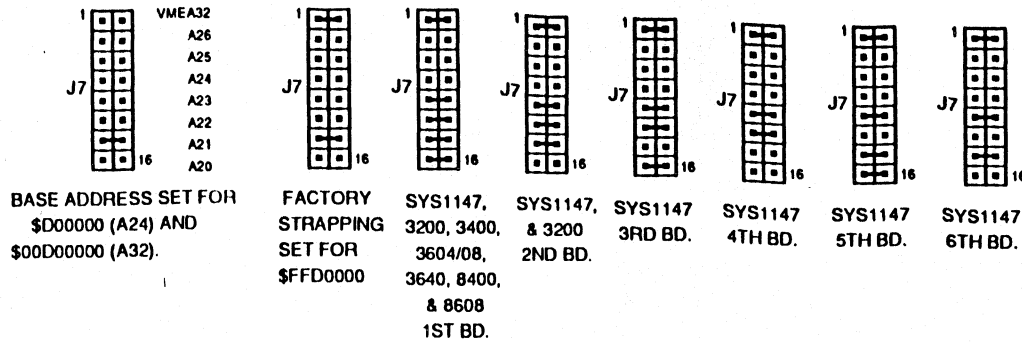
PINS 2-12 (IO) TO THE P2 CONNECTOR.
INSERT ALL JUMPERS FOR SYS3200.

VMEBUS ADDRESS LINE SELECT



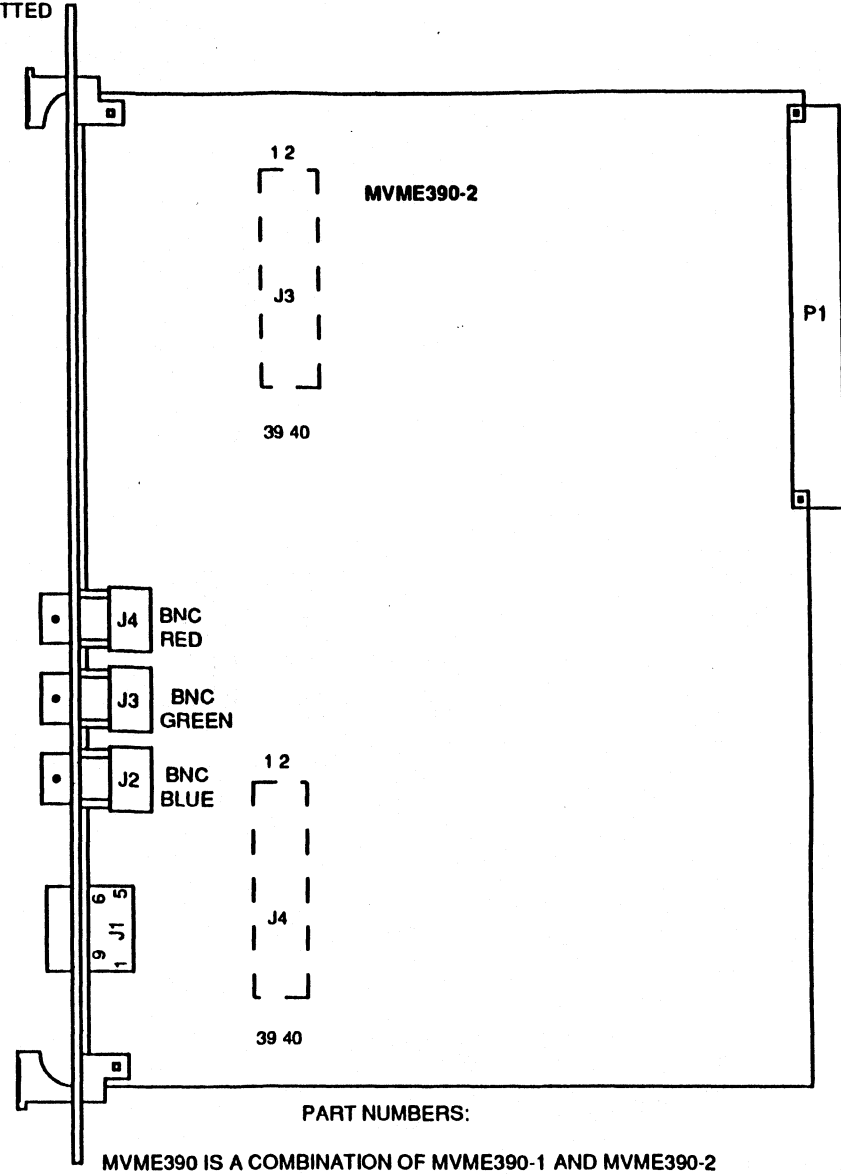
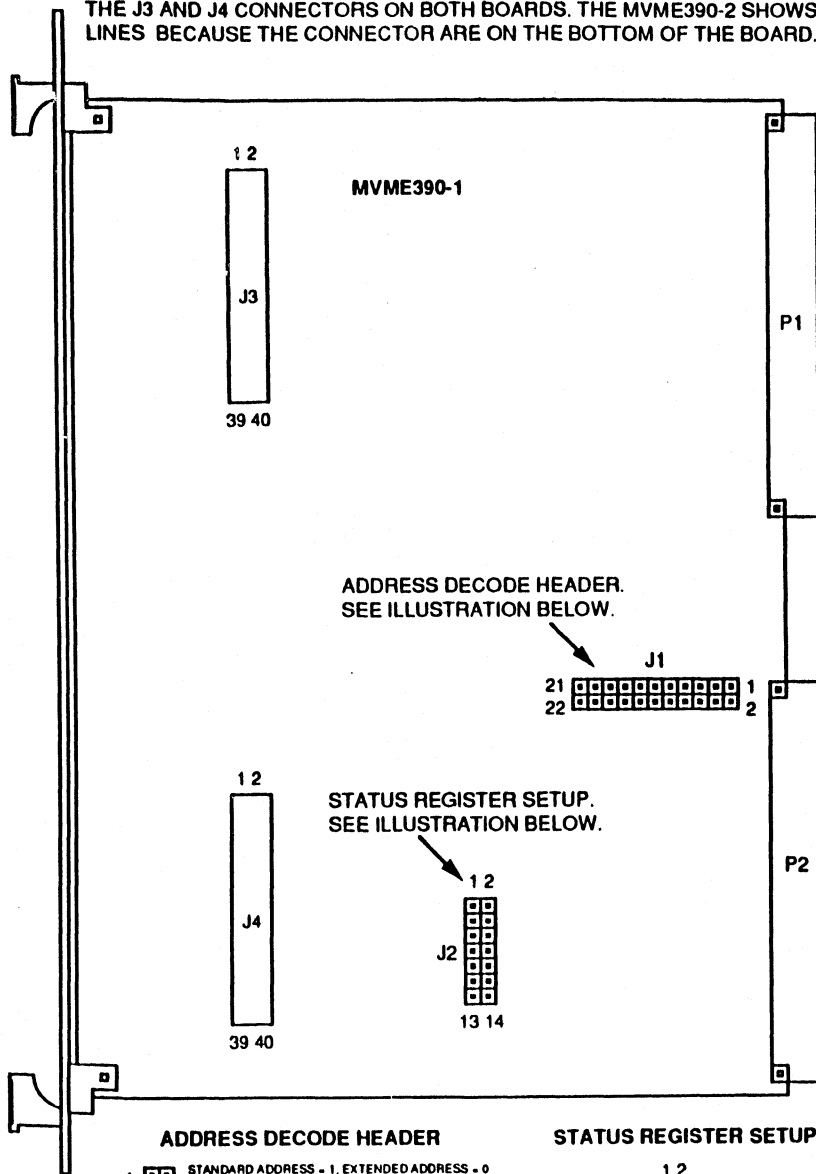
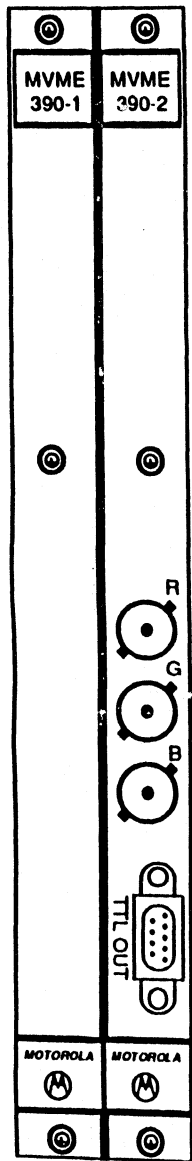
SET UP WITH NO LINES USED

BASE ADDRESS SELECT

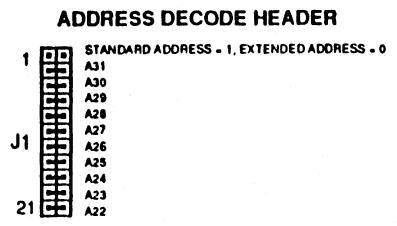


02/26/90

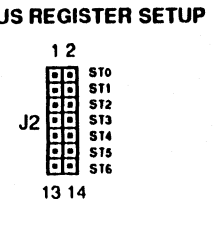
NOTE 1: MVME390-2 PIGGY BACKS THE MVME390-1 AND IS INTERCONNECTED BETWEEN THE J3 AND J4 CONNECTORS ON BOTH BOARDS. THE MVME390-2 SHOWS DOTTED LINES BECAUSE THE CONNECTOR ARE ON THE BOTTOM OF THE BOARD.



09/13/89



NO JUMPER = LOGIC 1
 JUMPER IN = LOGIC 0



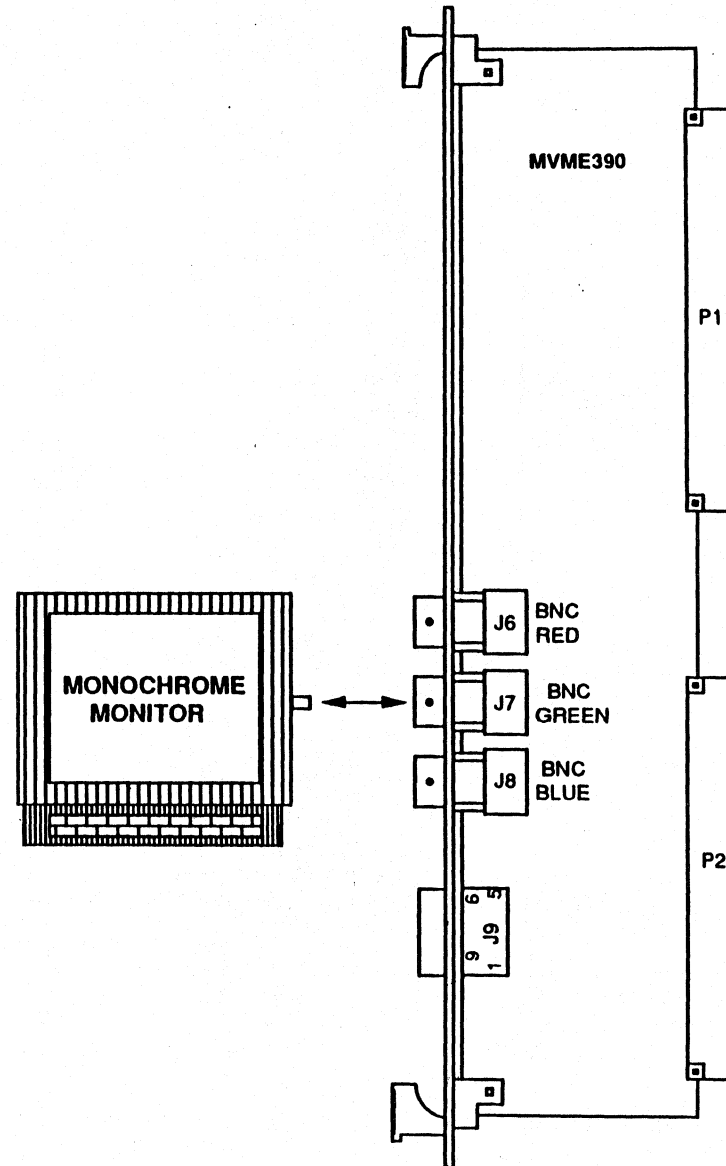
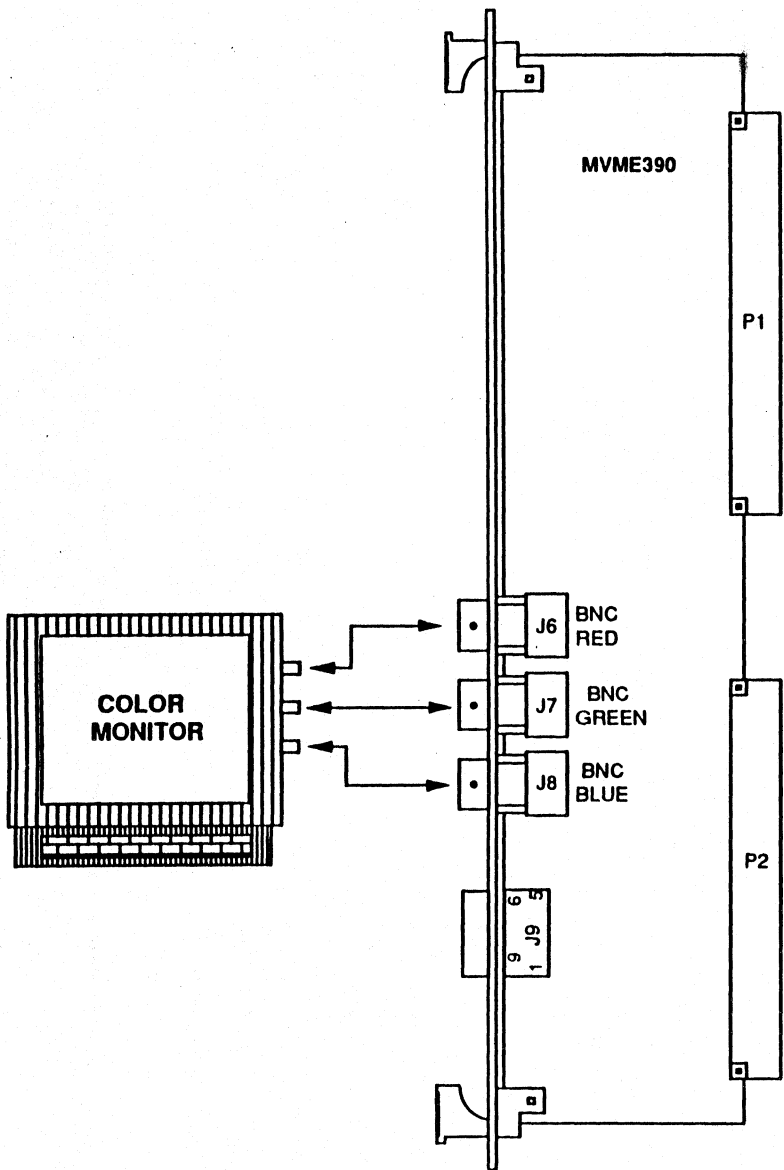
PART NUMBERS:

MVME390 IS A COMBINATION OF MVME390-1 AND MVME390-2
 AND IS NOT SUPPORTED BY FSD OR NTSC

- MVME390-1 01-W3409B01 76435128
- MVME390-1 01-W3409B02 76435130
- MVME390-2 01-W3416B01 76435131

SEE CURRENT REVISION LEVEL (CRL) FOR
 CURR: REVISION INFORMATION.

**MVME390 SET
 GRAPHICS
 CONTROLLER
 PAC OF 2**

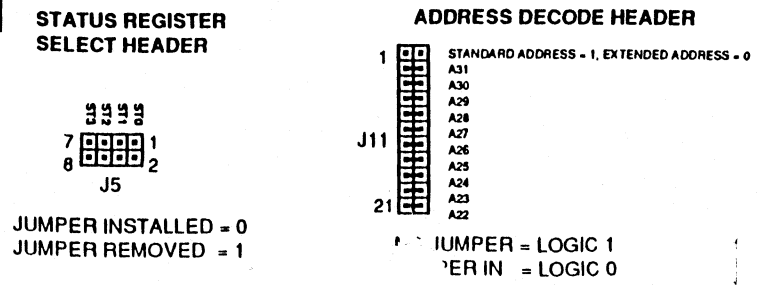
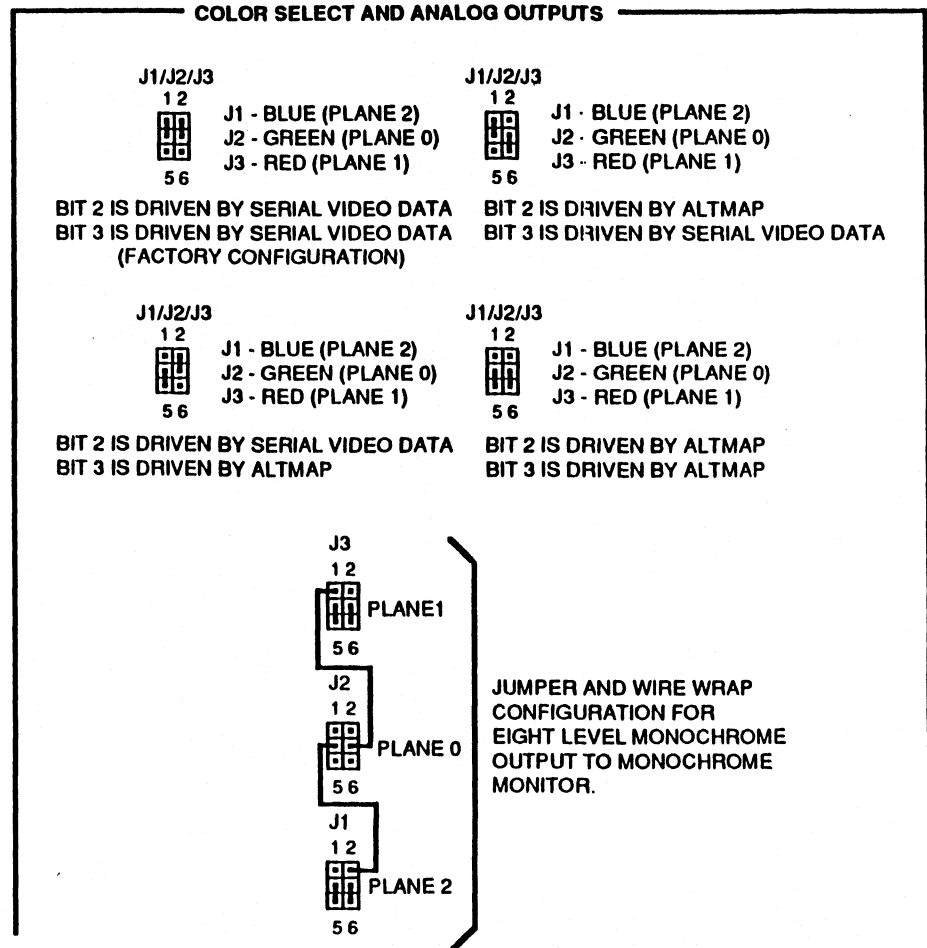
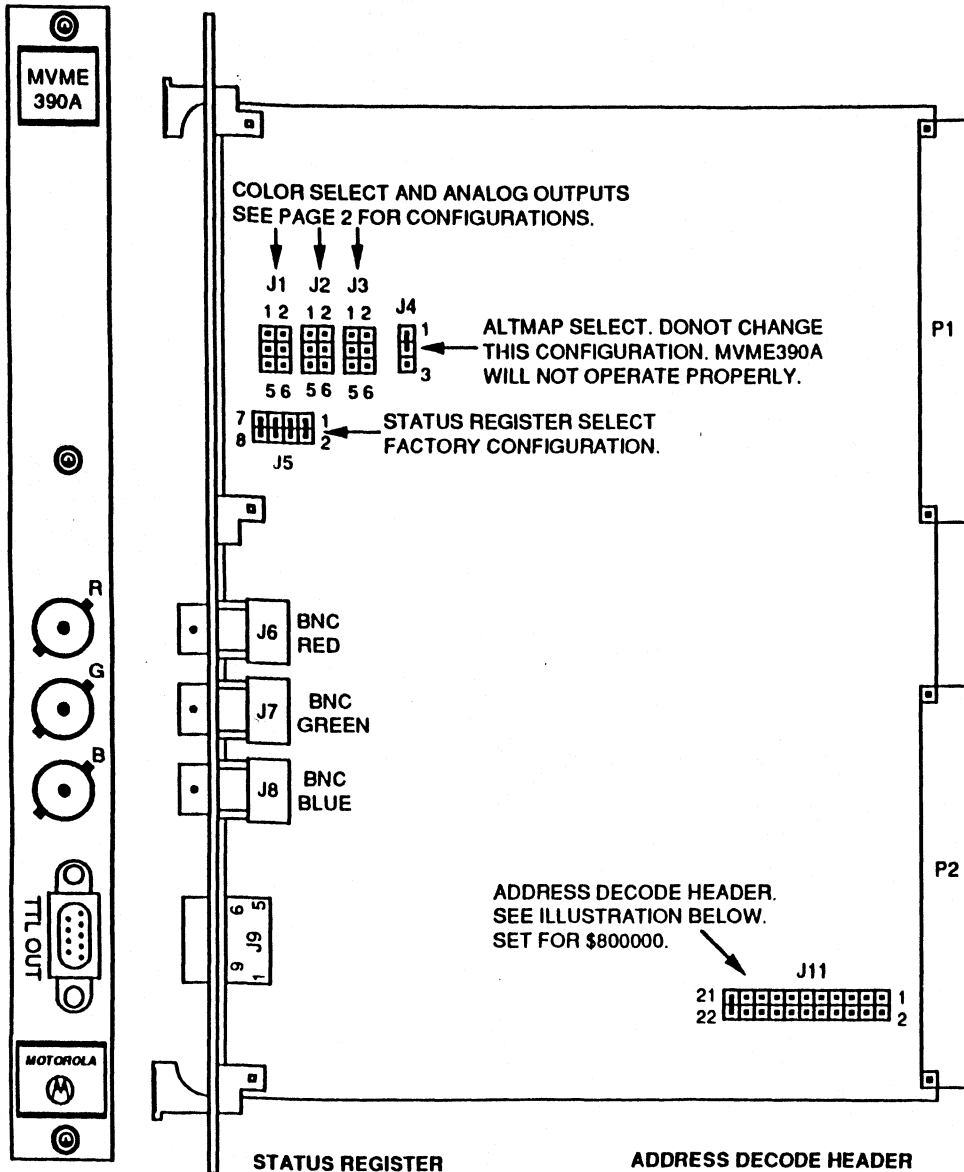


09/13/89

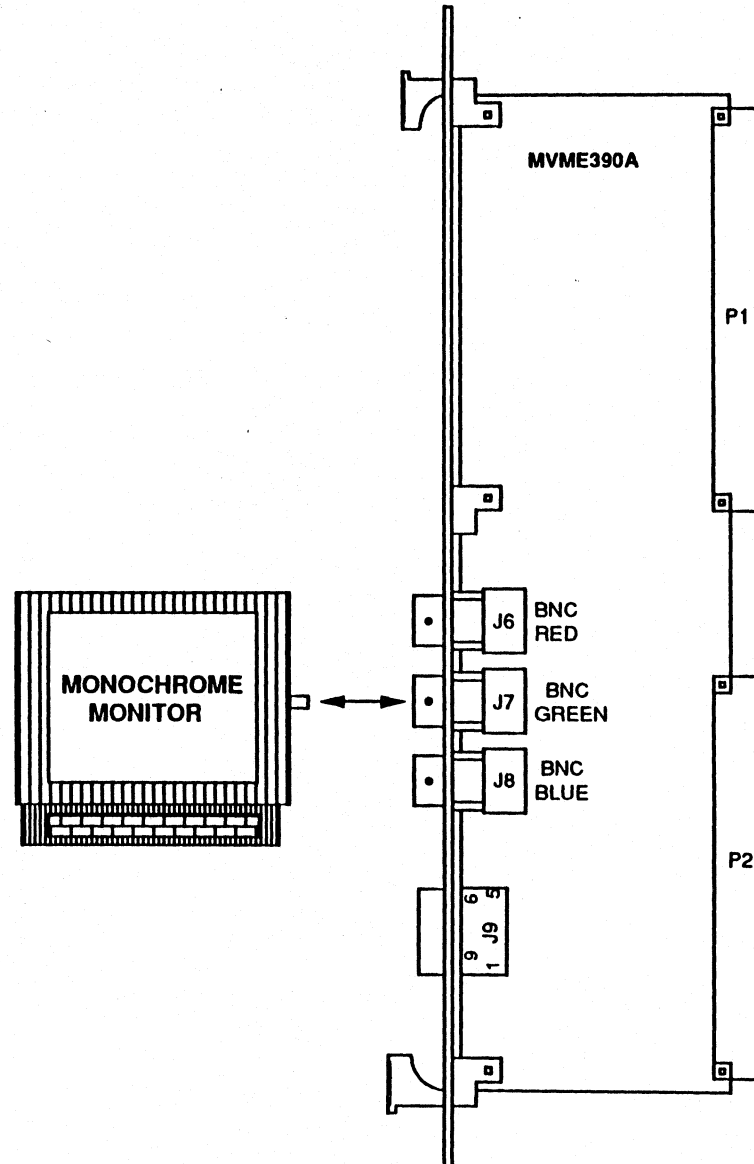
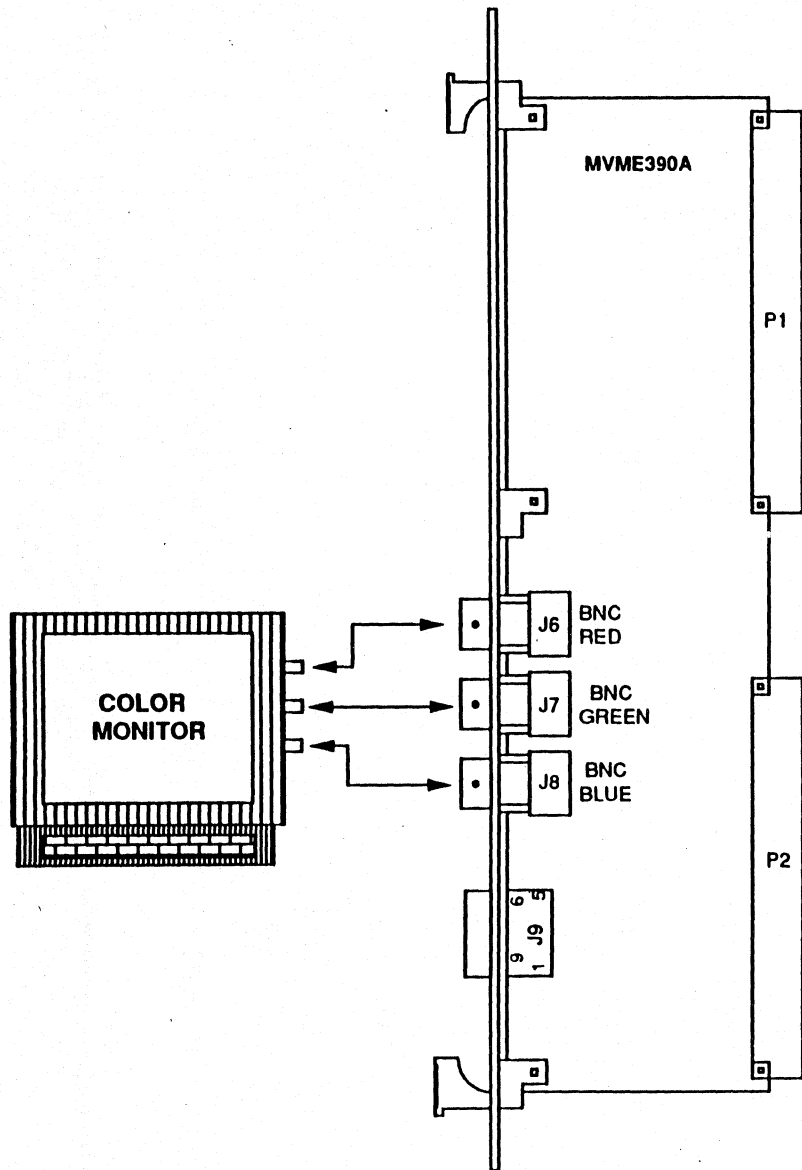
PART NUMBERS:

MVME390A 01-W3462B01 76435361

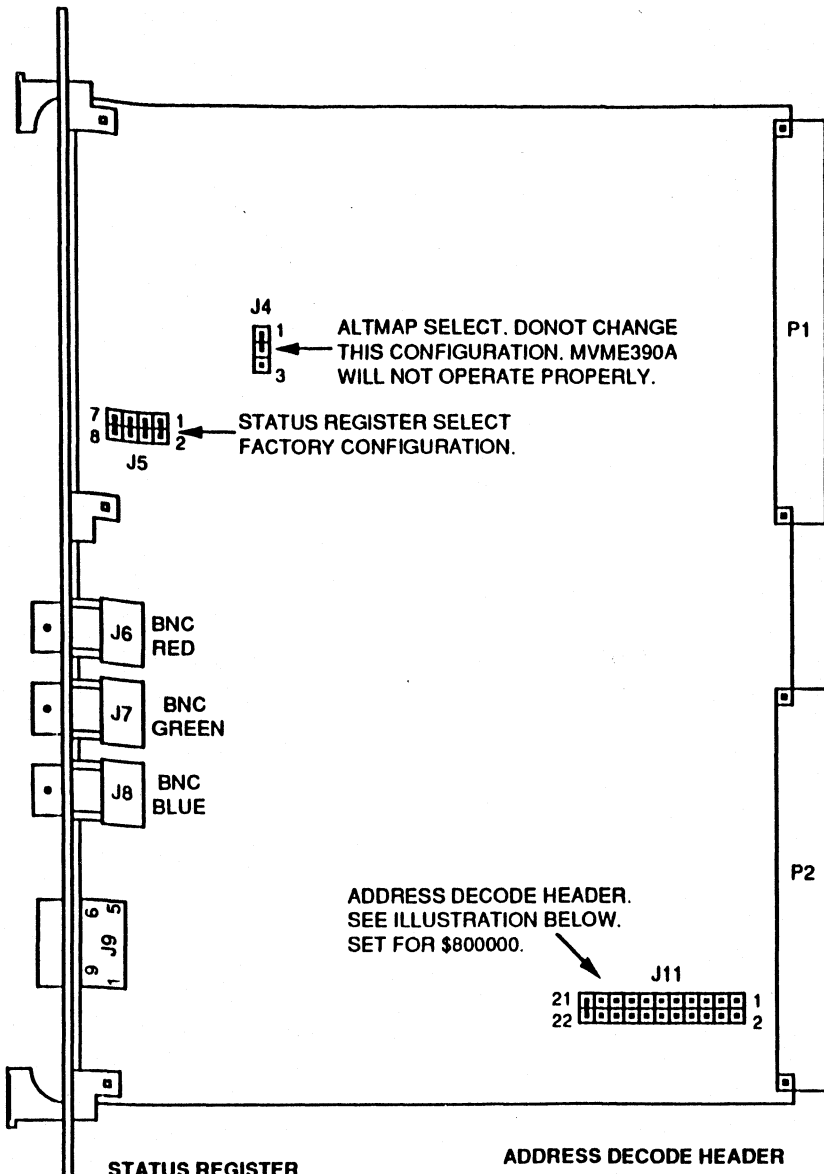
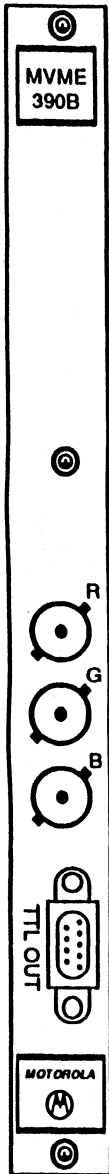
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.



09/13/89



09/13/89



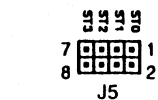
PART NUMBERS:

MVME390B 01-W3462B03 76435590

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

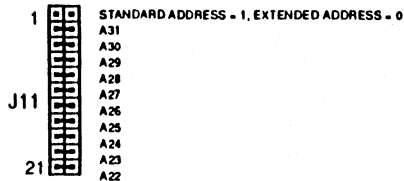
09/13/89

STATUS REGISTER SELECT HEADER

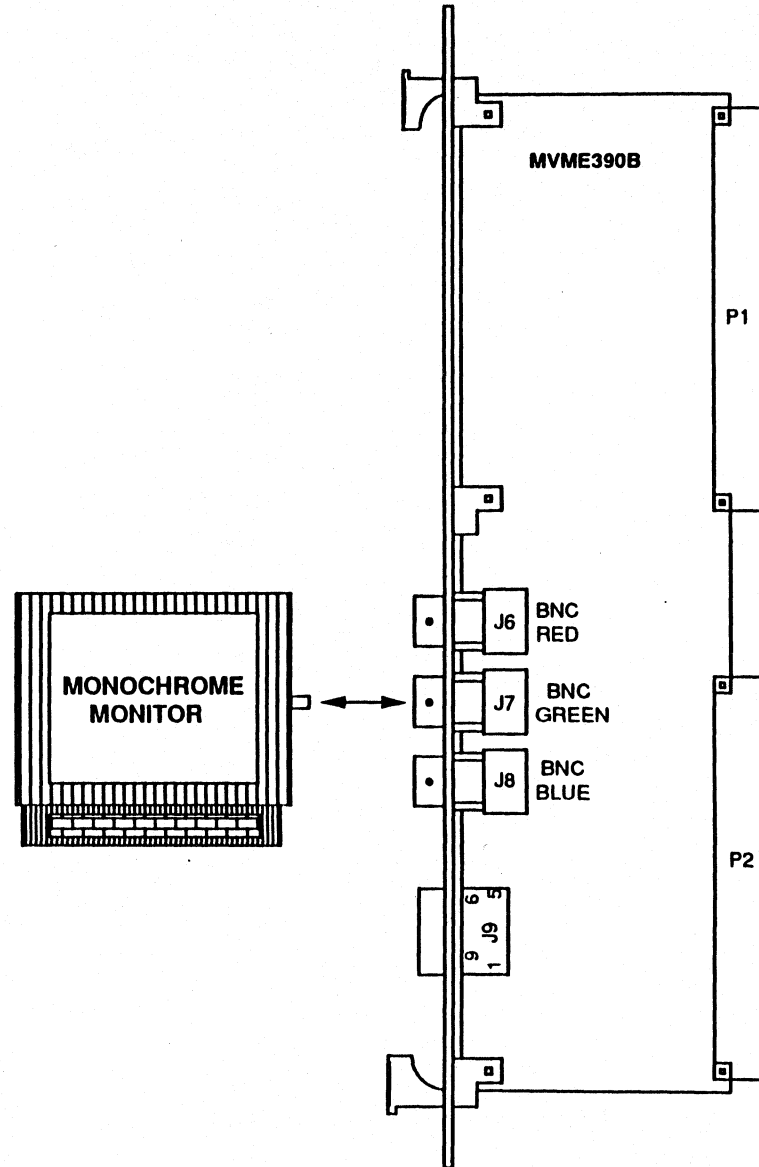
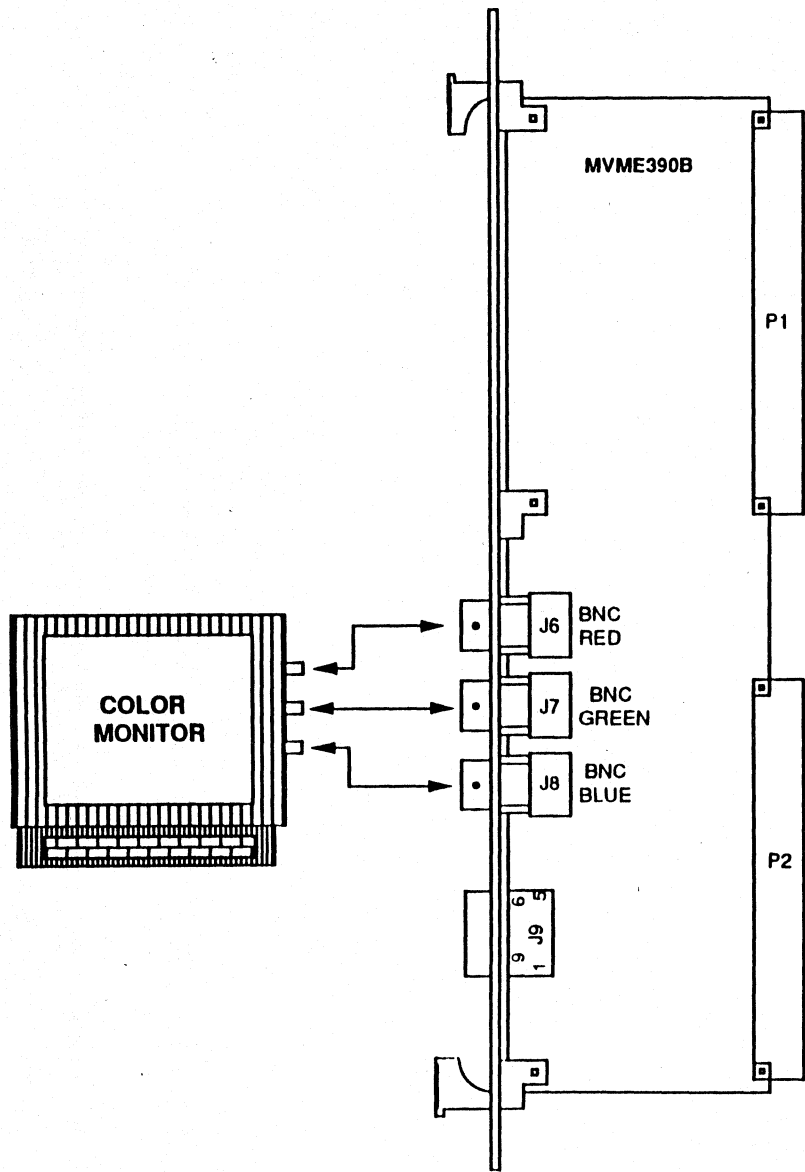


JUMPER INSTALLED = 0
JUMPER REMOVED = 1

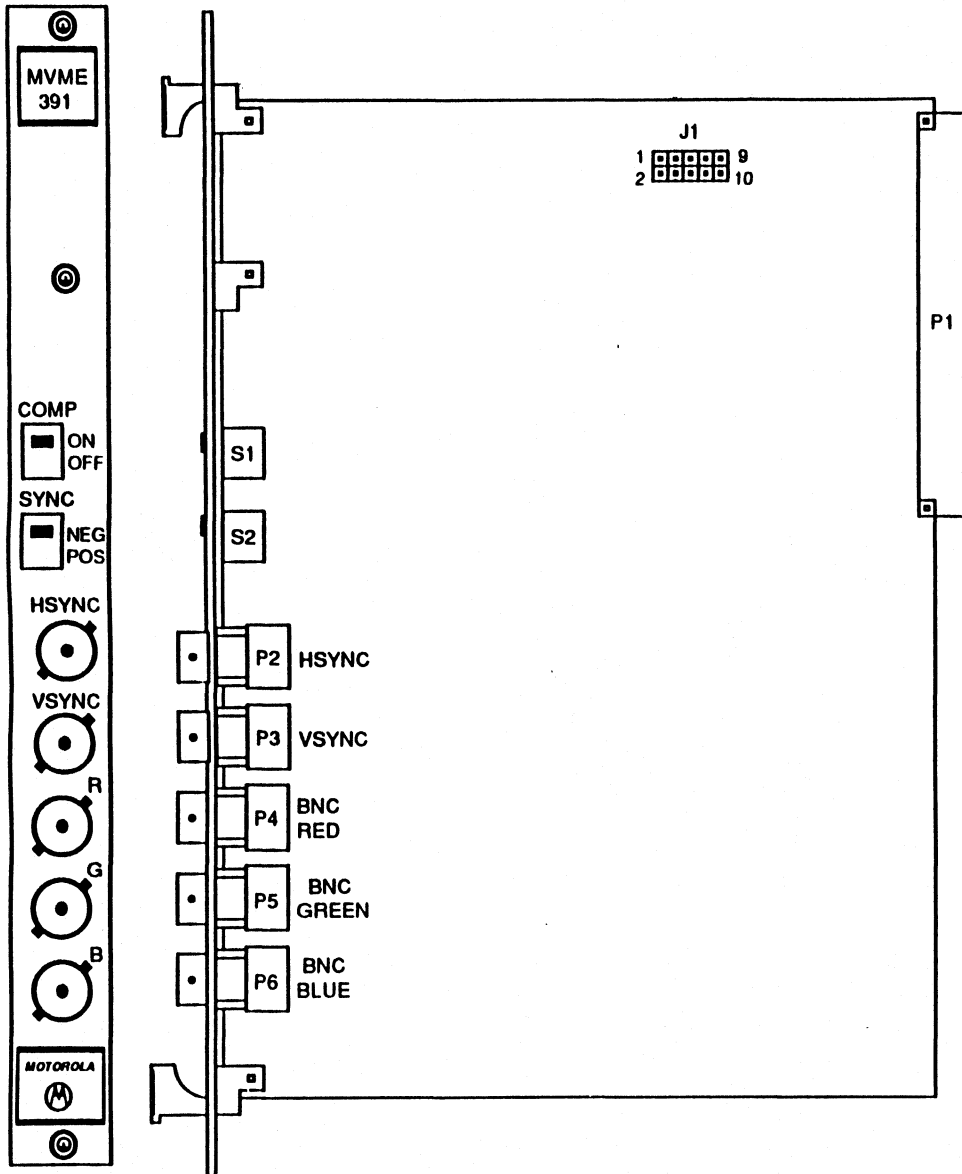
ADDRESS DECODE HEADER



NO JUMPER = LOGIC 1
JUMPER IN = LOGIC 0



09/13/89



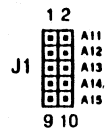
PART NUMBERS:

MVME391 01-W3578B01 76435598

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

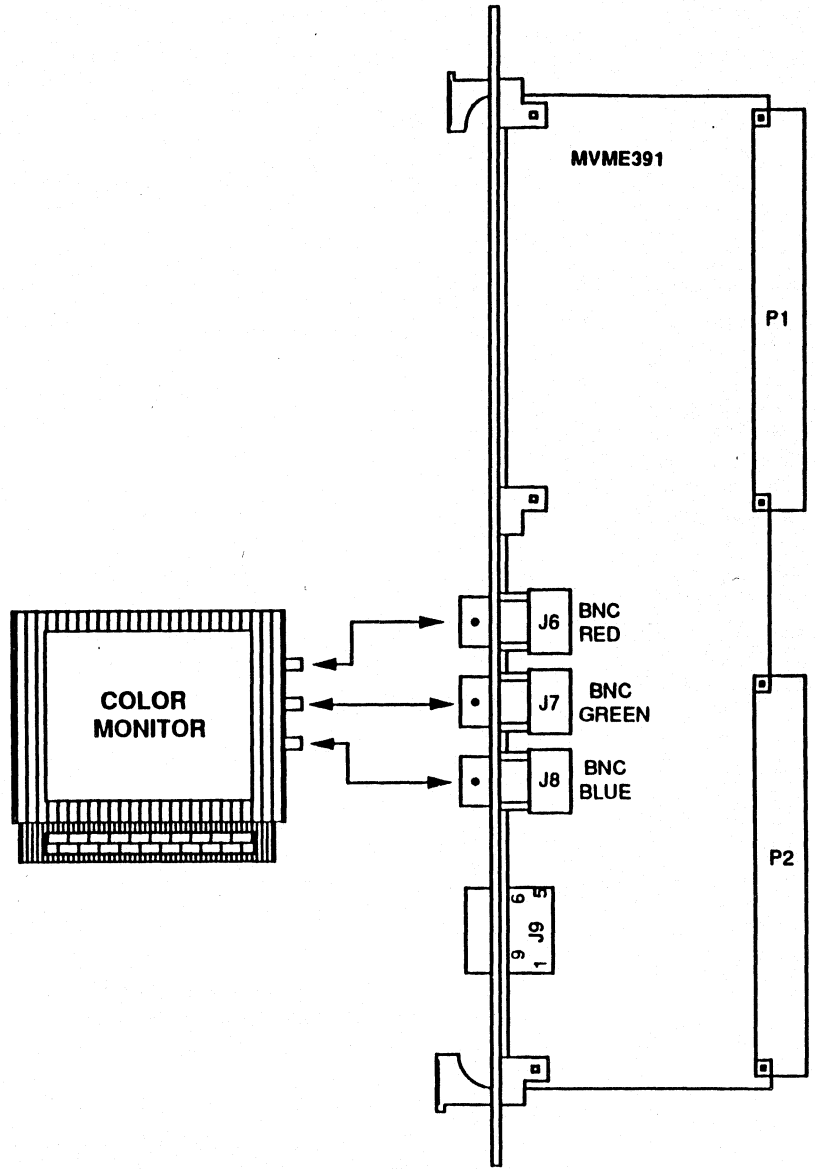
NOTE 1: "COMP" STANDS FOR COMPOSITE SYNC.

ADDRESS DECODE HEADER

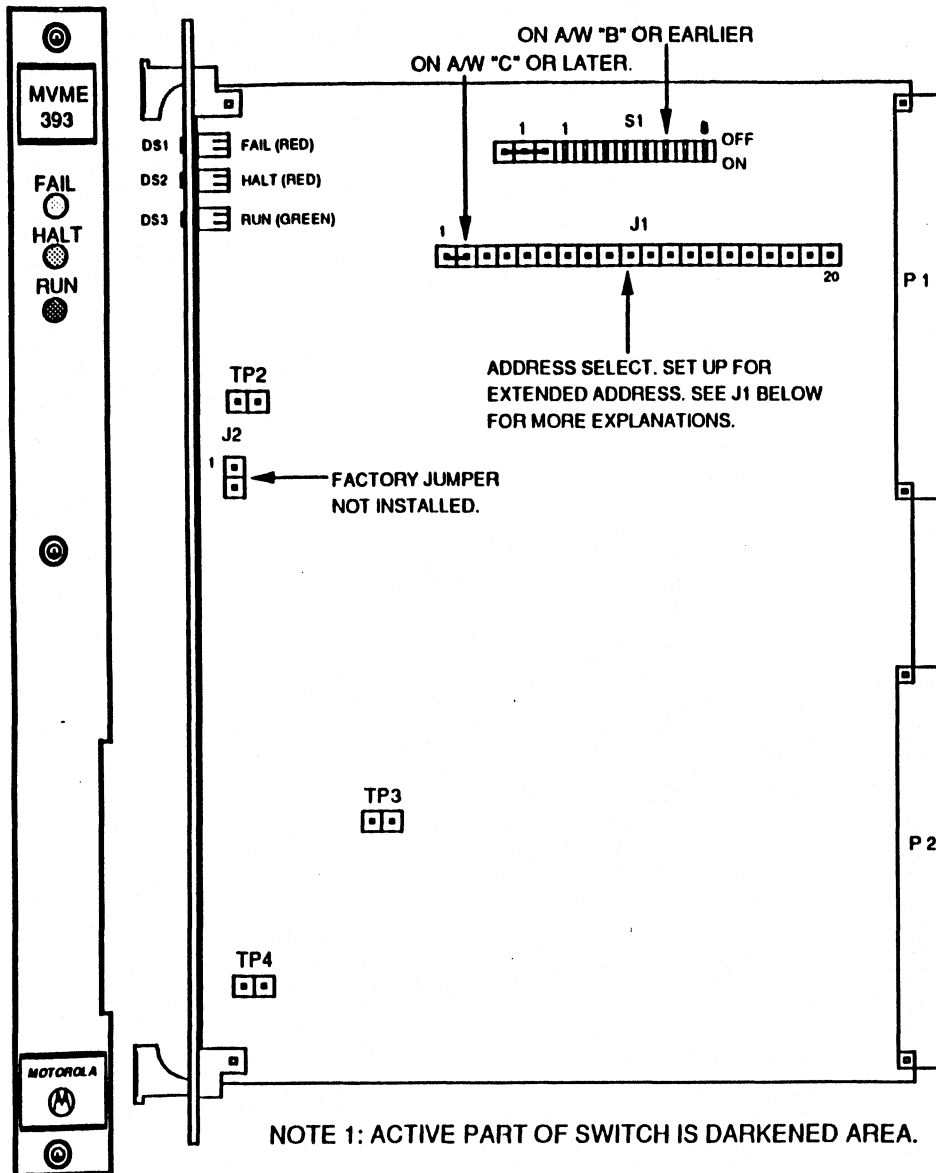


NO JUMPER = LOGIC 1
JUMPER IN = LOGIC 0

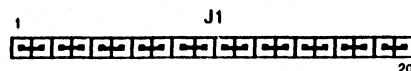
09/13/89



09/13/89



NOTE 1: ACTIVE PART OF SWITCH IS DARKENED AREA.

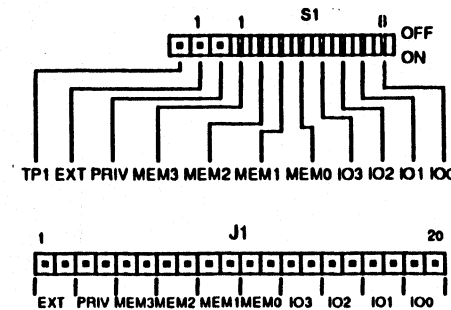


NOTE 2: SETUP FOR SYS1147, 3200, 3400, 3604/08, 3640, 8400 & 8608's.

PART NUMBERS:

MVME393 01-W3495B01 96011161

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.



EXT JUMPER OFF- STANDARD ADDRESS (24-BITS, ADDRESS MODIFIERS \$39, \$3B, \$3D, \$3F)
JUMPER ON - EXTENDED ADDRESS (32-BITS, ADDRESS MODIFIERS \$09, \$0B, \$0D, \$0F)

PRIV JUMPER OFF- NON-PRIVILEGED ACCESS (ADDRESS MODIFIERS \$09, \$0B, \$39, \$3B)
JUMPER ON - SUPERVISORY ACCESS (ADDRESS MODIFIERS \$09, \$0B, \$0D, \$0F, \$39, \$3B, \$3D, \$3F)

MEM3 MEMORY BLOCK SELECTS 512KB STANDARD OR SUPERVISORY ADDRESS BLOCK.

MEM2 (A22, A21, A20, A19, RESPECTIVELY).

MEM1 JUMPER OFF- LOGICAL "1"

MEM0 JUMPER ON - LOGICAL "0"

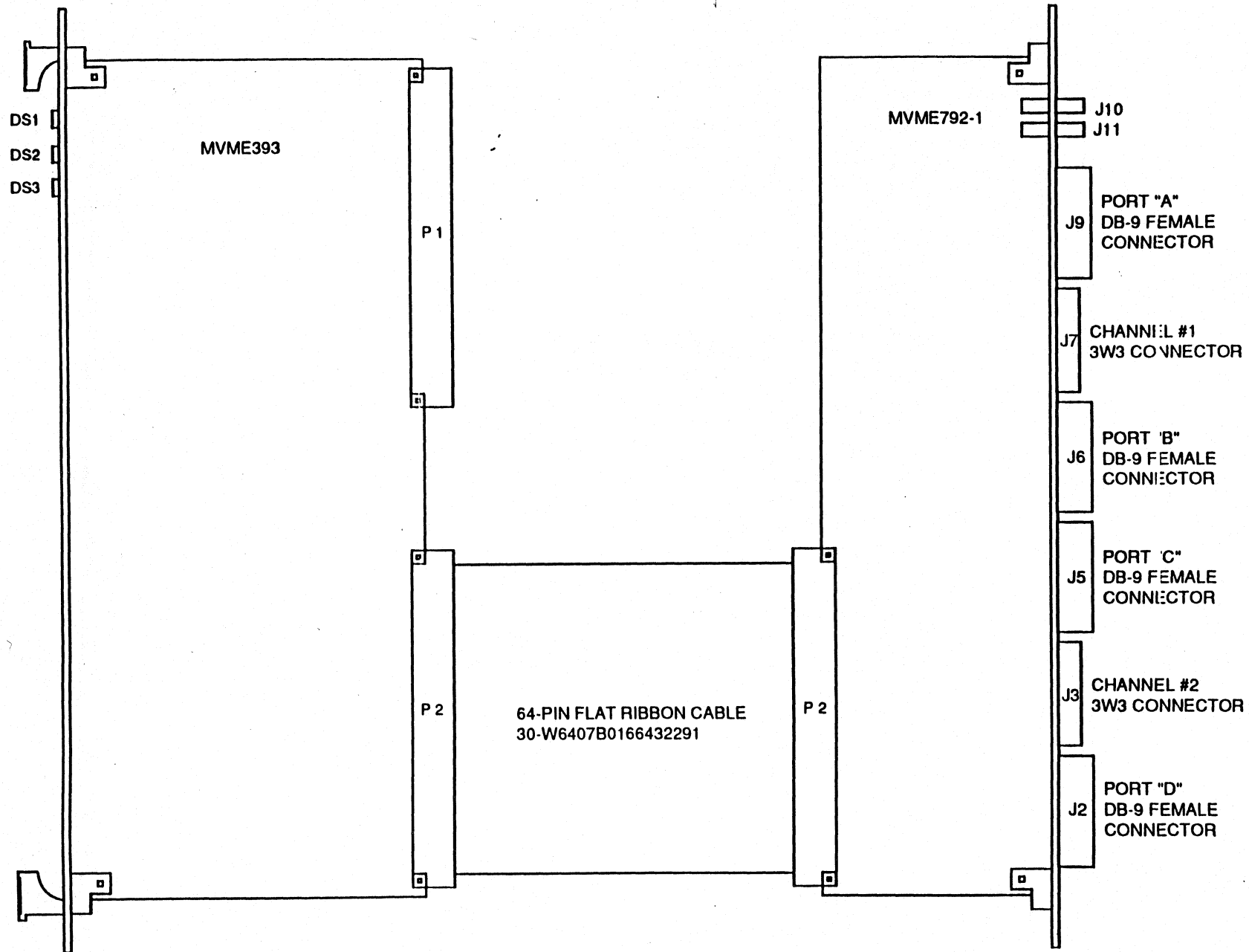
IO3 I/O BLOCK SELECT. SELECTS 16-BYTE SHORT I/O ADDRESS BLOCK.

IO2 (A07, A06, A05, A04, RESPECTIVELY).

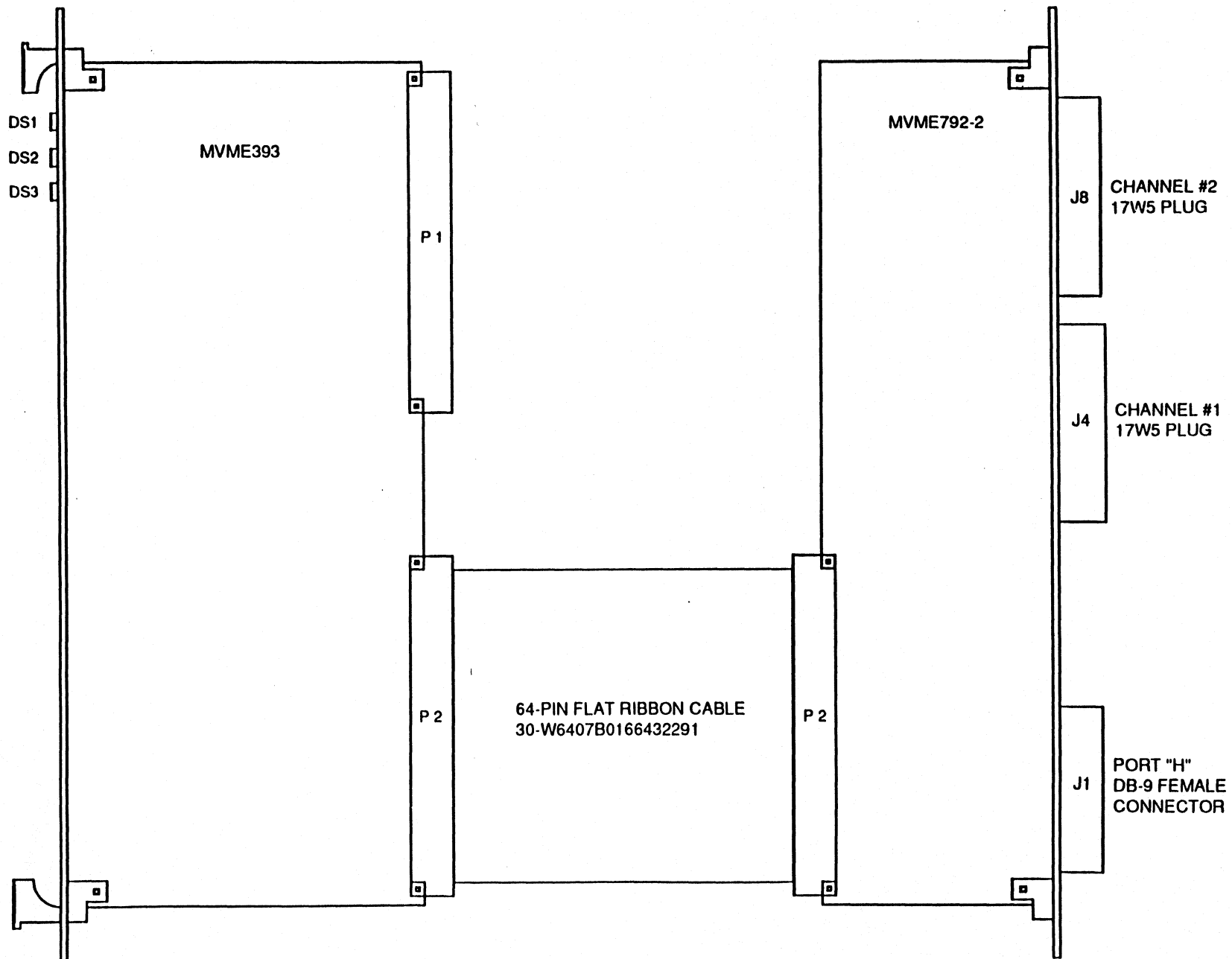
IO1 JUMPER OFF- LOGICAL "1"

IO0 JUMPER ON - LOGICAL "0"

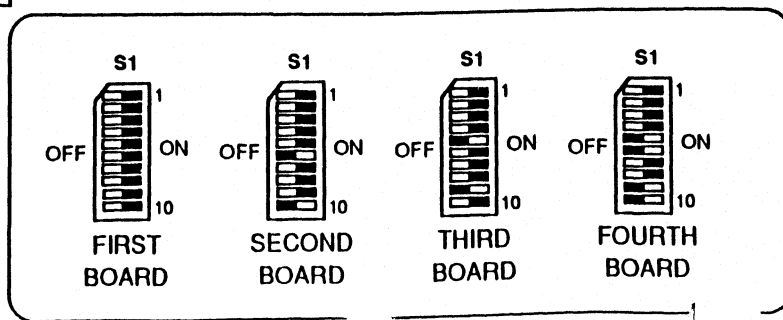
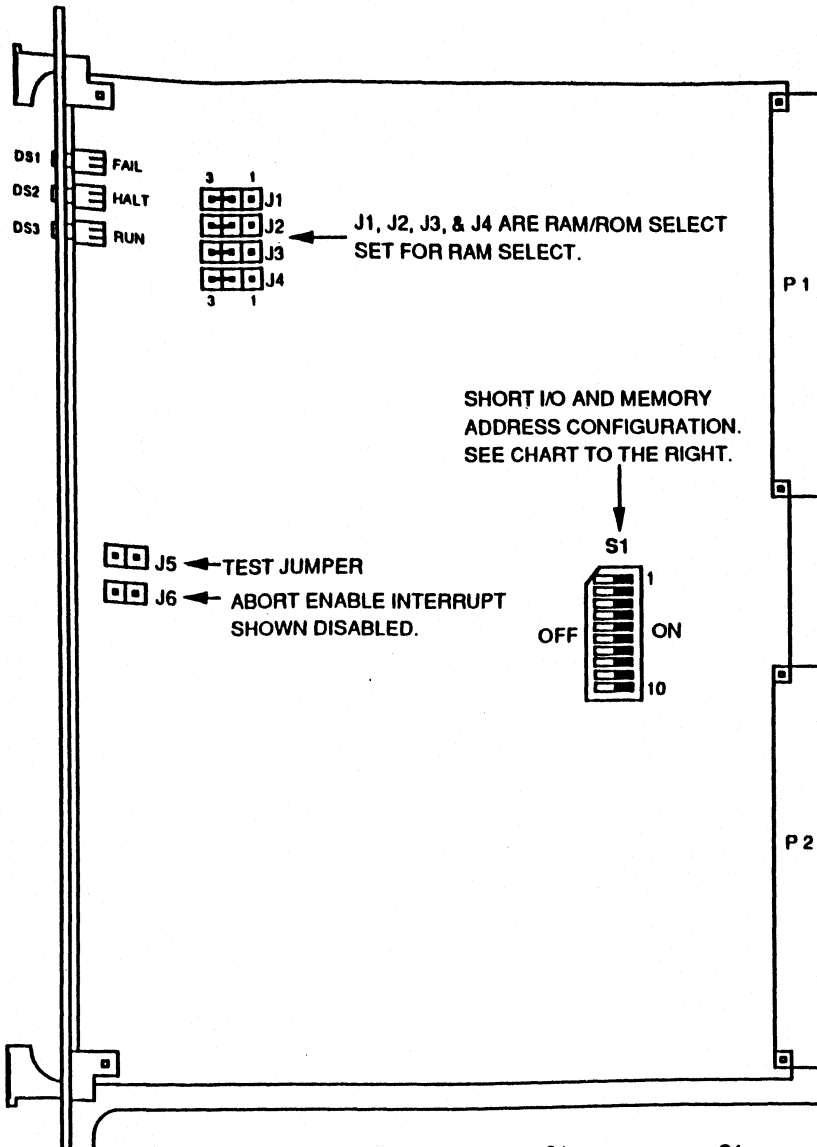
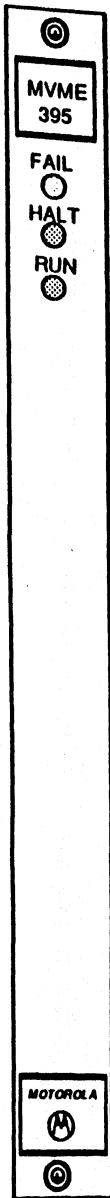
02/26/90



03/13/91



03/13/91



PART NUMBERS:

MVME395 01-W3525B01 96011020

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

SHORT I/O AND MEMORY ADDRESS CONFIGURATION

S1-1	2	3	4	5	6	7	8	9	10
EXT	PRIV	MEM3	MEM2	MEM1	MEM0	IO3	IO2	IO1	IO0

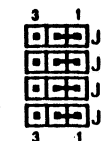
EXT JUMPER OFF- STANDARD ADDRESS (24-BITS, ADDRESS MODIFIERS \$39, \$3B, \$3D, \$3F)
JUMPER ON - EXTENDED ADDRESS (32-BITS, ADDRESS MODIFIERS \$09, \$0B, \$0D, \$0F)

PRIV JUMPER OFF- NON-PRIVILEGED ACCESS (ADDRESS MODIFIERS \$09, \$0B, \$39, \$3B)
JUMPER ON - SUPERVISORY ACCESS AND NON-PRIVILEGED ACCESS (ADDRESS MODIFIERS \$09, \$0B, \$0D, \$0F, \$39, \$3B, \$3D, \$3F)

MEM3 MEMORY BLOCK-- SELECTS 512KB STANDARD OR SUPERVISORY ADDRESS BLOCK.
MEM2 (A22, A21, A20, A19, RESPECTIVELY).
MEM1
MEM0

IO3 I/O BLOCK SELECT. SELECTS 16-BYTE SHORT I/O ADDRESS BLOCK.
IO2 (A07, A06, A05, A04, RESPECTIVELY).
IO1
IO0

RAM/ROM SELECT

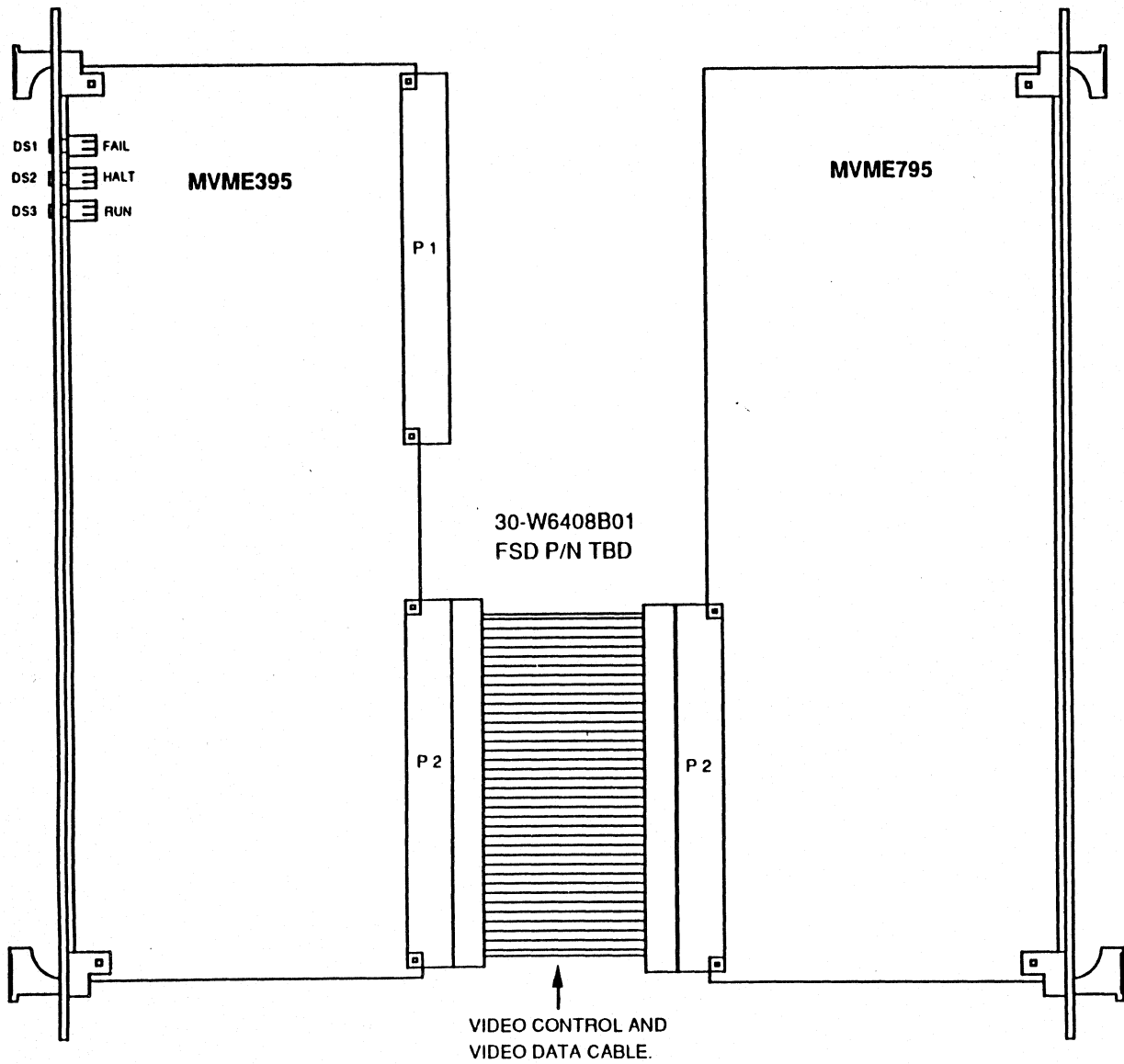


SET FOR ROM SELECT

NOTE 1: ACTIVE PART OF SWITCH S1 IS DARKENED AREA. IT IS CUSTOMER SELECTABLE.

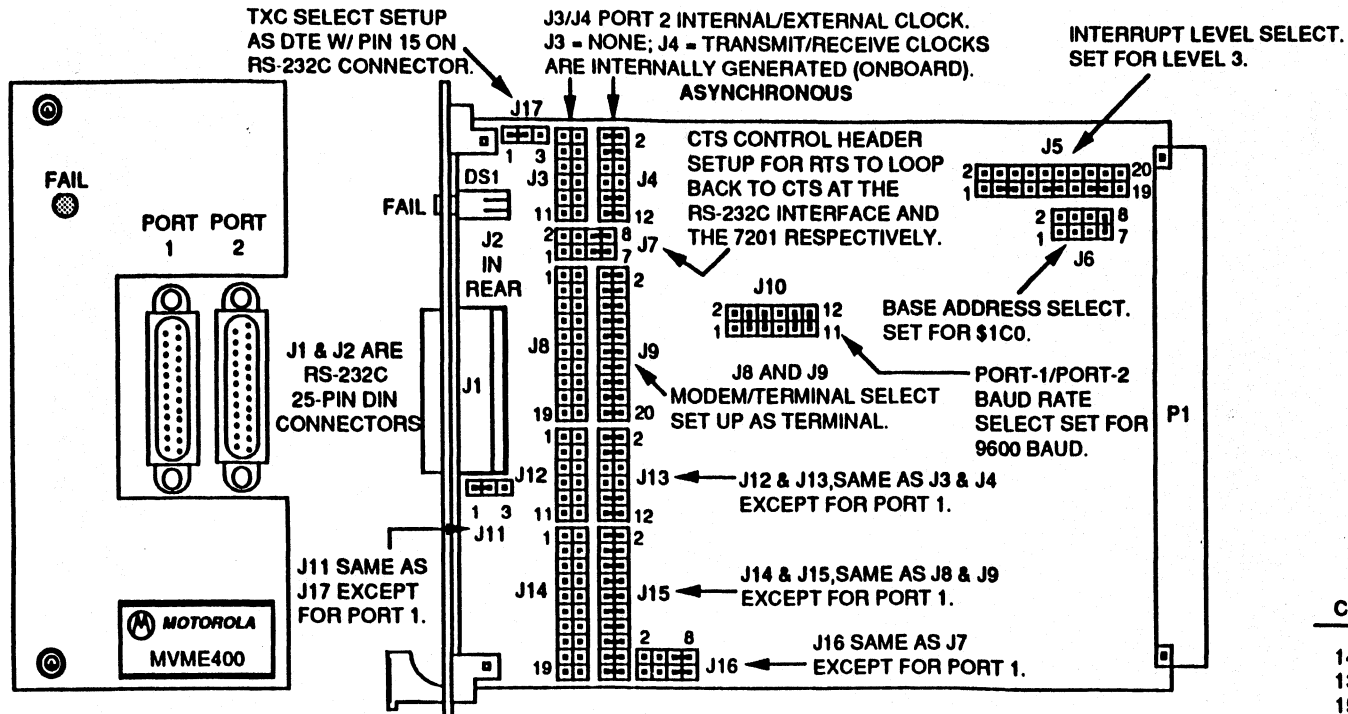
NOTE 2: REMOVE IACK* JUMPER AND EITHER LEAVE OR REMOVE ALL FOUR BG*/BR* JUMPERS.

04/05/91



03/13/91

SECTION 4



PART NUMBER:

MVME400 (OLD) 01-W3123B01 76430458

MVME400 (NEW) 01-W3432B01 76435174 (PREFERRED PWB)

MVME401 30-W2407B01 76431655 TEST LOOPBACK CABLE

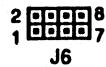
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

INTERRUPT REQUEST LEVEL

J5

PINS CONNECTED	REMARKS
14 - 16	7201 INT CONNECTED TO INT1*
13 - 15	7201 INT CONNECTED TO INT2*
15 - 17	7201 INT CONNECTED TO INT3*
16 - 18	7201 INT CONNECTED TO INT4*
2 - 4	PIA IRQA CONNECTED TO INT1*
1 - 3	PIA IRQA CONNECTED TO INT2*
3 - 5	PIA IRQA CONNECTED TO INT3*
4 - 6	PIA IRQA CONNECTED TO INT4*
8 - 10	PIA IRQB CONNECTED TO INT1*
7 - 9	PIA IRQB CONNECTED TO INT2*
9 - 11	PIA IRQB CONNECTED TO INT3*
10 - 12	PIA IRQB CONNECTED TO INT4*

BASE ADDRESS SELECT HEADER



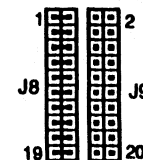
PINS JUMPERED	BLOCK SELECTED	OFFSET FOR HOST I/O CHANNEL BASE ADDRESS (1)
1 - 2; 3 - 4	0	\$0000
5 - 6; 7 - 8	1	\$020
1 - 2; 3 - 4; 5 - 6	2	\$040
1 - 2; 3 - 4; 7 - 8	3	\$060
1 - 2; 3 - 4	4	\$080
1 - 2; 5 - 6; 7 - 8	5	\$0A0
1 - 2; 5 - 6	6	\$0C0
1 - 2; 7 - 8	7	\$0E0
1 - 2	8	\$100
3 - 4; 5 - 6; 7 - 8	9	\$120
3 - 4; 5 - 6	10	\$140
3 - 4; 7 - 8	11	\$160
3 - 4	12	\$180
5 - 6; 7 - 8	13	\$1A0
5 - 6	14	\$1C0
7 - 8	15	\$1E0
NO PINS JUMPERED		

TXC SELECT HEADER



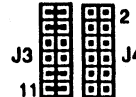
SETUP AS DCE W/ PIN 24 OF THE RS-232C CONNECTOR.

MODEM/TERMINAL SELECT

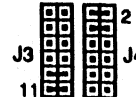


SET UP FOR MODEM

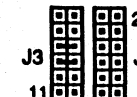
PORT 2 INTERNAL/EXTERNAL CLOCK HEADER SELECT



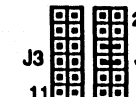
SYNCHRONOUS
J3 = TRANSMIT/RECEIVE CLOCKS ARE EXTERNALLY GENERATED (OFFBOARD); J4 = NONE.



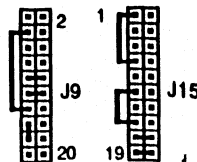
TEST
J3 = RECEIVE CLOCK IS EXTERNAL. J4 = TRANSMIT CLOCK IS INTERNAL.



MODEM
J3 = TRANSMIT/RECEIVE CLOCK LINES CONFIGURED TO INTERFACE WITH MODEM. J4 = NONE



TERMINAL
J3 = NONE J4 = TRANSMIT/RECEIVE CLOCK LINES CONFIGURED TO INTERFACE WITH TERMINAL.

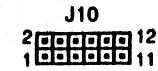


JUMPER CONFIG. FOR 3-WIRE RS-232 HOOKUP.

MVME400 DUAL RS-232C SERIAL PORT PAGE 1 OF 2

11/15/91

PORT-1/PORT-2 BAUD RATE SELECT

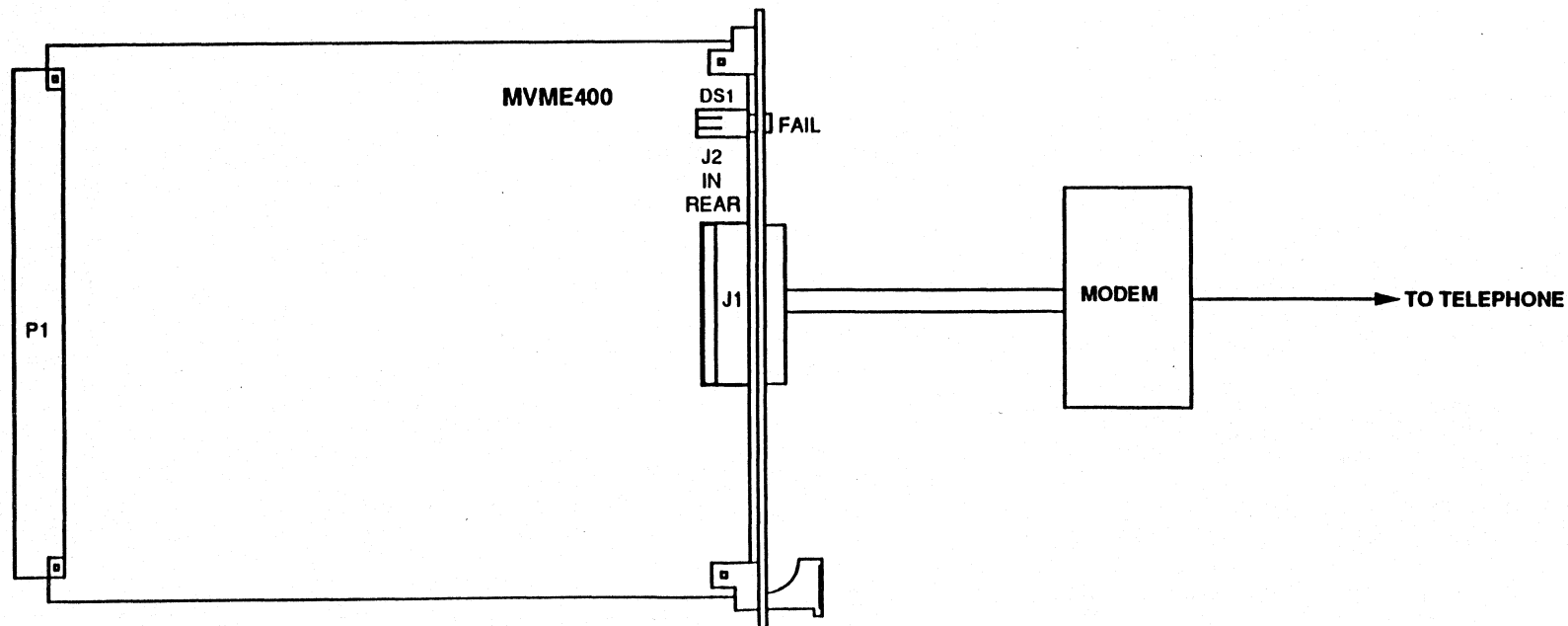
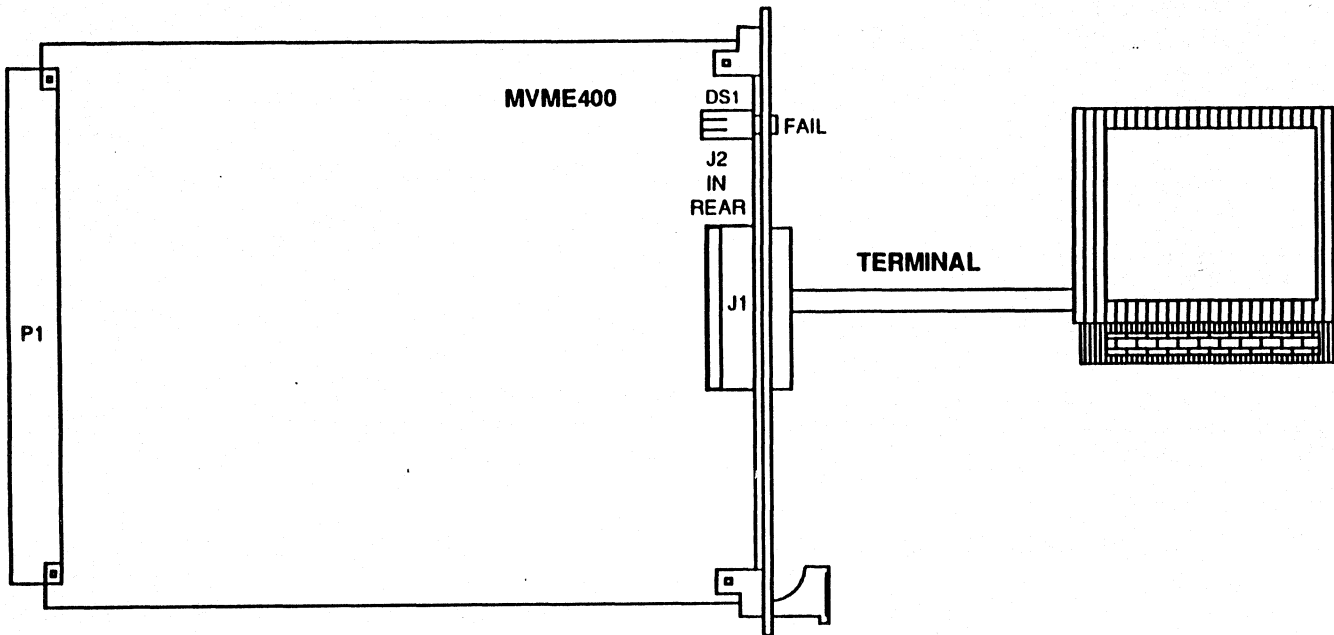


PORT 1 PINS CONNECTED	PORT 2 PINS CONNECTED	BAUD RATE
NONE	NONE	110
1 - 2	7 - 8	300
3 - 4	9 - 10	1200
1 - 2; 3 - 4	7 - 8; 9 - 10	2400
5 - 6	11 - 12	4800
1 - 2; 5 - 6	7 - 8; 11 - 12	7200
3 - 4; 5 - 6	9 - 10; 11 - 12	9600
ALL	ALL	19.2K

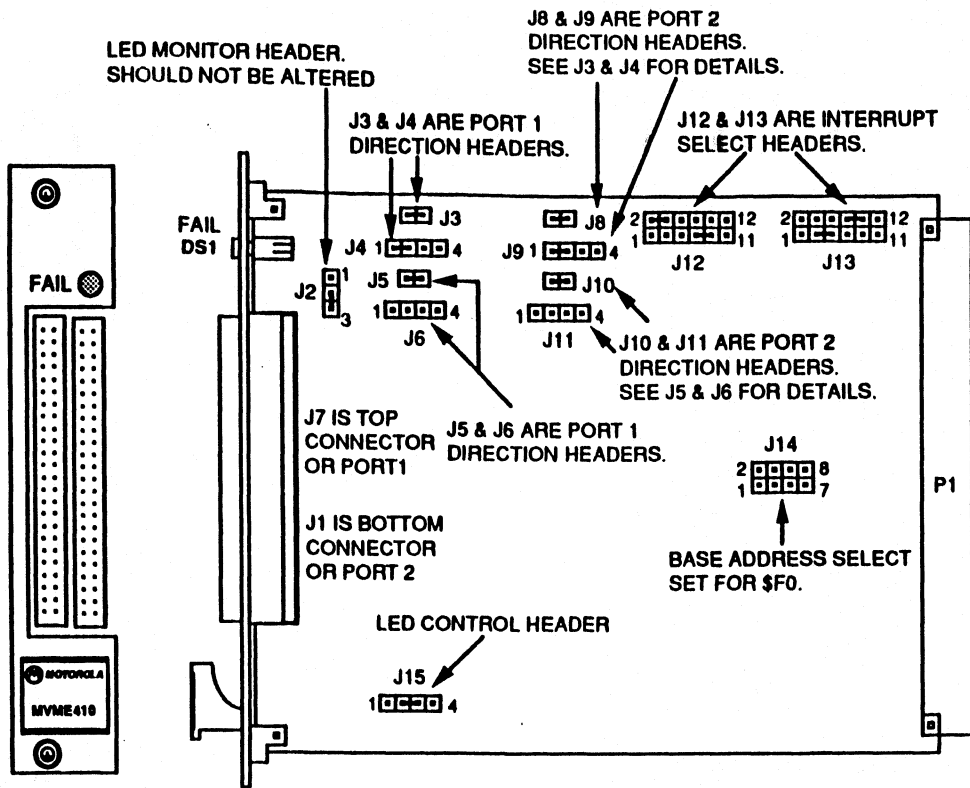
CTS CONTROL HEADER



DTR CONTROLS CTS FROM 7201 TO INHIBIT RECEIVED DATA.



09/14/89



PART NUMBERS:

MVME410 01-W3126B01 76430459

MVME411 01-W2272B01 76431330
LOOPBACK CABLE FOR TEST

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

NOTE 1: IF WANTING TO USE SECOND PARALLEL PORT,
YOU MUST FIRST SET UP THE M68B21 WITH
SOFTWARE TO ACCEPT IT AS THE DESIRED PORT.

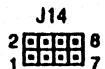
PORT 1 DIRECTION HEADER

PORT 1 DIRECTION HEADER

PORT 1 DIRECTION HEADER			PORT 1 DIRECTION HEADER		
HEADER	PINS	REMARKS	HEADER	PINS	REMARKS
J3	NONE	PICA2 IS NOT USED; PIPA0 - 7 ARE OUTPUTS	J5	NONE	PICB2 IS NOT USED; PIPB0 - 7 ARE OUTPUTS
J4	1 - 2		J6	1 - 2	
J3	NONE	PICA2 IS NOT USED; PIPA0 - 7 ARE INPUTS	J5	NONE	PICB2 IS NOT USED; PIPB0 - 7 ARE INPUTS
J4	2 - 3	WHEN U8-39 IS HIGH, AND OUTPUTS WHEN LOW	J6	2 - 3	WHEN U8-19 IS HIGH, AND OUTPUTS WHEN LOW
J3	NONE	PICA2 IS AN INPUT; PIPA0 - 7 ARE INPUTS	J5	NONE	PICB2 IS AN INPUT; PIPB0 - 7 ARE INPUTS
J4	3 - 4		J6	3 - 4	
J3	NONE	PICA2 IS AN INPUT; PIPA0 - 7 ARE OUTPUTS	J5	NONE	PICB2 IS AN INPUT; PIPB0 - 7 ARE OUTPUTS
J4	1 - 2; 3 - 4		J6	1 - 2; 3 - 4	
J3	NONE	PICA2 IS NOT USED; PIPA0 - 7 ARE INPUTS	J5	NONE	PICB2 IS NOT USED; PIPB0 - 7 ARE INPUTS
J4	NONE		J6	NONE	
J3	1 - 2	PICA2 IS AN OUTPUT; PIPA0 - 7 ARE OUTPUTS	J5	1 - 2	PICB2 IS AN OUTPUT; PIPB0 - 7 ARE OUTPUTS
J4	NONE		J6	NONE	(FACTORY CONFIGURED)
J3	1 - 2	PICA2 IS AN OUTPUT; PIPA0 - 7 ARE OUTPUTS	J5	1 - 2	PICB2 IS AN OUTPUT; PIPB0 - 7 ARE OUTPUTS
J4		(FACTORY CONFIGURATION)	J6	1 - 2	

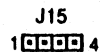
09/14/89

BASE ADDRESS SELECT HEADER



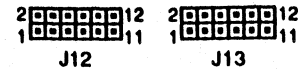
PINS CONNECTED	REMARKS
1 - 2; 3 - 4; 5 - 6; 7 - 8	BASE ADDRESS IS \$00
1 - 2; 3 - 4; 5 - 6	BASE ADDRESS IS \$10
1 - 2; 3 - 4; 7 - 8	BASE ADDRESS IS \$20
1 - 2; 3 - 4	BASE ADDRESS IS \$30
1 - 2; 5 - 6; 7 - 8	BASE ADDRESS IS \$40
1 - 2; 5 - 6	BASE ADDRESS IS \$50
1 - 2; 7 - 8	BASE ADDRESS IS \$60
1 - 2	BASE ADDRESS IS \$70
3 - 4; 5 - 6; 7 - 8	BASE ADDRESS IS \$80
3 - 4; 5 - 6	BASE ADDRESS IS \$90
3 - 4; 7 - 8	BASE ADDRESS IS \$A0
3 - 4	BASE ADDRESS IS \$B0
5 - 6; 7 - 8	BASE ADDRESS IS \$C0
5 - 6	BASE ADDRESS IS \$D0
7 - 8	BASE ADDRESS IS \$E0
NONE	BASE ADDRESS IS \$F0

LED CONTROL HEADER



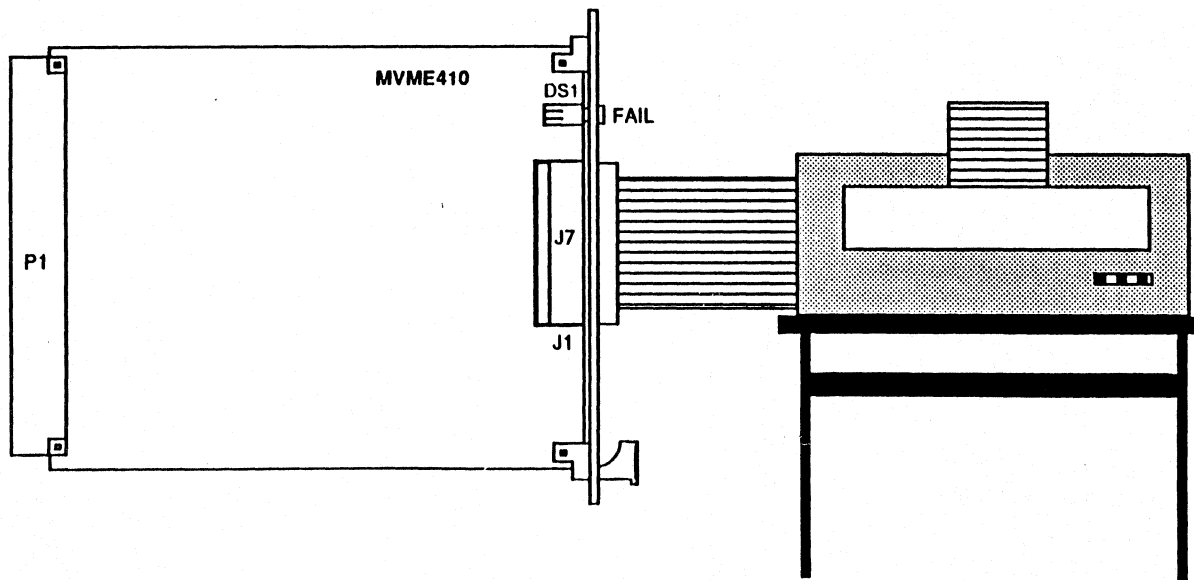
PINS	REMARKS
1 - 2	SIGNAL [PIPB7] AND FAIL ARE ENABLED.
2 - 3	FAIL LED IS FUNCTIONAL AND [PIPB7] IS DISABLED. (FACTORY CONFIGURATION)
3 - 4	LED IS DISABLED

INTERUPT SELECT HEADERS



HEADER	PINS	REMARKS
J12	1 - 3	[IRQ1A*] CONNECTED TO INT2*
J12	2 - 4 (1)	[IRQ1A*] CONNECTED TO INT1*
J12	3 - 5	[IRQ1A*] CONNECTED TO INT3*
J12	4 - 6	[IRQ1A*] CONNECTED TO INT4*
J12	7 - 9 (1)	[IRQ1B*] CONNECTED TO INT2*
J12	8 - 10	[IRQ1B*] CONNECTED TO INT1*
J12	9 - 11	[IRQ1B*] CONNECTED TO INT3*
J12	10 - 12	[IRQ1B*] CONNECTED TO INT4*
J13	1 - 3	[IRQ2A*] CONNECTED TO INT2*
J13	2 - 4	[IRQ2A*] CONNECTED TO INT1*
J13	3 - 5 (1)	[IRQ2A*] CONNECTED TO INT3*
J13	4 - 6	[IRQ2A*] CONNECTED TO INT4*
J13	7 - 9	[IRQ2B*] CONNECTED TO INT2*
J13	8 - 10 (1)	[IRQ2B*] CONNECTED TO INT1*
J13	9 - 11	[IRQ2B*] CONNECTED TO INT3*
J13	10 - 12	[IRQ2B*] CONNECTED TO INT4*

(1) FACTORY CONFIGURATION

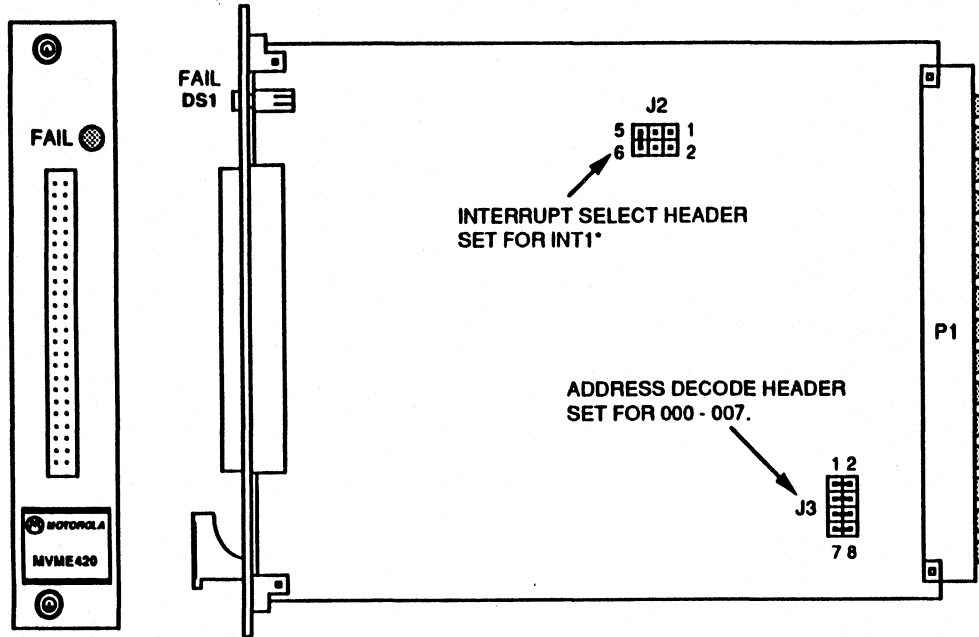


09/14/89

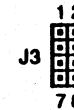
PART NUMBERS:

MVME420 01-W3129B01 76430460

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

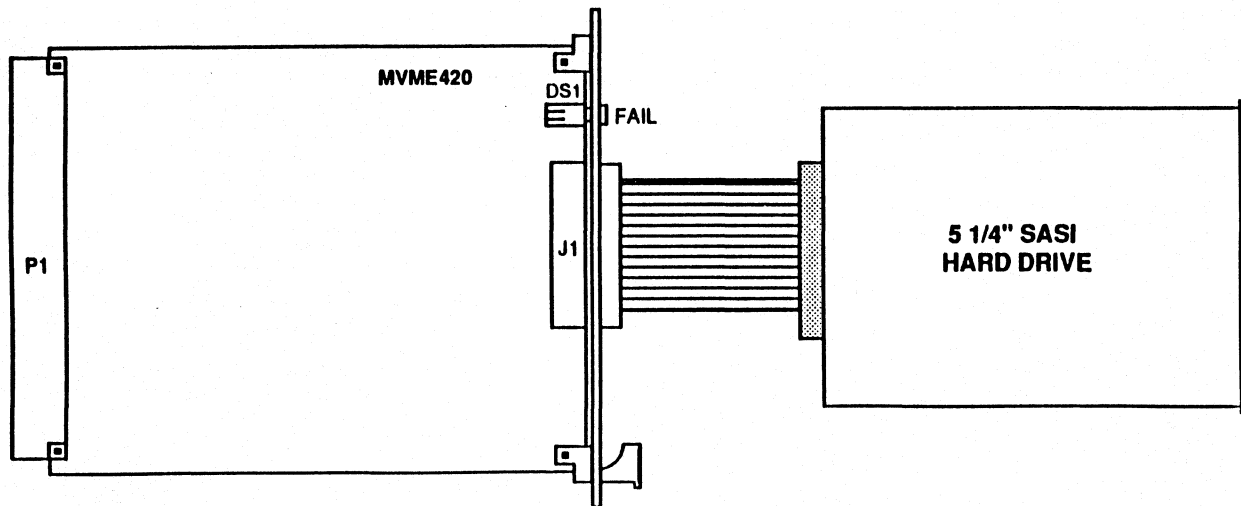


ADDRESS DECODE HEADER



PINS CONNECTED	REMARKS
1 - 2; 3 - 4; 5 - 6; 7 - 8	SELECT BLOCK 0 ADDRESSES 000 - 007
1 - 2; 3 - 4; 5 - 6	SELECT BLOCK 1 ADDRESSES 008 - 00F
1 - 2; 3 - 4; 7 - 8	SELECT BLOCK 2 ADDRESSES 010 - 007
1 - 2; 3 - 4	SELECT BLOCK 3 ADDRESSES 018 - 01F
1 - 2; 5 - 6; 7 - 8	SELECT BLOCK 4 ADDRESSES 020 - 027
1 - 2; 5 - 6	SELECT BLOCK 5 ADDRESSES 028 - 02F
1 - 2; 7 - 8	SELECT BLOCK 6 ADDRESSES 030 - 037
1 - 2	SELECT BLOCK 7 ADDRESSES 038 - 03F
3 - 4; 5 - 6; 7 - 8	SELECT BLOCK 8 ADDRESSES 040 - 047
3 - 4; 5 - 6	SELECT BLOCK 9 ADDRESSES 048 - 04F
3 - 4; 7 - 8	SELECT BLOCK 10 ADDRESSES 050 - 057
3 - 4	SELECT BLOCK 11 ADDRESSES 058 - 05F
5 - 6; 7 - 8	SELECT BLOCK 12 ADDRESSES 060 - 067
5 - 6	SELECT BLOCK 13 ADDRESSES 068 - 06F
7 - 8	SELECT BLOCK 14 ADDRESSES 070 - 077
NONE	SELECT BLOCK 15 ADDRESSES 078 - 07F

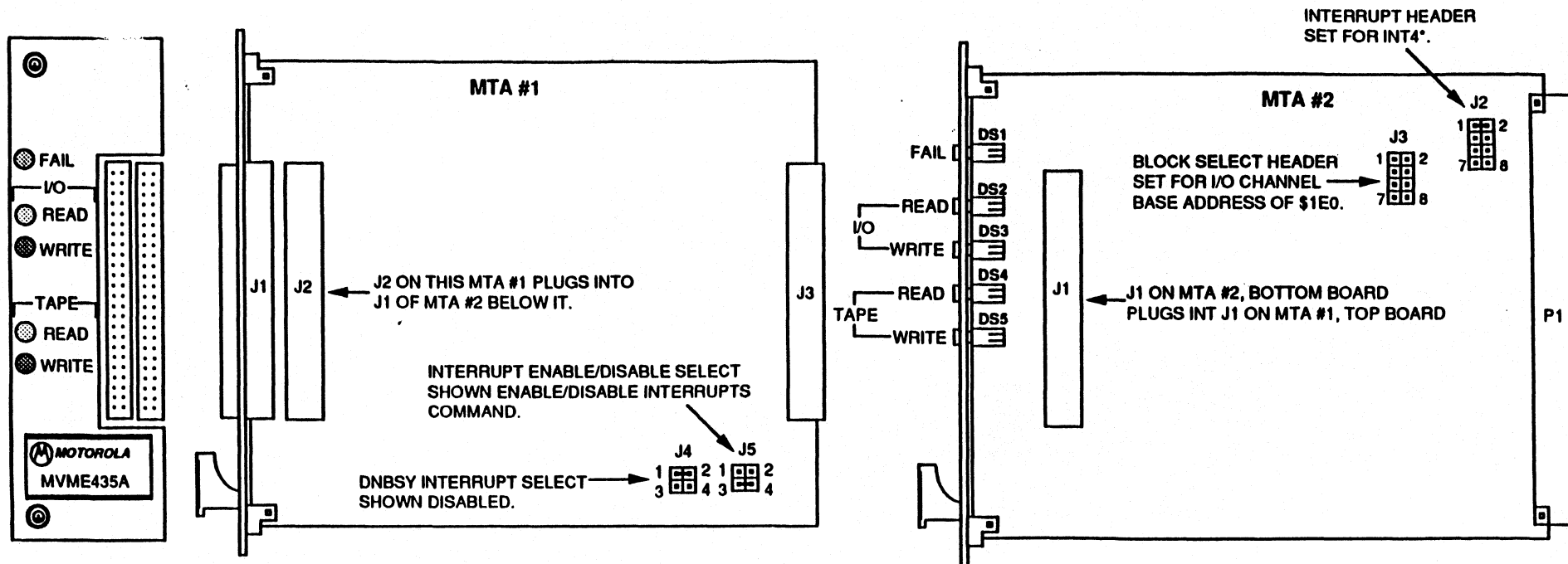
09/14/89



INTERRUPT SELECT HEADER

J2	PINS	REMARKS
5 1 6 2	1 - 2	INT3* SELECTED
	3 - 4	INT2* SELECTED
	5 - 6 (1)	INT1* SELECTED

(1) (FACTORY CONFIGURATION)



DNBSY INTERRUPT SELECT HEADER

J4	PINS	CONFIGURATION
1 2 3 4	1 - 2	DNBSY INTERRUPT DISABLED
	3 - 4	DNBSY INTERRUPT ENABLED

BASE ADDRESS SELECT HEADER



PINS CONNECTED	REMARKS
1 - 2; 3 - 4; 5 - 6; 7 - 8	I/O CHANNEL BASE ADDRESS IS \$000
1 - 2; 3 - 4; 5 - 6	I/O CHANNEL BASE ADDRESS IS \$020
1 - 2; 3 - 4; 7 - 8	I/O CHANNEL BASE ADDRESS IS \$040
1 - 2; 3 - 4	I/O CHANNEL BASE ADDRESS IS \$060
1 - 2; 5 - 6; 7 - 8	I/O CHANNEL BASE ADDRESS IS \$080
1 - 2; 5 - 6	I/O CHANNEL BASE ADDRESS IS \$0A0
1 - 2; 7 - 8	I/O CHANNEL BASE ADDRESS IS \$0C0
1 - 2	I/O CHANNEL BASE ADDRESS IS \$0E0
3 - 4; 5 - 6; 7 - 8	I/O CHANNEL BASE ADDRESS IS \$100
3 - 4; 5 - 6	I/O CHANNEL BASE ADDRESS IS \$120
3 - 4; 7 - 8	I/O CHANNEL BASE ADDRESS IS \$140
3 - 4	I/O CHANNEL BASE ADDRESS IS \$160
5 - 6; 7 - 8	I/O CHANNEL BASE ADDRESS IS \$180
5 - 6	I/O CHANNEL BASE ADDRESS IS \$1A0
7 - 8	I/O CHANNEL BASE ADDRESS IS \$1C0
NONE	I/O CHANNEL BASE ADDRESS IS \$1E0

INTERRUPT ENABLE/DISABLE SELECT

J5	PINS	CONFIGURATION
1 2 3 4	1 - 2	INTERRUPTS PERMANENTLY ENABLED
	3 - 4	ENABLE/DISABLE INTERRUPTS COMMAND

INTERRUPT HEADER

J2	PINS	REMARKS
1 2 3 4 7 8	1 - 2	I/O CHANNEL INTERRUPT INT4*
	3 - 4	I/O CHANNEL INTERRUPT INT3*
	5 - 6	I/O CHANNEL INTERRUPT INT2*
	7 - 8	I/O CHANNEL INTERRUPT INT1*

PART NUMBERS:

MVME435 01-W1250B01 76431495
(OBSOLETE USE MVME435A)

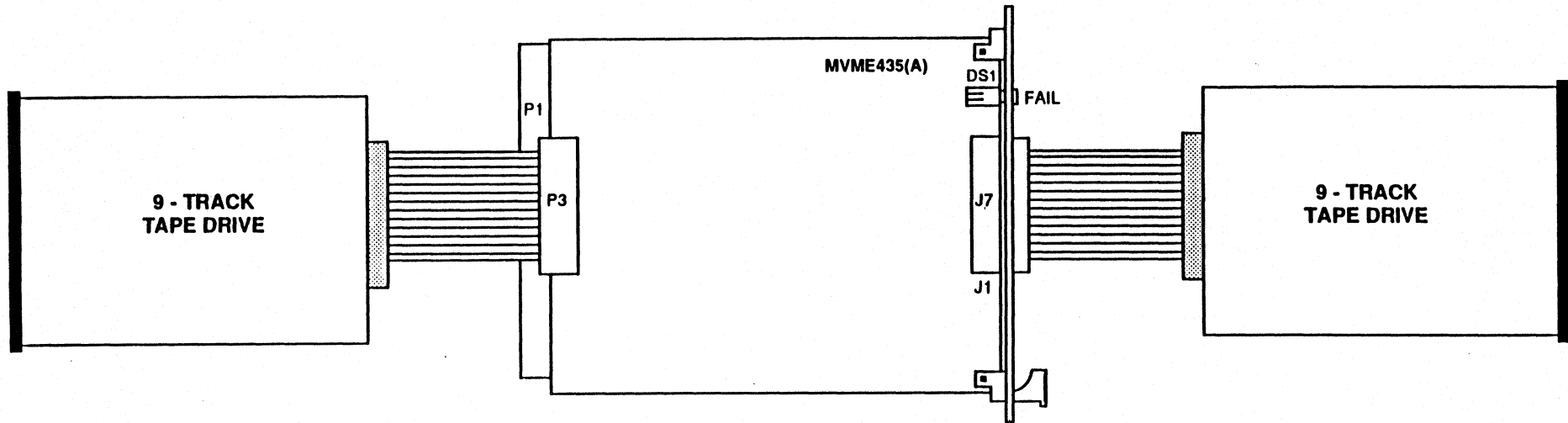
MVME435A 01-W1298B01 76432638

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

09/14/89

MTA #2 IS REAR BOARD WITH VME CONNECTOR

MTA #1 IS FRONT BOARD WITH 50-PIN RIBBON
CABLE FOR INTERNALLY HOOKED UP
9-TRACK TAPE DRIVE.



09/14/89

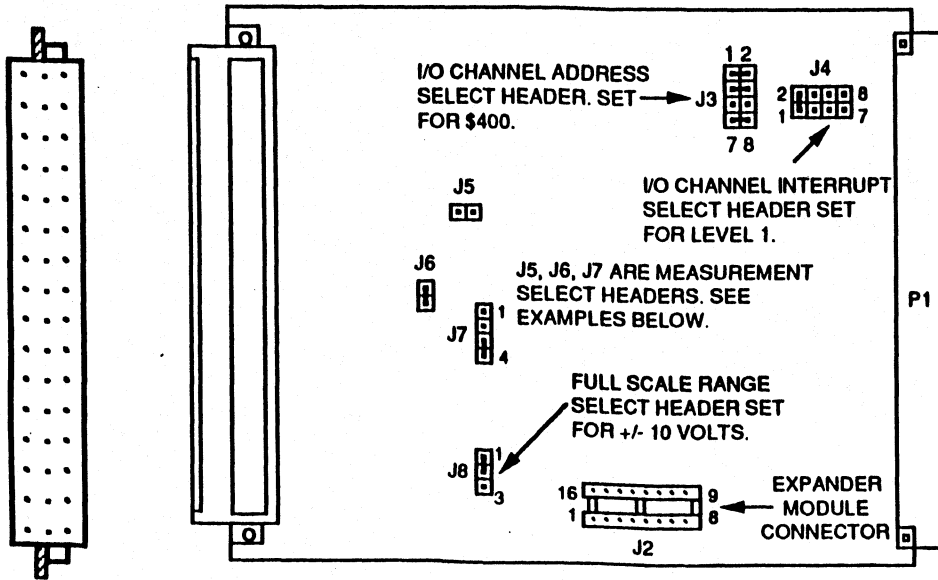
SECTION 6

PART NUMBERS:

MVME600 01-W3190B01 76431496

MVME601 01-W3270B01 76431497

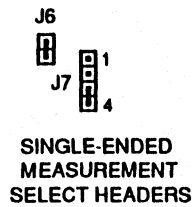
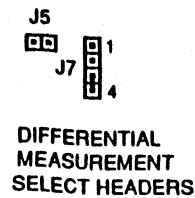
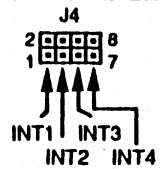
SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.



J3 I/O CHANNEL ADDRESS SELECT HEADER

PINS JUMPERED	BLOCK SELECTED	OFFSET FROM HOST I/O CHANNEL BASE ADDRESS
1-2, 3-4, 5-6, 7-8	0	\$000
1-2, 3-4, 5-6	1	\$200
1-2, 3-4, 7-8	2	\$400
1-2, 3-4	3	\$600
1-2, 5-6, 7-8	4	\$800
1-2, 5-6	5	\$A00
1-2, 7-8	6	\$C00
1-2	7	\$E00
3-4, 5-6, 7-8	8	\$1000
3-4, 5-6	9	\$1200
3-4, 7-8	10	\$1400
3-4	11	\$1600
5-6, 7-8	12	\$1800
5-6	13	\$1A00
7-8	14	\$1C00
NO PINS JUMPERED	15	\$1E00

I/O CHANNEL INTERRUPT SELECT HEADER

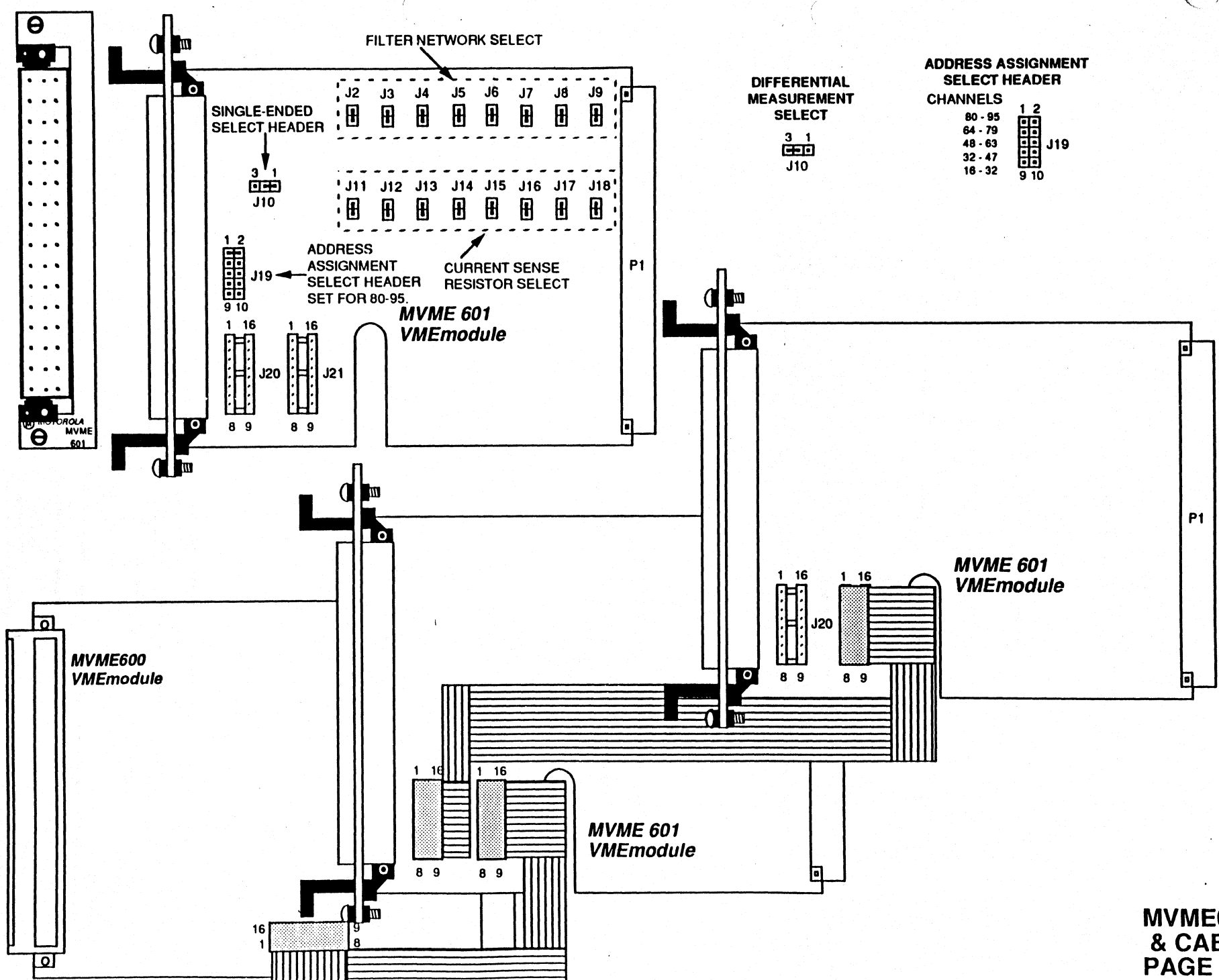


FULL SCALE RANGE SELECT HEADER



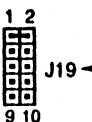
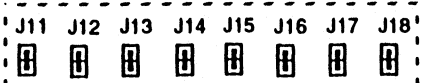
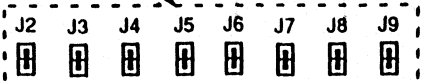
SET FOR +/- 5 VOLT SCALE

01/11/90



FILTER NETWORK SELECT

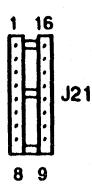
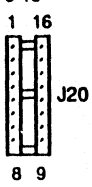
SINGLE-ENDED SELECT HEADER



ADDRESS ASSIGNMENT SELECT HEADER SET FOR 80-95.

CURRENT SENSE RESISTOR SELECT

MVME 601 VME module



DIFFERENTIAL MEASUREMENT SELECT



ADDRESS ASSIGNMENT SELECT HEADER

CHANNELS	1	2
80 - 95	□	□
64 - 79	□	□
48 - 63	□	□
32 - 47	□	□
16 - 32	□	□

J19

MVME600 VME module

MVME 601 VME module

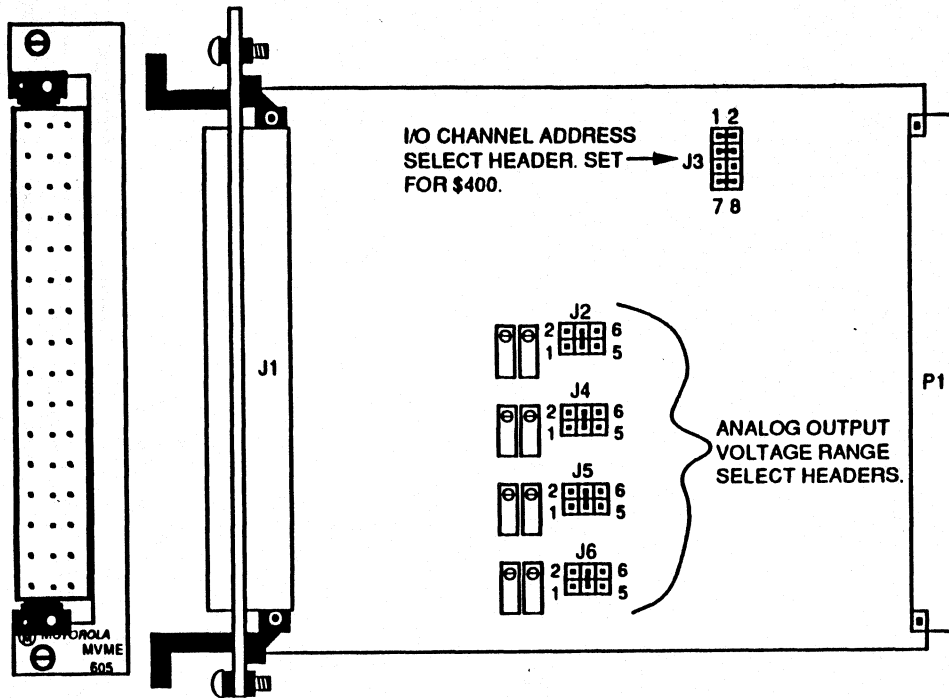
MVME 601 VME module

01/1/90

PART NUMBERS:

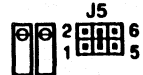
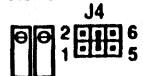
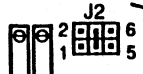
MVME605 01-W3191B01 76431498

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.



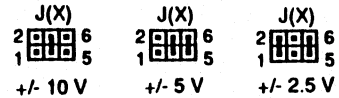
I/O CHANNEL ADDRESS
SELECT HEADER. SET
FOR \$400. → J3

1 2
7 8



ANALOG OUTPUT
VOLTAGE RANGE
SELECT HEADERS.

ANALOG OUTPUT VOLTAGE RANGE
SELECT HEADERS (J2, J4, J5, J6)



J6 - CHANNEL 1; J5 - CHANNEL 2;
J4 - CHANNEL 3; J2 - CHANNEL 4.

J3 I/O CHANNEL ADDRESS SELECT HEADER

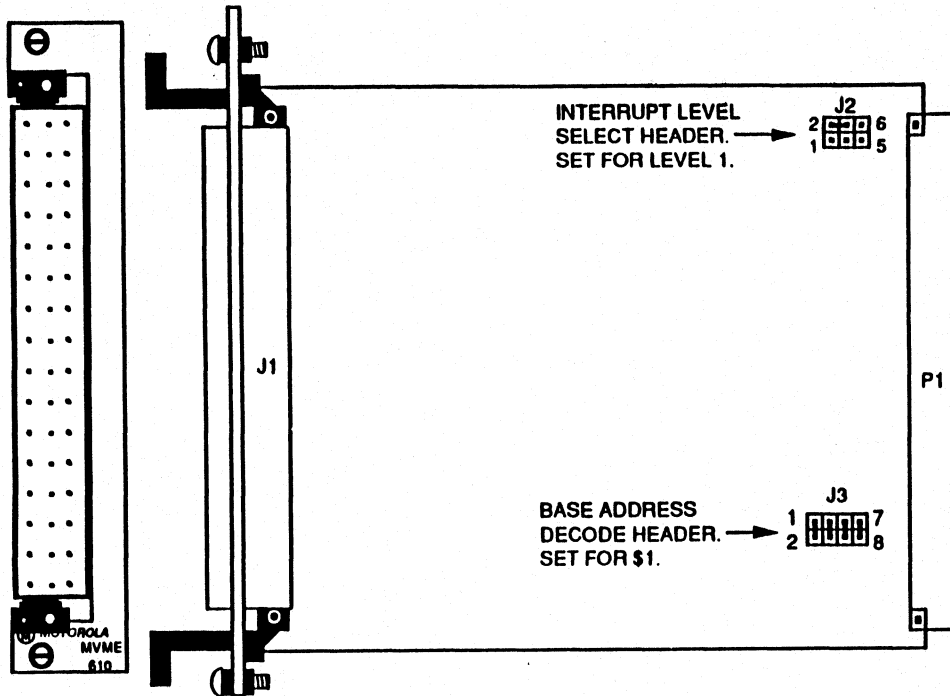
PINS JUMPERED	BLOCK SELECTED	OFFSET FROM HOST I/O CHANNEL BASE ADDRESS
1-2,3-4,5-6,7-8	0	\$000
1-2,3-4,5-6	1	\$200
1-2,3-4,7-8	2	\$400
1-2,3-4	3	\$600
1-2,5-6,7-8	4	\$800
1-2,5-6	5	\$A00
1-2,7-8	6	\$C00
1-2	7	\$E00
3-4,5-6,7-8	8	\$1000
3-4,5-6	9	\$1200
3-4,7-8	10	\$1400
3-4	11	\$1600
5-6,7-8	12	\$1800
5-6	13	\$1A00
7-8	14	\$1C00
NO PINS JUMPERED	15	\$1E00

02/08/90

PART NUMBERS:

MVME610 01-W3192B01 76430473

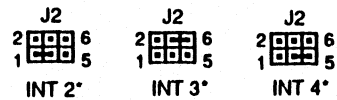
SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.



J3 I/O CHANNEL ADDRESS SELECT HEADER

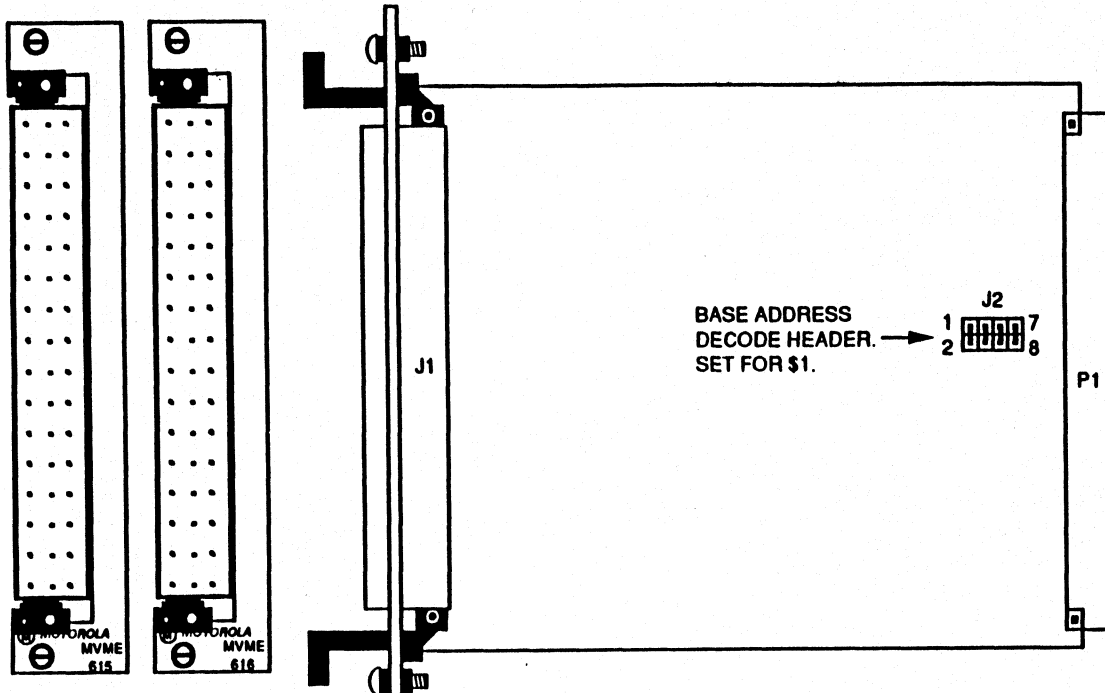
PINS JUMPERED	BLOCK SELECTED	BASE ADDRESS SELECTED HEXIDEcimal ADDRESS
1-2,3-4,5-6,7-8	0	\$1
1-2,3-4,5-6	1	\$5
1-2,3-4,7-8	2	\$9
1-2,3-4	3	\$D
1-2,5-6,7-8	4	\$11
1-2,5-6	5	\$15
1-2,7-8	6	\$19
1-2	7	\$1D
3-4,5-6,7-8	8	\$21
3-4,5-6	9	\$25
3-4,7-8	10	\$29
3-4	11	\$2D
5-6,7-8	12	\$31
5-6	13	\$35
7-8	14	\$39
NO PINS JUMPERED	15	\$3D

J2 INTERRUPT LEVEL SELECT HEADER



NO JUMPERS = NO INTERRUPT GENERATED

02/08/90



PART NUMBERS:

MVME615 01-W3193B01 76430474
W/ CROSSOVER

MVME616 01-W3193B02 76431328
W/O CROSSOVER

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

J2 I/O CHANNEL ADDRESS SELECT HEADER

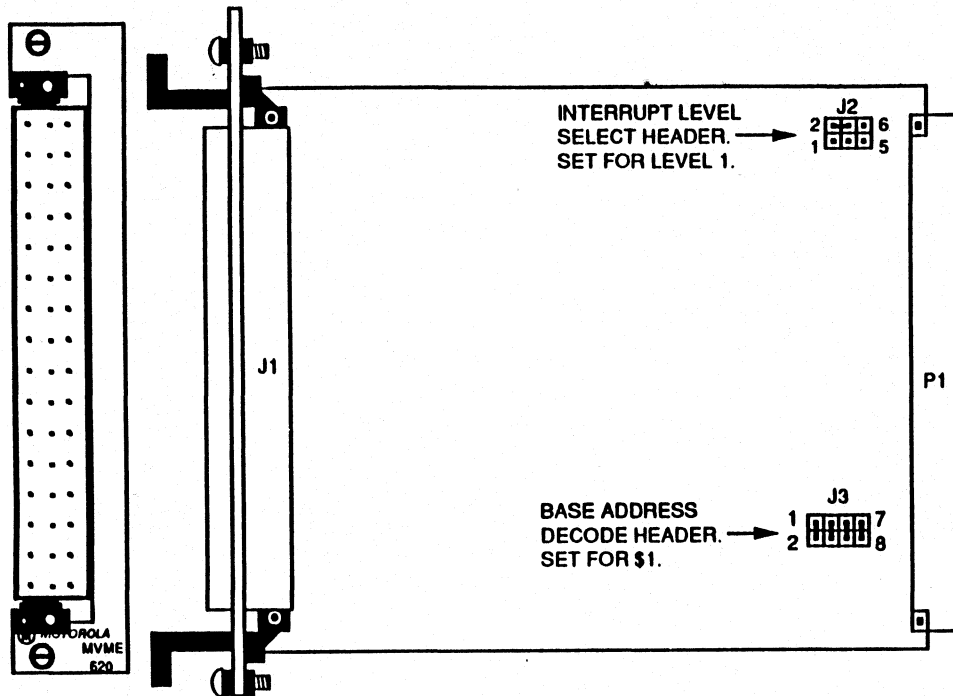
PINS JUMPERED	BLOCK SELECTED	BASE ADDRESS SELECTED HEXIDECIMAL ADDRESS
1-2,3-4,5-6,7-8	0	\$1
1-2,3-4,5-6	1	\$3
1-2,3-4,7-8	2	\$5
1-2,3-4	3	\$7
1-2,5-6,7-8	4	\$9
1-2,5-6	5	\$B
1-2,7-8	6	\$D
1-2	7	\$F
3-4,5-6,7-8	8	\$11
3-4,5-6	9	\$13
3-4,7-8	10	\$15
3-4	11	\$17
5-6,7-8	12	\$19
5-6	13	\$1B
7-8	14	\$1D
NO PINS JUMPERED	15	\$1F

02/08/90

PART NUMBERS:

MVME620 01-W3195B01 76430475

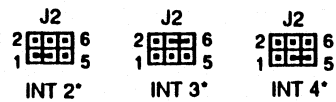
SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.



J3 I/O CHANNEL ADDRESS SELECT HEADER

PINS JUMPED	BLOCK SELECTED	BASE ADDRESS SELECTED HEXIDECIMAL ADDRESS
1-2, 3-4, 5-6, 7-8	0	\$1
1-2, 3-4, 5-6	1	\$5
1-2, 3-4, 7-8	2	\$9
1-2, 3-4	3	\$D
1-2, 5-6, 7-8	4	\$11
1-2, 5-6	5	\$15
1-2, 7-8	6	\$19
1-2	7	\$1D
3-4, 5-6, 7-8	8	\$21
3-4, 5-6	9	\$25
3-4, 7-8	10	\$29
3-4	11	\$2D
5-6, 7-8	12	\$31
5-6	13	\$35
7-8	14	\$39
NO PINS JUMPED	15	\$3D

J2 INTERRUPT LEVEL SELECT HEADER



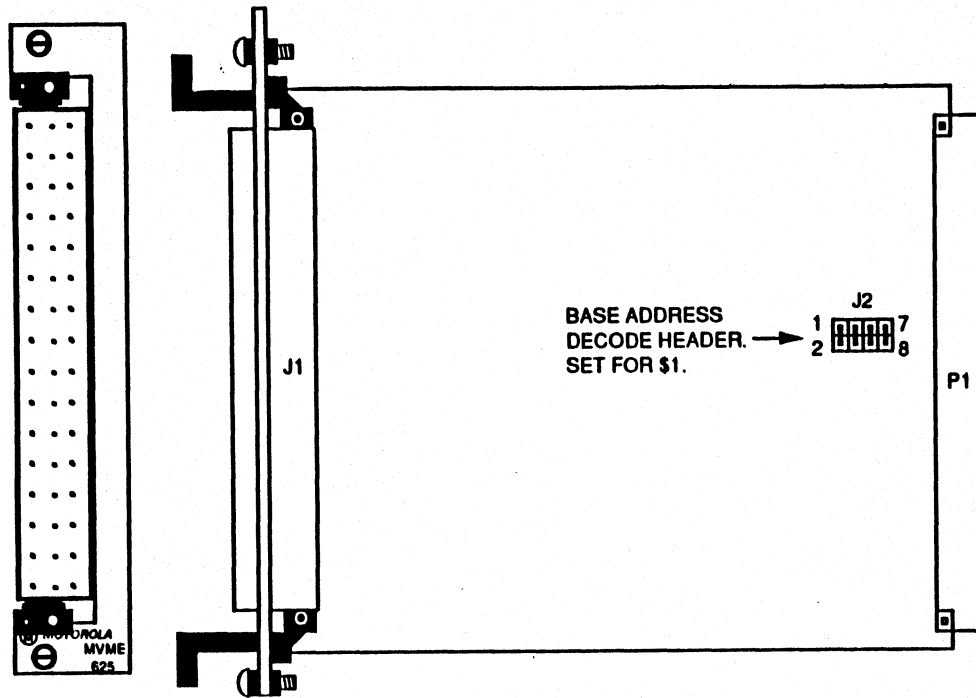
NO JUMPERS = NO INTERRUPT GENERATED

02/08/90

PART NUMBERS:

MVME625 01-W3196B01 76430476

SEE CURRENT REVISION LEVEL (CRL)
FOR CURRENT REVISION INFORMATION.

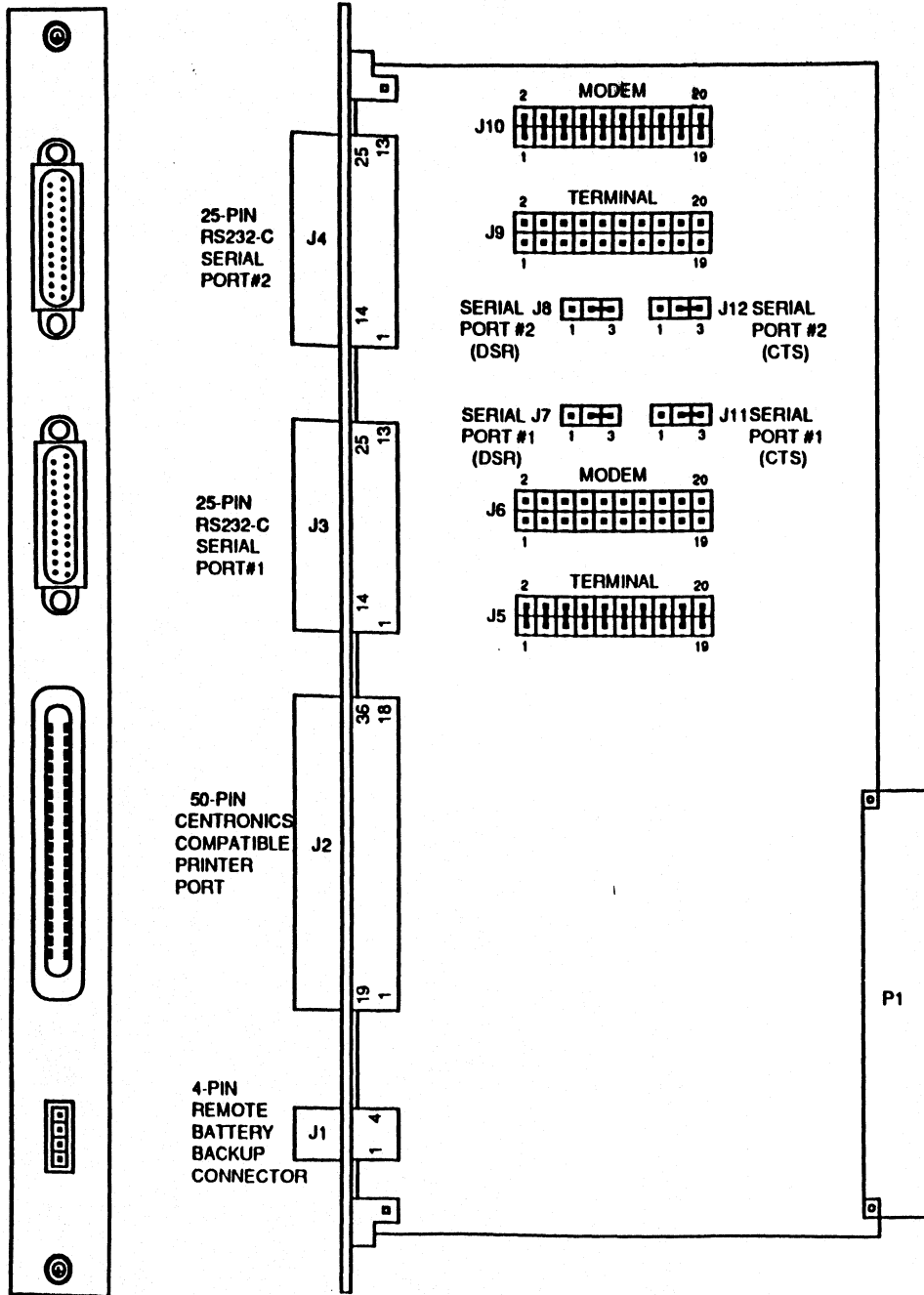


J2 I/O CHANNEL ADDRESS SELECT HEADER

PINS JUMPERED	BLOCK SELECTED	BASE ADDRESS SELECTED HEXIDECIMAL ADDRESS
1-2, 3-4, 5-6, 7-8	0	\$1
1-2, 3-4, 5-6	1	\$3
1-2, 3-4, 7-8	2	\$5
1-2, 3-4	3	\$7
1-2, 5-6, 7-8	4	\$9
1-2, 5-6	5	\$B
1-2, 7-8	6	\$D
1-2	7	\$F
3-4, 5-6, 7-8	8	\$11
3-4, 5-6	9	\$13
3-4, 7-8	10	\$15
3-4	11	\$17
5-6, 7-8	12	\$19
5-6	13	\$1B
7-8	14	\$1D
NO PINS JUMPERED	15	\$1F

02/08/90

SECTION 7



PART NUMBERS:

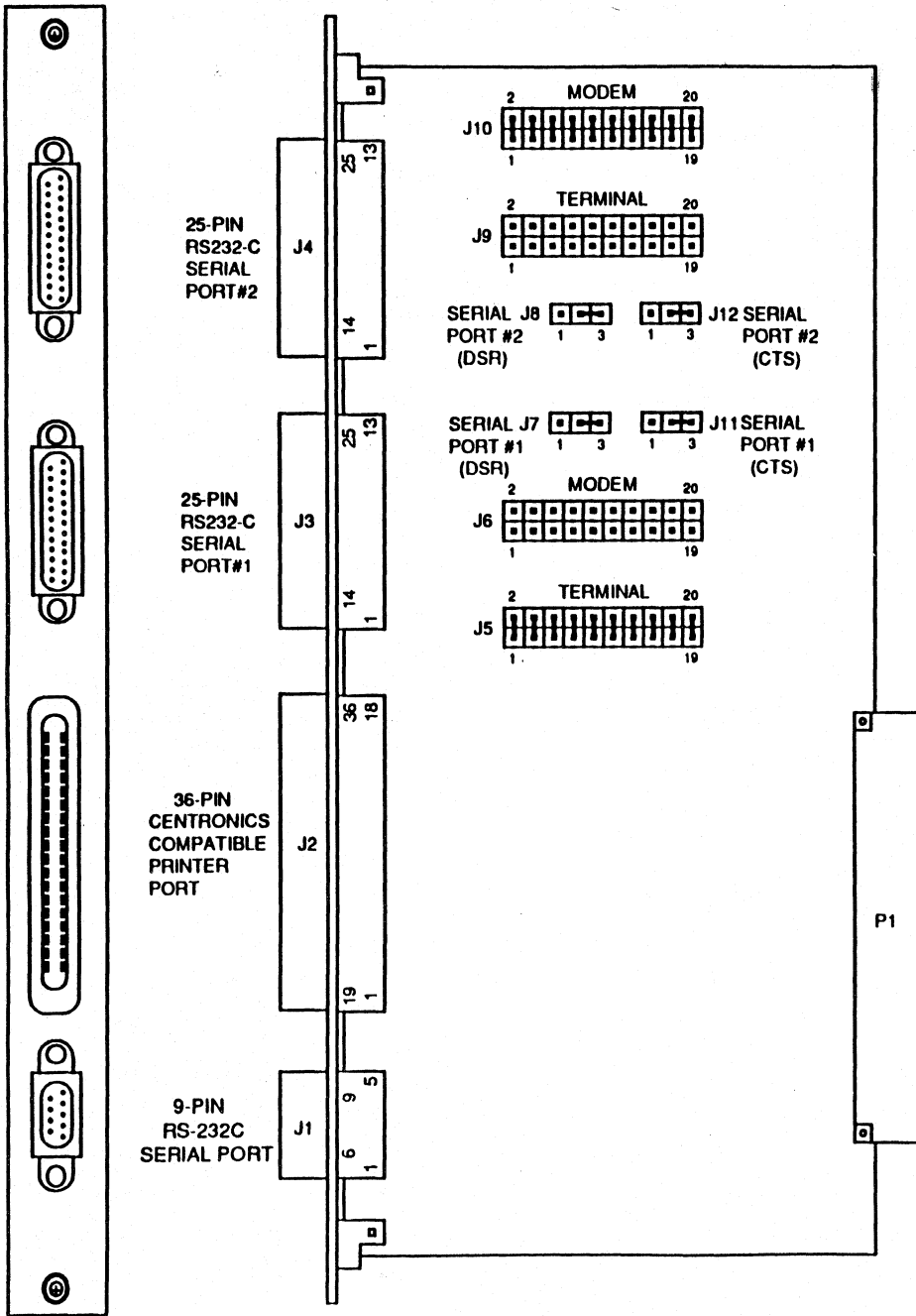
MVME701 01-W3306B01 76433001

MVME701 01-W3427B01 96010864

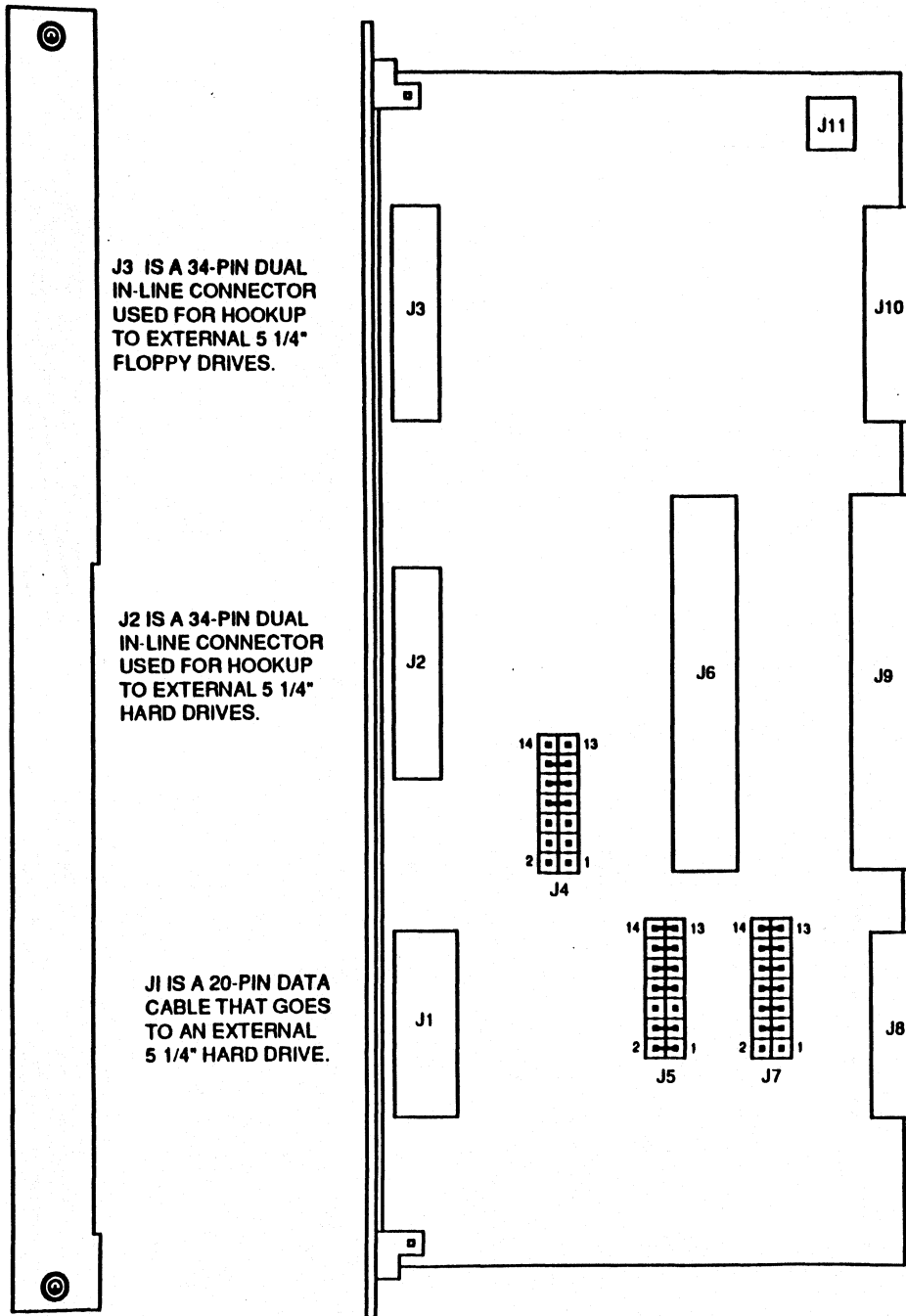
MVME701A 01-W3427B02 76435197

**SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.**

02/28/90



09/15/89



J3 IS A 34-PIN DUAL IN-LINE CONNECTOR USED FOR HOOKUP TO EXTERNAL 5 1/4" FLOPPY DRIVES.

J2 IS A 34-PIN DUAL IN-LINE CONNECTOR USED FOR HOOKUP TO EXTERNAL 5 1/4" HARD DRIVES.

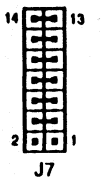
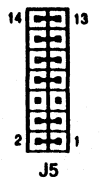
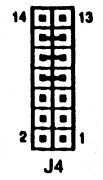
J1 IS A 20-PIN DATA CABLE THAT GOES TO AN EXTERNAL 5 1/4" HARD DRIVE.

J11 IS A 4-PIN CONNECTOR THAT SUPPLIES POWER TO THE 702(A) CARD AND PLUGS INTO THE BACKPLANE OF THE SYSTEM.

J10 IS A 34-PIN DUAL IN-LINE CONNECTOR USED FOR HOOKUP TO INTERNAL 5 1/4" FLOPPY DRIVES.

J9 IS A 50-PIN DUAL IN-LINE CONNECTOR THAT RECEIVES ITS CONTROL SIGNALS FROM J3 OF THE VME320(A/B) BOARD.

J8 IS A 20-PIN DUAL IN-LINE CONNECTOR THAT RECIEVES ITS DATA SIGNALS FROM J1 OR J2 OF THE VME320(A/B) BOARD.

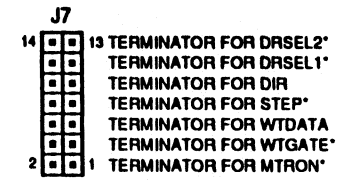
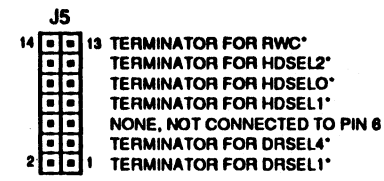
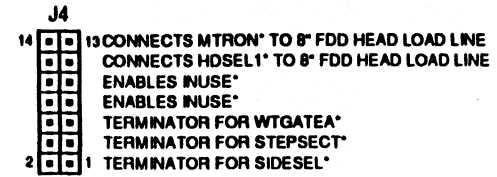


PART NUMBERS:

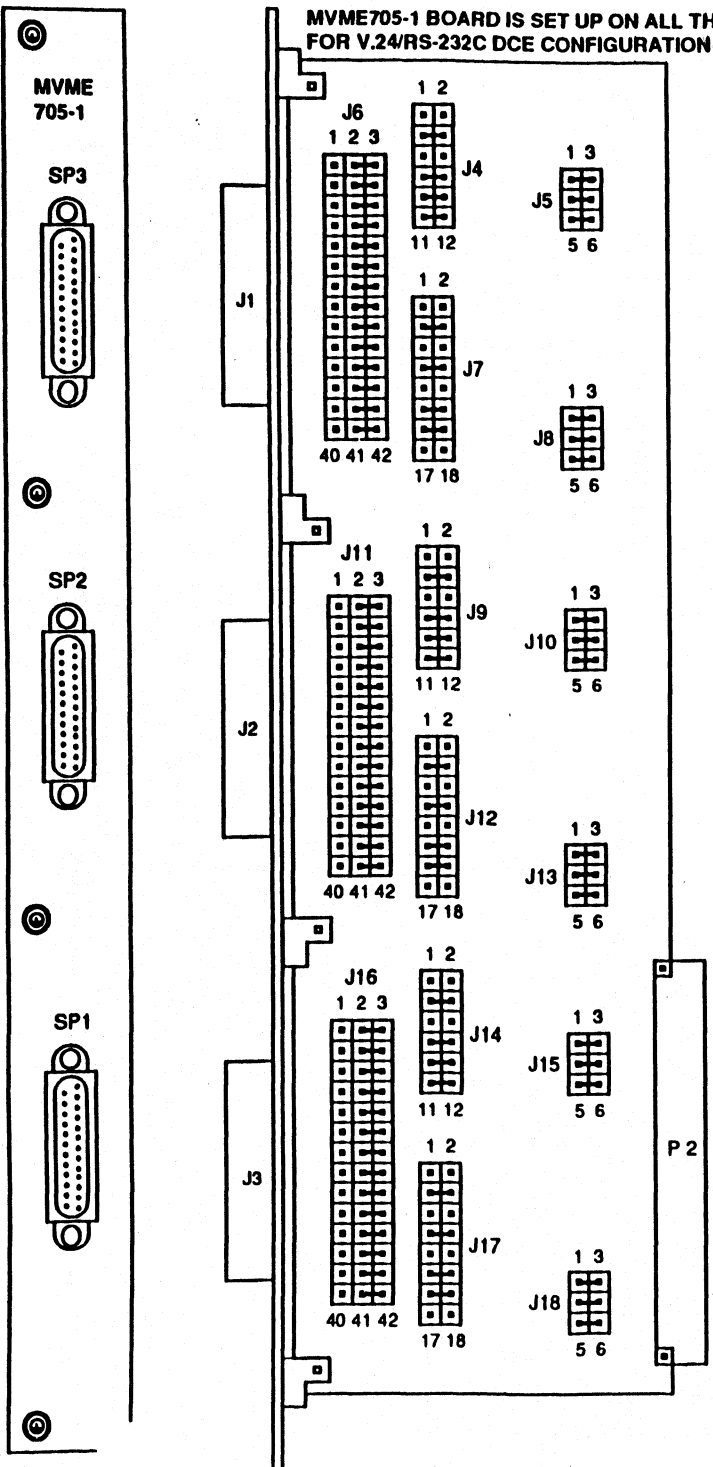
MVME702 01-W3319B01 76432616

MVME702A 01-W3319B02 76435069/5075

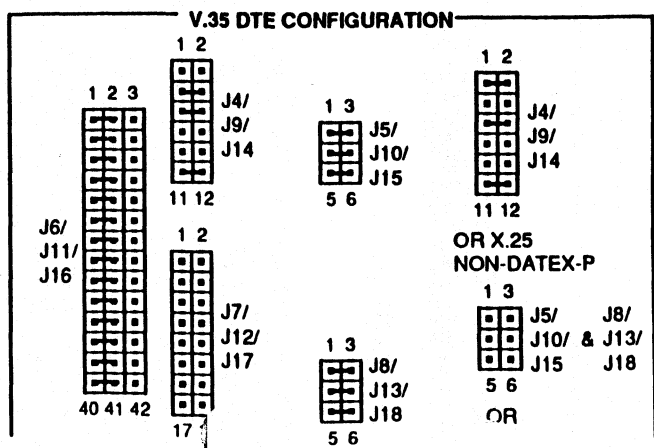
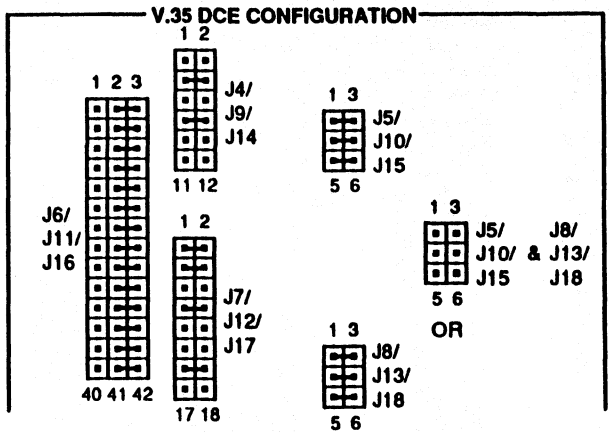
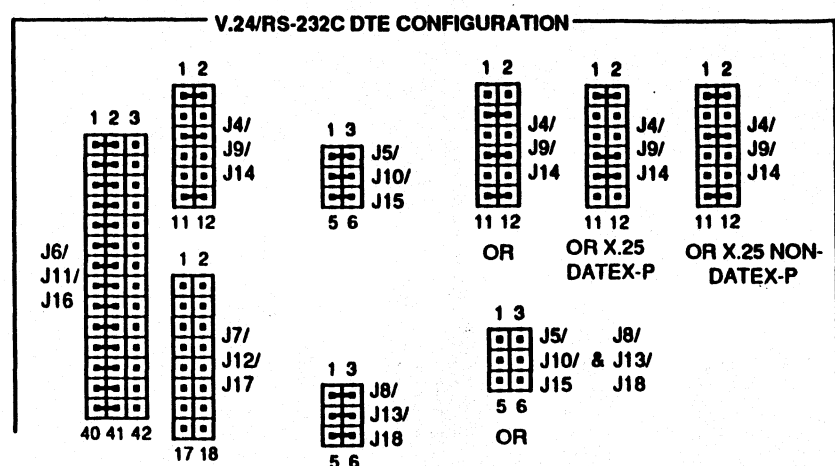
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.



09/15/89



MVME705-1 BOARD IS SET UP ON ALL THREE CHANNELS FOR V.24/RS-232C DCE CONFIGURATION



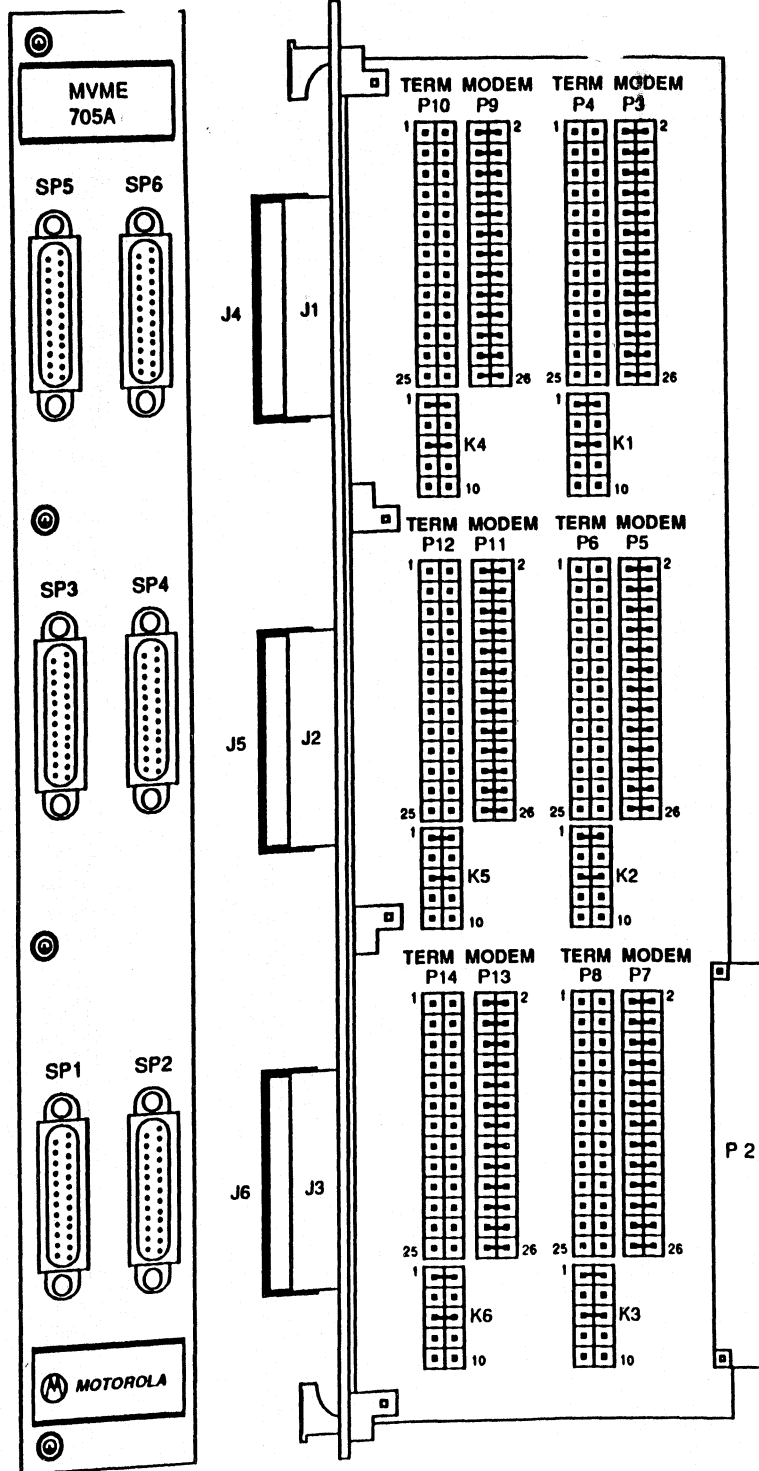
PART NUMBERS:

MVME705-1 01-W3584B01 XXXXXXXX

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: V.24/RS-232C DTE CONFIGURATION FOR SYS3640 IS DIFFERENT. J4/J9/J14 HAVE JUMPERS FROM 1-2, 5-6 AND 11-12 ALL OTHER JUMPERS ARE THE SAME.

11/15/91

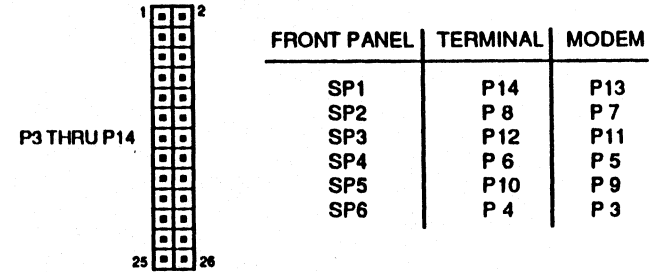


P. NUMBERS:

MVME705A 01-C3013A01 96010860
MUNICH PWB

MVME705A 01-W3505B01 96010860
US PWB

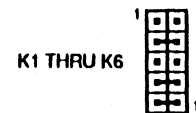
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.



FRONT PANEL	TERMINAL	MODEM
SP1	P14	P13
SP2	P8	P7
SP3	P12	P11
SP4	P6	P5
SP5	P10	P9
SP6	P4	P3

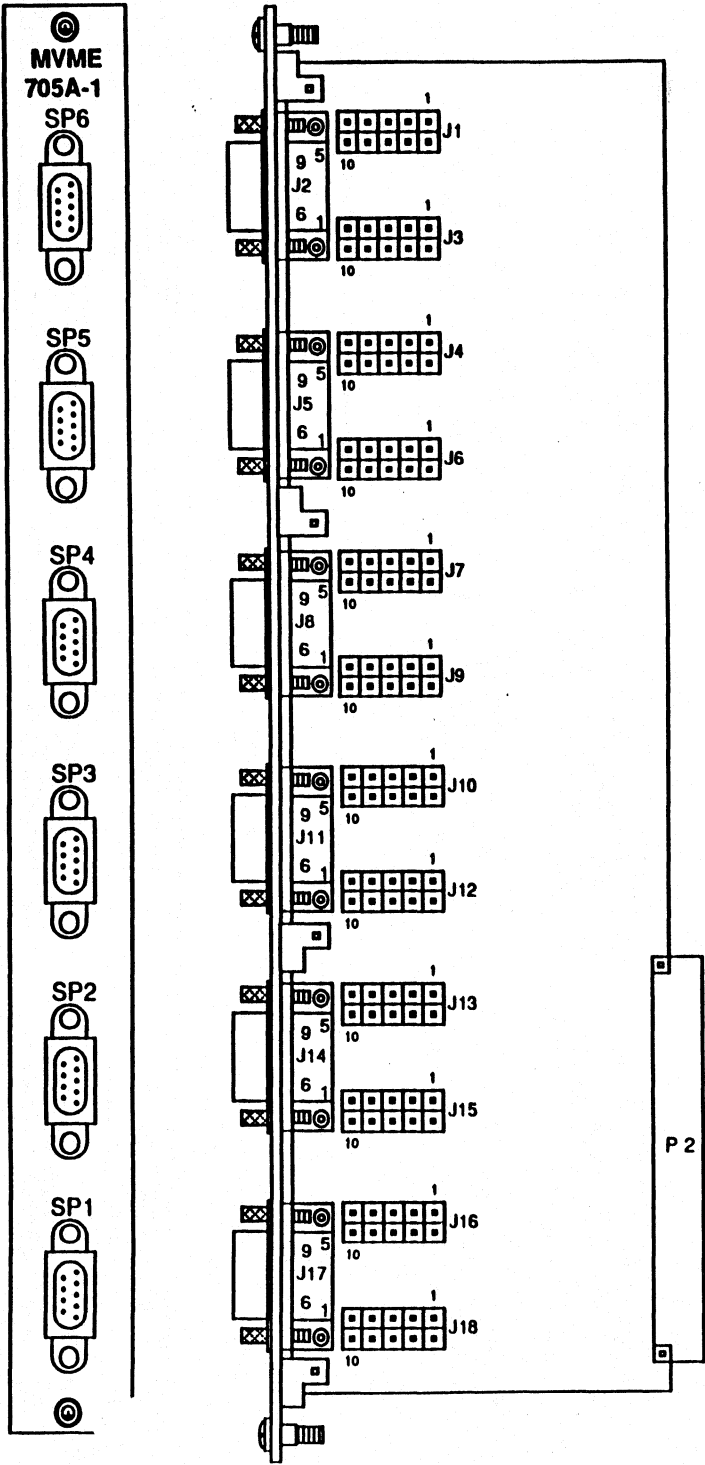
NOTE 1: ALL EVEN P(X) CONNECTORS ARE USED FOR
TERMINAL HOOKUP; ALL ODD P(X) CONNECTORS
ARE USED FOR MODEM HOOKUP. BOTH ARE
WIRED TO THE FRONT PANEL J1 THRU J6 THRU
RIBBON CABLE CONNECTORS. TO CHANGE FROM
TERMINAL TO MODEM JUST SWITCH FROM AN
EVEN TO AN ODD CABLE CONNECTOR OR VISA
VERSA AND CHANGE APPROPRIATE K(X) JUMPER.

NOTE 2: FOR SYS1147, 3200, 3400, 3604/08, 3640, & 8608, P3, P5,
P7, P9, P11, & P13 HAVE ALL JUMPERS INSTALLED. K1
THRU K6 ARE THE SAME AS SHOWN.



NOTE: DCE FOR CONNECT TO
TERMINAL IS SHOWN HERE.
DTE FOR CONNECT TO MODEM
IS SHOWN ON THE PWB
EXAMPLE TO THE LEFT.

08/31/90

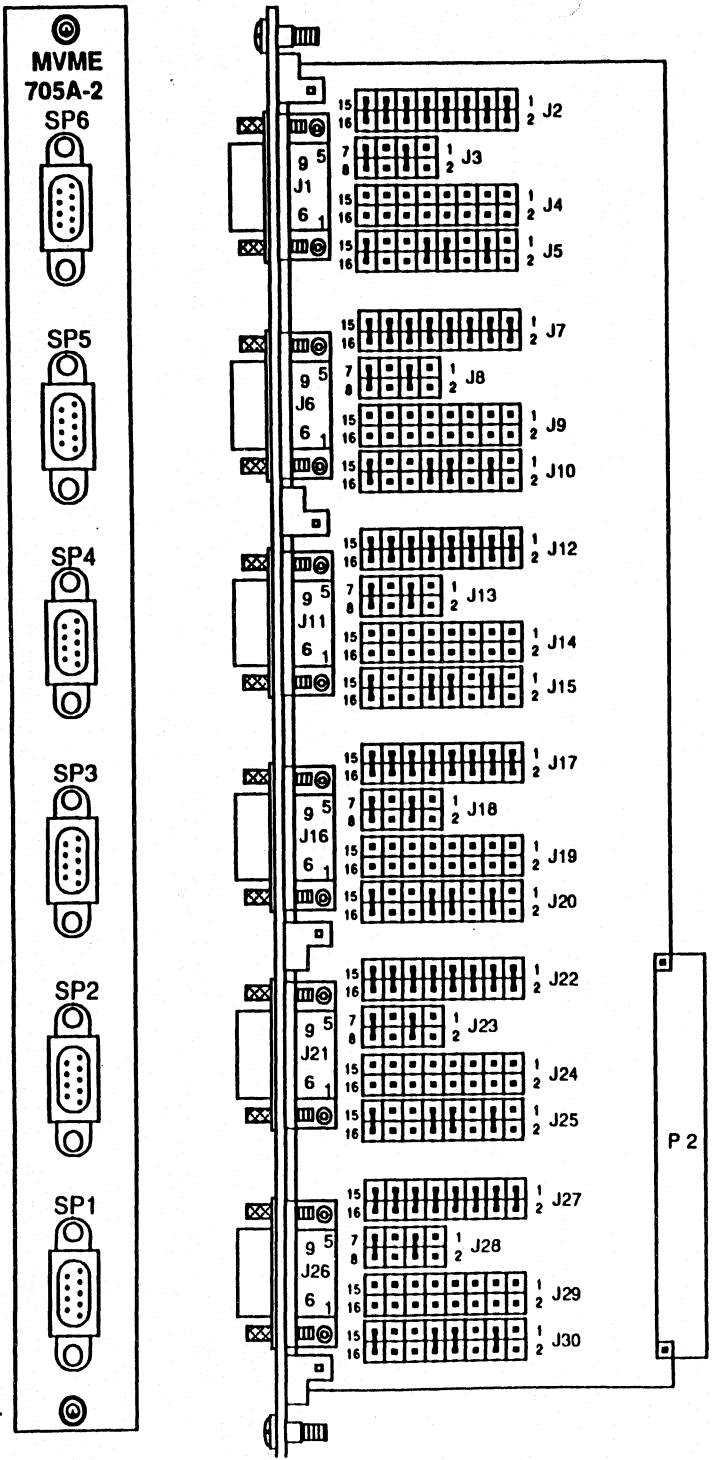


PART NUMBERS:

MVME705A-1 01-W3649B01 NONE

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

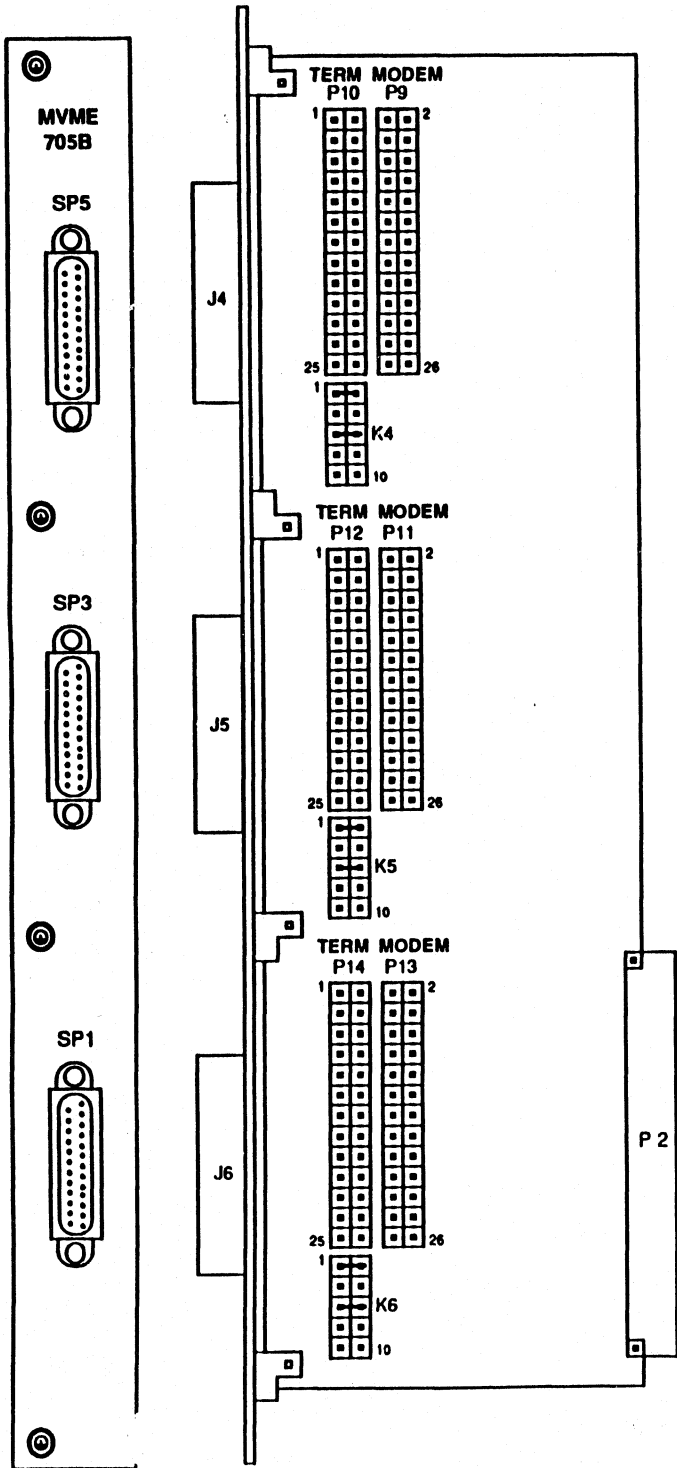
08/31/90



MVME705A-2 01-W3661B01 NONE

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

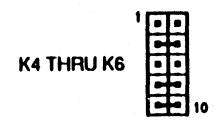
08/31/90



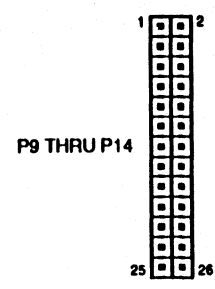
PART NUMBERS:

MVME705B 01-W3505B02 96011042

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.



NOTE: DCE FOR CONNECT TO TERMINALS IS SHOWN HERE. DTE FOR CONNECT TO MODEM IS SHOWN ON THE PWB EXAMPLE TO THE LEFT.

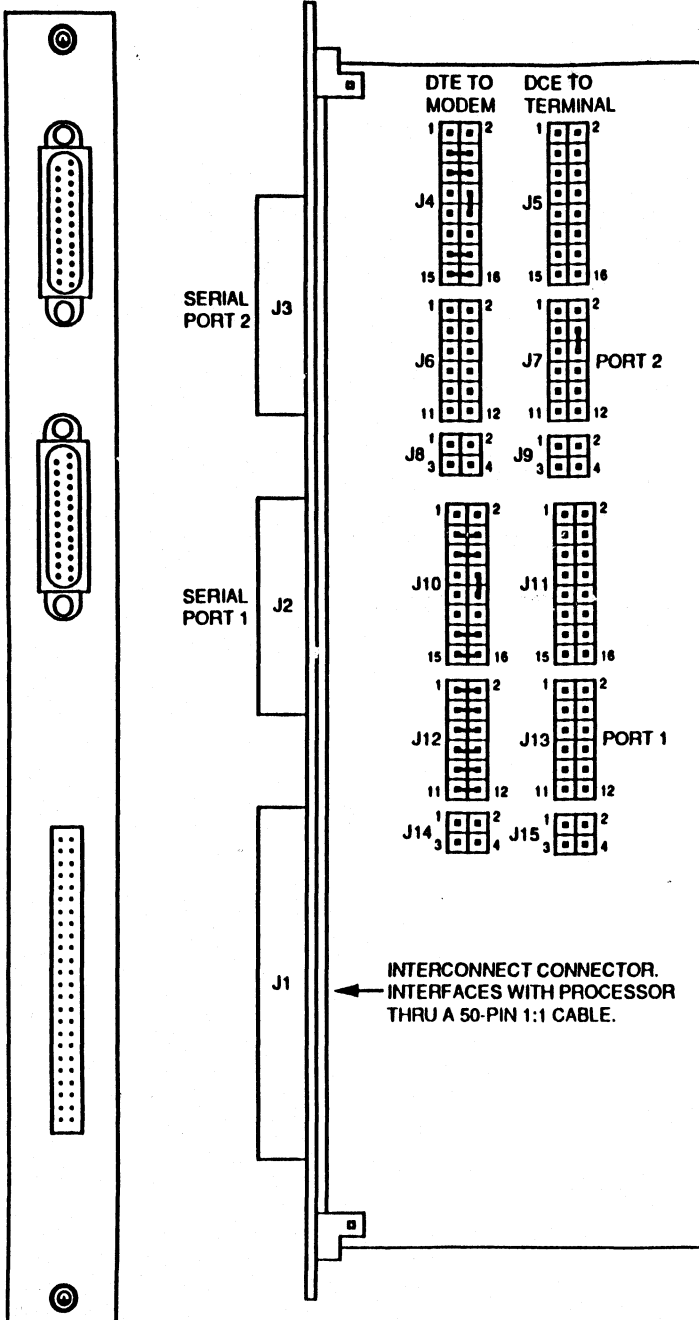


FRONT PANEL	TERMINAL	MODEM
SP1	P14	P13
SP3	P12	P11
SP5	P10	P9

NOTE 1: ALL EVEN P(X) CONNECTORS ARE USED FOR TERMINAL HOOKUP; ALL ODD P(X) CONNECTORS ARE USED FOR MODEM HOOKUP. BOTH ARE WIRED TO THE FRONT PANEL J1 THRU J6 THRU RIBBON CABLE CONNECTORS. TO CHANGE FROM TERMINAL TO MODEM JUST SWITCH FROM AN EVEN TO AN ODD CABLE CONNECTOR OR VISA VERSA AND CHANGE APPROPRIATE K(X) JUMPER.

NOTE 2: FOR SYS1147, 3200, 3400, 3604/08, 3640, & 8608, P9, P11, & P13 HAVE ALL JUMPERS INSTALLED. K1 THRU K6 ARE THE SAME AS SHOWN.

08/31/90



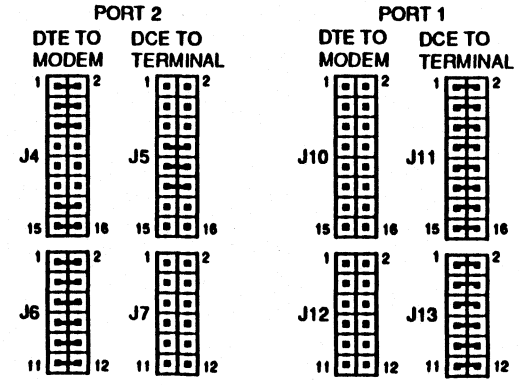
PART NUMBERS:

- MVME707 01-W3390B01 76433038
- SMM1442 01-W3390B02 76435442
- MVME707A 01-W3453B01 96010823
- SMM1442 01-W3453B01 96010823

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: SHOWN JUMPED FOR USE WITH INTERNAL MODEM.

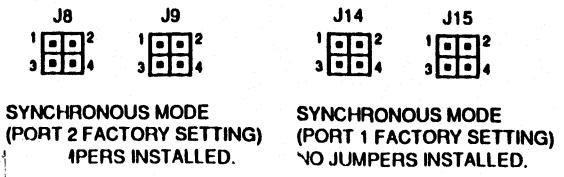
NOTE 2: J7 JUMPERS 3-4 MAKES DCE COME FROM THE MODEM.



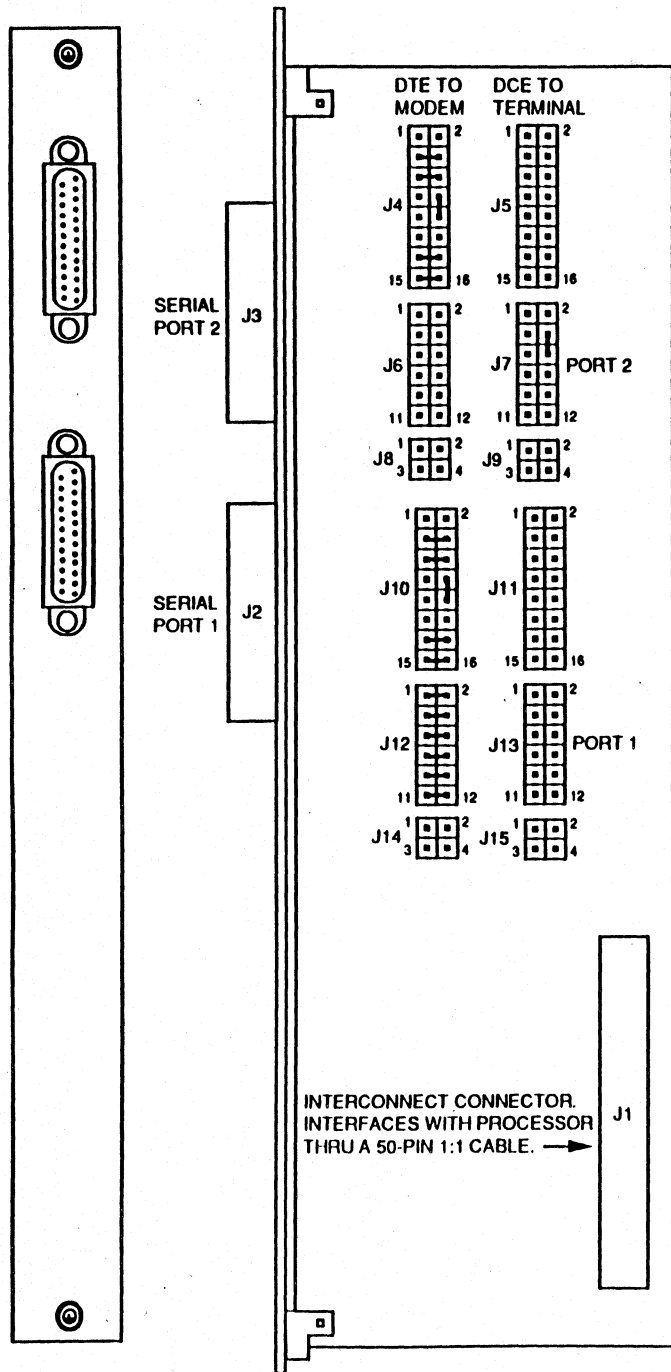
PORT 2 CONFIGURES CONNECTOR J2 AS TERMINAL PORT (DTE MODE FACTORY SETTING).

PORT 1 CONFIGURES CONNECTOR J1 AS HOST PORT (DCE MODE FACTORY SETTING).

SYNCHRONOUS/ASYNCHRONOUS MODE SELECT



02/28/90

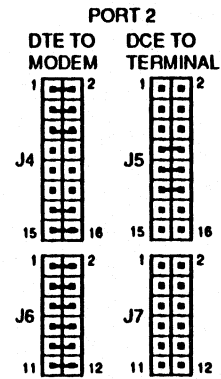


SYNCHRONOUS/ASYNCHRONOUS MODE SELECT

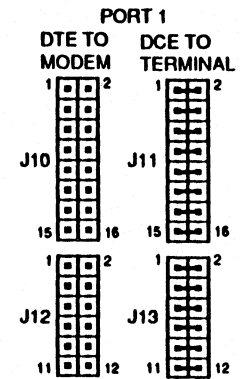


SYNCHRONOUS MODE
(PORT 2 FACTORY SETTING)
NO JUMPERS INSTALLED.

SYNCHRONOUS MODE
(PORT 1 FACTORY SETTING)
NO JUMPERS INSTALLED.

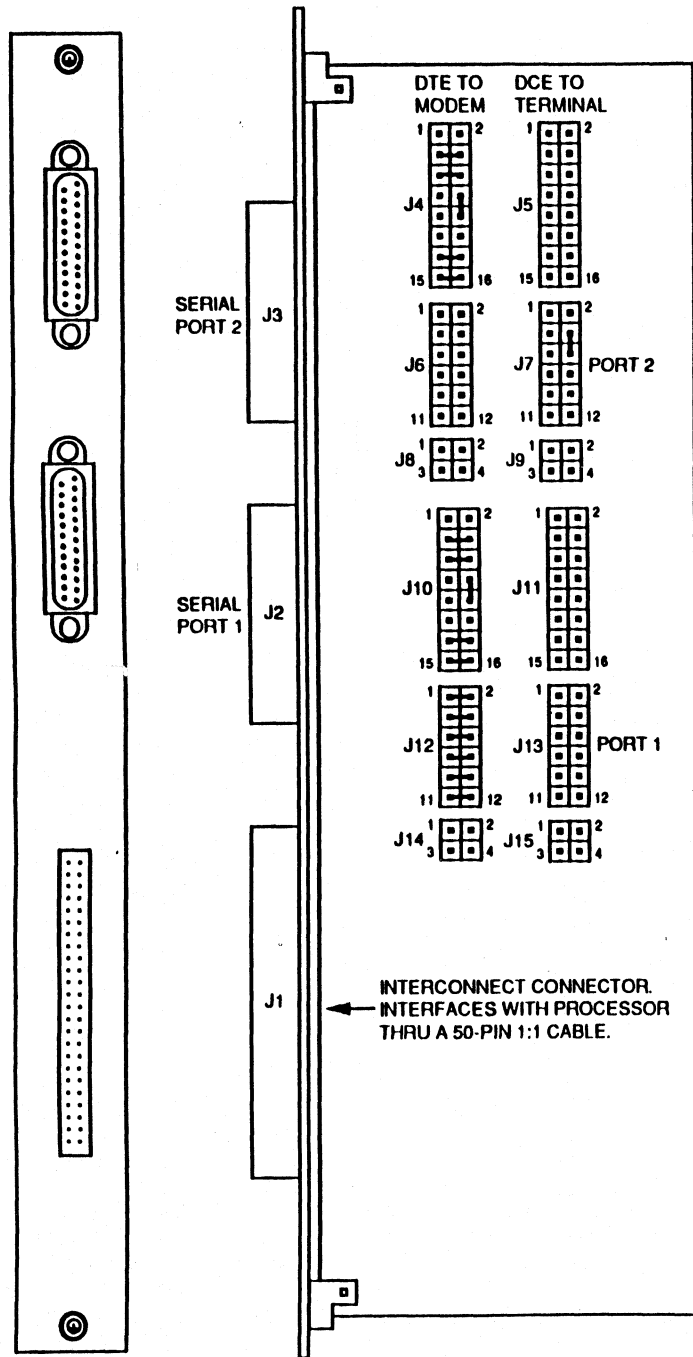


PORT 2 CONFIGURES CONNECTOR
J2 AS TERMINAL PORT (DTE MODE
FACTORY SETTING).



PORT 1 CONFIGURES CONNECTOR
J1 AS HOST PORT (DCE MODE
FACTORY SETTING).

09/15/89



PART NUMBERS:

MVME707 01-W3390B01 76433038

SMM1442 01-W3390B02 76435442

MVME707A 01-W3453B01 96010823

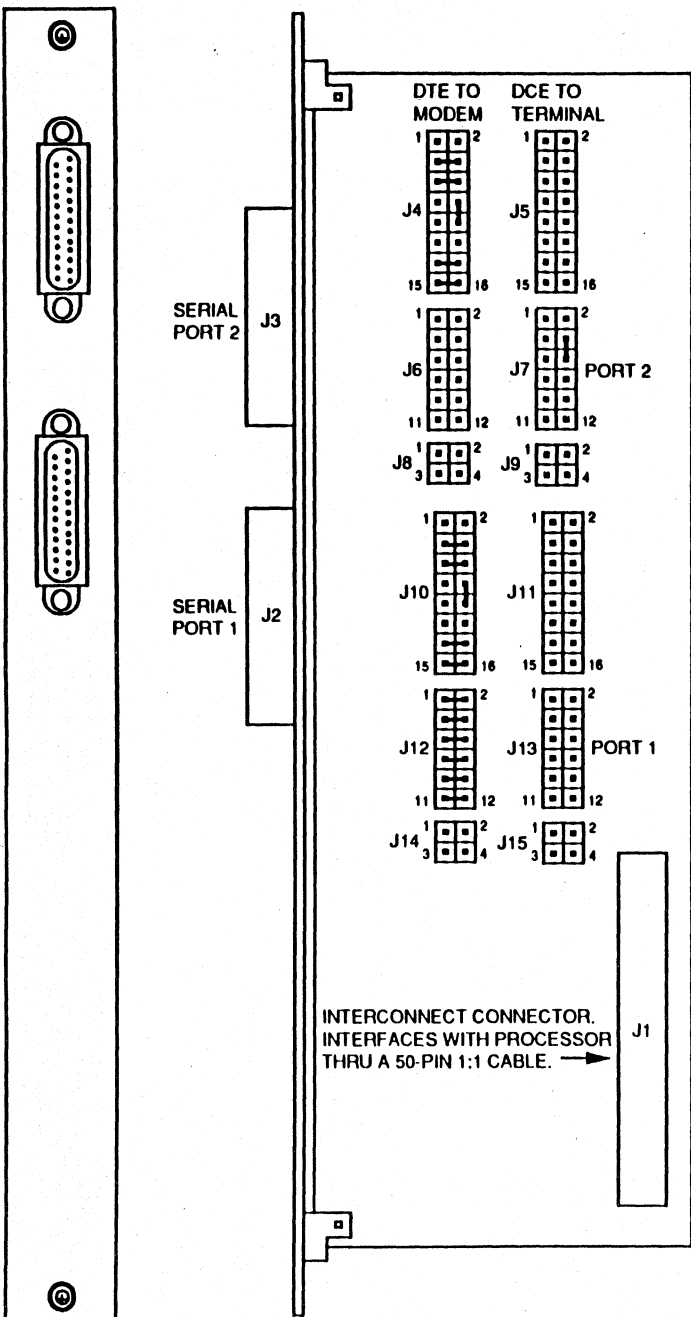
SMM1442 01-W3453B01 96010823

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

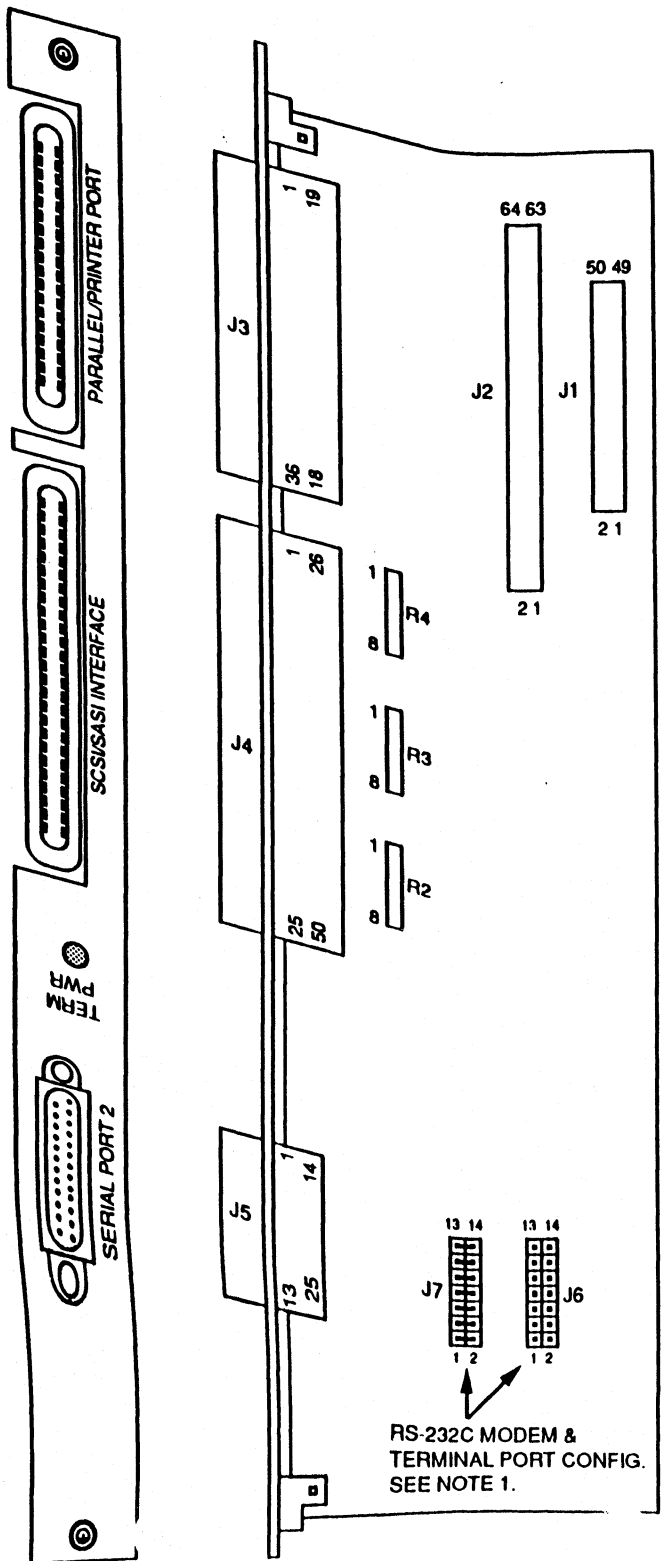
NOTE 1: SHOWN JUMPED FOR USE WITH INTERNAL MODEM.

NOTE 2: J7 JUMPERS 3-4 MAKES DCE COME FROM THE MODEM.

02/28/90



09/15/89

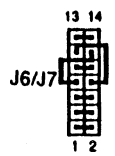


PART NUMBERS:

- MVME708 01-W3392B01 76432932
- MVME708-1 01-W3392B02 76433049
- MVME708-1 01-W3428B01 76433095
- MVME708A 01-W3473B01 76435335

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

DTE/DCE SELECT HEADER

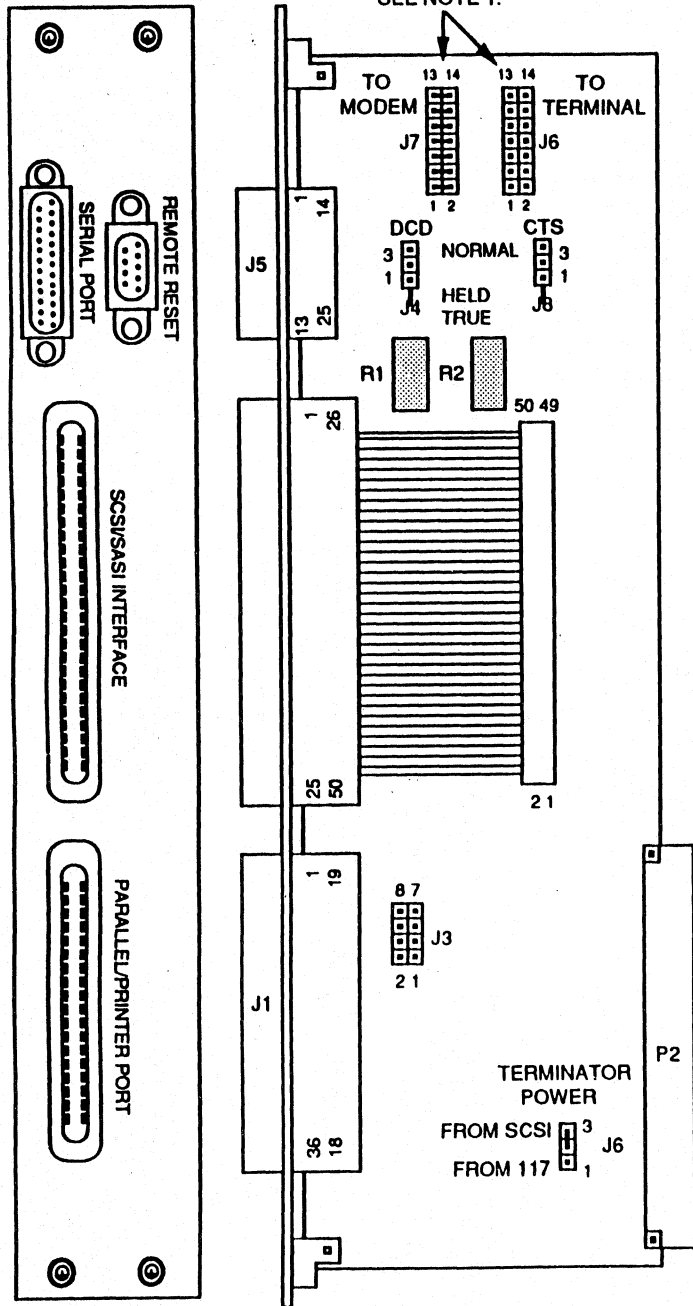


FOR VERSAdos 4.5 OR CTS/RTS FLOW CONTROL (DCE TO TERMINAL)

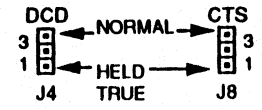
NOTE 1: EITHER J6 OR J7 IS USED FOR MODEM OR TERMINAL BUT NOT BOTH AT THE SAME TIME. SOME RS-232C LINES MAY BE TIED TOGETHER AND RENDER THE PORTS INOPERATIVE. ALL JUMPERS IN = DTE TO MODEM; ALL JUMPERS OUT = DCE TO TERMINAL

RS-232C MODEM & TERMINAL PORT CONFIG. SEE NOTE 1.

02/28/90



RS-232C MODEM &
TERMINAL PORT CONFIG.
SEE NOTE 1.

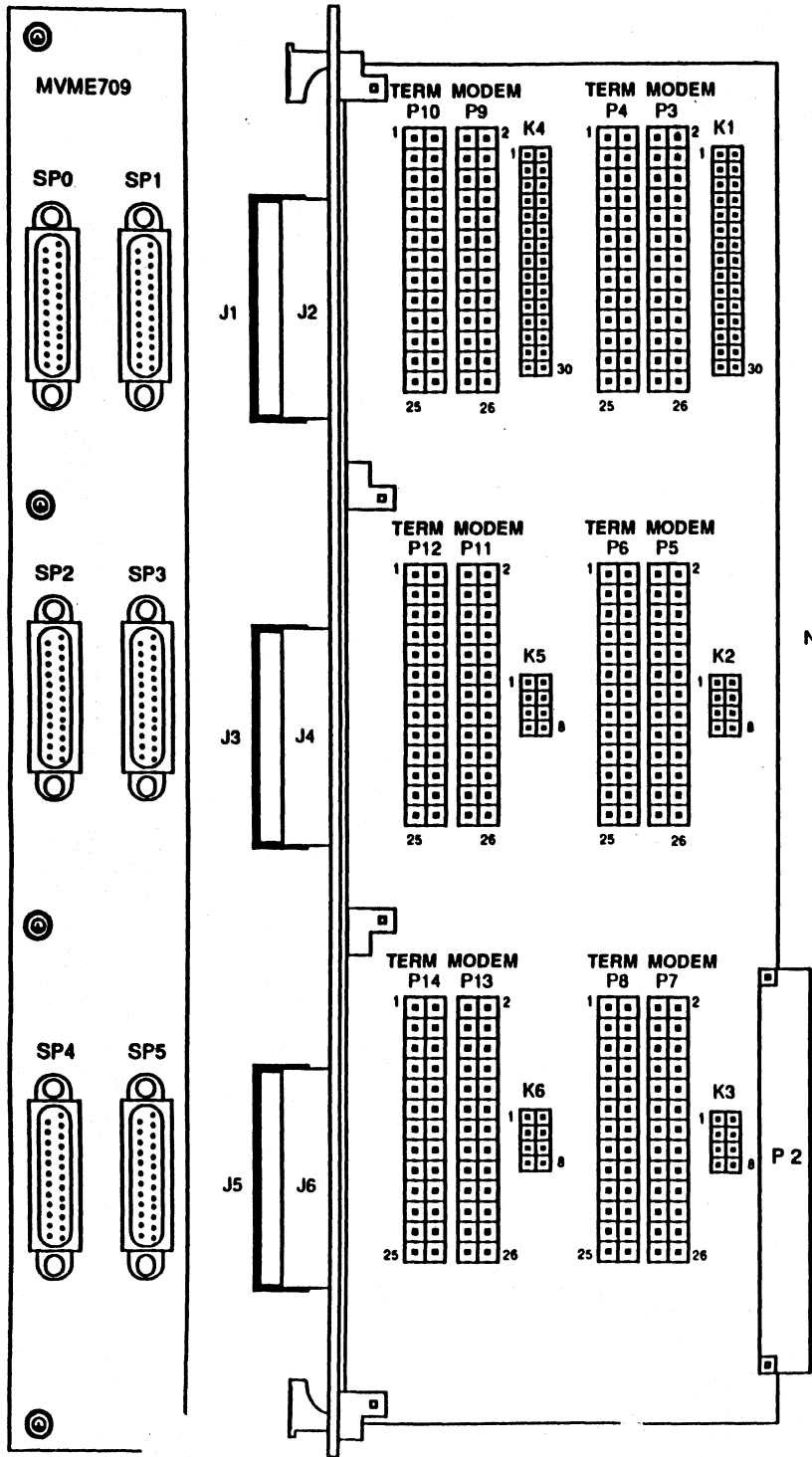


J4 & J8 JUMPERS BETWEEN 2 - 3
ARE NORMAL HOOKUPS. JUMPERS
BETWEEN 1 - 2 ARE HELD TRUE
SIGNALS.

NOTE 1: COULDN'T FIND ANY EXPLANATION ON J3

NOTE 2: J6 IS SELF EXPLANATORY. IF JUMPER IS
BETWEEN 1 - 2, POWER COMES FROM MVME117
BOARD AND IF IT'S BETWEEN 2 - 3 IT COMES FROM
THE SCSI SOURCE TO SUPPLY POWER TO THE
TERMINATOR RESISTORS (R1 AND R2).

09/15/89

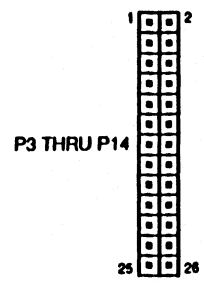


PART NUMBERS:

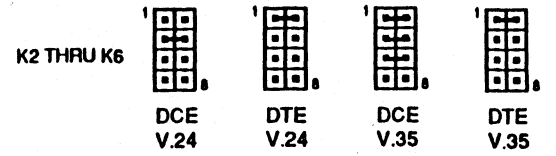
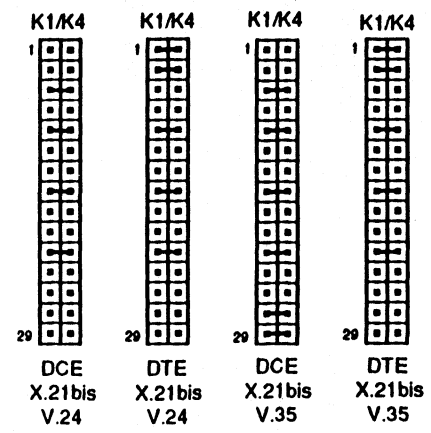
MVME709 01-G3028M01 TBD
MUNICH PWB

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

FRONT PANEL	MODEM	TERMINAL
SP0	P10	P9
SP1	P4	P3
SP2	P12	P11
SP3	P6	P5
SP4	P14	P13
SP5	P8	P7

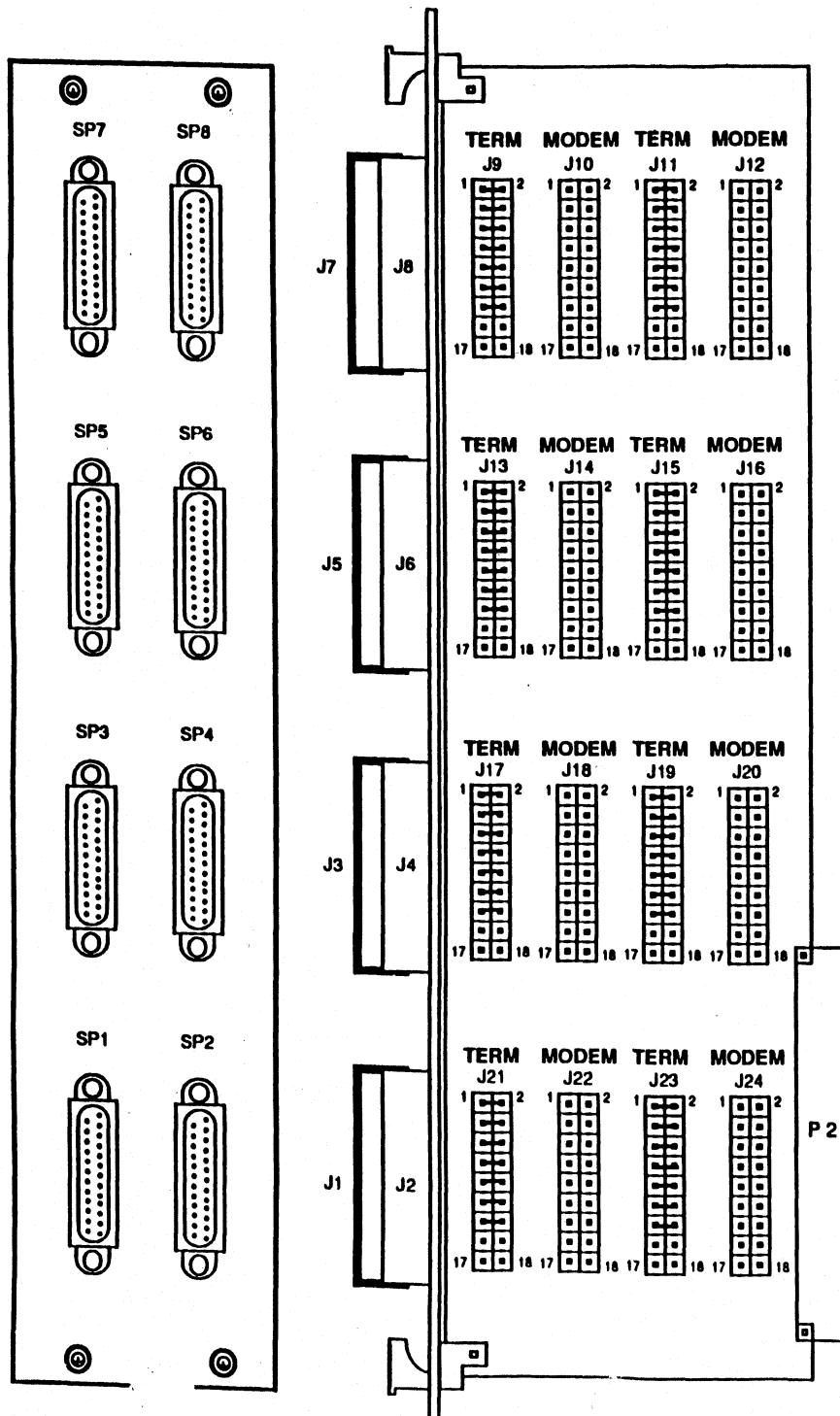


NOTE: ALL EVEN P(X) CONNECTORS ARE USED FOR
MODEM HOOKUP; ALL ODD P(X) CONNECTORS
ARE USED FOR TERMINAL HOOKUP. BOTH ARE
WIRED TO THE FRONT PANEL J1 THRU J6 THRU
RIBBON CABLE CONNECTORS. TO CHANGE FROM
TERMINAL TO MODEM JUST SWITCH FROM AN
EVEN TO AN ODD CABLE CONNECTOR OR VISA
VERSA AND CHANGE APPROPRIATE K(X) JUMPER.



NOTE: DCE FOR CONNECT TO
TERMINAL IS SHOWN HERE.
DTE FOR CONNECT FOR MODEM
IS SHOWN ON THE PWB
EXAMPLE TO THE LEFT.

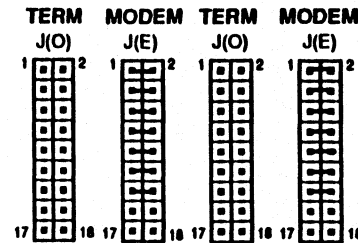
02/28/90



PART NUMBERS:

MVME710/SMM1437 01-W3451B01 96010822

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.



ODD HEADERS:		EVEN HEADERS:	
PORT HEADER		PORT HEADER	
1	J21	1	J22
2	J23	2	J24
3	J17	3	J18
4	J19	4	J20
5	J13	5	J14
6	J15	6	J16
7	J9	7	J10
8	J11	8	J12

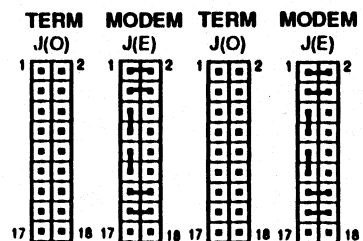
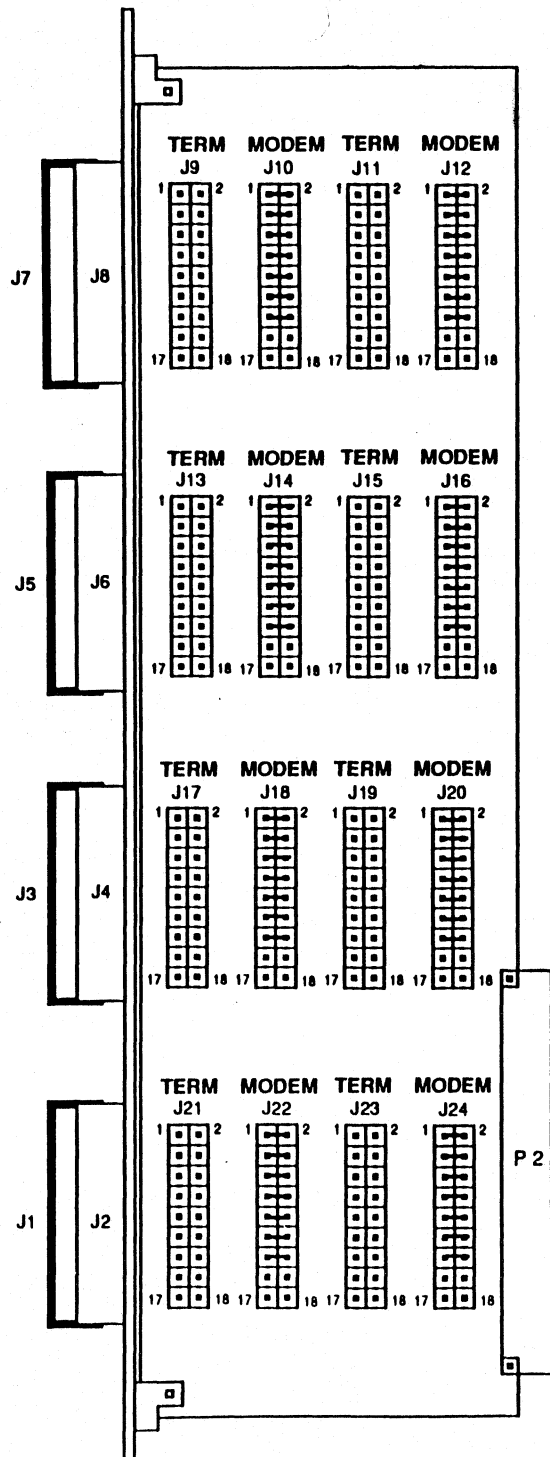
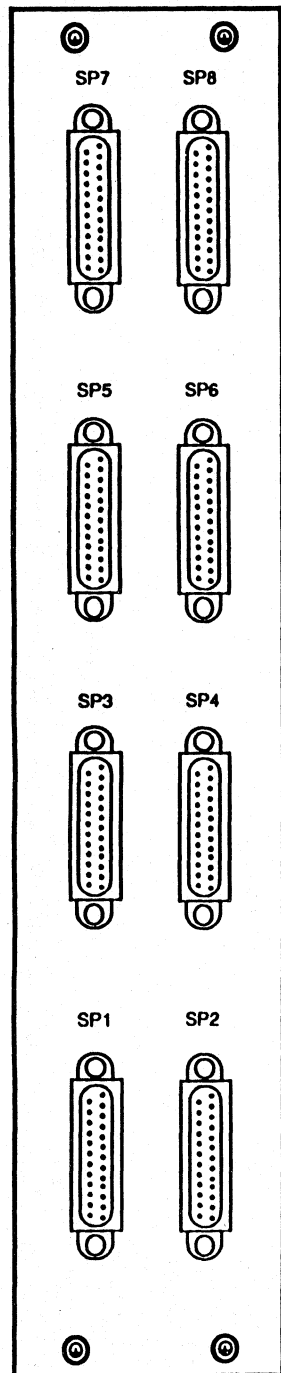
NOTE 1: J(O)- ODD, J(E)- EVEN. ODD JUMPERS ARE USED FOR DTE CONFIGURATION. EVEN JUMPERS ARE USED FOR DCE CONFIGURATION. SEE PAGE 2 FOR SYS3200, 3400, 3304/08, 3640, 8400 & 8608 CONFIGURATIONS.

NOTE 2: J1 THRU J8 ARE STANDARD RS-232C 25-PIN DIN CONNECTORS.

NOTE 3: THIS CONFIGURATION IS ALSO USED FOR SYS1147s.

NOTE 4: IF SYSTEM GROUND IS REQUIRED, INSTALL 17 - 18 ON ANY JUMPER BLOCK (RS232) CHANNEL REQUIRING IT.

02/28/90



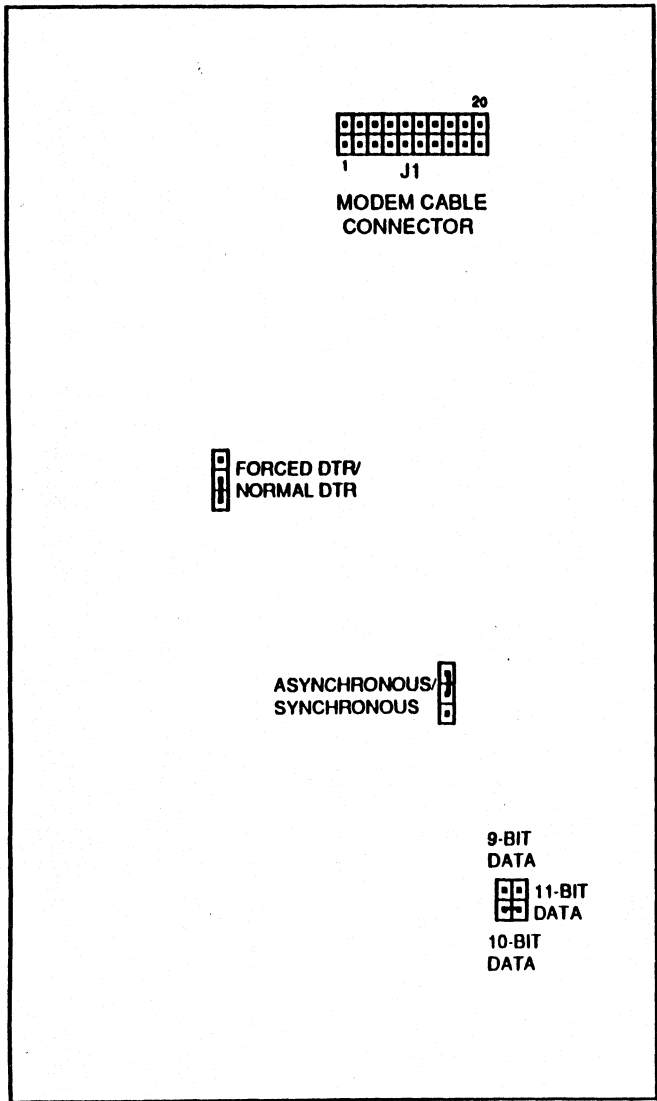
NOTE 1: J(O)= ODD, J(E)= EVEN. ODD JUMPERS ARE USED FOR DTE CONFIGURATION. EVEN JUMPERS ARE USED FOR DCE CONFIGURATION. SEE PAGE 2 FOR SYS3200, 3400, 3304/08, 3640, 8400 & 8608 CONFIGURATIONS.

NOTE 2: J1 THRU J8 ARE STANDARD RS-232C 25-PIN DIN CONNECTORS.

NOTE 3: IF SYSTEM GROUND IS REQUIRED, INSTALL 17 - 18 ON ANY JUMPER BLOCK (RS232) CHANNEL REQUIRING IT.

ODD HEADERS:		EVEN HEADERS:	
PORT HEADER		PORT HEADER	
1	J21	1	J22
2	J23	2	J24
3	J17	3	J18
4	J19	4	J20
5	J13	5	J14
6	J15	6	J16
7	J9	7	J10
8	J11	8	J12

02/28/90



PART NUMBERS:

MVME710F/731 01-W2809B01 96010843
 VENDOR PART NUMBER 2122662

SEE CURRENT REVISION LEVEL (CRL) FOR
 CURRENT REVISION INFORMATION.

02/28/90

SYSTEM/CPU	SIGNAL CABLE	FSD-LOG	POWER CABLE	FSD-LOG
2016/13X	30-W2810B01	96010844	*	*
2616/13X	SAME AS 2016/13X-----			
2316/132X	30-W2930B01	96010389	*	*
STINGER/132X	SAME AS 2316/132X -----		**	96010395
2334/134	30-W2043C01	96010937	30-W2044C01	96010936
330X/147	SAME AS 2334/134		30-W2927B01	96010374
3640/141	30-W2194C01	96010575	SAME AS 2334	
3640/147	SAME AS 3640/141-----			
1132/13X	SAME AS 2316/132X -----		*	*
1147/147	TBD	TBD	TBD	TBD

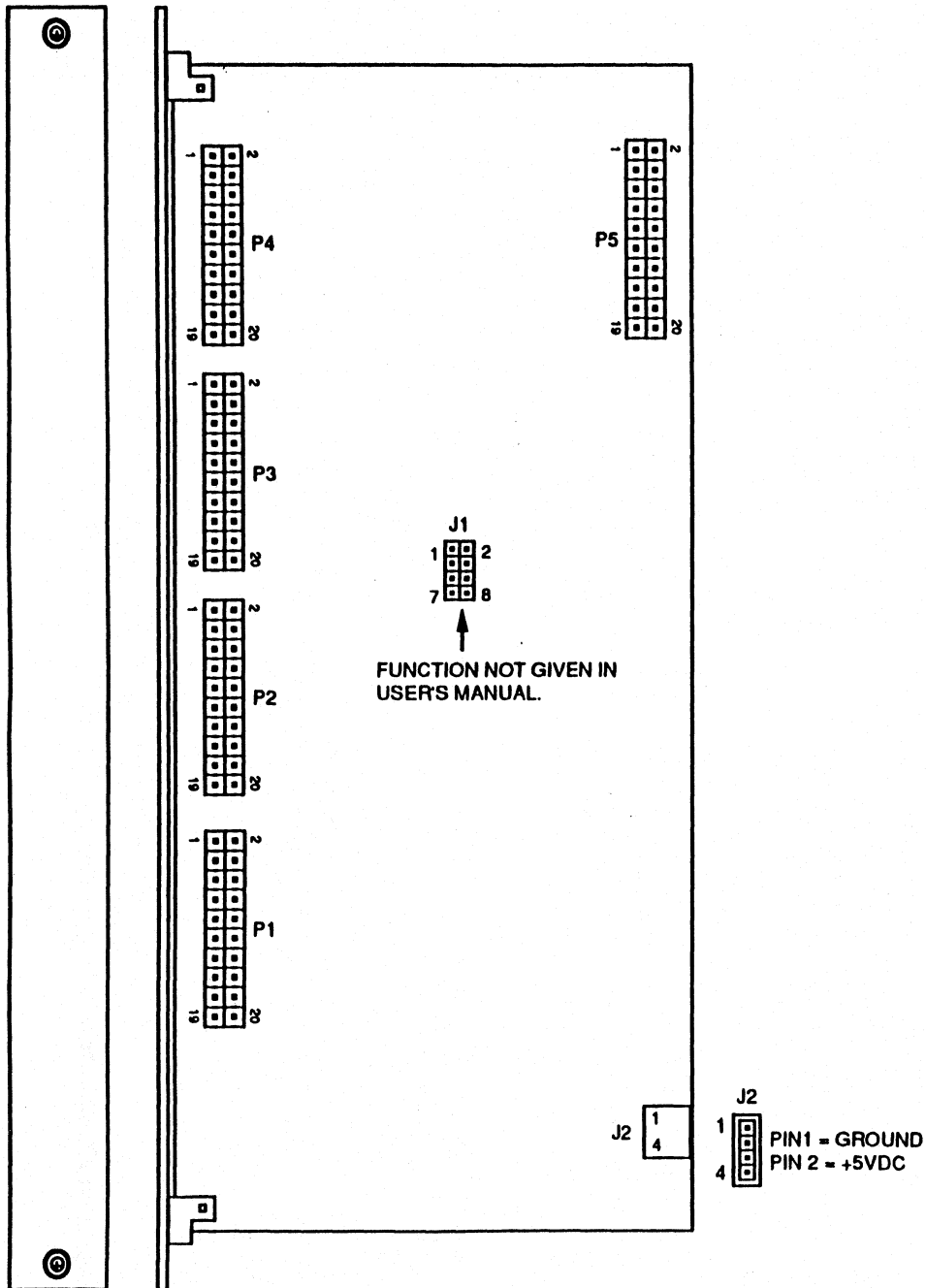
* PART OF SIGNAL CABLE.

** VENDOR IS CSD-ASE, VENDOR WILL USE A PLUG-ON MODEM FOR THE
 TRANSITION PWA (SEE MVME714M).

PART NUMBERS:

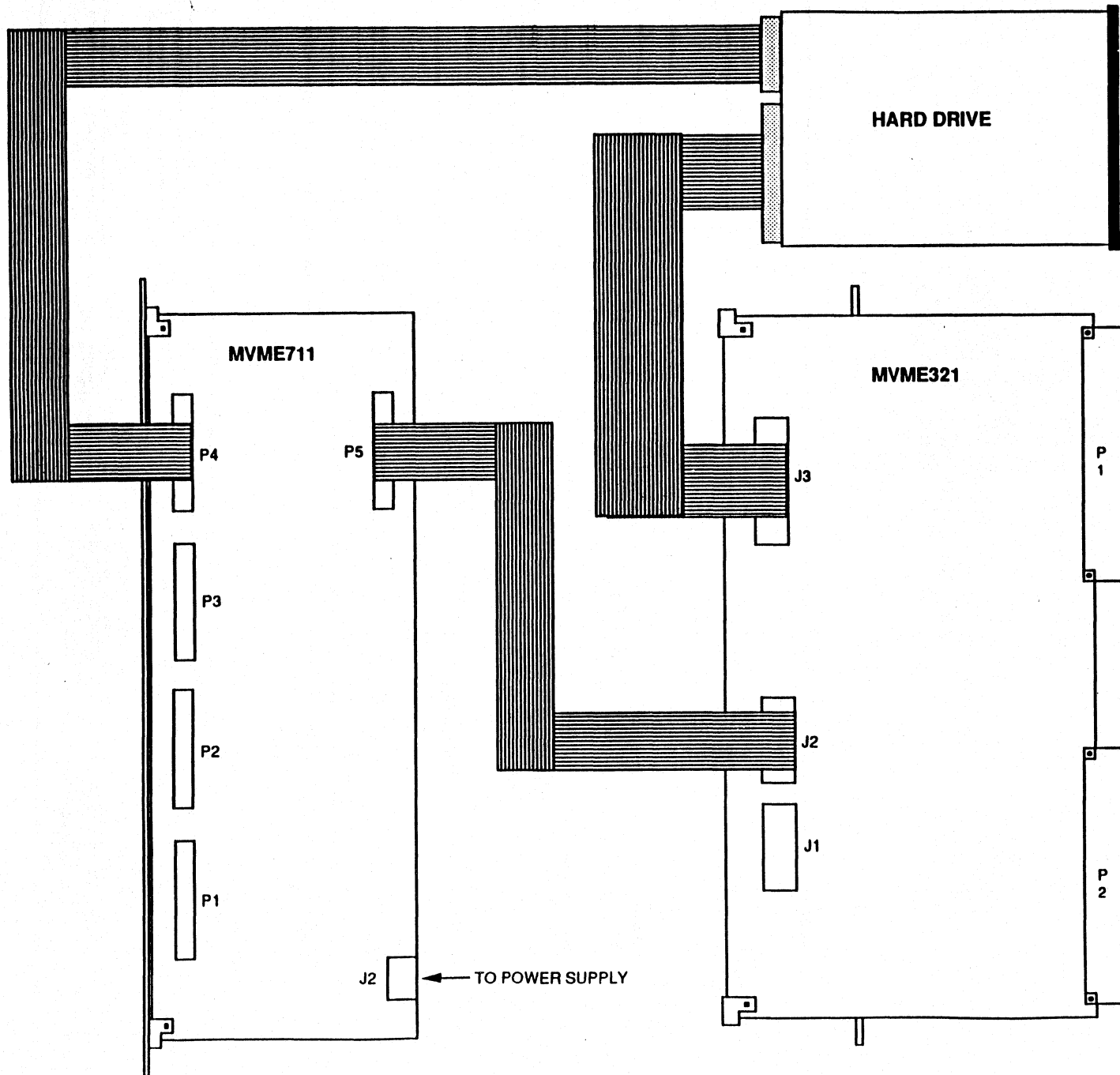
MVME711 01-W3468B01 76435374

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

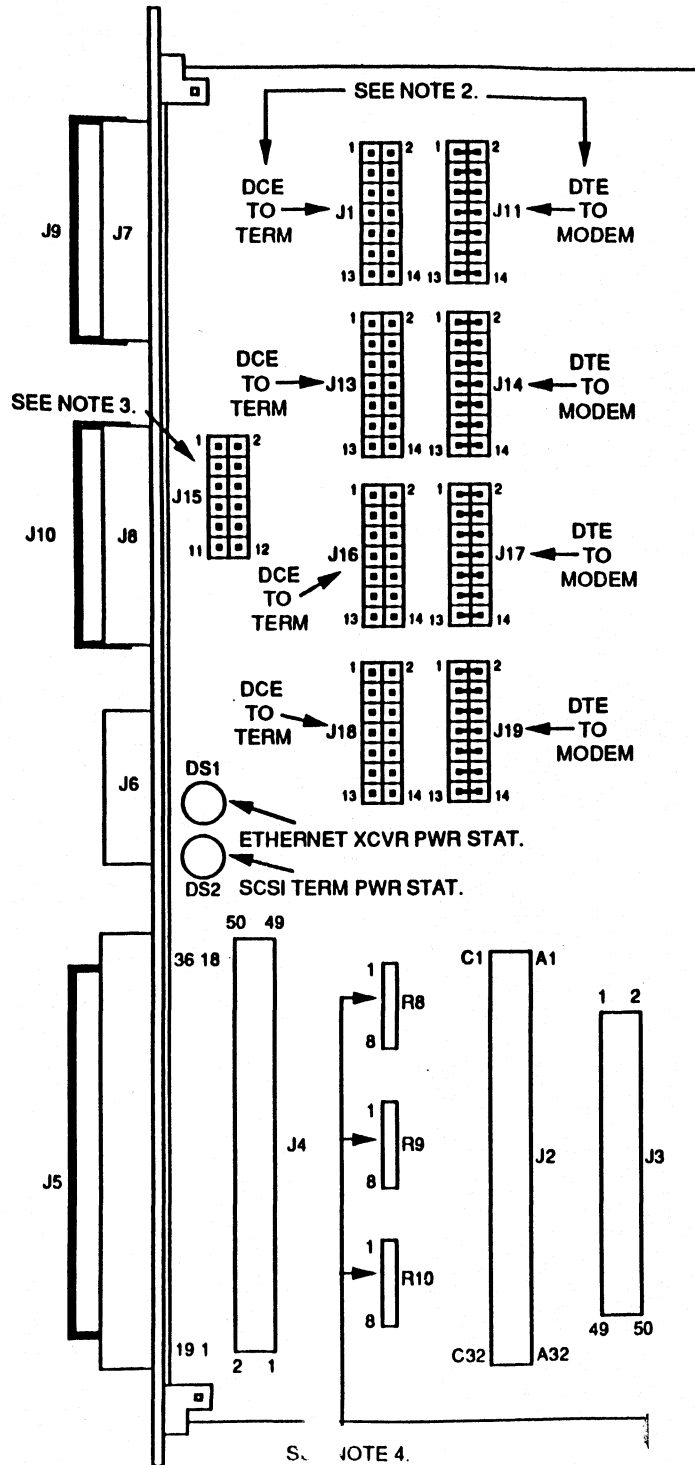
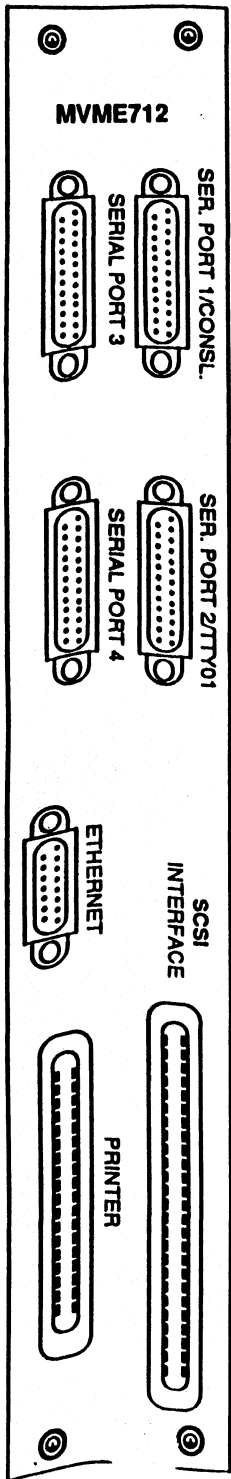


02/28/90

MVME711
[MVME321
TRANSITION]
PAGE 1 OF 2



09/15/89



NOTE 1: FRONT PANEL SCSI CONNECTOR HAS NO "J" NUMBER ASSIGNED TO IT. A 50-PIN RIBBON CABLE IS CONNECTED TO IT FROM J4 (30-W2959B01A).

NOTE 2: SERIAL PORT:

	DCE TO TERMINAL CONN.	DTE TO MODEM CONN.
1	J1	J11
2	J16	J17
3	J13	J14
4	J18	J19

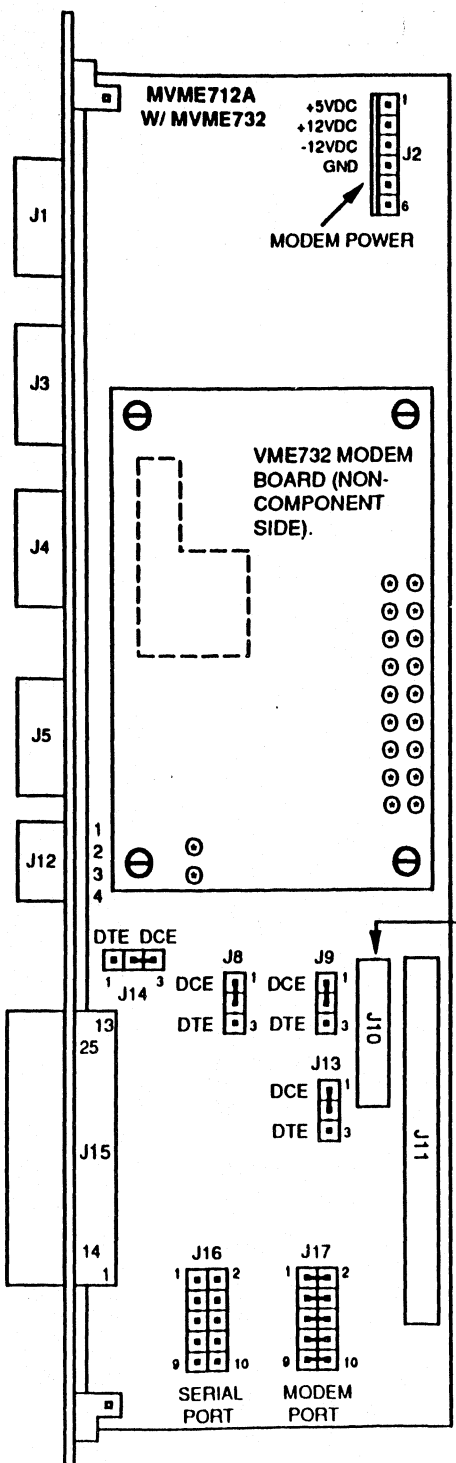
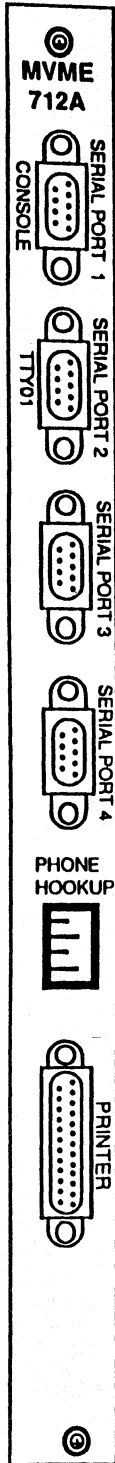
NOTE 3: J15 JUMPER USED FOR:

1-2	TRXC4 TO PIN 15
3-4	TRXC4 TO PIN 17
5-6	TRXC4 TO PIN 24
7-8	RTXC4 TO PIN 24
9-10	RTXC4 TO PIN 17
11-12	RTXC4 TO PIN 15

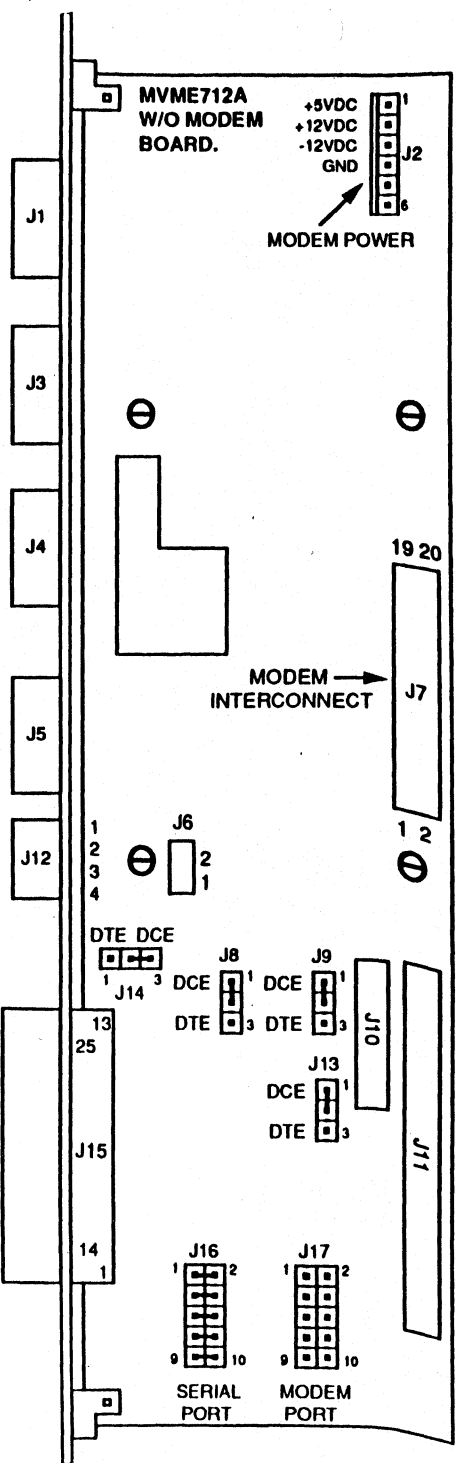
NOTE 4: TERMINATOR RESISTOR PACKS R8, R9 AND R10 ARE INSTALLED IF NO EXTERNAL SCSI DEVICE IS ATTACHED. MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 5: SEE PAGE 2 OF 5 FOR SYS3200 JUMPERING.

03/15/91



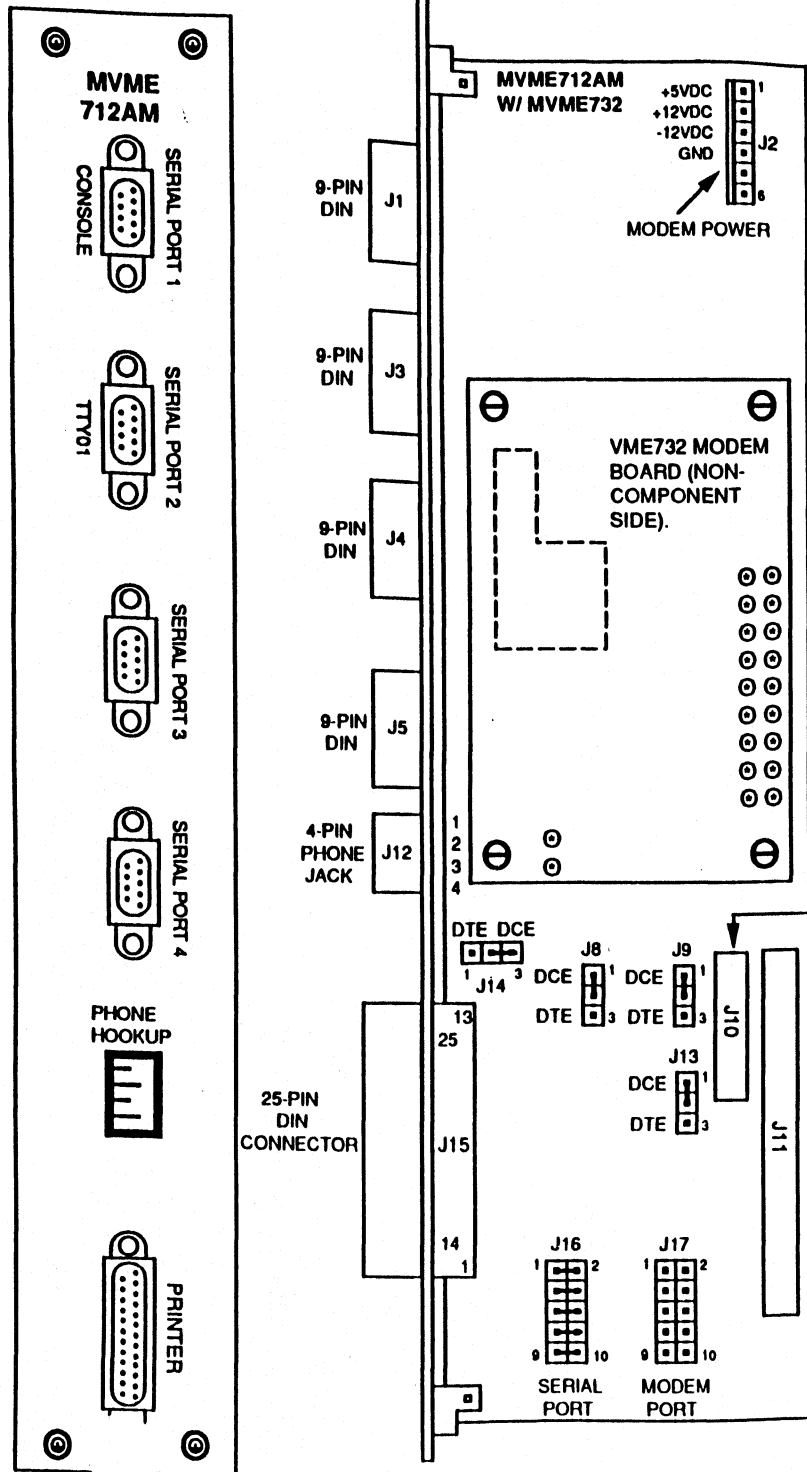
ETHERNET
INTERFACE GOES
OFF INTO A
20-PIN RIBBON
CABLE THAT TIES
INTO THE
MVME147(X) CPU
ETHERNET
INTERFACE
CONNECTOR.



NOTE 1: THIS MVME712A IS USED WITH THE
SYS3200, 3400, & SYS3604/08's.

NOTE 2 : J1 - J13 SERIAL PORT 1
J3 - J9 SERIAL PORT 2
J4 - J8 SERIAL PORT 3
J5 - J14 SERIAL PORT 4

09/12/90



NOTE 1: ACTUAL MODEM BOARD COVERS J7 & J6 AND PART OF THE CIRCUITRY BY THE SERIAL PORT CONNECTORS AND IS INVERTED. (i.e. COMPONENTS ARE FACING EACH OTHER ON MODEM BOARD AND MVME712A/AM.)

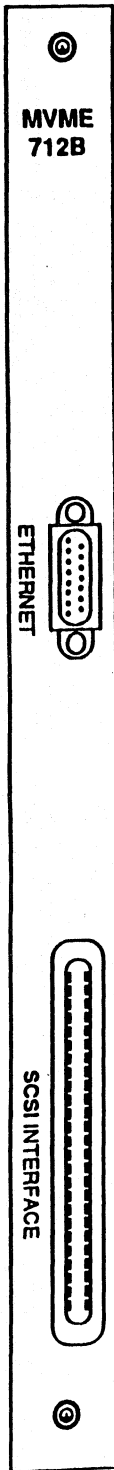
NOTE 2: J16 IF ALL JUMPERS ARE INSTALLED IS SET UP FOR A SERIAL PORT. IF J17 HAS ALL ITS JUMPERS, IT'S CONFIGURED FOR A MODEM.

NOTE 3: J8, J9, J13 AND J14 ARE DTE/DCE HEADERS. JUMPERS BETWEEN PINS 1-2 CONFIGURE ANY OF THEM FOR DCE. JUMPERS BETWEEN 2-3 CONFIGURE ANY OF THEM FOR DTE.

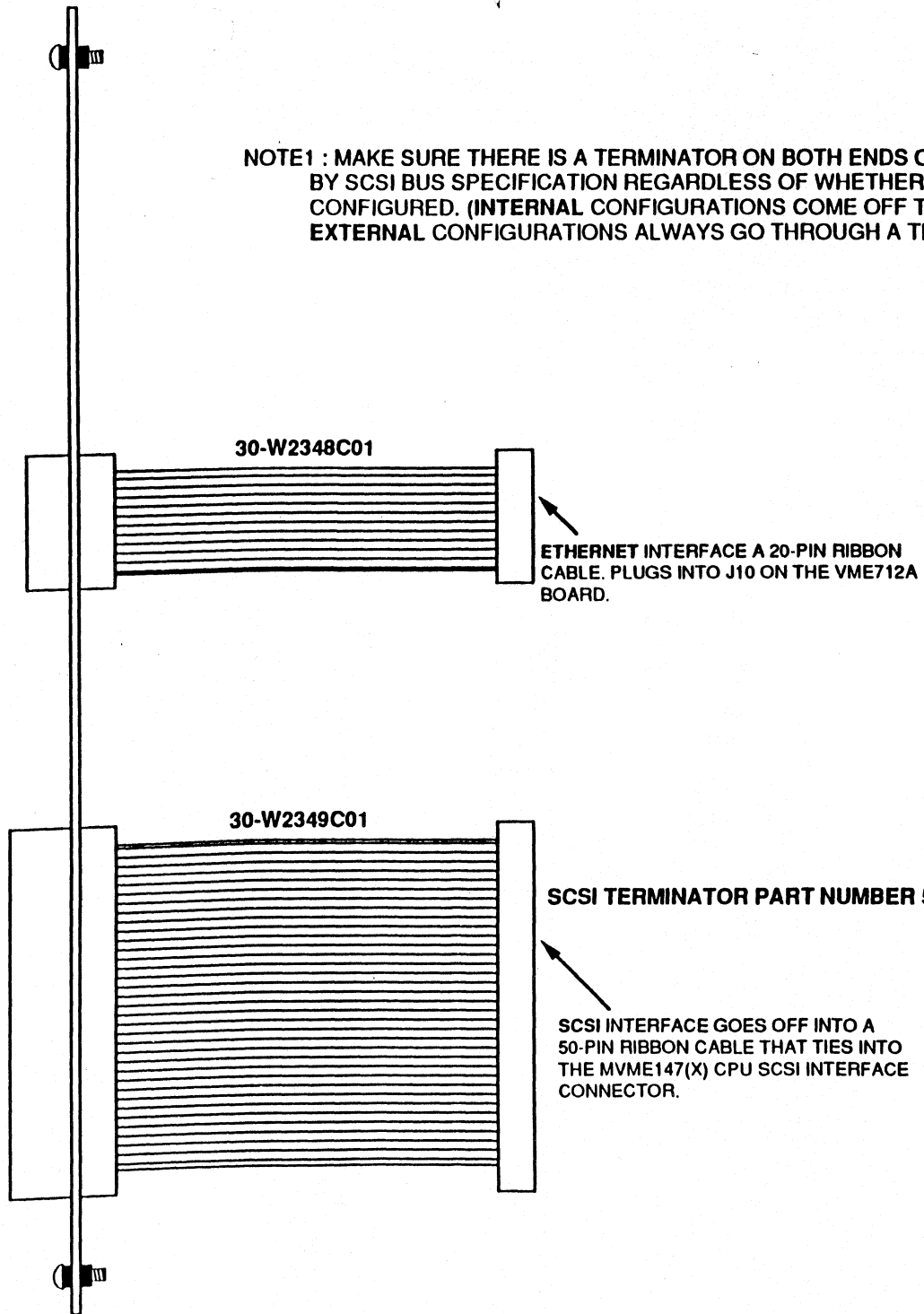
NOTE 4: FINE POINT JUMPERS ON J8, J13, J14 AND J17 DESIGNATE JUMPER CONFIGURATION FOR SYS3200 SYSTEMS.

ETHERNET INTERFACE GOES OFF INTO A 20-PIN RIBBON CABLE THAT TIES INTO THE MVME147(X) CPU ETHERNET INTERFACE CONNECTOR.

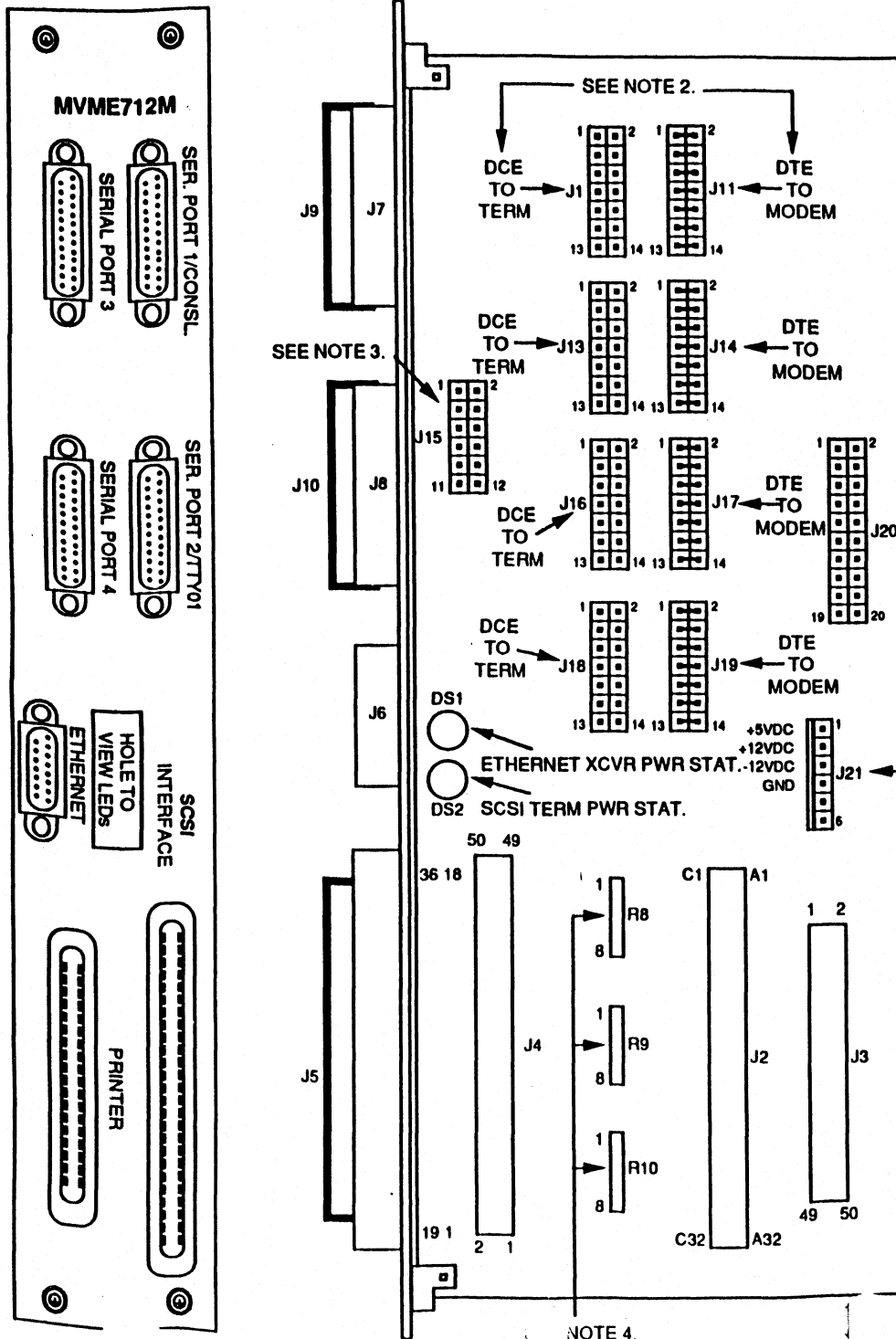
09/12/90



NOTE 1 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)



09/25/91



PART NUMBERS:

MVME712	01-W3494B01	96010992
MVME712M	01-W3538B01	96011005
MVME712A/AM	01-W3587B01	96011138
MVME712B	01-W2292C02	96011139
UDS 2243382	01-W2455C01	XXXXXXXXX PLUG-IN MODEM

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: FRONT PANEL SCSI CONNECTOR HAS NO "J" NUMBER ASSIGNED TO IT. A 50-PIN RIBBON CABLE IS CONNECTED TO IT FROM J4 (30-W2959B01A).

NOTE 2: SERIAL PORT: DCE TO TERMINAL CONN. DTE TO MODEM CONN.

1	J1	J11
2	J16	J17
3	J13	J14
4	J18	J19

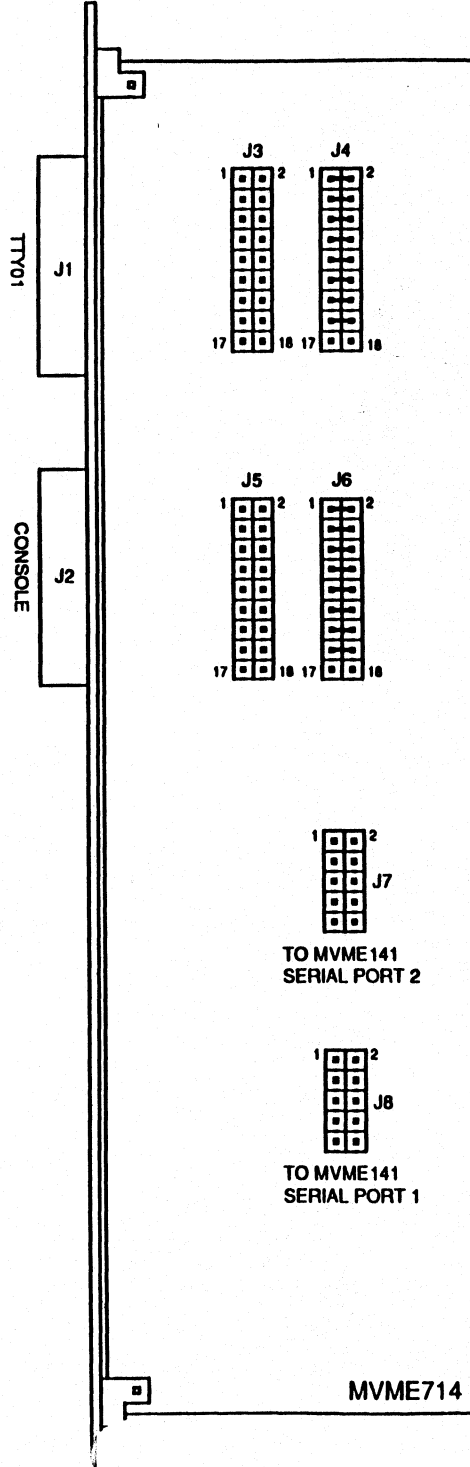
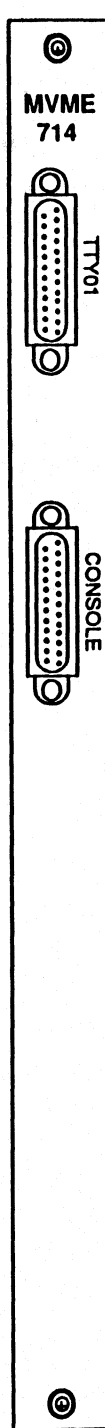
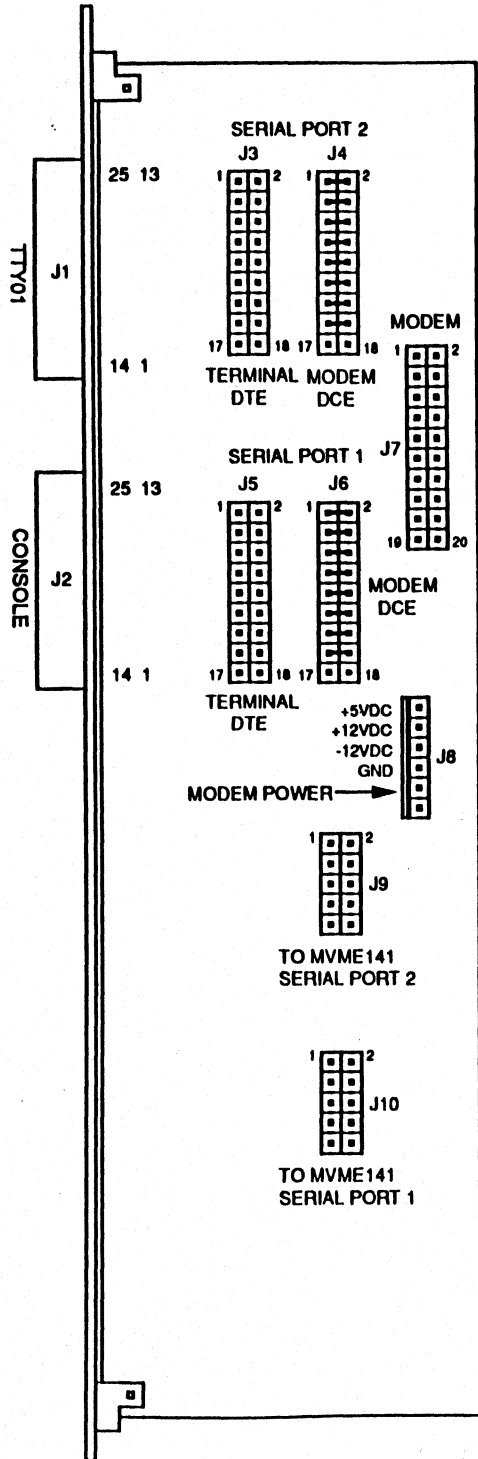
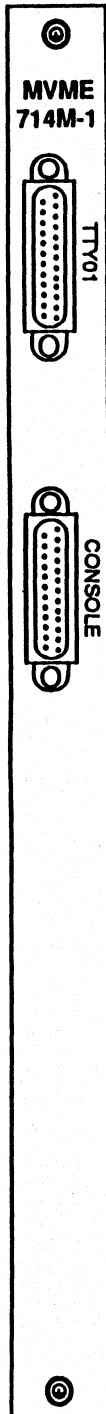
NOTE 3: J15 JUMPER USED FOR:

1-2	TRXC4 TO PIN 15
3-4	TRXC4 TO PIN 17
5-6	TRXC4 TO PIN 24
7-8	RTXC4 TO PIN 24
9-10	RTXC4 TO PIN 17
11-12	RTXC4 TO PIN 15

NOTE 4: TERMINATOR RESISTOR PACKS R8, R9 AND R10 ARE INSTALLED IF NO EXTERNAL SCSI DEVICE IS ATTACHED. MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 5: SEE PAGE 2 OF 5 FOR SYS3200 JUMPERING.

03/15/91



PART NUMBERS:

MVME714	01-W3519B01	NONE
MVME714M	01-W3551B01	96011039
MVME714M-1	01-W3540B01	96011028
UDS 2243382	01-W2455C01	XXXXXXXXX PLUG-IN MODEM

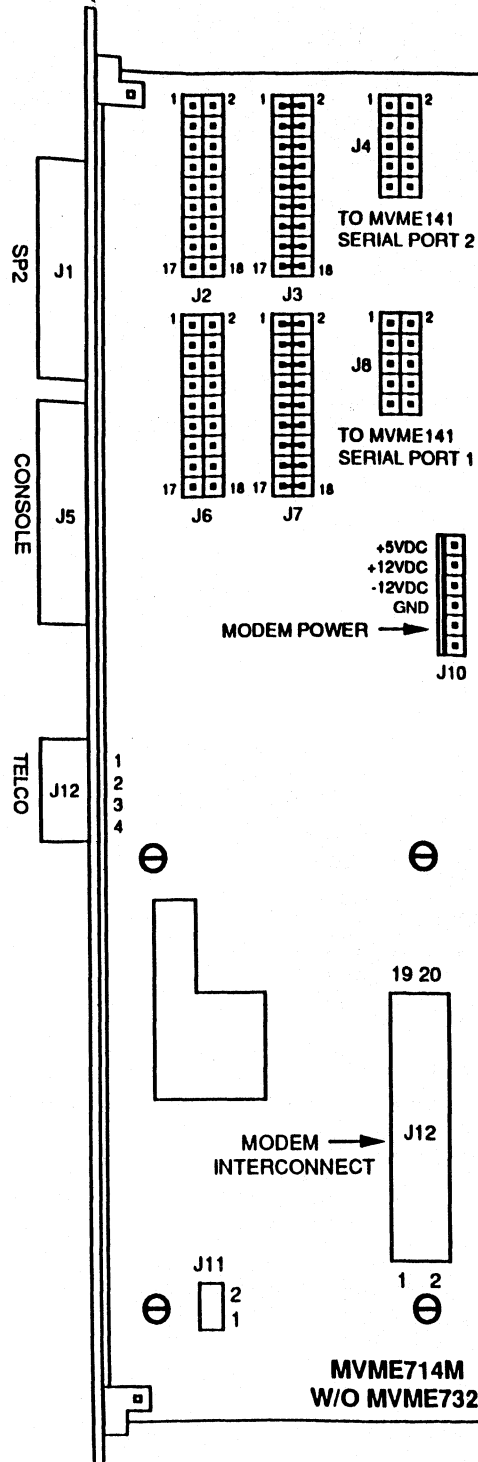
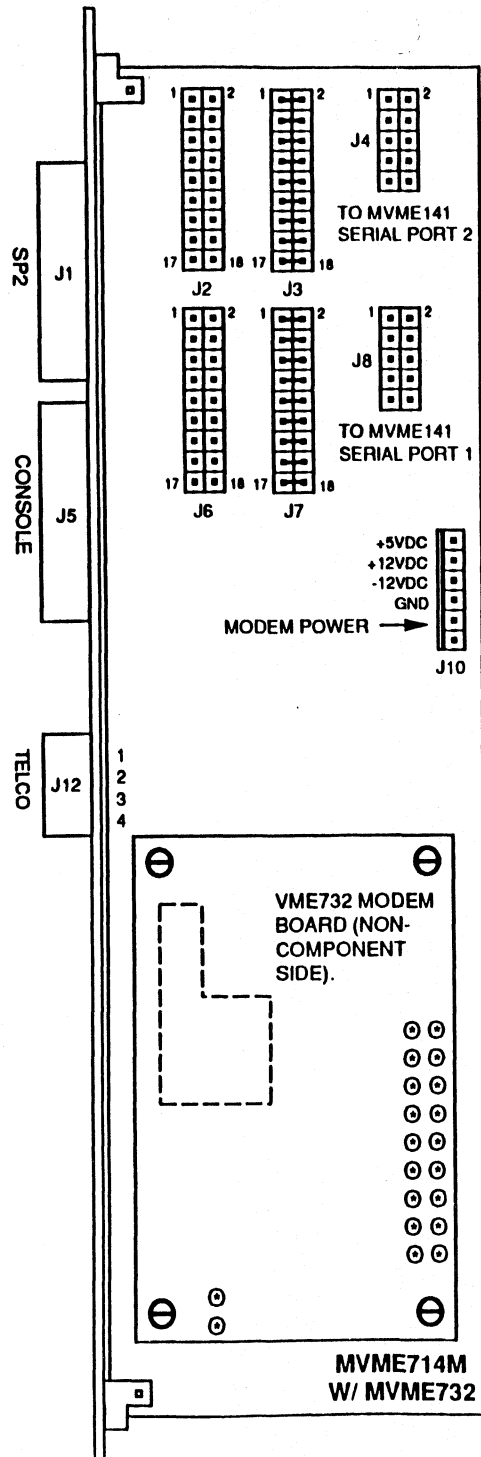
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: J3/J5 ARE FOR DCE/DTE SELECT. J4/J6 ARE FOR DTE SELECT AND ARE FACTORY SHIPPED THIS WAY.

NOTE 2: J1 AND J2 ARE 25-PIN RS-232C DIN CONNECTORS.

NOTE 3: SAME CONFIGURATIONS FOR SYS3640, 8400 & 8608's.

11/18/91



NOTE 1: ACTUAL MODEM BOARD COVERS J10 & J11 AND PART OF THE CIRCUITRY BY THE SERIAL PORT CONNECTORS AND IS INVERTED. (i.e. COMPONENTS ARE FACING EACH OTHER ON MODEM BOARD AND MVME714M.)

NOTE 2: SAME CONFIGURATION FOR SYS3400 & 8400's.

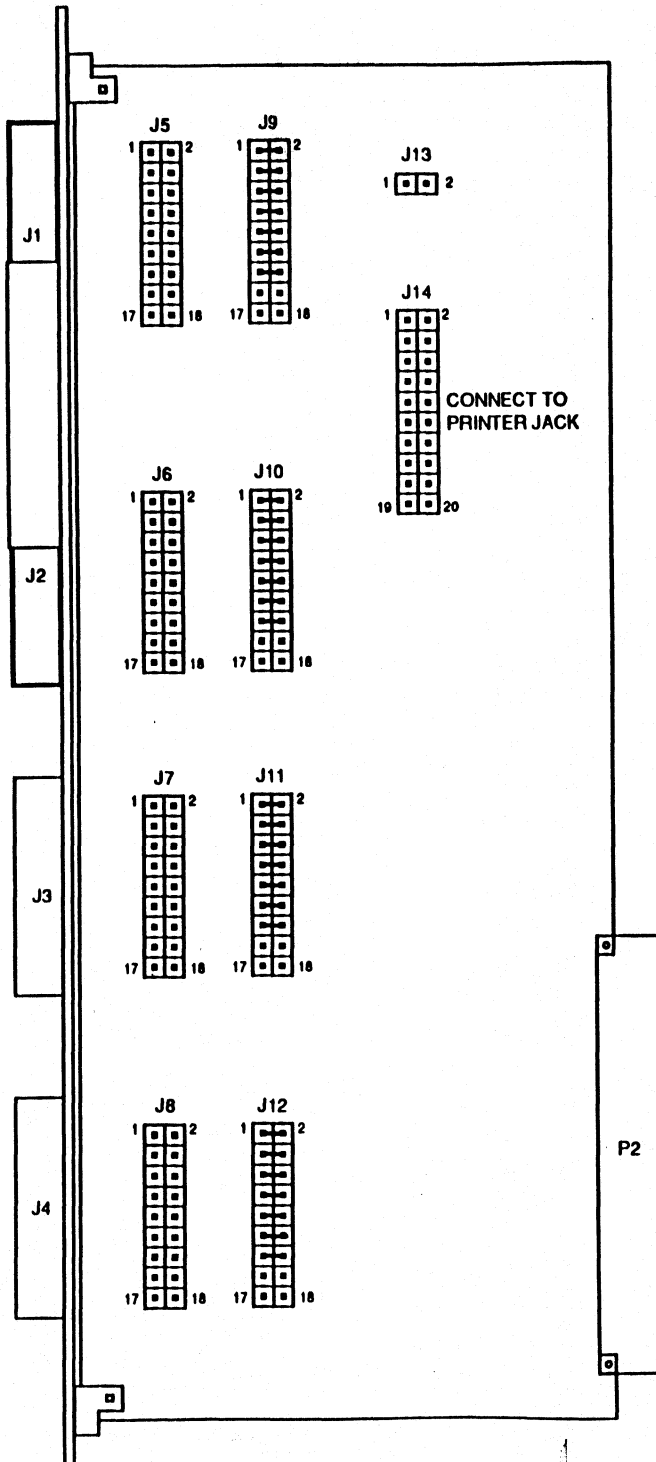
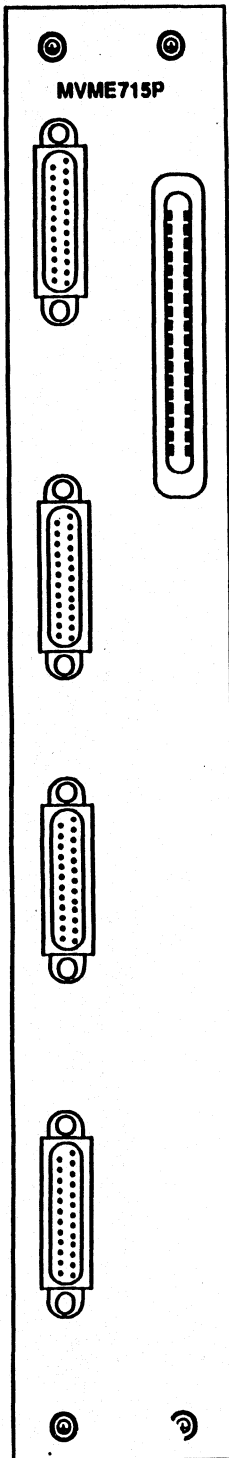
Jumper pins	Functional Description
J2/J6 1 2	Connects MPU RxD to DB25 TxD, Pin 2
3 - 4	Connects MPU TxD to DB25 RxD, Pin 3
5 - 6	Connects MPU CTS to DB25 RTS, Pin 4
7 - 8	Connects MPU RTS to DB25 CTS, Pin 5
9 - 10	Connects MPU DTR to DB25 DSR, Pin 6
11 - 12	Connects MPU DCD to DB25 DCD, Pin 8
13 - 14	Connects MPU DSR to DB25 DTR, Pin 20
15 - 16	Connects MPU Signal Gnd to DB25 SGND, Pin 7
17 - 18	Connects MMVME714/714M front panel (chassis GND), to DB25 PGND, Pin 1 (SEE NOTE)

Jumper pins	Functional Description
J3/J7 1 2	Connects MPU RxD to DB25 RxD, Pin 3
3 - 4	Connects MPU TxD to DB25 TxD, Pin 2
5 - 6	Connects MPU CTS to DB25 CTS, Pin 5
7 - 8	Connects MPU RTS to DB25 RTS, Pin 4
9 - 10	Connects MPU DTR to DB25 DTR, Pin 20
11 - 12	Connects MPU DCD to DB25 DCD, Pin 8
13 - 14	No Function
15 - 16	Connects MPU Signal Gnd to DB25 SGND, Pin 7
17 - 18	Connects MMVME714/714M front panel (chassis GND), to DB25 PGND, Pin 1 (SEE NOTE)

NOTE : Pins 17 and 18 are not installed. If a chassis ground connection to pin 1 on DB25 is required, install pins 17 and 18 and insert a shorting jumper. Making this connection normally helps reduce noise related problems.

PGND (Pin 1) is an RS-232C term that typically refers to equipment chassis ground, and may refer to the ground lead on the system 3-wire ac power connection, depending upon ac wiring. The PGND (pin 1) signal is NOT connected to the SGND (pin 7) signal.

J2/J6 SP2 Console (DTE- to terminal) select headers.
J3/J7 SP2 Console (DCE- to modem) select headers.



PART NUMBERS:

MVME715P 01-W3497B02 96010910

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: J5 THRU J12 ARE DCE/DTE SELECT.
J5 THRU J8 ARE OPEN AND J9 THRU J12 ARE ALL JUMPED PINS 1-2, 3-4, 5-6, 7-8, 9-10, 11-12, AND 13-14.

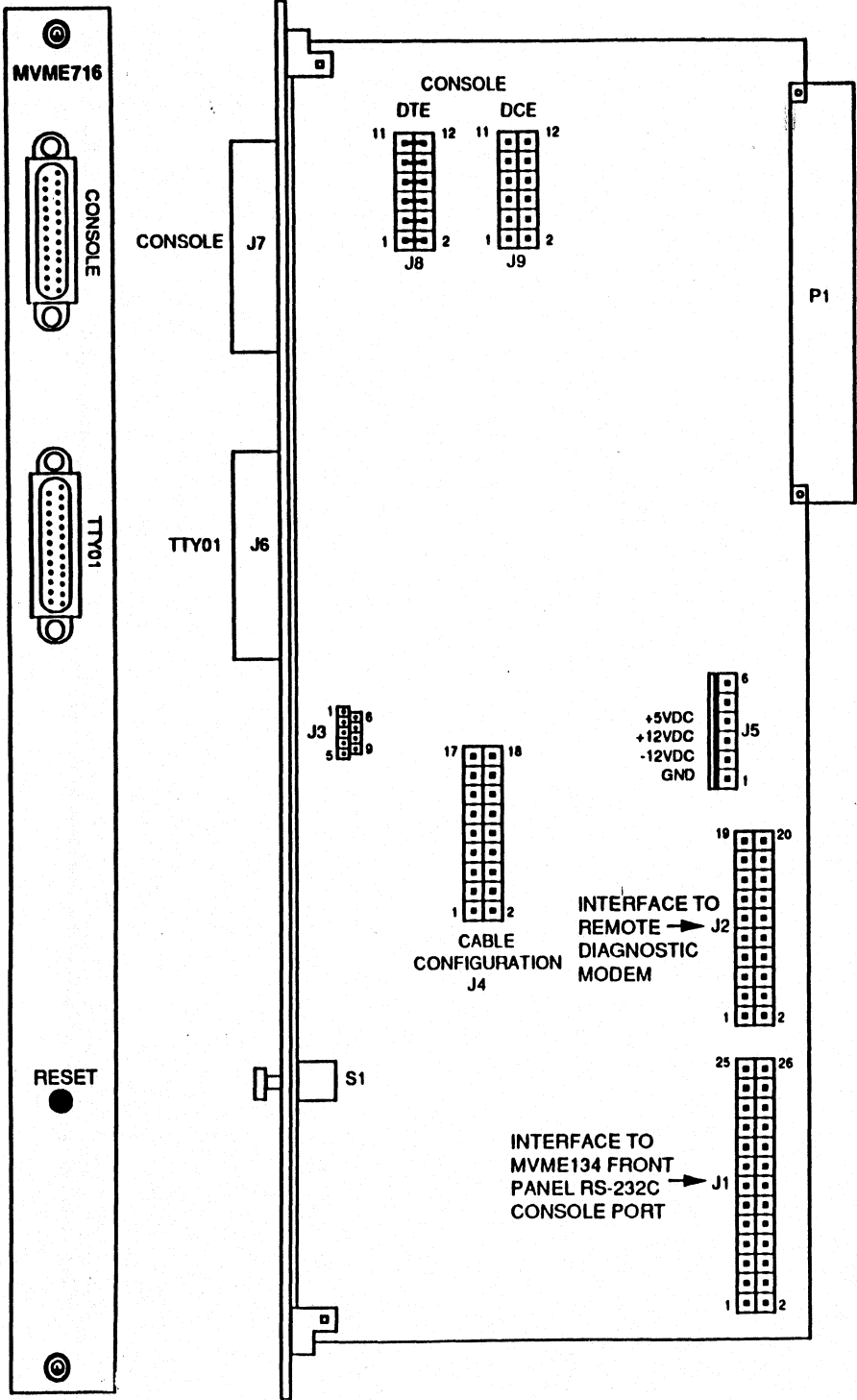
NOTE 2: J13 HAS NO JUMPER INSTALLED.

NOTE 3: PRINTER PORT HAS NO J NUMBER ASSIGNED TO IT.

NOTE 4: CONFIGURATION IS THE SAME FOR SYS3200, 3400, 8400 & 8608 SYSTEMS.

02/28/90

MVME715P
[MVME335
TRANSITION]
P/ 1 OF 1

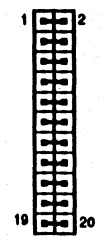


PART NUMBERS:

MVME716 01-W3524B01 96010935

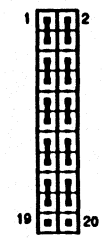
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

**J21 ON MVME134
SET TO TERMINAL**



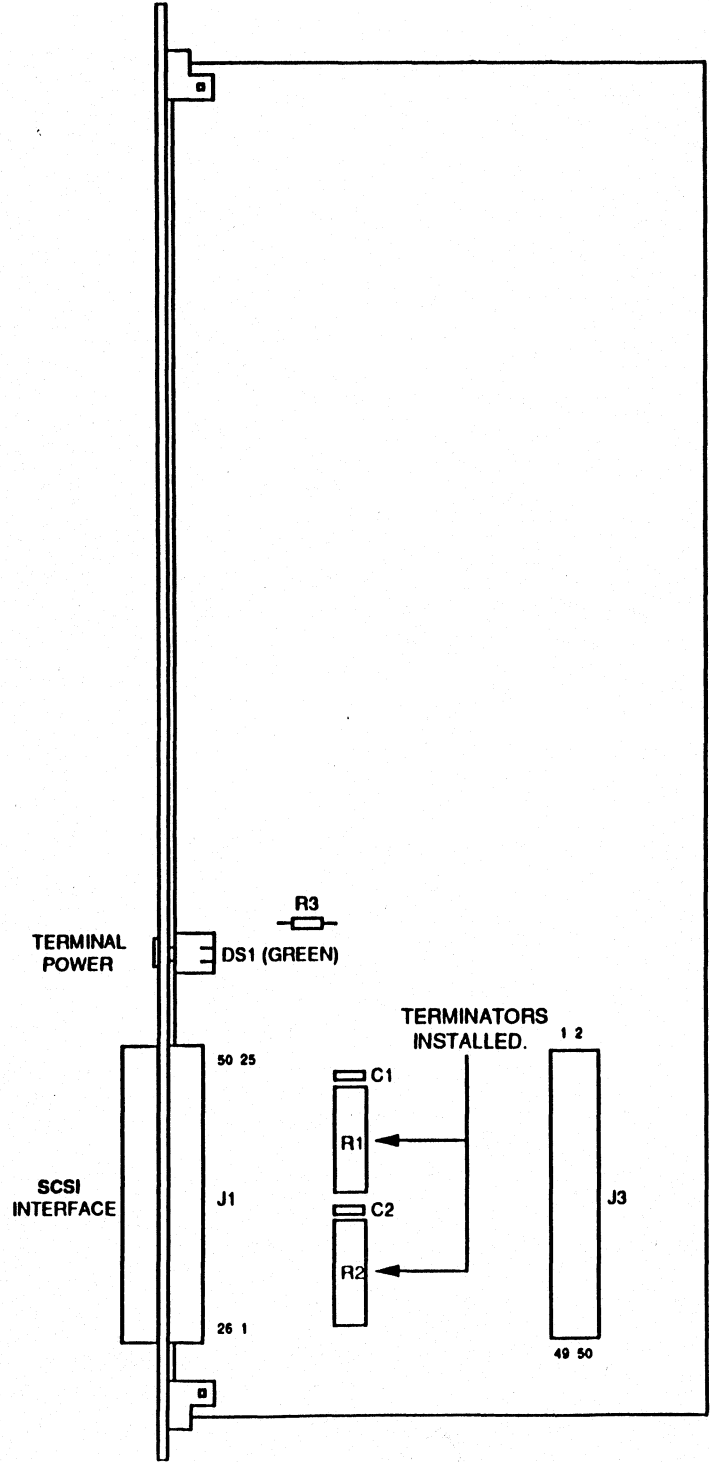
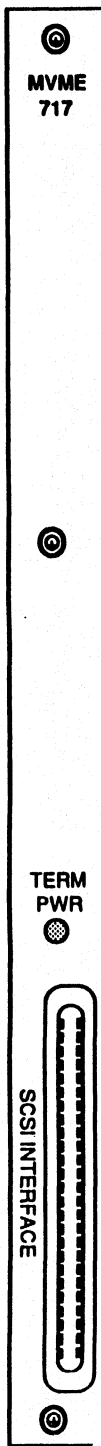
IF USING J9 ON
THE MVME716

**J21 ON MVME134
SET TO MODEM**



IF USING J8 ON
THE MVME716

02/28/90



PART NUMBERS:

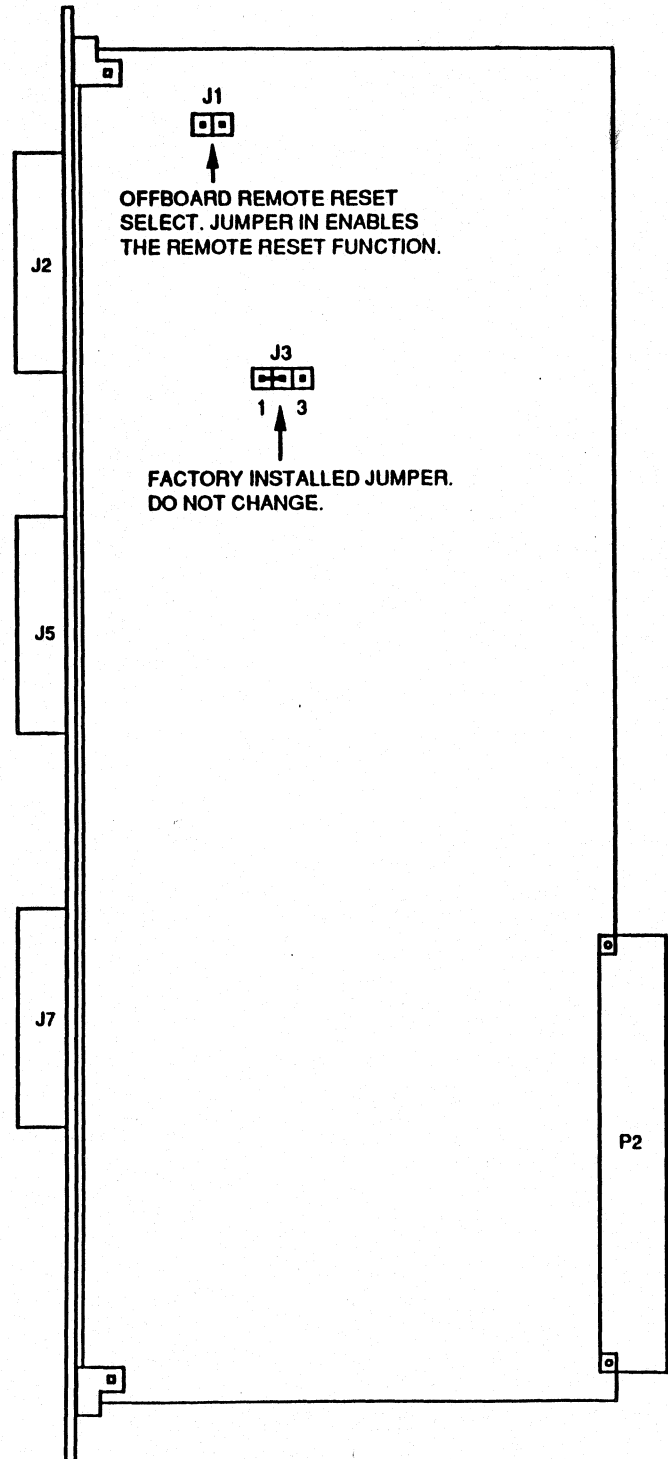
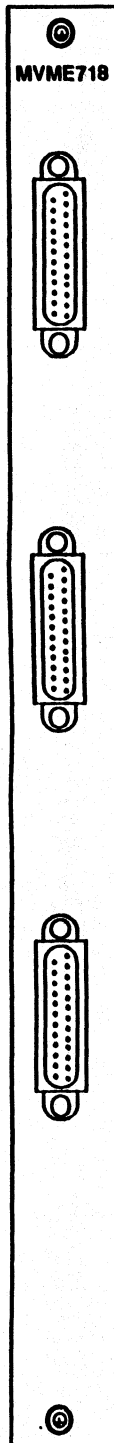
MVME717 01-W3543B01 96011027

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

NOTE 1 : SAME CONFIGURATION FOR SYS3640, 8400
& 8608'S.

NOTE 2 : JUMPERS SHOULD BE INSTALLED ON BEGINNING AND END OF CHAIN.
MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS
SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S
INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS
COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS
ALWAYS GO THROUGH A TRANSITION BOARD.)

03/15/91

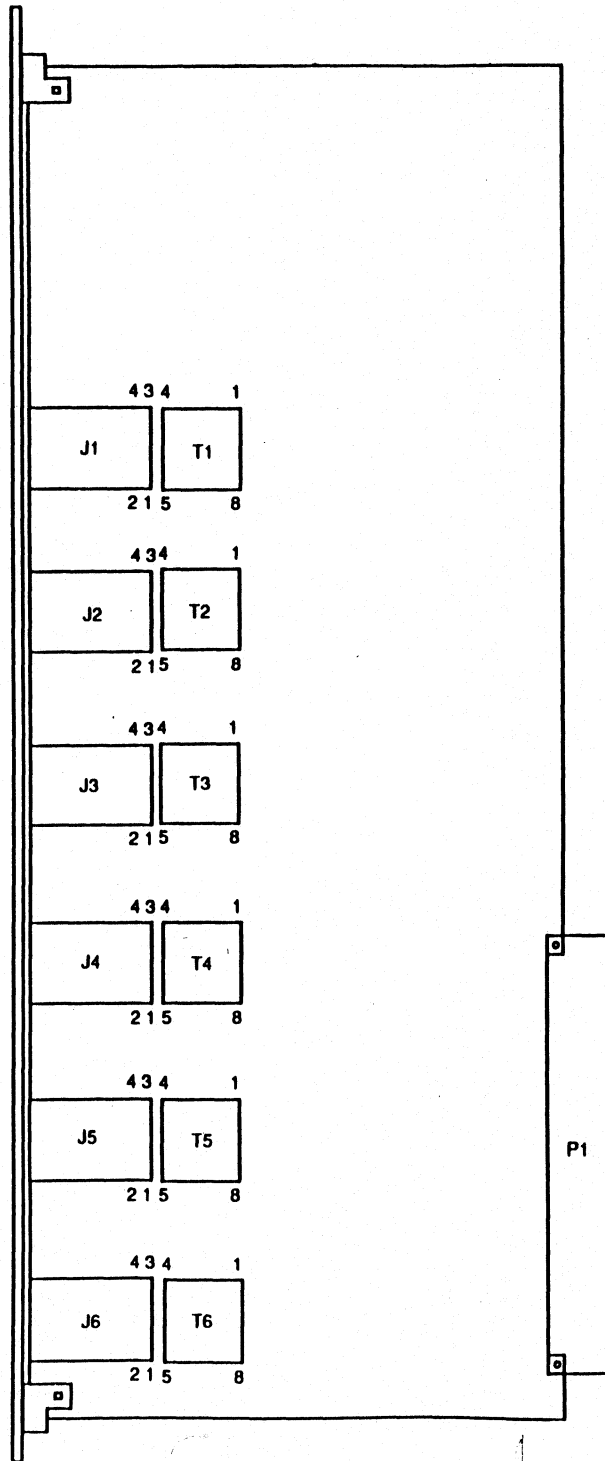
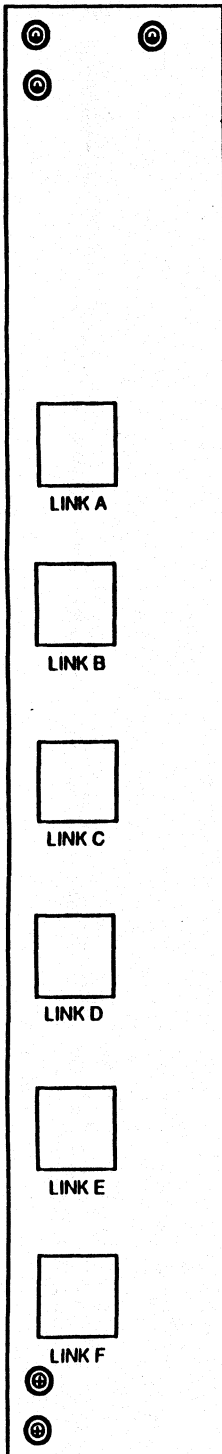


PART NUMBERS:

MVME718 01-W3600B01 TBD

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

02/28/90



PART NUMBERS:

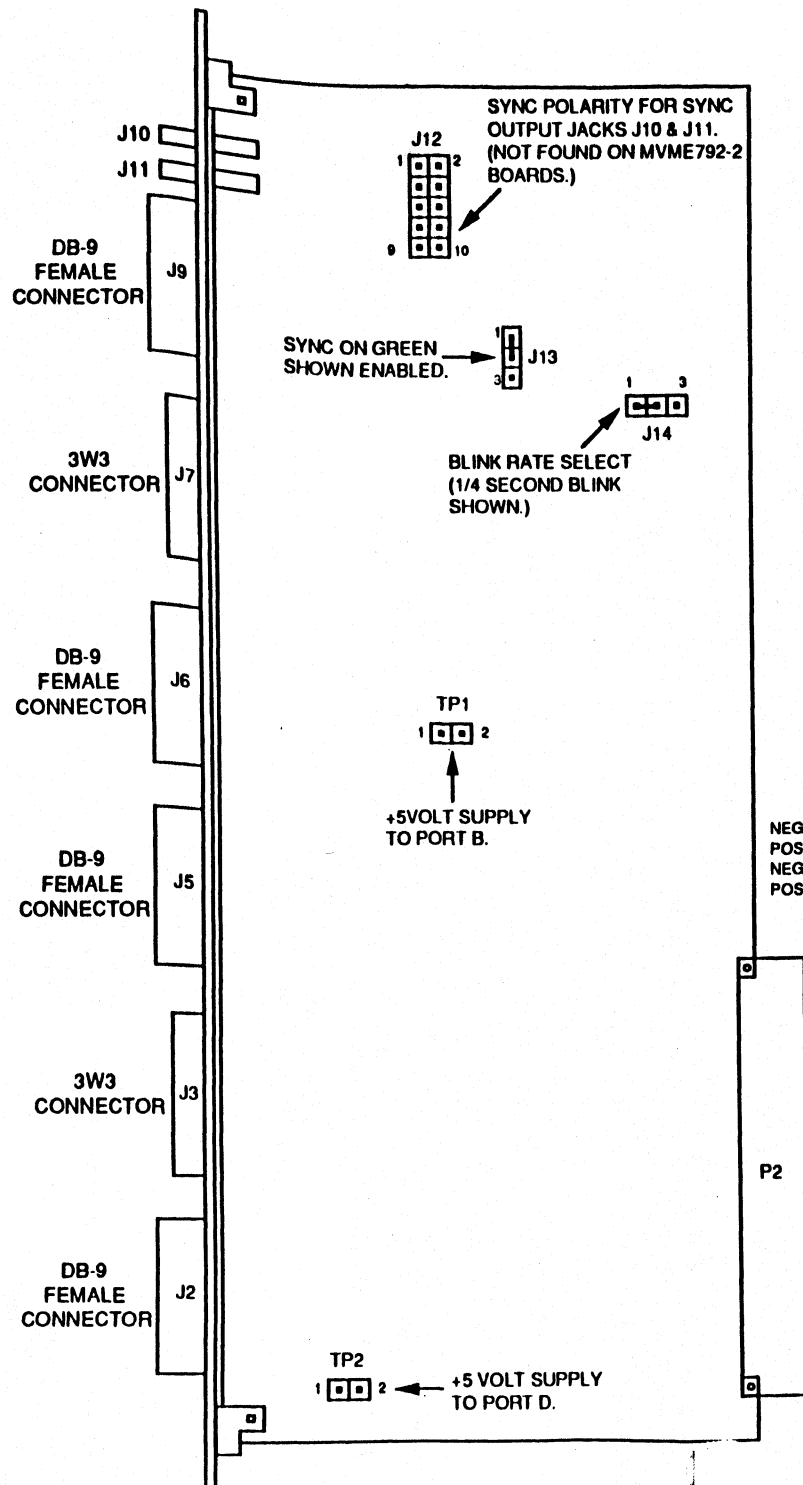
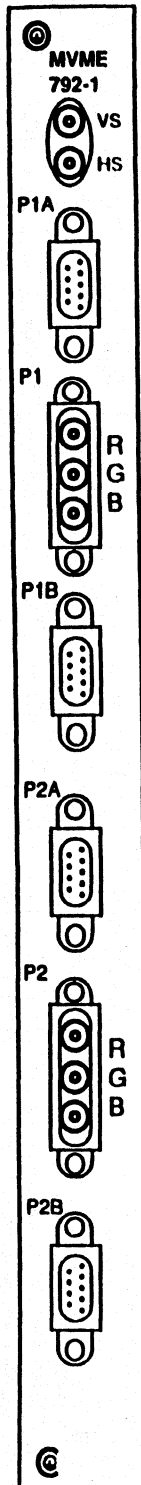
MVME751 01-W3484B01 96010943

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

NOTE 1: THE CONFIGURATION FOR SYS1147, 3200, 3400
& 8400 ARE THE SAME.

02/28/90

MVME751
[MVME336
TRANSITION]
PA 1 OF 1



PART NUMBERS:

MVME792-1 01-W3523B01 96011029

MVME792-2 01-W3523B02 96011023

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

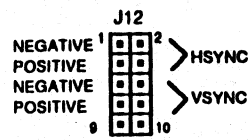
SYNC POLARITY FOR SYNC OUTPUT JACKS J10 & J11. (NOT FOUND ON MVME792-2 BOARDS.)

SYNC ON GREEN SHOWN ENABLED.

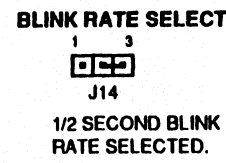
BLINK RATE SELECT (1/4 SECOND BLINK SHOWN.)

TP1
+5VOLT SUPPLY TO PORT B.

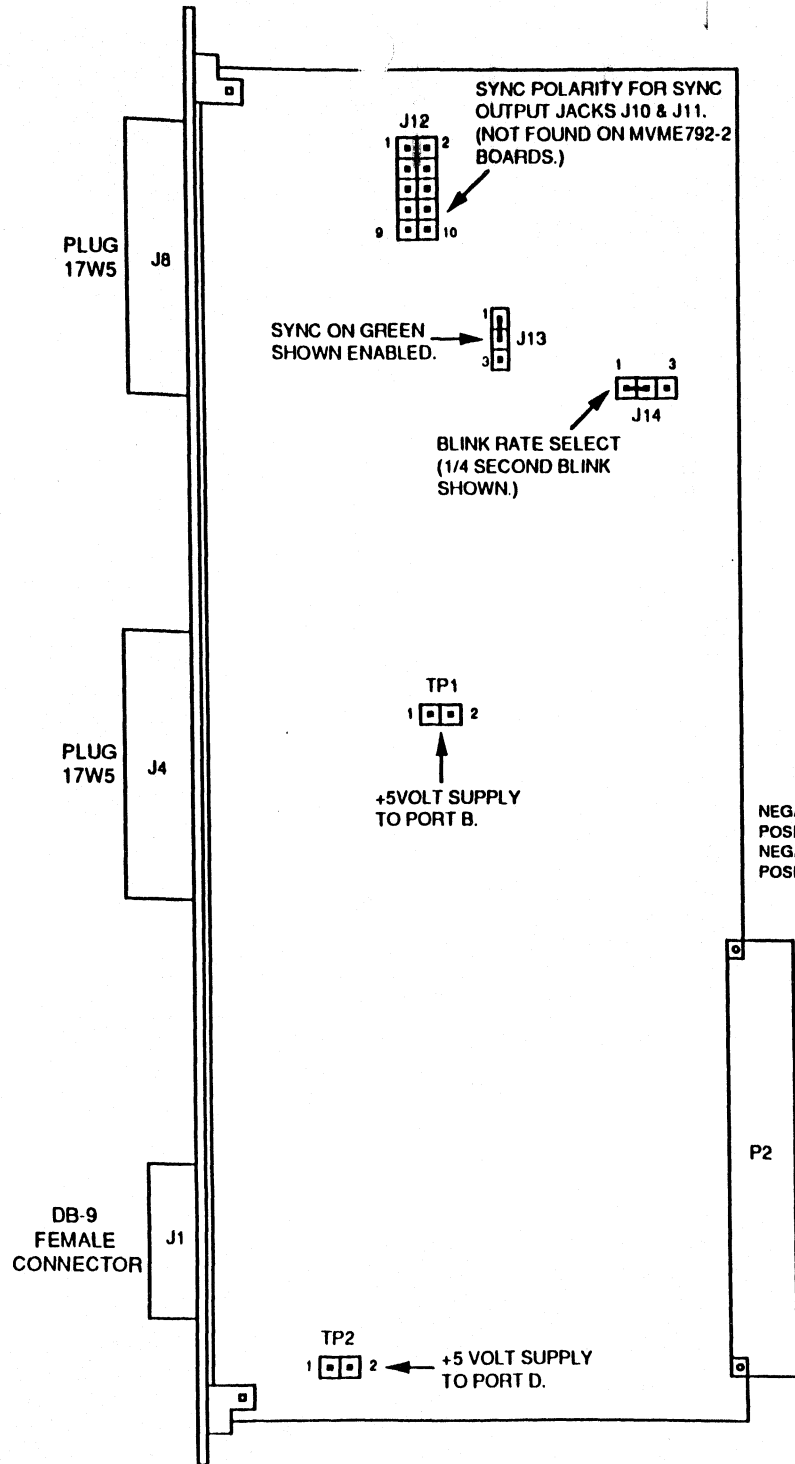
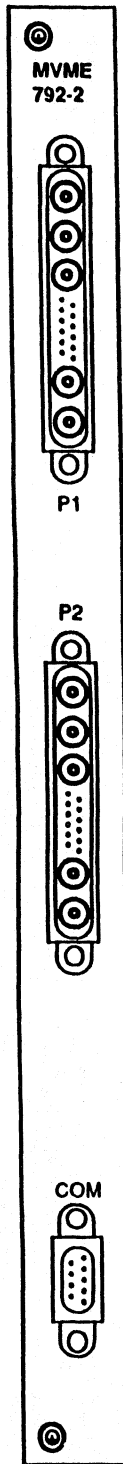
TP2
+5 VOLT SUPPLY TO PORT D.



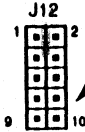
CASE #	DESCRIPTION
A (J11 OR 17W5 PIN #18)	J12 (1-2) NEGATIVE GOING CSYNC
B (J11 OR 17W5 PIN #18)	J12 (3-4) POSITIVE GOING CSYNC
C (J10 OR 17W5 PIN #17)	J12 (5-6) NEGATIVE GOING VSYNC
D (J10 OR 17W5 PIN #17)	J12 (7-8) POSITIVE GOING VSYNC



09/12/90



SYNC POLARITY FOR SYNC OUTPUT JACKS J10 & J11. (NOT FOUND ON MVME792-2 BOARDS.)



SYNC ON GREEN SHOWN ENABLED.



BLINK RATE SELECT (1/4 SECOND BLINK SHOWN.)



TP1
+5VOLT SUPPLY TO PORT B.



TP2
+5 VOLT SUPPLY TO PORT D.



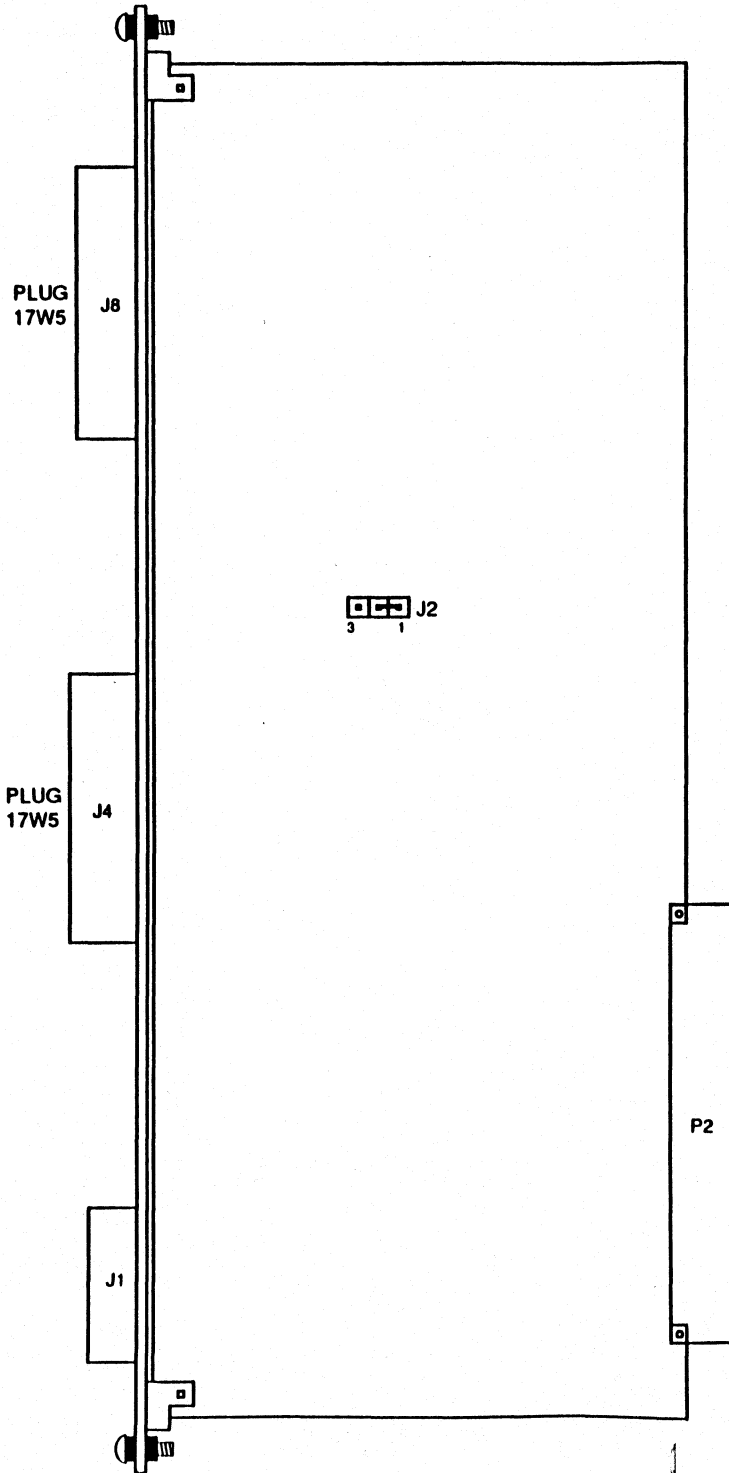
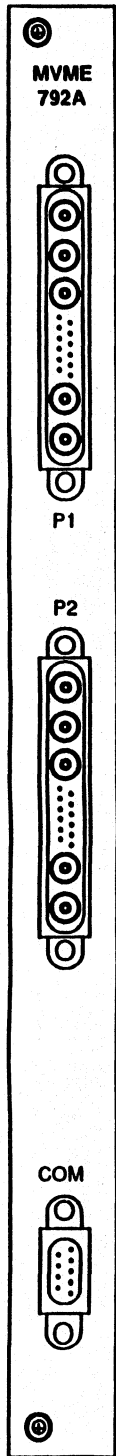
NOTE 1 : SAME CONFIGURATION FOR SYS1147, 3200, 3304/08, 3400, 3640, 8400 & 8608's.

	CASE #	DESCRIPTION
Negative	A (J11 OR 17W5 PIN #18)	J12 (1-2) NEGATIVE GOING CSYNC
Positive	B (J11 OR 17W5 PIN #18)	J12 (3-4) POSITIVE GOING CSYNC
Negative	C (J10 OR 17W5 PIN #17)	J12 (5-6) NEGATIVE GOING VSYNC
Positive	D (J10 OR 17W5 PIN #17)	J12 (7-8) POSITIVE GOING VSYNC

BLINK RATE SELECT
1/2 SECOND BLINK RATE SELECTED.



09/12/90

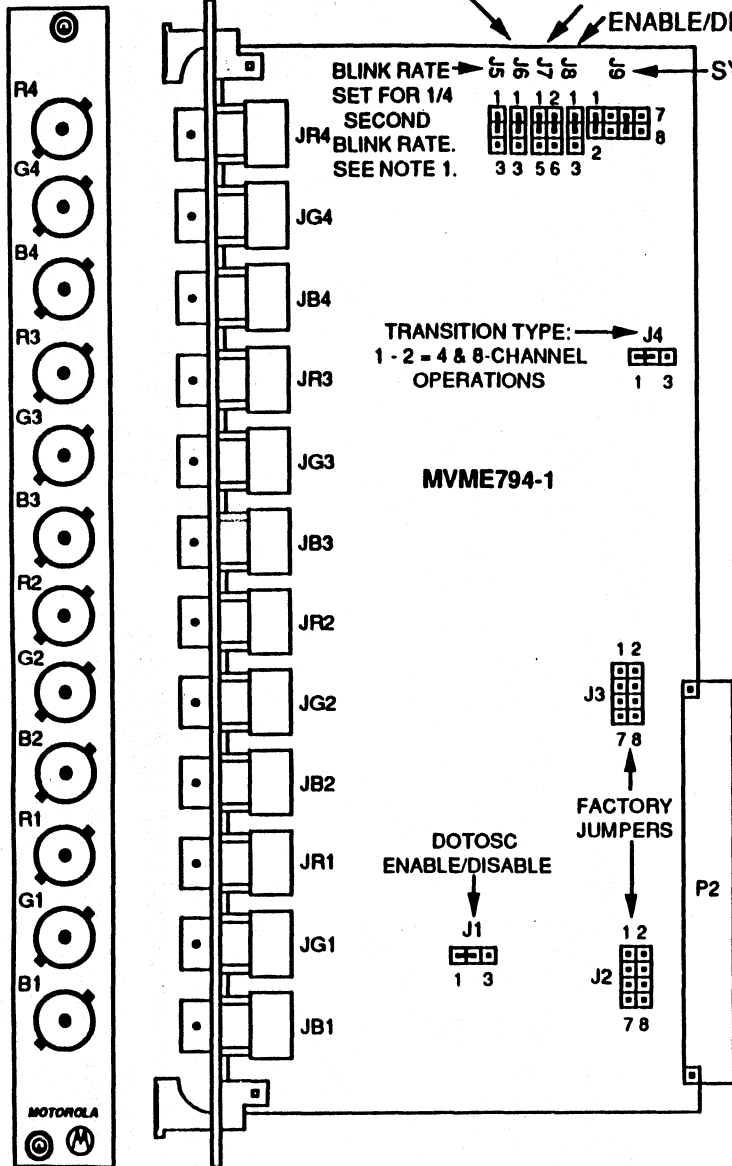


09/12/90

TR1 SELECT. SET FOR 4 - CHANNEL OPERATION. SEE NOTE 2.

MASTER/SLAVE SELECT SET FOR MASTER BOTH MVME794 & MVME798.

ENABLE/DISABLE SYNC ON GREEN. SHOWN ENABLES. SEE NOTE 4.



SYNC SIGNAL SELECT SET FOR NEG. GOING CSYNC AND NEG. GOING VSYNC.

PART NUMBERS:

MVME794-1 01-W3499B01 XXXXXXXX

MVME794-2 01-W3499B02 XXXXXXXX

MVME798-1 01-W3499B03 XXXXXXXX

MVME798-2 01-W3499B04 XXXXXXXX

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

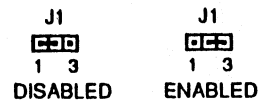
NOTE 1: J5 2 - 3 IS SETTING FOR 1/2 SECOND BLINK RATE.

NOTE 2: J6 2 - 3 IS SETTING FOR TR1 8 - CHANNEL OPERATION. ON THE MVME798-1/-2, BOTH MASTER AND SLAVE ARE JUMPED BETWEEN PINS 2 - 3.

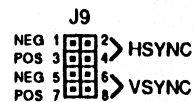
NOTE 3: JUMPERS BETWEEN J7 3 - 5 AND 4 - 6 ARE FOR SLAVE OPERATION FOR BOTH VME794 AND VME798 SERIES.

NOTE 4: J8 2 - 3 IS SETTING FOR DISABLING SYNC ON GREEN.

4/8-CHANNEL DOTOSC ENABLE/DISABLE



SYNC SIGNAL SELECT



CASE #	DESCRIPTION
A	J9 1 - 2 = NEGATIVE GOING CSYNC
B	J9 3 - 4 = POSITIVE GOING CSYNC
C	J9 5 - 6 = NEGATIVE GOING VSYNC
D	J9 7 - 8 = POSITIVE GOING VSYNC

MVME794-1/-2 & MVME798-1/-2
 COLOR GRAPHICS CONTROLLER
 PAGE 4 OF 4

09/13/89

TR1 SELECT. SET FOR 4 - CHANNEL OPERATION. SEE NOTE 2.

MASTER/SLAVE SELECT SET FOR MASTER BOTH THE MVME794 & VME798.

ENABLE/DISABLE SYNC ON GREEN. SHOWN ENABLES. SEE NOTE 4.

SYNC SIGNAL SELECT SET FOR NEG. GOING CSYNC AND NEG. GOING VSYNC.

BLINK RATE → 5 6 5 6 5
 SET FOR 1/4 SECOND
 BLINK RATE. 1 1 1 2 1 1 7
 3 3 5 6 3 2 8
 SEE NOTE 1.

TRANSITION TYPE: → J4
 1 - 2 = 4 & 8-CHANNEL OPERATIONS
 1 3

MVME794-2

J3
 1 2
 7 8

FACTORY JUMPERS

DOTOSC ENABLE/DISABLE

J1
 1 3

J2
 1 2
 7 8

P2

4/8-CHANNEL DOTOSC ENABLE/DISABLE

J1 J1
 1 3 1 3
 DISABLED ENABLED

SYNC SIGNAL SELECT

		CASE #	DESCRIPTION
NEG 1	2	A	J9 1 - 2 = NEGATIVE GOING CSYNC
POS 3	4	B	J9 3 - 4 = POSITIVE GOING CSYNC
NEG 5	6	C	J9 5 - 6 = NEGATIVE GOING VSYNC
POS 7	8	D	J9 7 - 8 = POSITIVE GOING VSYNC

NOTE 1: J5 2 - 3 IS SETTING FOR 1/2 SECOND BLINK RATE.

NOTE 2: J6 2 - 3 IS SETTING FOR TR1 8 - CHANNEL OPERATION. ON THE MVME798-1/-2, BOTH MASTER AND SLAVE ARE JUMPED BETWEEN PINS 2 - 3.

NOTE 3: JUMPERS BETWEEN J7 3 - 5 AND 4 - 6 ARE FOR SLAVE OPERATION FOR BOTH VME794 AND VME798 SERIES.

NOTE 4: J8 2 - 3 IS SETTING FOR DISABLING SYNC ON GREEN.

09/13/89

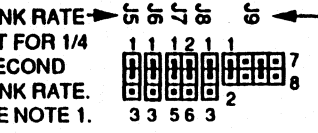
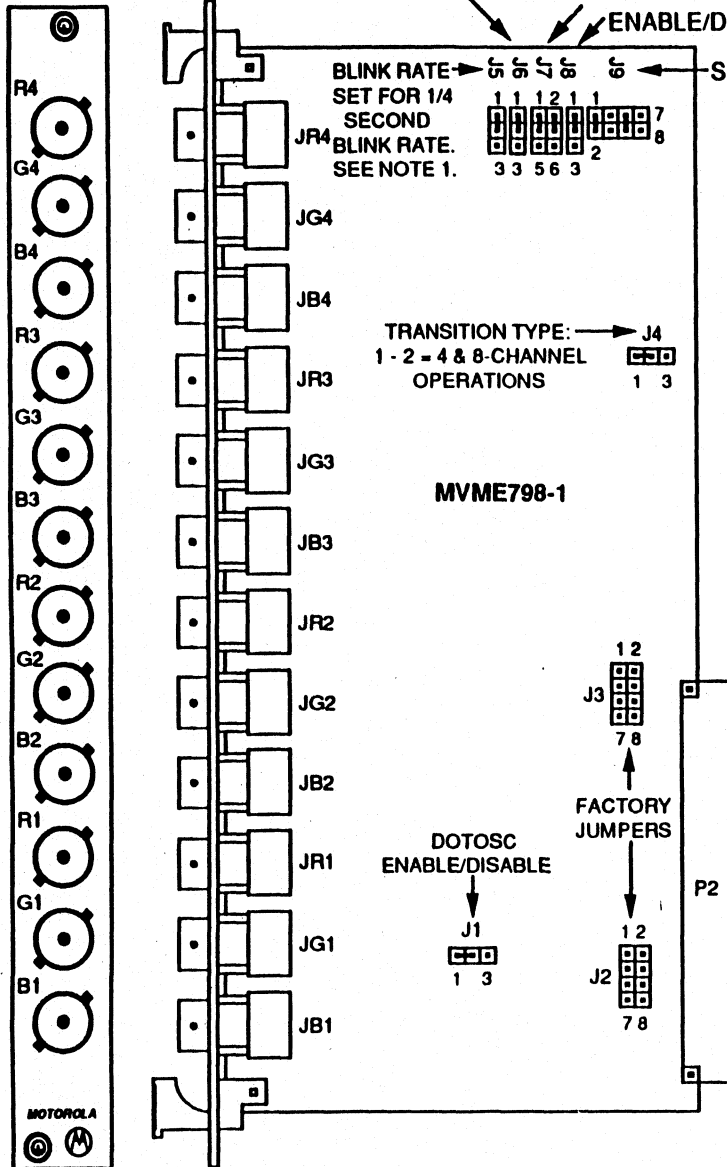
MVME794-1/-2 &
 MVME798-1/-2
 COLOR GRAPHICS
 CONTROLLER
 PAGE 2 OF 4

TR1 SELECT. SET FOR 4 - CHANNEL OPERATION. SEE NOTE 2.

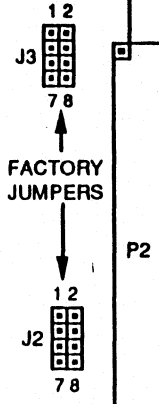
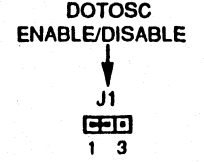
MASTER/SLAVE SELECT SET FOR MASTER BOTHE MVME794 & MVME798.

ENABLE/DISABLE SYNC ON GREEN. SHOWN ENABLES. SEE NOTE 4.

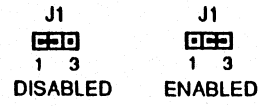
SYNC SIGNAL SELECT SET FOR NEG. GOING CSYNC AND NEG. GOING VSYNC.



MVME798-1



4/8-CHANNEL DOTOSC ENABLE/DISABLE



SYNC SIGNAL SELECT

J9	CASE #	DESCRIPTION
NEG 1	A	J9 1 - 2 = NEGATIVE GOING CSYNC
POS 3	B	J9 3 - 4 = POSITIVE GOING CSYNC
NEG 5	C	J9 5 - 6 = NEGATIVE GOING VSYNC
POS 7	D	J9 7 - 8 = POSITIVE GOING VSYNC

NOTE 1: J5 2 - 3 IS SETTING FOR 1/2 SECOND BLINK RATE.

NOTE 2: J6 2 - 3 IS SETTING FOR TR1 8 - CHANNEL OPERATION. ON THE MVME798-1/-2, BOTH MASTER AND SLAVE ARE JUMPED BETWEEN PINS 2 - 3.

NOTE 3: JUMPERS BETWEEN J7 3 - 5 AND 4 - 6 ARE FOR SLAVE OPERATION FOR BOTH VME794 AND VME798 SERIES.

NOTE 4: J8 2 - 3 IS SETTING FOR DISABLING SYNC ON GREEN.

09/13/89

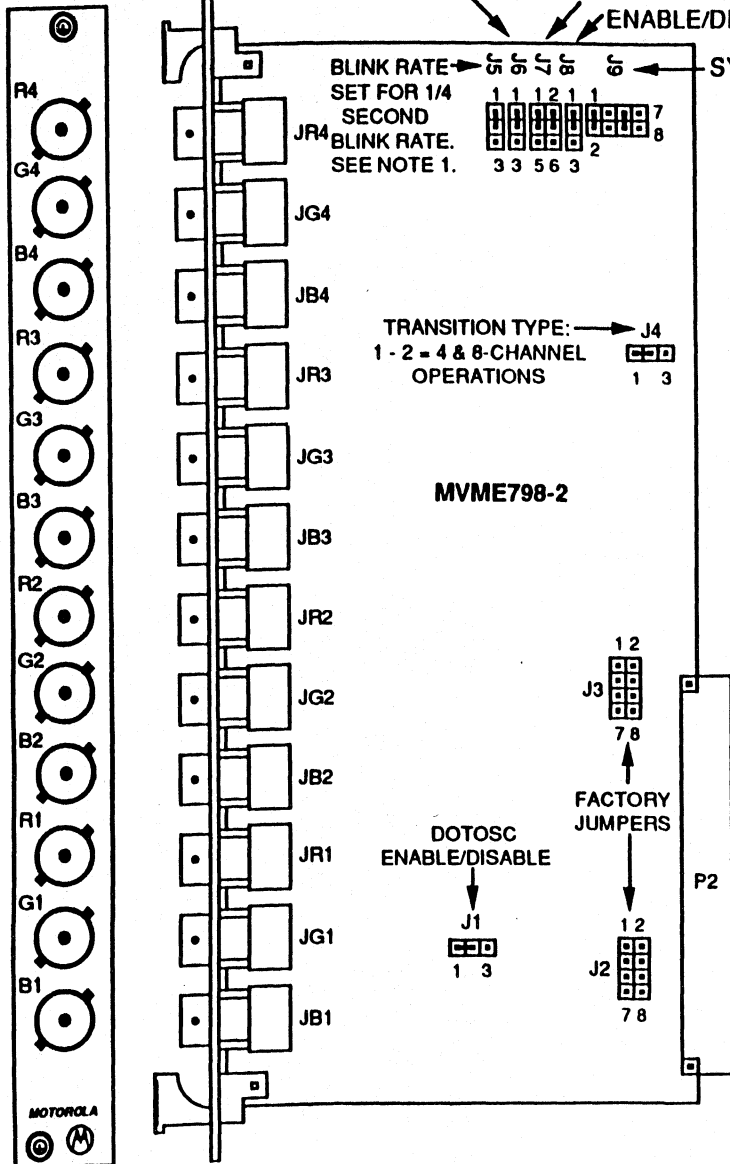
**MVME794-1/-2 &
MVME798-1/-2
COLOR GRAPHICS
CONTROLLER
PAGE 1 OF 4**

TR1 SELECT. SET FOR 4 - CHANNEL OPERATION. SEE NOTE 2.

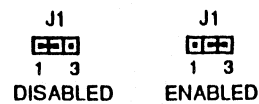
MASTER HAVE SELECT SET FOR MASTER BOTHE MVME7: MVME798.

ENABLE/DISABLE SYNC ON GREEN. SHOWN ENABLES. SEE NOTE 4.

SYNC SIGNAL SELECT SET FOR NEG. GOING CSYNC AND NEG. GOING VSYNC.



4/8-CHANNEL DOTOSC ENABLE/DISABLE



SYNC SIGNAL SELECT

		CASE #	DESCRIPTION
NEG 1	J9	A	J9 1 - 2 = NEGATIVE GOING CSYNC
POS 3		B	J9 3 - 4 = POSITIVE GOING CSYNC
NEG 5		C	J9 5 - 6 = NEGATIVE GOING VSYNC
POS 7		D	J9 7 - 8 = POSITIVE GOING VSYNC

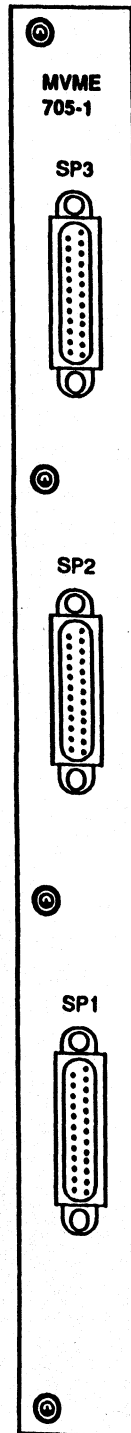
NOTE 1: J5 2 - 3 IS SETTING FOR 1/2 SECOND BLINK RATE.

NOTE 2: J6 2 - 3 IS SETTING FOR TR1 8 - CHANNEL OPERATION. ON THE MVME798-1/-2, BOTH MASTER AND SLAVE ARE JUMPED BETWEEN PINS 2 - 3.

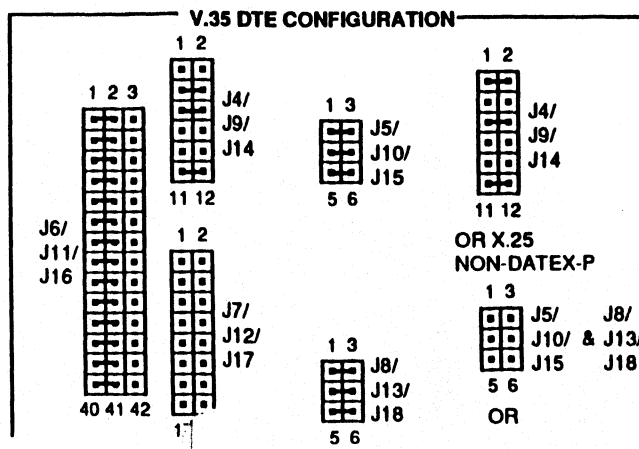
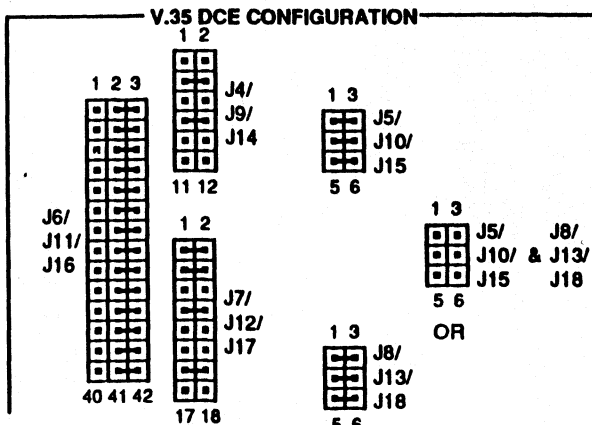
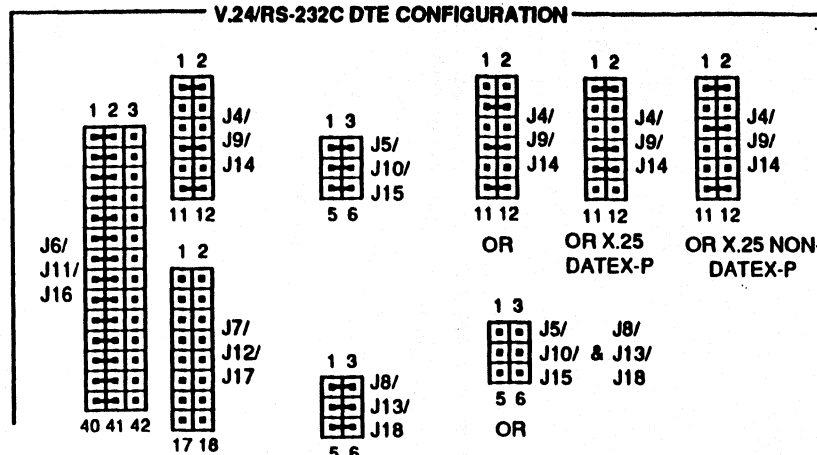
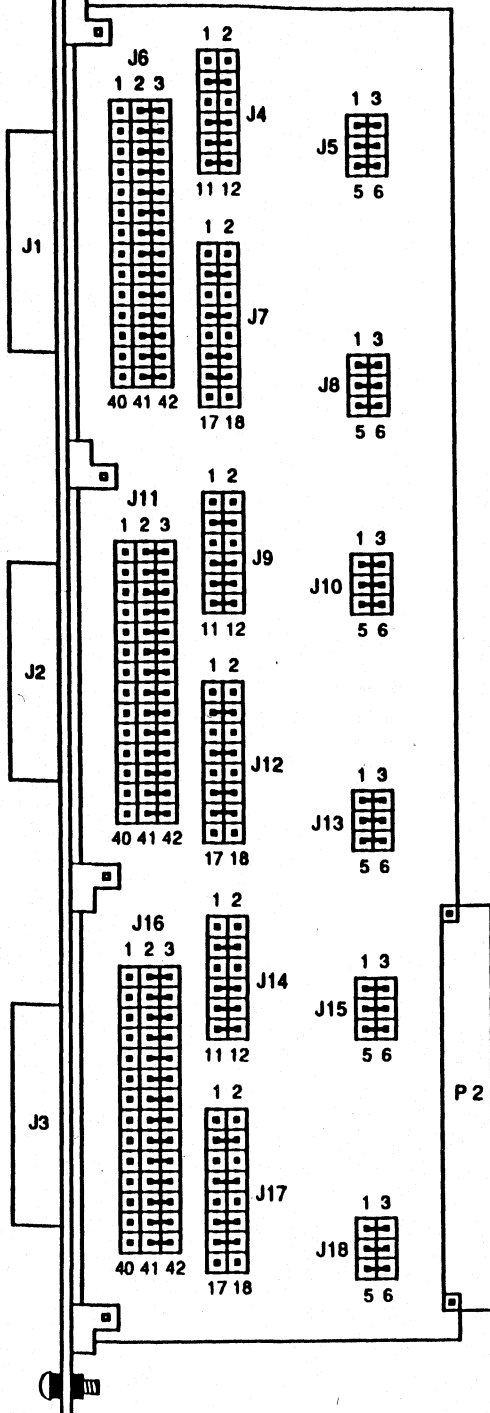
NOTE 3: JUMPERS BETWEEN J7 3 - 5 AND 4 - 6 ARE FOR SLAVE OPERATION FOR BOTH VME794 AND VME798 SERIES.

NOTE 4: J8 2 - 3 IS SETTING FOR DISABLING SYNC ON GREEN.

09/13/89



MVME705-1 BOARD IS SET UP ON ALL THREE CHANNELS FOR V.24/RS-232C DCE CONFIGURATION



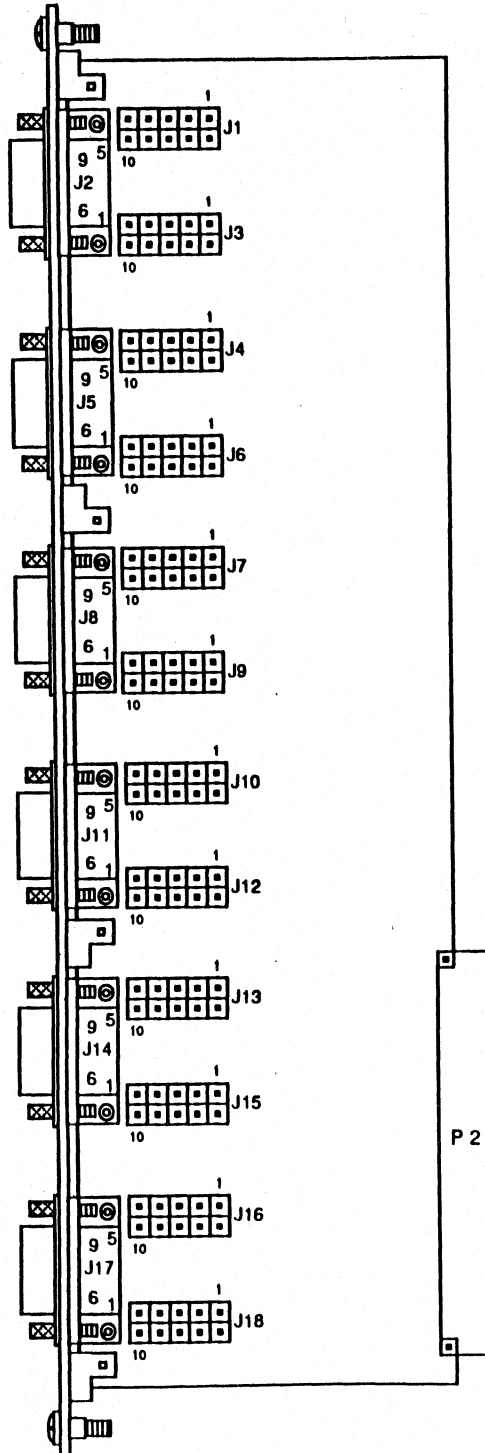
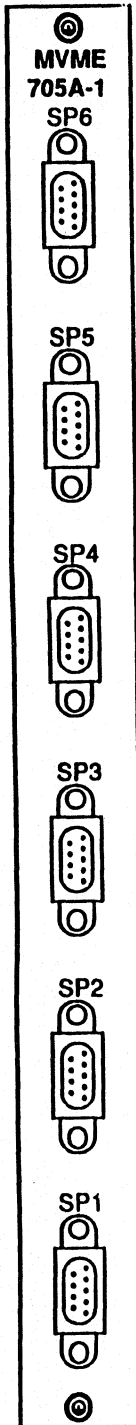
PART NUMBERS:

SMM705RC1-A 01-W3584B06 96011672

SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

NOTE 1: V.24/RS-232C DTE CONFIGURATION FOR SYS3640 IS DIFFERENT. J4/J9/J14 HAVE JUMPERS FROM 1-2, 5-6 AND 11-12 ALL OTHER JUMPERS ARE THE SAME.

11/18/91



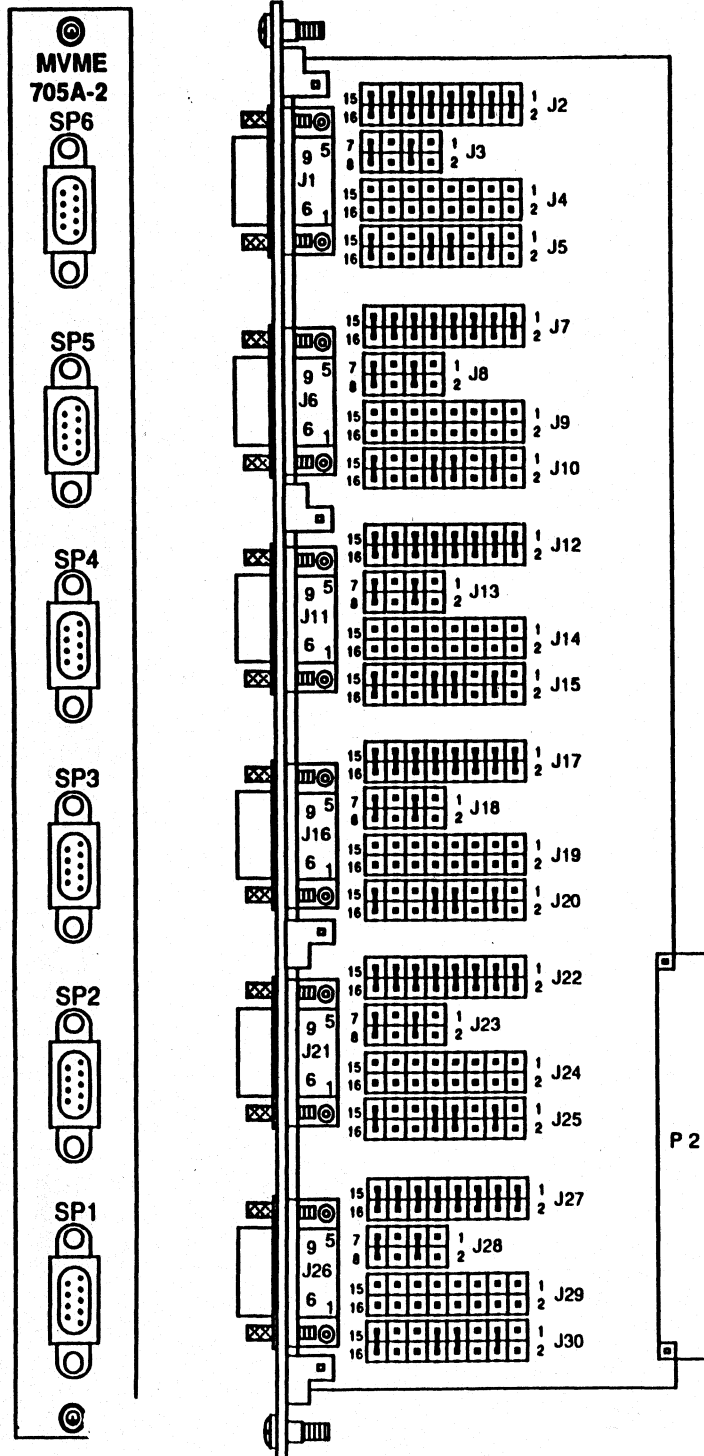
RT NUMBERS:

SMM705ATA1-A 01-W3649B06 TBD

SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

03/11/91

SMM705ATA1-A
MVME333-(X)
TRANSITION]
PAGE 2 OF 3

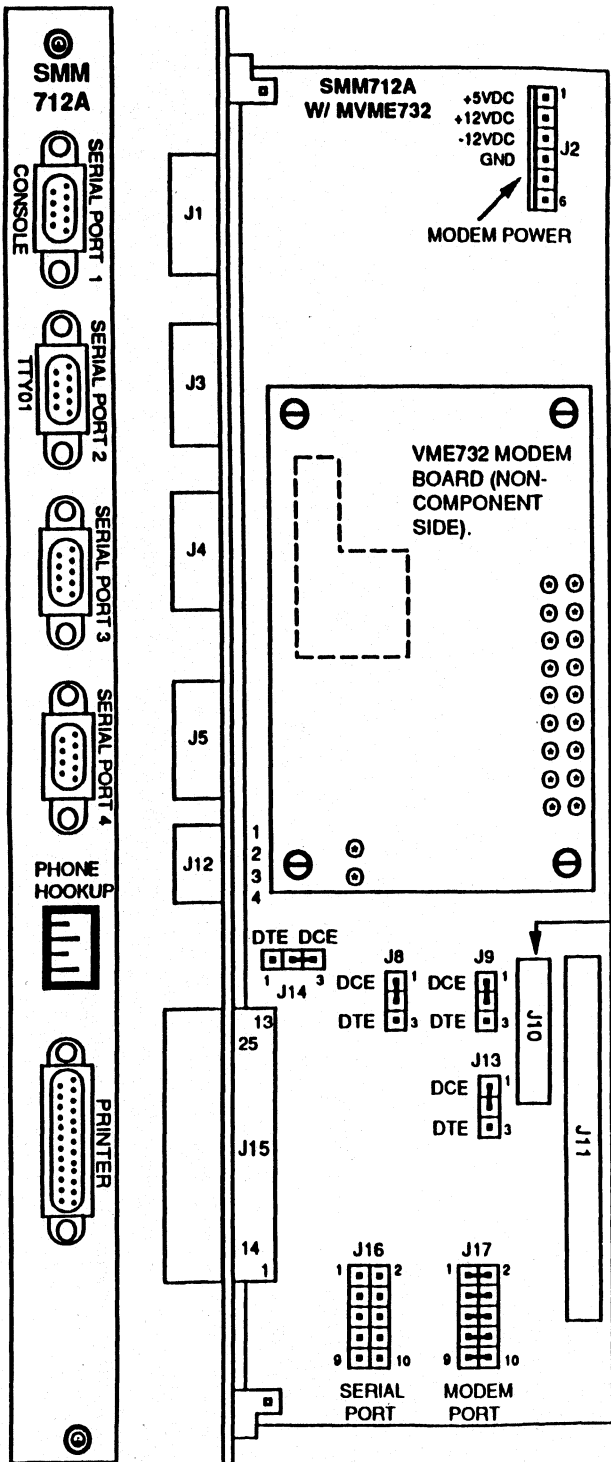


SMM705ATA2-A 01-W3661B06 NONE

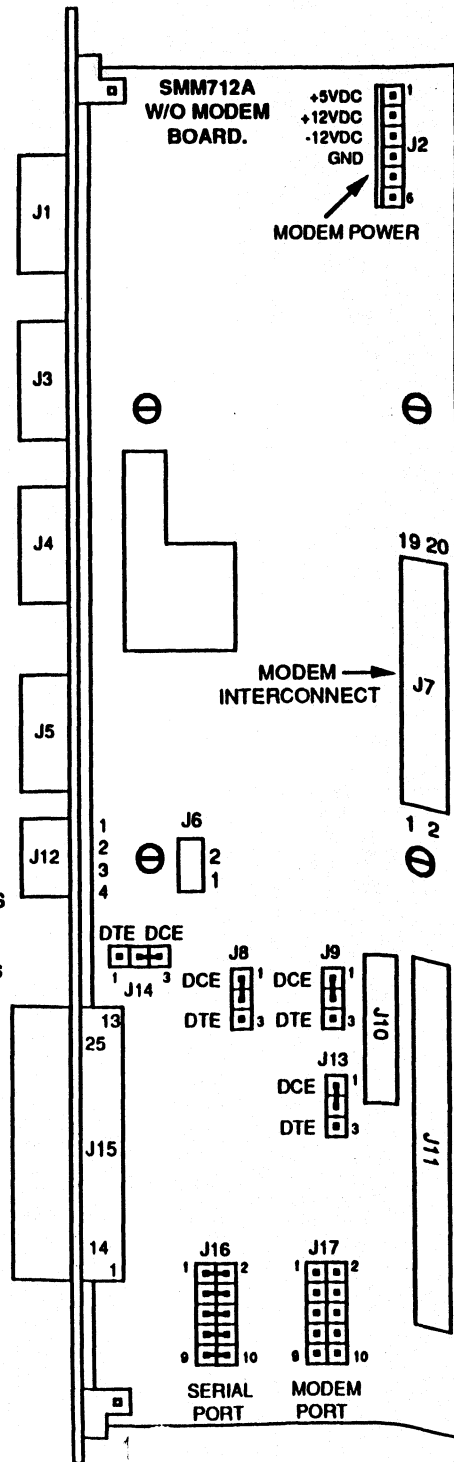
SEE CURRENT REVISION LEVEL (CRL) FOR
CURRENT REVISION INFORMATION.

03/11/91

SMM705ATA2-A
MVME333-(X)
TRANSITION]
PAGE 1 OF 3



ETHERNET INTERFACE GOES OFF INTO A 20-PIN RIBBON CABLE THAT TIES INTO THE SMM147(X) CPU ETHERNET INTERFACE CONNECTOR.



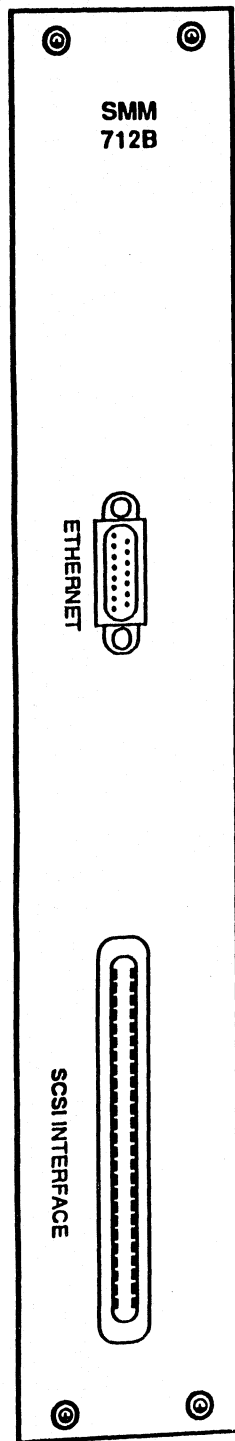
PART NUMBERS:

- SMM712AAT-A 01-W3587B11 TBD
- SMM712BAT-A 01-W2292C11 TBD
- SMM712C 01-W2066D01 96011703
- UDS 2243382 01-W2455C01 TBD
PLUG-IN MODEM

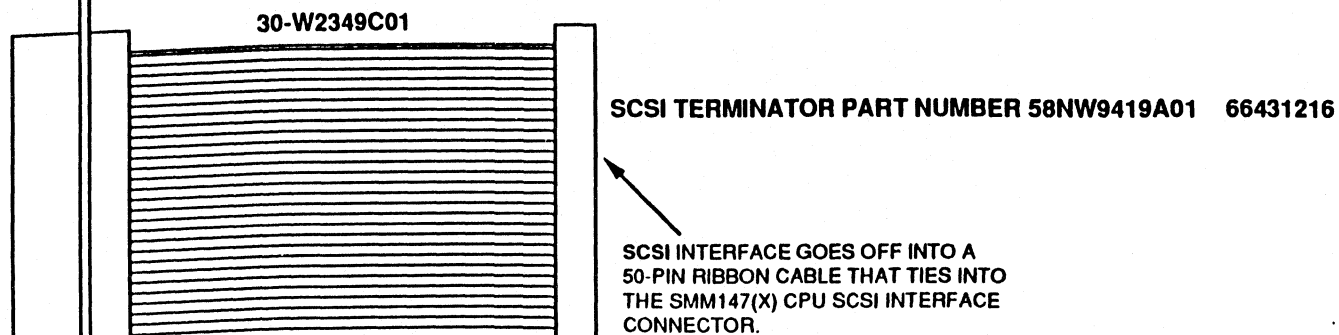
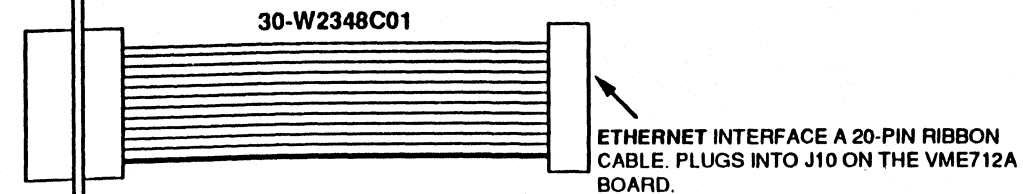
SEE CURRENT REVISION LEVEL (CRL) FOR CURRENT REVISION INFORMATION.

- NOTE 1: J1 - J13 SERIAL PORT 1
 J3 - J9 SERIAL PORT 2
 J4 - J8 SERIAL PORT 3
 J5 - J14 SERIAL PORT 4

11/18/91



NOTE 1 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)



04/02/91

SECTION 8

	HARD DRIVE	HARD DRIVE	FLOPPY DRIVE	FLOPPY DRIVE	FLOPPY DRIVE	SYSTEMS
DRIVE	PRIME	SECONDARY	PRIME	SECONDARY	THIRD CHOICE	USED IN
ASSEMBLY						
820	15MB ST506		1.2MB ST506	1.2MB ST506	1.2MB ST506	MVME121DVSTS
VNDR/MODEL	CMI CM-5619		TEAC FD55GFV-17	TEAC FD55FV-13	TEAC FD55GFR606	
MCD P/N	01-W0306B03		01-W0316B01	01-W0308B03	01-W0316B02	
FSD P/N	76430419		96010908	96010814	96010908	
SIZE	5.25" FULL-HT		5.25" HALF-HT	5.25" HALF-HT	5.25" HALF-HT	
821			1.2MB ST506	1.2MB ST506	1.2MB ST506	FLOPPY DRIVE KIT
VNDR/MODEL			TEAC FD55GFV-17	TEAC FD55FV-13	TEAC FD55GFR606	FOR
MCD P/N			01-W0316B01	01-W0308B03	01-W0316B02	SYS1121
FSD P/N			96010908	96010814	96010908	SYS1131
SIZE			5.25" HALF-HT	5.25" HALF-HT	5.25" HALF-HT	
822	40MB ST506	40MB ST506	1.2MB ST506	1.2MB ST506	1.2MB ST506	SYS1121
VNDR/MODEL	TOSH. MK54FA	MICROP. 1304EM	TEAC FD55GFV-17	TEAC FD55FV-13	TEAC FD55GFR606	SYS1131
MCD P/N	01-W0306B07	01-W0306B05	01-W0316B01	01-W0308B03	01-W0316B02	
FSD P/N	96010940	76432562	96010908	96010814	96010908	
SIZE	5.25" FULL-HT	5.25" FULL-HT	5.25" HALF-HT	5.25" HALF-HT	5.25" HALF-HT	
823	70MB ST506	70MB ST506	1.2MB ST506	1.2MB ST506	1.2MB ST506	SYS1131
VNDR/MODEL	MICROP. 1325M	TOSH. MK56A/B	TEAC FD55GFV-17	TEAC FD55FV-13	TEAC FD55GFR606	
MCD P/N	01-W0306B06	01-W0306B06	01-W0316B01	01-W0308B03	01-W0316B02	
FSD P/N	96010272	96010272	96010908	96010814	96010908	
SIZE	5.25" FULL-HT	5.25" FULL-HT	5.25" HALF-HT	5.25" HALF-HT	5.25" HALF-HT	

NOTE 1: THE 01-W0316B01 & B02 ARE ALSO BOTH REPLACABLE WITH THE 01-W0316B03/96011195 TEAC's FD55GFV152.

MVME82(X) SERIES DRIVES

03/06/90

MVME83(X) SERIES DRIVES

	HARD DRIVE	CD DRIVE	FLOPPY DRIVE	FLOPPY DRIVE	FLOPPY DRIVE	STREAMING TAPE	SYSTEMS
DRIVE	PRIME	SECONDARY	PRIME	SECONDARY	THIRD CHOICE	NO ALTERNATE	USED IN
ASSEMBLY							
831			1.2MB ST506				SYS1132
VNDR/MODEL			TEAC FD55GFR606				SYS2016 DEMO
MCD P/N			01-W0316B02				SYS2316/2334
FSD P/N			96010908				SYS2616 MODEL1
SIZE			5.25" HALF-HT				SYS8400MOD1
832			1.2MB ST506				NOTE : KIT ONLY. NOT
VNDR/MODEL			TEAC FD55GFR606				SOLD AS AN ASSEMBLY
MCD P/N			01-W0316B02				IN ANY SYSTEM
FSD P/N			96010908				
SIZE			5.25" HALF-HT				
833	70MB ST506	70MB ST506	1.2MB ST506	1.2MB ST506	1.2MB ST506		SYS1121
VNDR/MODEL	MICROP. 1325M	TOSH. MK56A/B	TEAC FD55GFV-17	TEAC FD55FV-13	TEAC FD55GFR606		SYS1131
MCD P/N	01-W0306B06	01-W0306B06	01-W0316B01	01-W0308B03	01-W0316B02		SYS2316
FSD P/N	96010272	96010272	96010908	96010814	96010908		
SIZE	5.25" FULL-HT	5.25" FULL-HT	5.25" HALF-HT	5.25" HALF-HT	5.25" HALF-HT		
834	70MB ST506	70MB ST506	1.2MB ST506	1.2MB ST506	1.2MB ST506	60MB QIC-02	SYS1121
VNDR/MODEL	MICROP. 1325M	TOSH. MK56A/B	TEAC FD55GFV-17	TEAC FD55FV-13	TEAC FD55GFR606	ARCHIVE 5945-L2	SYS1131
MCD P/N	01-W0306B06	01-W0306B06	01-W0316B01	01-W0308B03	01-W0316B02	01-W2597B04	SYS1132
FSD P/N	96010272	96010272	96010908	96010814	96010908	76435247	
SIZE	5.25" FULL-HT	5.25" FULL-HT	5.25" HALF-HT	5.25" HALF-HT	5.25" HALF-HT	5.25" FULL-HT	
835	70MB ST506	70MB ST506				60MB QIC-02	SYS1132
VNDR/MODEL	MICROP. 1325M	CDC 94155-85				ARCHIVE 5945-L2	
MCD P/N	01-W0306B06	01-W2052C01				01-W2597B04	
FSD P/N	96010272	96010272				76435247	
SIZE	5.25" FULL-HT	5.25" FULL-HT				5.25" FULL-HT	
836 WREN III	161MB ESDI					60MB QIC-02	SYS1132
VNDR/MODEL	CDC 94166-182					ARCHIVE 5945-L2	
MCD P/N	01-W2873B01					01-W2597B04	
FSD P/N	96010889					76435247	
SIZE	5.25" FULL-HT					5.25" FULL-HT	

03/06/90

PAGE 2

03/06/90

	HARD DRIVE	SYSTEMS
DRIVE	PRIME	USED IN
ASSEMBLY		
841	70MB ST506	SYS2016
VNDR/MODEL	MICROP. 1325M	SYS2316
MCD P/N	01-W0306B06	SYS2334
FSD P/N	96010272	SYS2616
SIZE	5.25" FULL-HT	
842 WREN III	161MB ESDI	SYS2316/2334
VNDR/MODEL	CDC 94166-182	SYS2616
MCD P/N	01-W2873B01	SYS3640/3840
FSD P/N	96010889	SYS8608/88SDP
SIZE	5.25" FULL-HT	
843 WREN V	390MB ESDI	SYS3640
VNDR/MODEL	CDC 94186-442	SYS3840
MCD P/N	01-W2160C01	
FSD P/N	96011015	
SIZE	5.25" FULL-HT	

MVME84(X) SERIES DRIVES

	STREAMING TAPE	STREAMING TAPE	SYSTEMS
DRIVE	DRIVE QIC-02	DRIVE SCSI	USED IN
ASSEMBLY			
851	60MB QIC-02		SYS2016
VNDR/MODEL	ARCHIVE 5945-L2		SYS2316
MCD P/N	01-W2597B04		SYS2334
FSD P/N	76435247		SYS2616
SIZE	5.25" FULL-HT		
ASSEMBLY			
852	60MB QIC-02	60MB SCSI	SYS3640
VNDR/MODEL	ARCHIVE 5945-L2	ARCHIVE 2060S	
MCD P/N	01-W2597B04	01-W2159C01	
FSD P/N	76435247	96011036	
SIZE	5.25" FULL-HT	5.25" HALF-HT	
ASSEMBLY			
853	150MB QIC-02	150MB SCSI	SYS3304/08
VNDR/MODEL	ARCHIVE 2150L	ARCHIVE 2150S	SYS3404/08/16
MCD P/N	01-W2168C01	01-W2013C01	SYS3604/08/40/45
FSD P/N	76435589	96011002	SYS8608/8864
SIZE	5.25" FULL-HT	5.25" FULL-HT	
ASSEMBLY			
855		155MB SCSI	SYS3404/08/16
VNDR/MODEL		TEAC MT-2ST/N50	
MCD P/N		01-W2275C01/C02	
FSD P/N		96011142/1214	
SIZE		3.5" HALF-HT	
ASSEMBLY			
856		2.3GB SCSI	NOTE : KIT ONLY
VNDR/MODEL		EXABYTE 8200	NOT SOLD AS AN
MCD P/N		01-W2272C01	ASSEMBLY IN ANY
FSD P/N		96011089	SYSTEM.
SIZE		3.5" HALF-HT	

	9-TRACK	SYSTEMS
DRIVE	TAPE DRIVE	USED IN
ASSEMBLY		
858	9-TRACK SCSI	NOTE : KIT ONLY
VNDR/MODEL	KENNEDY 6250	NOT SOLD AS AN
MCD P/N	01-W2190C01	ASSEMBLY IN ANY
FSD P/N	96011090	SYSTEM.
SIZE	EXT. CABINET	
ASSEMBLY		
859	9-TRACK	NOTE : KIT ONLY
VNDR/MODEL	PERTEC FS1000	NOT SOLD AS AN
MCD P/N	01-W5310B01	ASSEMBLY IN ANY
FSD P/N	96010896	SYSTEM.
SIZE	EXT. CABINET	

NOTE 1 : VME852 AND 853 HAVE KITS THAT HAVE EITHER QIC-02 OR SCSI STREAMING TAPES. BOTH ARE LISTED SIDE BY SIDE BUT ARE NOT PRIMARY OR SECONDARY REPLACEMENTS FOR EACH OTHER. THEY ARE EITHER QIC-02 OR SCSI BUT NOT BOTH.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

MVME85(X) SERIES DRIVES

03/15/91

NOTE 1: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

	SMD	HARD DRIVE	SYSTEMS
DRIVE	DRIVE	SCSI	USED IN
ASSEMBLY			
861	269MB SMD		NOTE : KIT ONLY
VNDR/MODEL	FUJITSU M2333		NOT SOLD AS AN
MCD P/N	01-W2843B01		ASSEMBLY IN ANY
FSD P/N	96010818		SYSTEM.
SIZE	EXT. CABINET		
862		48MB SCSI	SYS3204
VNDR/MODEL		SEAG.ST157N/M	
MCD P/N		01-W2095C02	
FSD P/N		96011140	
SIZE		3.5" HALF-HT	
863		104MB SCSI	SYS3204/08
VNDR/MODEL		CDC 94351-126	SYS3404/08
MCD P/N		01-W2314C01/C11	
FSD P/N		96011143/1264	
SIZE		3.5" HALF-HT	
864		172MB SCSI	SYS3204/08
VNDR/MODEL		CDC 94351-201	SYS3404/08/16
MCD P/N		01-W2314C02/C12	
FSD P/N		96011144/1263	
SIZE		3.5" HALF-HT	

03/15/91

MVME86(X) SERIES DRIVES

NOTE 1 : STREAMING TAPES AND THE PWB ARE ONLY USED IN MVME87XFTA-5 ASSEMBLIES.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

	HARD DRIVE	STREAMING TAPE	SCSI PWB	SYSTEMS
DRIVE	NO ALTERNATE	NO ALTERNATE	NO ALTERNATE	USED IN
ASSEMBLY				
872	48MB SCSI			SYS3304/08
VNDR/MODEL	SEAG. ST125N/M			
MCD P/N	01-W2095C01			
FSD P/N	96011012			
SIZE	5.25" HALF-HT			
ASSEMBLY				
873	85MB SCSI	150MB SCSI	SCSI CORRECTION	SYS1147
VNDR/MODEL	SEAG. ST296N/M	ARCHIVE 2150S	UNKNOWN	SYS3304/08
MCD P/N	01-W2096C01	01-W2013C01	01-W3575B01	SYS3604/08
FSD P/N	96011013	96011002	96011104	
SIZE	5.25" HALF-HT	5.25" HALF-HT	N/A	
ASSEMBLY				
874 WREN III	155MB SCSI	150MB SCSI	SCSI CORRECTION	SYS1147
VNDR/MODEL	CDC 94161-155	ARCHIVE 2150S	UNKNOWN	SYS3304/08
MCD P/N	01-W2097C01	01-W2013C01	01-W3575B01	SYS3604/08/40
FSD P/N	96011001	96011002	96011104	
SIZE	5.25 FULL-HT	5.25" HALF-HT	N/A	
ASSEMBLY				
875 WREN IV	300MB SCSI	150MB SCSI	SCSI CORRECTION	SYS1147/3308
VNDR/MODEL	CDC 94171-307	ARCHIVE 2150S	UNKNOWN	SYS3408/3608/40
MCD P/N	01-W2098C01	01-W2013C01	01-W3575B01	SYS3708/3840
FSD P/N	96011000	96011002	96011104	SYS8608/8864
SIZE	5.25" FULL-HT	5.25" HALF-HT	N/A	
ASSEMBLY				
876 WREN V	600MB SCSI	150MB SCSI	SCSI CORRECTION	SYS1147/3308
VNDR/MODEL	CDC 94181-702	ARCHIVE 2150S	UNKNOWN	SYS3408/16
MCD P/N	01-W2264C01	01-W2013C01	01-W3575B01	SYS3608/40/45
FSD P/N	96011086	96011002	96011104	SYS3708/3840
SIZE	5.25" FULL-HT	5.25" HALF-HT	N/A	SYS8408/8608/8864

03/15/91

MVME87(X) SERIES DRIVES

NOTE 1 : STREAMING TAPES AND THE PWB ARE ONLY USED IN MVME87XFTA-5 ASSEMBLIES.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

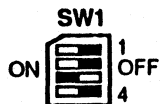
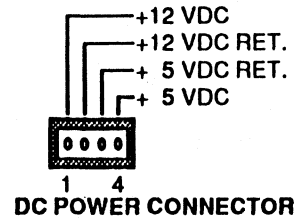
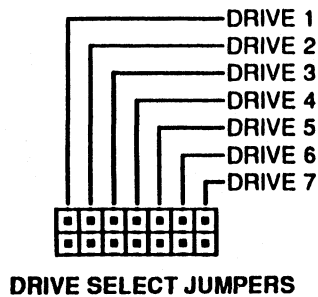
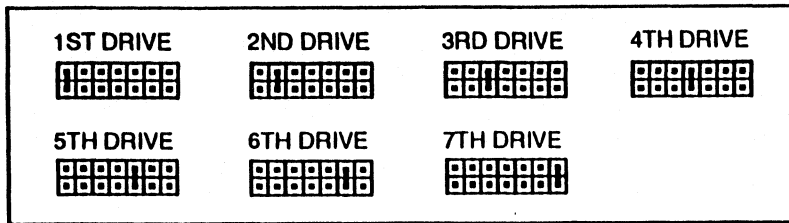
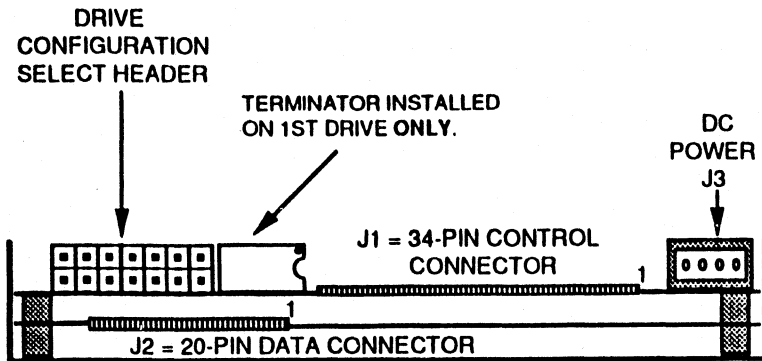
	FLOPPY DRIVE	FLOPPY DRIVE	SCSI PWB	SYSTEMS
DRIVE	PRIME	SECONDARY	NO ALTERNATE	USED IN
ASSEMBLY				
881	1.2MB SCSI	1.2MB SCSI	SCSI INTERFACE	SYS1147
VNDR/MODEL	TEAC FD55GFR152	TEAC FD55GFR606	OMTI	
MCD P/N	01-W0316B03	01-W0316B02	01-W2091C01	
FSD P/N	96011195	96010908	76435612	
SIZE	5.25" HALF-HT	5.25" HALF-HT	5.25" CONTROLLER	
883	1.2MB SCSI	1.2MB SCSI		NOTE : KIT ONLY
VNDR/MODEL	TEAC FD55GFR152	TEAC FD55GFR606		NOT SOLD AS AN
MCD P/N	01-W0316B03	01-W0316B02		ASSEMBLY IN ANY
FSD P/N	96011195	96010908		SYSTEM.
SIZE	5.25" HALF-HT	5.25" HALF-HT		
884	1.2MB SCSI			SYS3204/08
VNDR/MODEL	TEAC FD235JS			SYS3408
MCD P/N	01-W2273C01/02			
FSD P/N	96011141/1215			
SIZE	3.5" HALF-HT			

03/15/91

MVME88(X) SERIES DRIVES

APPENDIX A

161 MByte ESDI WINCHESTER DRIVE



SW1 IS LOCATED IN THE FRONT OF THE WREN III DRIVE

PART NUMBERS:

161MB (182 UNFORMATTED) ESDI 01-W2873B01 96010889
 F/W REVISION N/A
 (141 UNFORMATTED) ESDI 01-W2873B02 NONE
 (101 UNFORMATTED) ESDI 01-W2873B03 NONE

CDC WREN III

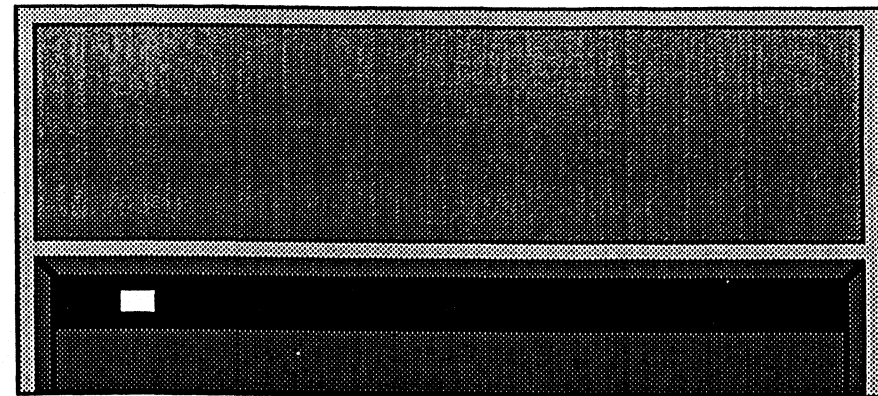
MODEL # 94166-182 PART# 77774336
 FULL HEIGHT 5 1/4" WINCHESTER DRIVE.

NOTE 1: THE 161 MB ESDI DRIVES USES THE MVME323(-1/-2) CONTROLLER.

NOTE 2 : SAME CONFIGURATION FOR SYS3640 & 8608's.

NOTE 3 : THE FOLLOWING KITS USE THE 161 MB DRIVE:
 MVME836F-5, MVME836K-5, MVME836DT, MVME842F,
 MVME842K, MVME842F-3, MVME842K-3, MVME842F-6,
 MVME842K-6, MVME842F-8 & MVME842K-8.

09/13/90

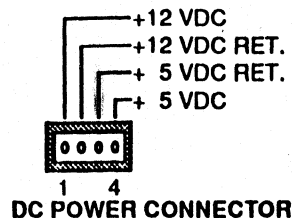
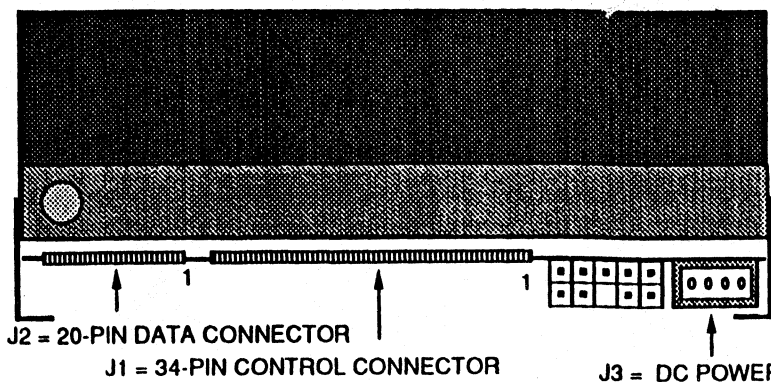


161 MByte
 CDC ESDI DRIVE
 PA 1

380 MByte ESDI WINCHESTER DRIVE

PART NUMBERS:

380MB (410 UNFORMATTED) ESDI 01NC9817A10 NONE
F/W REVISION N/A

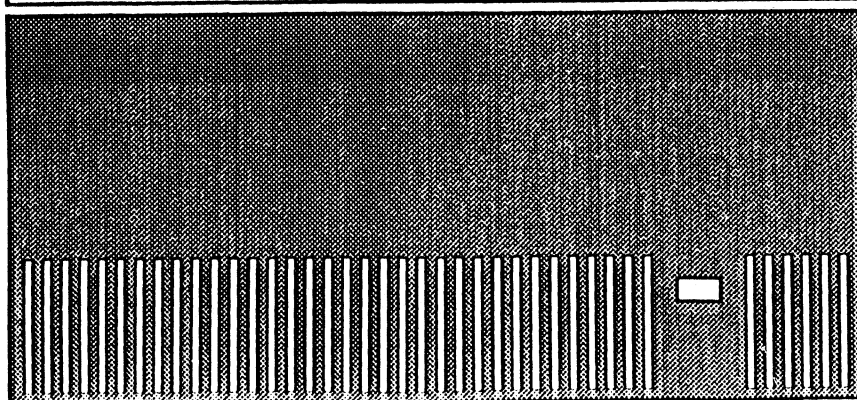
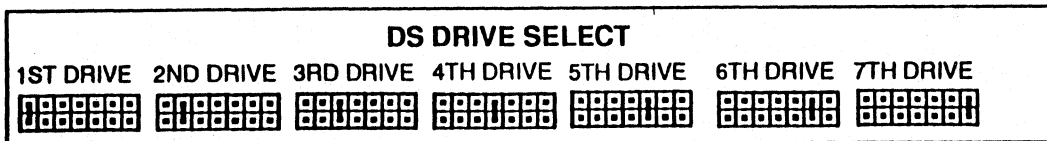
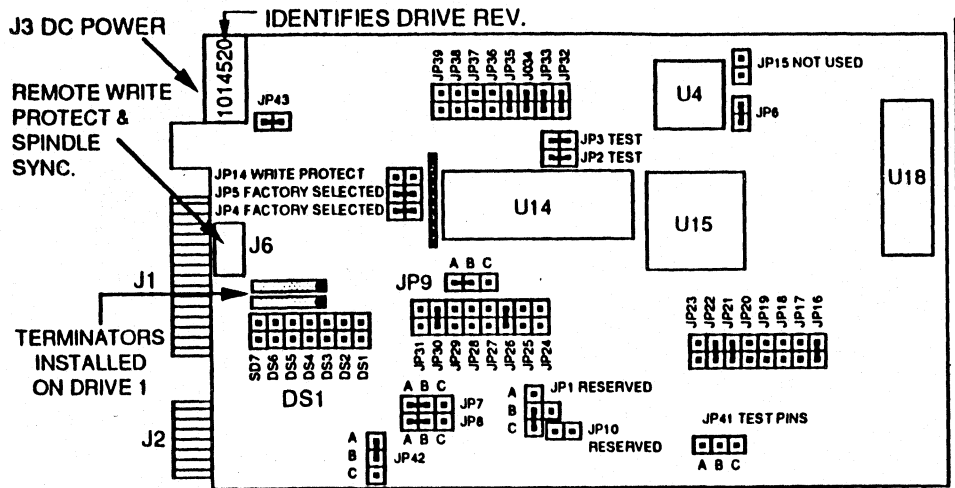


MAXTOR MODEL # XT-8380 FULL HEIGHT
5 1/4" WINCHESTER DRIVE.

NOTE 1: USES THE MVME323(-1/-2) CONTROLLER.

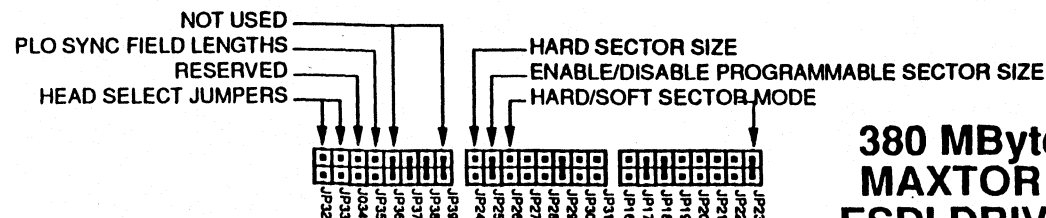
NOTE 2: SAME CONFIG. FOR SYS8XXX SPECIALS SERIES.

NOTE 3: THE SMM3008K-8 KIT USES THE 380 MB DRIVE.



JUMPER	TYPE	DESCRIPTION
JP2 (IN)	F	NEED FOR PHASE MARGIN TESTING: ECL LEVEL DATA OUTPUT = PIN 18. INPUT = PIN 19
JP3 (IN)	F	USED FOR PHASE MARGIN TESTING: ECL LEVEL DATA OUTPUT = PIN 20. INPUT = PIN 21
JP4 (IN)	F	IN = 2, 7 ENCODING
JP5 (IN)	F	IN = 15 Mbit/sec. TRANSFER RATE
JP6 (IN)	C	IN = MOTOR REMOTE SPINUP OPTION DISABLED OUT = MOTOR SPINUP OPTION ENABLED
DS1 - DS7	C	DRIVE SELECT
JP7	C	READ GATE DELAY OPTION
JP8	C	READ GATE DELAY OPTION
JP9 (A-B)	C	INDEX WIDTH SELECTION. A-B = 3usec. B-C = 70 usec.
JP14 (OUT)	C	IN = WRITE PROTECT
JP16 - 29	C	HARD SECTOR SIZE
JP30	C	OUT = DISABLE ESDI PROGRAMMABLE SECTOR SIZE (Hard sector mode only) IN = ENABLE ESDI PROGRAMMABLE SECTOR SIZE (Hard sector mode only)
JP31	C	IN = SOFT SECTOR MODE; OUT = HARD SECTOR MODE
JP32 - 35	F	HEAD CONFIGURATION
JP37	F	IN FOR 24-BYTE PLO SYNC FIELD, OUT FOR 14-BIT.
JP40	F	TEST JUMPER
JP41	F	TEST PIN
JP43 (IN)	F	TEST OUT DISABLES ONBOARD ROM

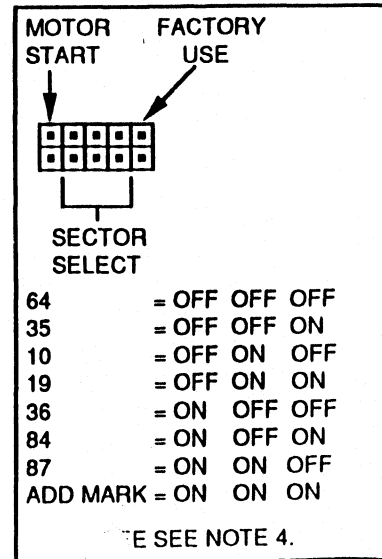
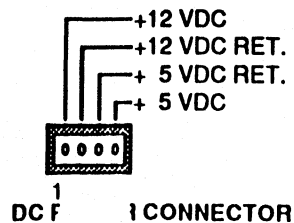
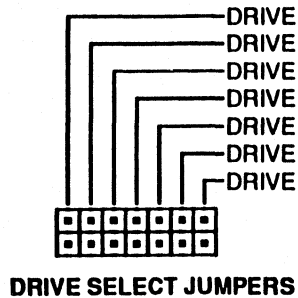
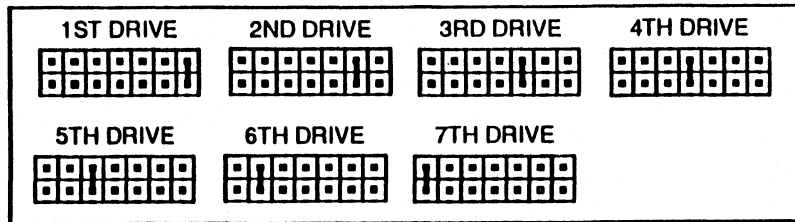
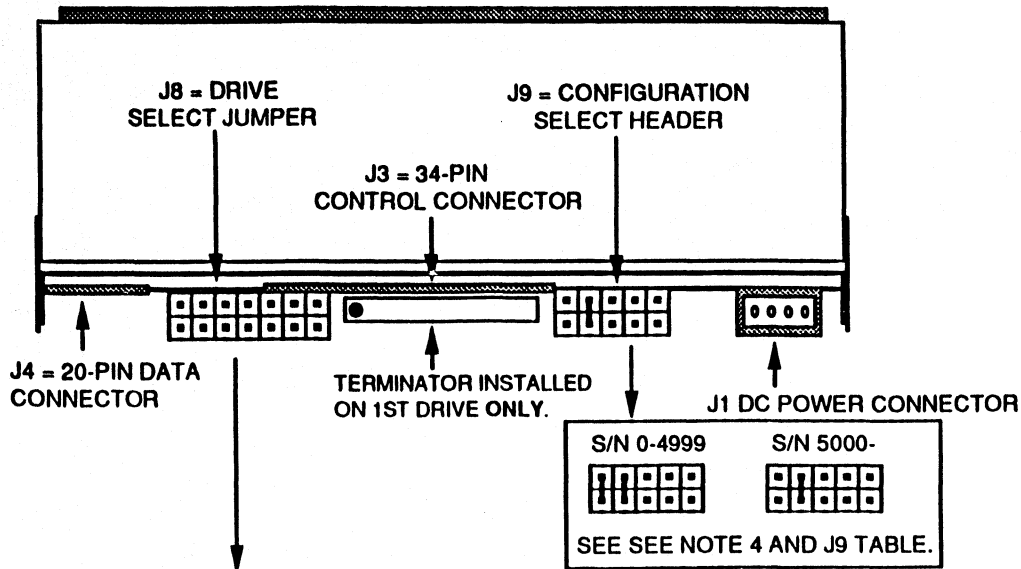
C = CUSTOMER CONFIGURABLE; F = FACTORY SELECT



380 MByte
MAXTOR
ESDI DRIVE
PAGE 2

09/13/90

390 MByte ESDI WINCHESTER DRIVE



PART NUMBERS:

390MB (442 UNFORMATTED) ESDI 01-W2160C01 96011015
F/W REVISION N/A

CDC WREN V

MODEL # 94186-442 PART # 7777602
FULL HEIGHT 5 1/4" WINCHESTER DRIVE.

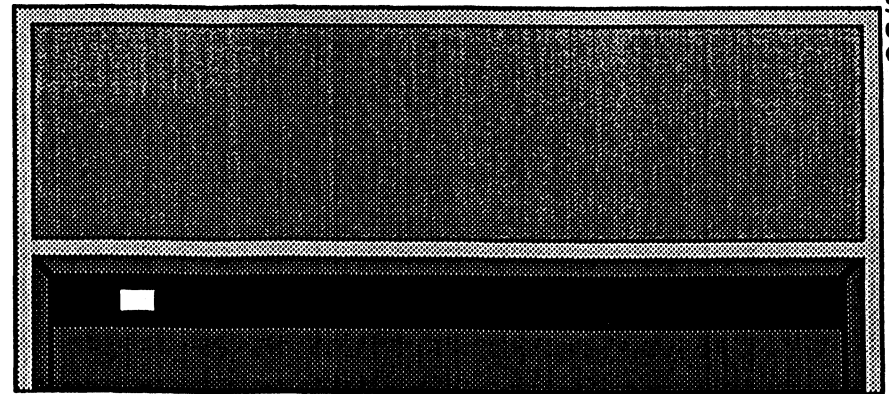
NOTE 1: THE 390 MB ESDI DRIVE USES THE MVME323 (-1/-2) CONTROLLER.

NOTE 2: SAME CONFIGURATION FOR SYS3640 & 8608's.

NOTE 3: THE FOLLOWING KITS USE THE 390 MB DRIVE:
MVME843F-6, MVME843K-6, MVME843F-8 & MVME843K-8.

NOTE 4: J9 1-2 IS FOR MOTOR CONTROL AND NEEDS TO BE INSTALLED FOR DRIVES WITH S/N 0 TO 4999. FROM S/N 5000 UP, IT CAN BE REMOVED.

09/13/90



**390 MByte
CDC ESDI DRIVE**
PA 3

768 MByte ESDI WINCHESTER DRIVE

PART NUMBERS:

701MB (768 UNFORMATTED) ESDI 01NC9817A18 NONE
F/W REVISION N/A

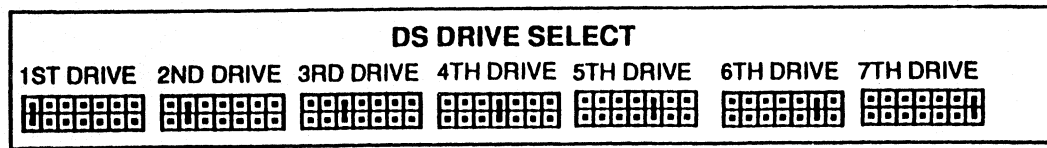
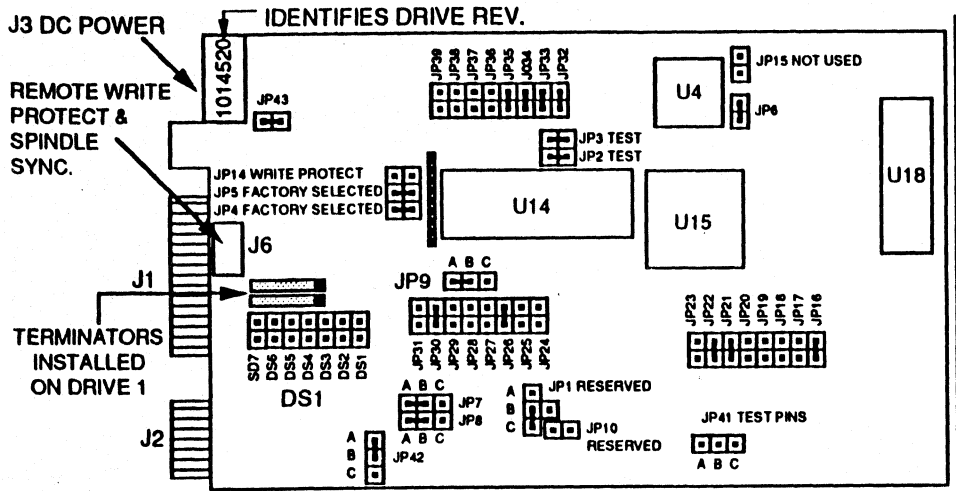
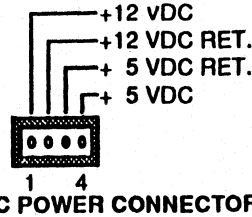
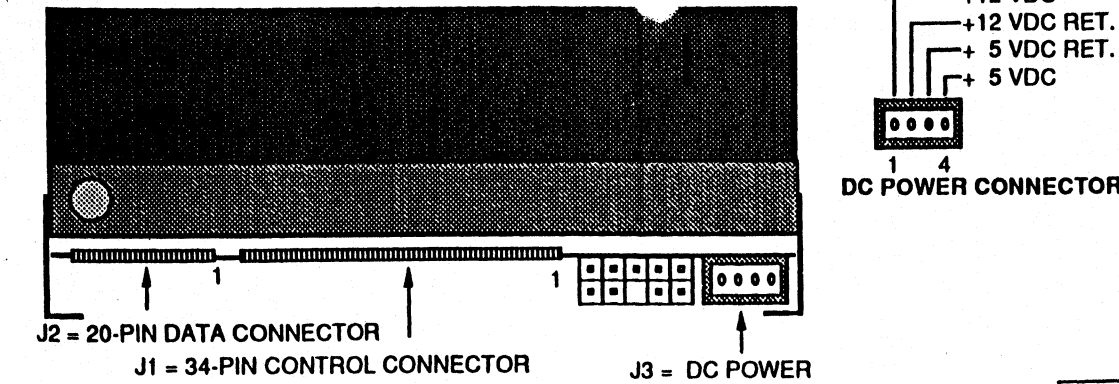
MAXTOR MODEL # XT-8760 FULL HEIGHT
5 1/4" WINCHESTER DRIVE.

NOTE 1: USES THE MVME323(-1/-2) CONTROLLER.

NOTE 2: SAME CONFIG. FOR SYS8XXX SPECIALS SERIES.

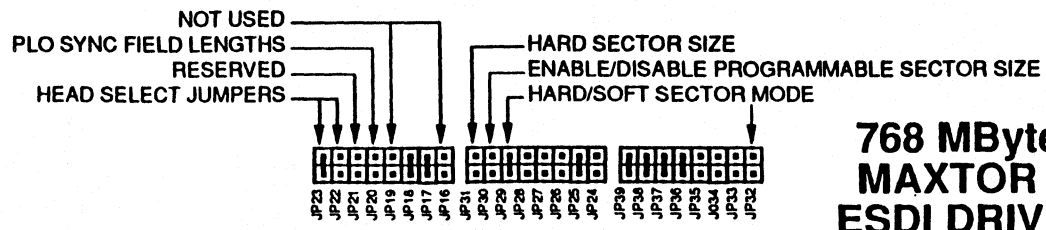
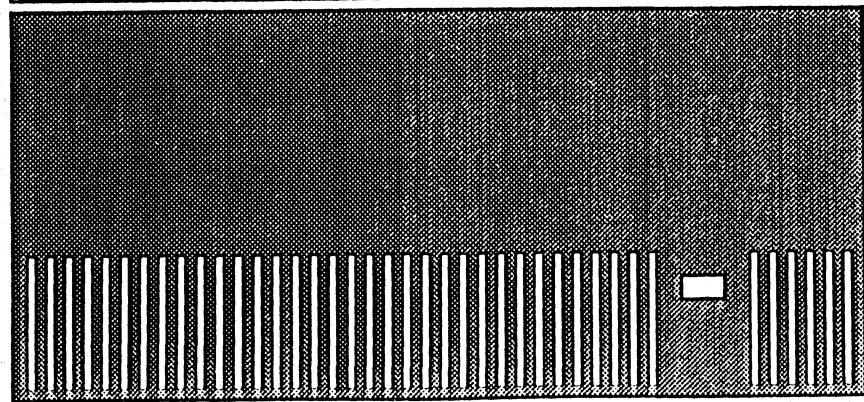
NOTE 3: THE SMM3001K-8 KIT USES THE 768 MB DRIVE:

11/19/91



JUMPER	TYPE	DESCRIPTION
JP2 (IN)	F	NEED FOR PHASE MARGIN TESTING: ECL LEVEL DATA OUTPUT = PIN 18. INPUT = PIN 19
JP3 (IN)	F	USED FOR PHASE MARGIN TESTING: ECL LEVEL DATA OUTPUT = PIN 20. INPUT = PIN 21
JP4 (IN)	F	IN = 2, 7 ENCODING
JP5 (IN)	F	IN = 15 Mbit/sec. TRANSFER RATE
JP6 (IN)	C	IN = MOTOR REMOTE SPINUP OPTION DISABLED OUT = MOTOR SPINUP OPTION ENABLED
DS1 - DS7	C	DRIVE SELECT
JP7	C	READ GATE DELAY OPTION
JP8	C	READ GATE DELAY OPTION
JP9 (A-B)	C	INDEX WIDTH SELECTION. A-B = 3usec. B-C = 70 usec.
JP14 (OUT)	C	IN = WRITE PROTECT
JP16 - 29	C	HARD SECTOR SIZE
JP30	C	OUT = DISABLE ESDI PROGRAMMABLE SECTOR SIZE (Hard sector mode only) IN = ENABLE ESDI PROGRAMMABLE SECTOR SIZE (Hard sector mode only)
JP31	C	IN = SOFT SECTOR MODE; OUT = HARD SECTOR MODE
JP32 - 35	F	HEAD CONFIGURATION
JP37	F	IN FOR 24-BYTE PLO SYNC FIELD, OUT FOR 14-BIT.
JP40	F	TEST JUMPER
JP41	F	TEST PIN
JP43 (IN)	F	TEST OUT DISABLES ONBOARD ROM

C = CUSTOMER CONFIGURABLE; F = FACTORY SELECT



768 MByte
MAXTOR
ESDI DRIVE
PAGE 4

APPENDIX B

PART NUMBERS:

655KB FLOPPY 01-W2911C01 NONE MODEL FD-55FV
 TEAC VENDOR # FD-55FV-13-U
 HALF-HEIGHT 5 1/4" DRIVE.

01-W0308B03 96010814 ALSO

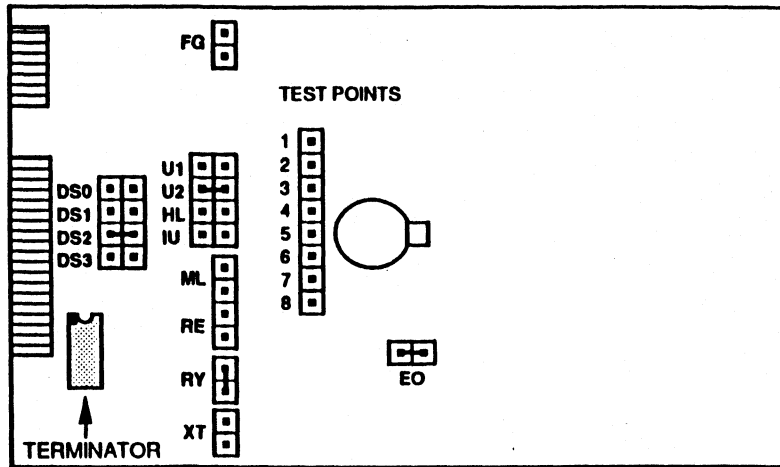
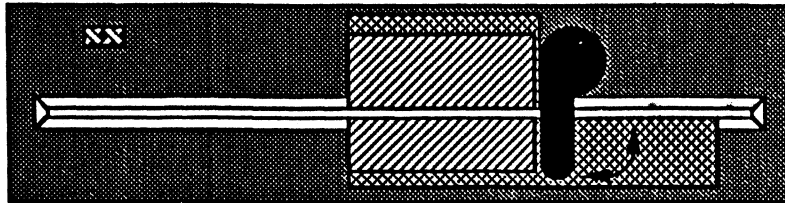
BLANK FLOPPY 99NW9809A11 NO FSD P/N 5 1/4" DISKETTE
 SSDD DYSAN P/N 801187-02
 SSDD VERBADIM P/N MD525-01
 SSDD XIDEX P/N 5012-1000

BLANK FLOPPY 99NW9809A13 NO FSD P/N 5 1/4" UNFORMATTED
 DSDD DYSAN P/N 802060-02
 DSDD VERBADIM P/NMD550-01
 DSDD WABSH P/N M14A211X
 DSDD XIDEX P/N 5022-1000

BLANK FLOPPY 99NW9809A18 76431334 5 1/4" UNFORMATTED
 DSDD BRNDS P/N 50-0096-00
 DSDD DATMG P/N DC3496
 DSDD DYSAN P/N 805001-05
 DSDD MEMOREX P/N 32013541
 DSDD XIDEX P/N 5022-2000

BLANK DISKETTES 99NW9809A26 NO FSD P/N 5 1/4" UNFORMATTED
 DSHK DATMG P/N S5602S

09/14/90



655 KB TEAC FLOPPY DRIVE CONFIGURATION BOARD

NOTE 1: DRIVE SELECT HEADERS:
 DRIVE SELECT 0 = U2 AND DS2
 DRIVE SELECT 1 = U2 AND DS3, ETC.

NOTE 2: INSTALL TERMINATOR FOR FIRST DRIVE & REMOVE FOR SECOND.

NOTE 3: USED ONLY IN M68K102B1 & M68K102B2'S.

SEL #	IU	U1	U2	
1	-	-	ON	DRIVE SELECT
2	ON	-	ON	DS + IN USE
3	ON	ON	-	IN USE
4	-	ON	ON	DS + READY
5	ON	N	ON	IN USE (DS) + READY

NOMENCLATURE LIST:

- DS0-3 - DRIVE SELECT
- RE - AUTO RECALIBRATION
- HL - HEAD LOAD
- HS - HEAD SELECT
- RY - READY
- ML - MOTOR "ON" SELECT
- FG - FRAME GROUND
- IL - INITIALIZE (IN USE) LEADING EDGE OF "DRIVE SELECT"
- EO - "INDEX" & "READY DATA" OUTPUT SELECT
- IU/U1/U2 - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT"
- XT - ??????????????

655 KB TEAC
 FLOPPY DRIVES
 PAGE 1

PART NUMBERS:

1.2MB FLOPPY 01-W0316B01 96010908 MODEL FD-55GFV
TEAC VENDOR # FD-55GFV-17-V
HALF-HEIGHT 5 1/4" DRIVE.
F/W REVISION N/A

BLANK FLOPPY 99NW9809A11 NO FSD P/N 5 1/4" DISKETTE
SSDD DYSAN P/N 801187-02
SSDD VERBADIM P/N MD525-01
SSDD XIDEX P/N 5012-1000

BLANK FLOPPY 99NW9809A13 NO FSD P/N 5 1/4" UNFORMATTED
DSDD DYSAN P/N 802060-02
DSDD VERBADIM P/NMD550-01
DSDD WABSH P/N M14A211X
DSDD XIDEX P/N 5022-1000

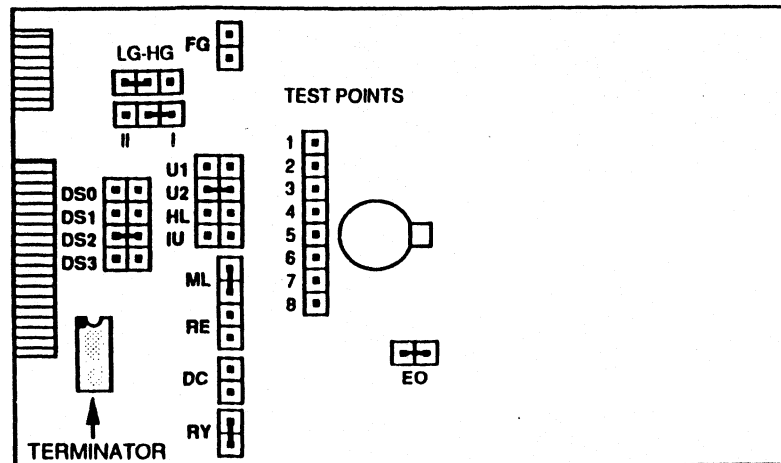
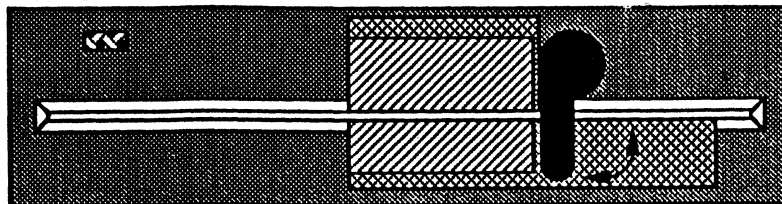
BLANK FLOPPY 99NW9809A18 76431334 5 1/4" UNFORMATTED
DSDD BRNDS P/N 50-0096-00
DSDD DATMG P/N DC3496
DSDD DYSAN P/N 805001-05
DSDD MEMOREX P/N 32013541
DSDD XIDEX P/N 5022-2000

BLANK DISKETTES 99NW9809A26 NO FSD P/N 5 1/4" UNFORMATTED
DSHK DATMG P/N S5602S

NOMENCLATURE LIST:

DS0-3 - DRIVE SELECT
RE - AUTO RECALIBRATION
HL - HEAD LOAD
RY - READY
ML - MOTOR "ON" SELECT
FG - FRAME GROUND
EO - "INDEX" & "READY DATA" OUTPUT SELECT
IU/U1/U2 - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT"
DC - CLOSE DOOR SELECT
LG - LOW DENSITY (655KB)
HG - HIGH DENSITY (1.2MB)
I - LOW SPEED
II - HIGH SPEED

11/19/91



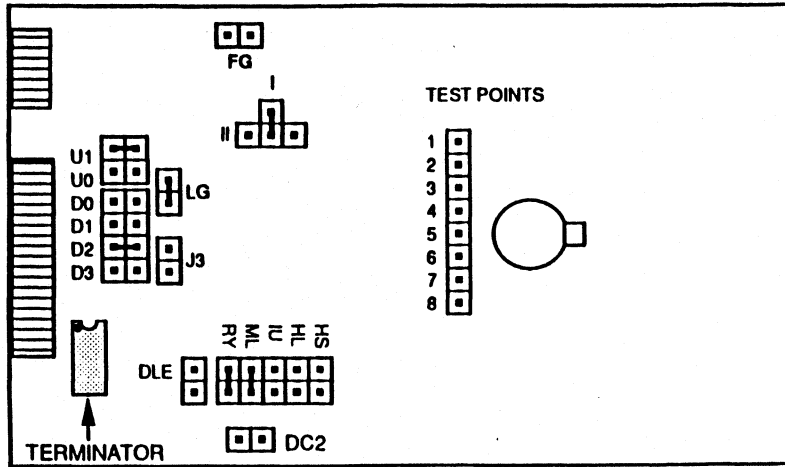
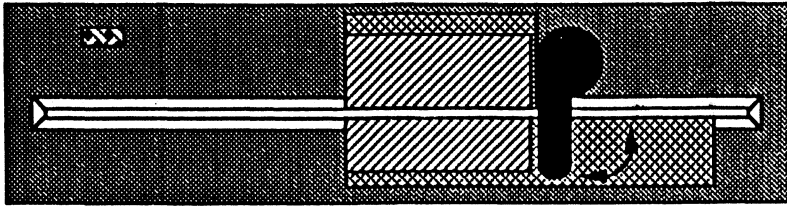
1.2 MB TEAC FLOPPY DRIVE CONFIGURATION BOARD

NOTE 1: DRIVE SELECT HEADERS:
DRIVE SELECT 0 = DS2
DRIVE SELECT 1 = DS3, ETC.

NOTE 2: INSTALL TERMINATOR FOR FIRST DRIVE & REMOVE FOR SECOND.

NOTE 3: USED IN THE FOLLOWING KITS: MVME831FXT-5 & MVME831KST-5.

NOTE 4 : THIS DRIVE IS OBSOLETE. REPLACE WITH 01-W0316B03/96011195 OR
01-W0316B04.



1.2 MB TEAC FLOPPY DRIVE CONFIGURATION BOARD

NOTE 1: DRIVE SELECT HEADERS: WITH MVME320(X) CONTROLLER
 DRIVE SELECT 0 = D2
 DRIVE SELECT 1 = D3, ETC.

NOTE 2: INSTALL TERMINATOR FOR FIRST DRIVE & REMOVE FOR SECOND.

NOTE 3: USED IN THE FOLLOWING KITS : MVME820, MVME821, MVME822, MVME831F, MVME831K, MVME831FXT, MVME831KXT, MVME831FXT-3, MVME831KXT-3, MVME831FXT-5, MVME831KXT-5, MVME832F, MVME832K, MVME832F-3, MVME832FXT, MVME832KXT, MVME833, MVME833F-3, MVME833K-3 & MVME834.

NOTE 4 : THIS DRIVE IS OBSOLETE.REPLACE WITH 01-W0316B03/96011195 OR 01-W0316B04.

PART NUMBERS:

1.2 MB FLOPPY 01-W0316B02 96010814 MODEL FD-55FV
 TEAC VENDOR # FD-55GFR-606-U
 HALF-HEIGHT 5 1/4" DRIVE.
 F/W REVISION N/A

BLANK FLOPPY 99NW9809A11 NO FSD P/N 5 1/4" DISKETTE
 SSDD DYSAN P/N 801187-02
 SSDD VERBADIM P/N MD525-01
 SSDD XIDEX P/N 5012-1000

BLANK FLOPPY 99NW9809A13 NO FSD P/N 5 1/4" UNFORMATTED
 DSDD DYSAN P/N 802060-02
 DSDD VERBADIM P/NMD550-01
 DSDD WABSH P/N M14A211X
 DSDD XIDEX P/N 5022-1000

BLANK FLOPPY 99NW9809A18 76431334 5 1/4" UNFORMATTED
 DSDD BRNDS P/N 50-0096-00
 DSDD DATMG P/N DC3496
 DSDD DYSAN P/N 805001-05
 DSDD MEMOREX P/N 32013541
 DSDD XIDEX P/N 5022-2000

BLANK DISKETTES 99NW9809A26 NO FSD P/N 5 1/4" UNFORMATTED
 DSHK DATMG P/N S5602S

NOMENCLATURE LIST:

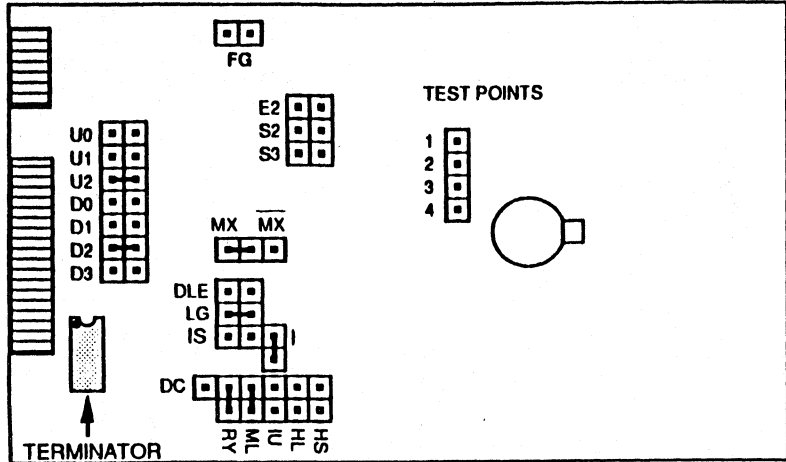
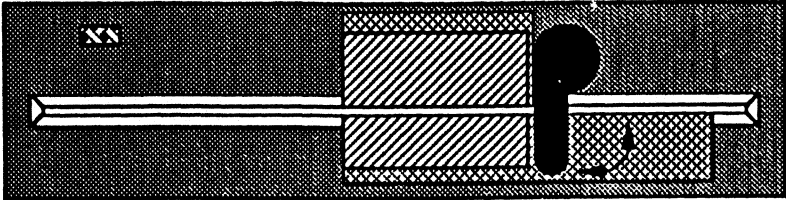
- D0-3 - DRIVE SELECT
- HL - HEAD LOAD
- HS - HEAD SELECT
- RY - READY
- ML - MOTOR "ON" SELECT
- FG - FRAME GROUND
- IU/U1/U2 - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT"
- DLE - HIGH/LOW DENSITY LATCH ENABLE
- LG - LOW DENSITY (655KB)
- J3 - HIGH DENSITY (1.2 MB)
- I - LOW SPEED
- II - HIGH SPEED
- DC2 - DOOR CLOSE SELECT

11/19/91

1.2 MB TEAC
 FLOPPY DRIVES
 PAC 3

PART NUMBERS:

1.2 MB FLOPPY 01-W0316B03 96011195 MODEL FD-55FV
 TEAC VENDOR # FD-55GFR-152
 HALF-HEIGHT 5 1/4" DRIVE.
 F/W REVISION N/A



TERMINATOR
 INSTALLED
 ON FIRST DRIVE

1.2 MB TEAC FLOPPY DRIVE CONFIGURATION BOARD

NOTE 1: DRIVE SELECT HEADERS:
 DRIVE SELECT 0 = D2
 DRIVE SELECT 1 = D3, ETC.

NOTE 2: INSTALL TERMINATOR FOR FIRST DRIVE & REMOVE FOR SECOND.

NOTE 3: SEE ALSO 1.2MB SCSI FLOPPY.

NOTE 4: USED IN THE FOLLOWING KITS : MVME820, MVME821, MVME822,
 MVME831F, MVME831K, MVME831FXT, MVME831KXT, MVME831FXT-3,
 MVME831KXT-3, MVME831FXT-5, MVME831KXT-5, MVME832F,
 MVME832K, MVME832F-3, MVME832FXT, MVME832KXT, MVME833,
 MVME833F-3, MVME833K-3 & MVME834.

NOTE 5: SAME CONFIGURATION USED IN SYS3400, 3604/08/40, 8400 & 8608's.

BLANK FLOPPY 99NW9809A11 NO FSD P/N 5 1/4" DISKETTE
 SSDD DYSAN P/N 801187-02
 SSDD VERBADIM P/N MD525-01
 SSDD XIDEX P/N 5012-1000

BLANK FLOPPY 99NW9809A13 NO FSD P/N 5 1/4" UNFORMATTED
 DSDD DYSAN P/N 802060-02
 DSDD VERBADIM P/NMD550-01
 DSDD WABSH P/N M14A211X
 DSDD XIDEX P/N 5022-1000

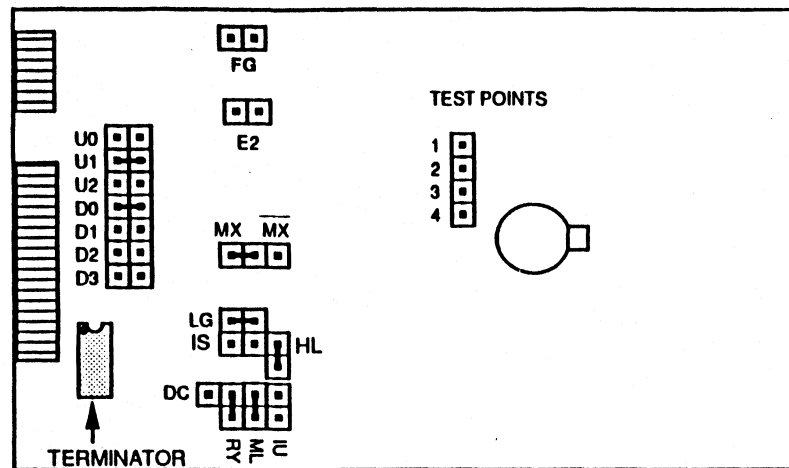
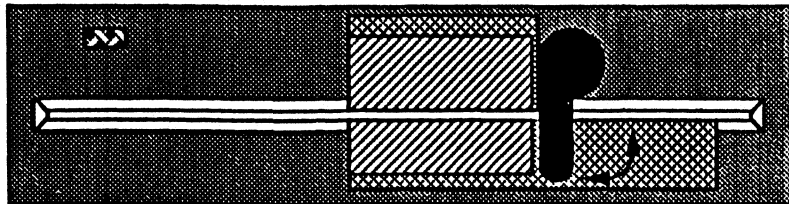
BLANK FLOPPY 99NW9809A18 76431334 5 1/4" UNFORMATTED
 DSDD BRNDS P/N 50-0096-00
 DSDD DATMG P/N DC3496
 DSDD DYSAN P/N 805001-05
 DSDD MEMOREX P/N 32013541
 DSDD XIDEX P/N 5022-2000

BLANK DISKETTES 99NW9809A26 NO FSD P/N 5 1/4" UNFORMATTED
 DSHK DATMG P/N S5602S

NOMENCLATURE LIST:

- D0-3 - DRIVE SELECT
- HL - HEAD LOAD
- RY - READY
- ML - MOTOR "ON" SELECT
- FG - FRAME GROUND
- IU/U1/U2 - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT"
- LG - LOW DENSITY (655KB)
- I - LOW SPEED
- IS - HIGH SPEED
- HS - HEAD SELECT
- DLE - HIGH/LOW DENSITY LATCH ENABLE
- MX - ????????
- MX\ - ????????
- ES - ????????
- S2 - ????????
- S3 - ????????

09/14/90



TERMINATOR
INSTALLED
ON FIRST DRIVE

1.2 MB TEAC FLOPPY DRIVE CONFIGURATION BOARD

NOTE 1: DRIVE SELECT HEADERS: WITH MVME320(X) CONTROLLER.

DRIVE SELECT 0 = D2
DRIVE SELECT 1 = D3, ETC.

NOTE 2: INSTALL TERMINATOR FOR FIRST DRIVE & REMOVE FOR SECOND.

NOTE 3: STRAP DRIVE AS D0 WHEN USED WITH OMTI CONTROLLER.

NOTE 4: USED IN THE FOLLOWING KITS : MVME820, MVME821, MVME822,
MVME831F, MVME831K, MVME831FXT, MVME831KXT, MVME831FXT-3,
MVME831KXT-3, MVME831FXT-5, MVME831KXT-5, MVME832F,
MVME832K, MVME832F-3, MVME832FXT, MVME832KXT, MVME833,
MVME833F-3, MVME833K-3 & MVME834.

NOTE 5: : CONFIGURATION USED IN SYS 100, 3604/08/40, 8400 & 9

PART NUMBERS:

1.2 MB FLOPPY 01-W0316B04 TBD MODEL FD-55FV
TEAC VENDOR # FD-55GFR-165
HALF-HEIGHT 5 1/4" DRIVE.
F/W REVISION N/A

BLANK FLOPPY 99NW9809A11 NO FSD P/N 5 1/4" DISKETTE
SSDD DYSAN P/N 801187-02
SSDD VERBADIM P/N MD525-01
SSDD XIDEX P/N 5012-1000

BLANK FLOPPY 99NW9809A13 NO FSD P/N 5 1/4" UNFORMATTED
DSDD DYSAN P/N 802060-02
DSDD VERBADIM P/NMD550-01
DSDD WABSH P/N M14A211X
DSDD XIDEX P/N 5022-1000

BLANK FLOPPY 99NW9809A18 76431334 5 1/4" UNFORMATTED
DSDD BRNDS P/N 50-0096-00
DSDD DATMG P/N DC3496
DSDD DYSAN P/N 805001-05
DSDD MEMOREX P/N 32013541
DSDD XIDEX P/N 5022-2000

BLANK DISKETTES 99NW9809A26 NO FSD P/N 5 1/4" UNFORMATTED
DSHK DATMG P/N S5602S

NOMENCLATURE LIST:

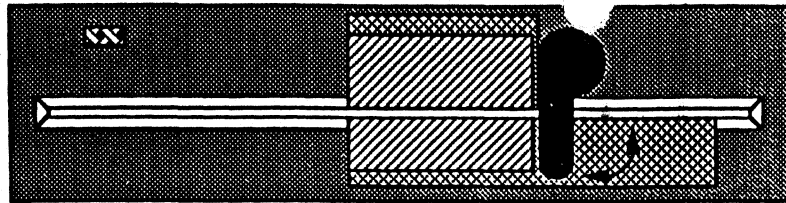
- D0-3 - DRIVE SELECT
- HL - HEAD LOAD
- RY - READY
- ML - MOTOR "ON" SELECT
- FG - FRAME GROUND
- IU/U1/U2 - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT"
- LG - LOW DENSITY (655KB)
- I - LOW SPEED
- IS - HIGH SPEED
- HS - HEAD SELECT
- DLE - HIGH/LOW DENSITY LATCH ENABLE
- MX - ????????
- MX\ - ????????
- ES - ????????
- S2 - ????????
- S3 - ????????

11/19/91

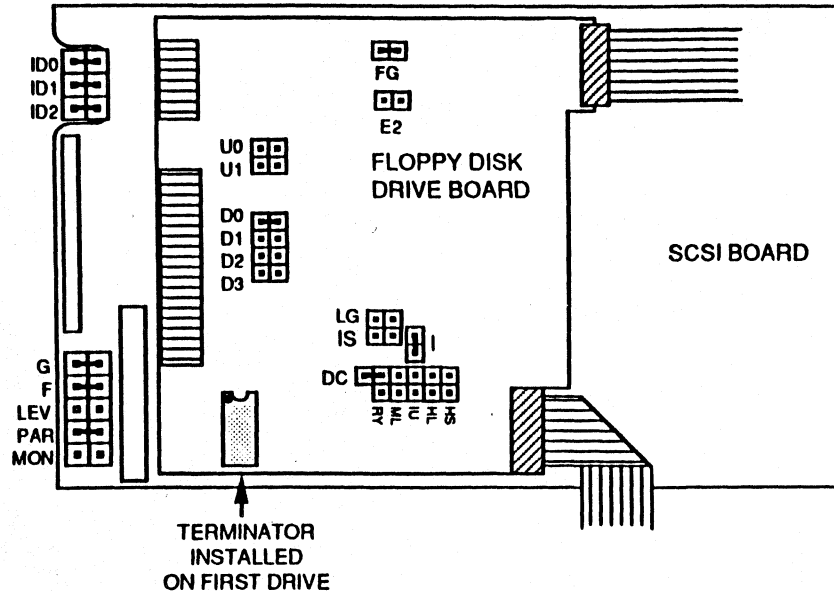
**1.2 MB TEAC
FLOPPY DRIVES
PA 5**

PART NUMBERS:

1.2 MB SCSI FLOPPY 01-w2484C01 96011262 TEAC VENDOR # FD-55GS751
 HALF-HEIGHT 5 1/4" DRIVE. FIRMWARE REV. J



SELECT 6 IS FIRST DRIVE



1.2 MB TEAC SCSI FLOPPY DRIVE CONFIGURATION

NOTE 1: REPLACEMENT FOR 01-W0316B01, B02, & B03's. (96010908, 0814 & TBD CONSECUTIVELY) SET UP AS 5 1/4" SCSI HALF-HEIGHT DRIVES.

NOTE 2: USED IN MVME881AK KITS SYS3708, MPC's. THEY ARE DIRECT SCSI CONTROLLERS. (i.e. mvme147, 328.)

NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.

SCSI ADDRESS	CHAN 0	CHAN 1
FLOPPY 6	30	70
7	38	78

SCSI ID ADDRESS	ID0	ID1	ID2
SELECT 0	1	1	1
SELECT 1	0	1	1
SELECT 2	1	0	1
SELECT 3	0	0	1
SELECT 4	1	1	0
SELECT 5	0	1	0
SELECT 6	1	0	0
SELECT 7	0	0	0

1 DESIGNATES JUMPER ON
 0 DESIGNATES JUMPER OFF

NOMENCLATURE LIST:

- D0-3 - DRIVE SELECT
- RY - READY
- ML - MOTOR "ON" SELECT
- FG - FRAME GROUND
- IU/U0/U1 - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT"
- LG - LOW DENSITY (655KB)
- I - LOW SPEED
- IS - HIGH SPEED
- E2 - ????????
- ID0/1/2 - DRIVE SELECT HEADER ON SCSI BD.
- G - 1.6MB DRIVE CAPACITY
- F - 1 MB DRIVE CAPACITY
- PAR - PARITY ENABLE
- LEV - DENSITY LEVEL ENABLE (USED W/ G OR H)
- MON - MOTOR ON
- DC - DOOR CLOSE SELECT

11/08/91

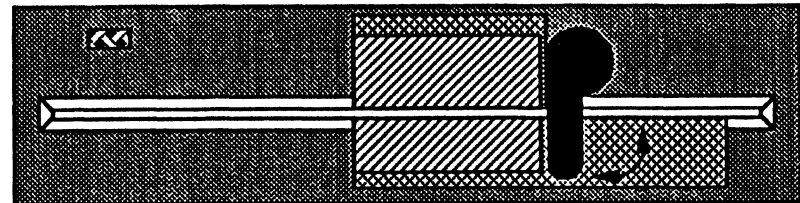
PART NUMBERS:

SEL #	IU	U1	U2	
1	-	-	ON	DRIVE SELECT
2	ON	-	ON	DS + IN USE
3	ON	ON	-	IN USE
4	-	ON	ON	DS + READY
5	ON	ON	ON	IN USE (DS) + READY

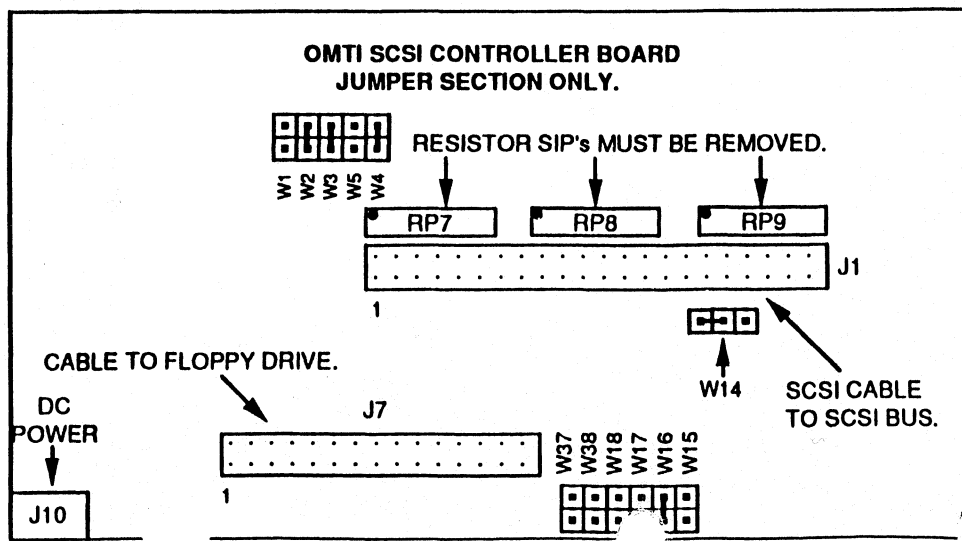
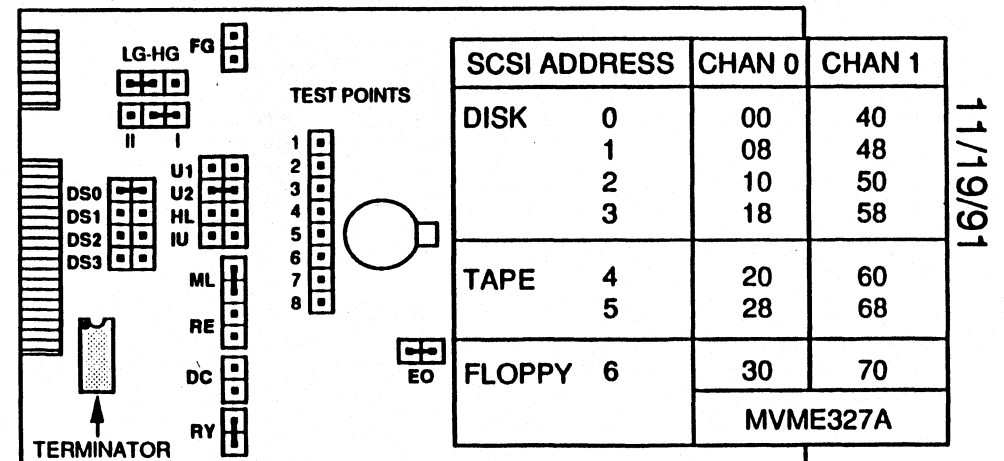
- NOMENCLATURE LIST:**
- DS0-3 - DRIVE SELECT
 - RE - AUTO RECALIBRATION
 - HL - HEAD LOAD
 - RY - READY
 - ML - MOTOR "ON" SELECT
 - FG - FRAME GROUND
 - IU/U1/U2 - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT"
 - LG - LOW DENSITY (655KB)
 - HG - HIGH DENSITY (1.2MB)
 - I - LOW SPEED
 - II - HIGH SPEED
 - DC - CLOSE DOOR SELECT
 - EO - "INDEX" & "READY DATA" OUTPUT SELECT

1.2MB FLOPPY 01-W0316B01 96010908 TEAC # FD-55GFV-17-U
 HALF-HEIGHT 5 1/4" DRIVE. OBSOLETE REPLACE
 WITH 01-W0316B03/96011195 OR 01-W0316B04.

SCSI CONTROLLER 01-W2091C01 76435612 OMTI CONTROLLER
 SCIENTIFIC MICRO SYSTEMS INC. MODEL #7000



- BLANK FLOPPY 99NW9809A11 NO FSD P/N 5 1/4" DSKT
- SSDD DYSAN P/N 801187-02
- SSDD VERBADIM P/N MD525-01
- SSDD XIDEX P/N 5012-1000
- BLANK FLOPPY 99NW9809A13 NO FSD P/N 5 1/4" UNFMT
- DSDD DYSAN P/N 802060-02
- DSDD VERBADIM P/NMD550-01
- DSDD WABSH P/N M14A211X
- DSDD XIDEX P/N 5022-1000
- BLANK FLOPPY 99NW9809A18 76431334 5 1/4" UNFORMATTED
- DSDD BRNDS P/N 50-0096-00
- DSDD DATMG P/N DC3496
- DSDD DYSAN P/N 805001-05
- DSDD MEMOREX P/N 32013541
- DSDD XIDEX P/N 5022-2000
- BLANK DISKETTES 99NW9809A26 NO FSD P/N 5 1/4" UNFORMATTED
- DSHK DATMG P/N S5602S



1.2 MB TEAC FLOPPY DRIVE CONFIGURATION BOARD

NOTE 1: DRIVE SELECT HEADERS:

DRIVE SELECT 0 = DS2 ; DRIVE SELECT 1 = DS3

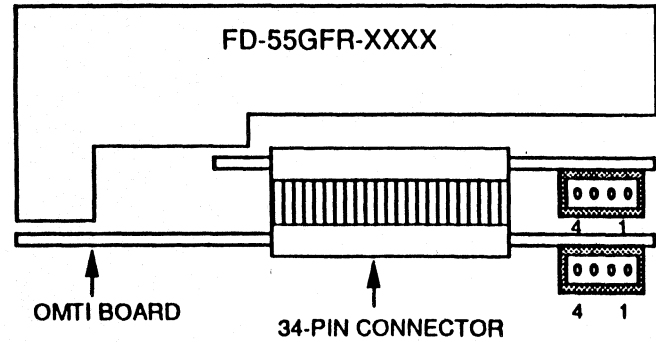
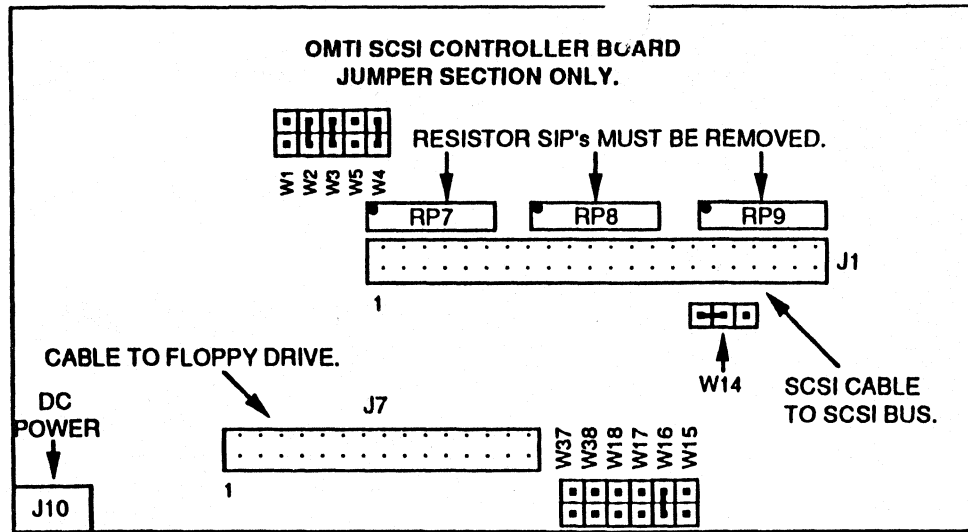
NOTE 2: INSTALL TERMINATOR FOR END OF CHAIN DRIVE AND

REMOVE FROM OTHER DRIVES. NOTE: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 3: WHEN USING WITH OMTI

CONTROLLER OR MVME327A, SET UP AS DRIVE 0, NOT DRIVE 2.

**1.2 MB TEAC
 AS SCSI
 FLOPPY DRIVES
 PAGE 7**



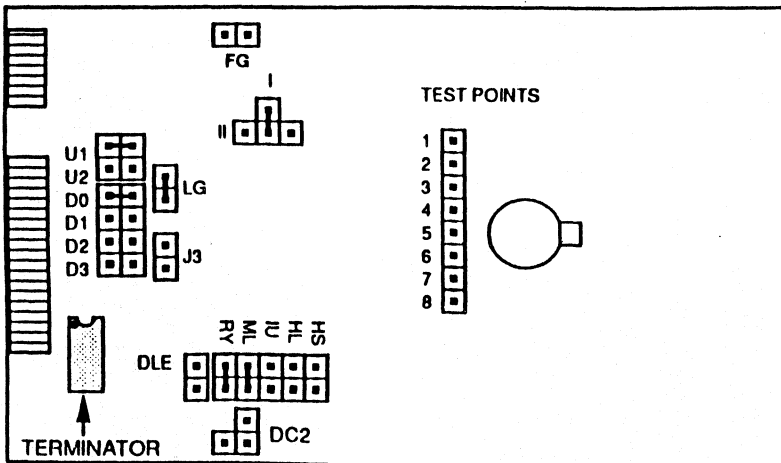
NOTE 1: DRIVE SELECT HEADERS:
DRIVE SELECT 0 = D2; DRIVE SELECT 1 = D3

NOTE 2: WHEN USING WITH OMTI CONTROLLER SET DRIVE UP AS DRIVE 0, AND OMTI AS SELECT #6 (W2 & W3 INSTALLED).

NOTE 3: SAME CONFIGURATION IN SYS1147, 3400, 3604/08, 3640, 8400 & 8608's.

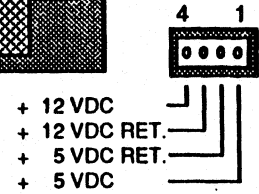
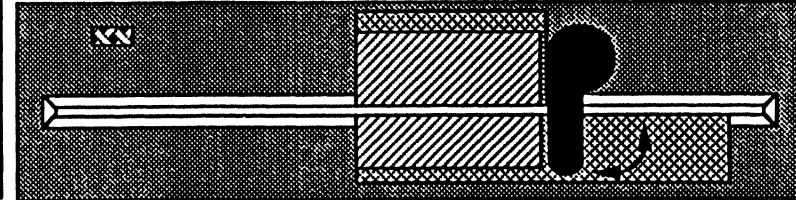
NOTE 4: USED IN THE FOLLOWING KITS: MVME881F-3, MVME881K-3, MVME881F-5, MVME881K-5, MVME881F-6, MVME881K-6, MVME881F-7, MVME881K-7, MVME881F-8, MVME881K-8, MVME883F-4, MVME883K-4, MVME883F-6 & MVME883K-6.

NOTE 6: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)



1.2 MB TEAC FLOPPY DRIVE CONFIGURATION BOARD

PART NUMBERS:
 1.2 MB FLOPPY 01-W0316B02 96010908 TEAC #FD-55FR-606
 OBSOLETE. REPLACE WITH B03 OR B04.
 1.2 MB FLOPPY 01-W0316B03 96011195 TEAC #FD-55GFR-152
 1.2 MB FLOPPY 01-W0316B04 96011412 TEAC #
 (B02, B03 & B04 ARE ALL H/H 5 1/4" DRIVES
 SCSI CONTROLLER 01-W2091C01 76435612 OMTI CONTROLLER
 SCIENTIFIC MICRO SYSTEMS INC. MODEL # 7000



DC POWER CONNECTOR

1/1/9/91

SCSI ADDRESS	CHAN 0	CHAN 1
FLOPPY 6	30	70
MVME327A		

DRIVE NOMENCLATURE LIST:

- | | | | |
|----------|---|-----|---------------------------------|
| D0-3 | - DRIVE SELECT | HS | - HEAD SELECT |
| HL | - HEAD LOAD | LG | - LOW DENSITY (655KB) |
| RY | - READY | J3 | - HIGH DENSITY (1.2 MB) |
| ML | - MOTOR "ON" SELECT | I | - LOW SPEED |
| FG | - FRAME GROUND | II | - HIGH SPEED |
| IU/U0/U1 | - FRONT PANEL INDICATOR ACCORDING TO "DRIVE SELECT" | DC2 | - CLOSE DOOR SELECT |
| | | DLE | - HIGH/LOW DENSITY LATCH ENABLE |

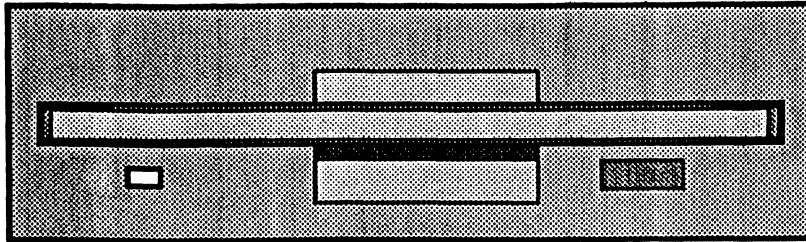
**1.2 MB TEAC
W/ SCSI INTF BD.
FLOPPY DRIVE
PAGE 8**

PART NUMBERS: OLD DRIVE

01-W2273C01 96011141 TEAC FD-235JS-502 W/O 5 1/4" BRACKET W/ F/W REV. J

01-W2273C02 96011215 TEAC FD-235JS-502 W/ 5 1/4" BRKT.

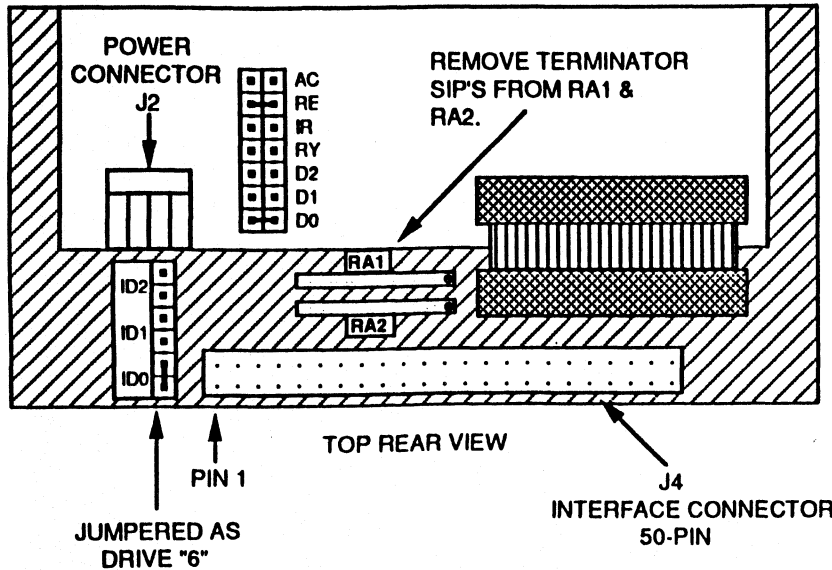
BLANK FLOPPY 99NW9809A33 66431206 3.5" 135 TPI UNFORMATTED TOSHIBA PART # PMF-2ED



NOTE 1: SAME CONFIGURATION FOR SYS3200, 3400 & 8400's.

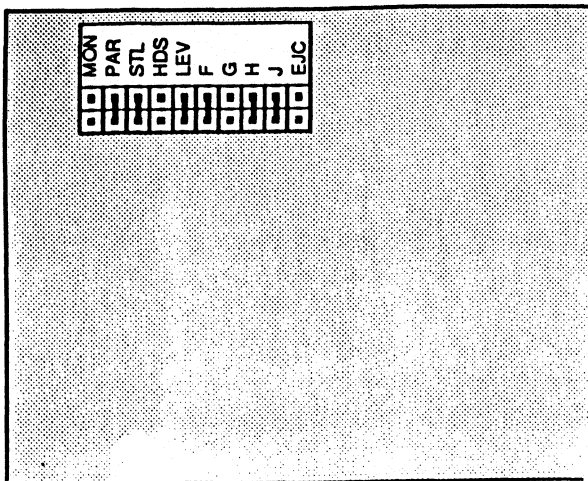
NOTE 2: USED IN THE FOLLOWING KITS: MVME884F-3, MVME884K-3, MVME884F-4, MVME884K-4, MVME884F-5, MVME884K-5, MVME884F-6 & MVME884K-6.

NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)



JUMPED AS DRIVE "6"

BOTTOM FRONT VIEW

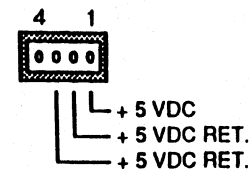


SCSI ADDRESS	CHAN 0	CHAN 1
FLOPPY 6	30	70
7	38	78

NOMENCLATURE LIST:

- AC - OFF STATE AUTO CHUCKING
- D0-2 - DRIVE SELECT FOR DAISY CHAINING DRIVES
- ID0-3 - SDSI ID ADDRESS SELECT
- IR - DRIVE INDICATOR READY
- RE - AUTO RECALIBRATION
- RY - READY
- MON - MOTOR ON
- PAR - PARITY ENABLE
- STL - SET HEAD LOAD TIMING (NOT USED)
- LEV - DENSITY LEVEL ENABLE (USED W/ G, H & J)
- F - 1 MB DRIVE CAPACITY
- G - 1.6 MB DRIVE CAPACITY
- H - 2 MB DRIVE CAPACITY
- J - 4 MB DRIVE CAPACITY
- EJC - MEDIA EJECT

SCSI ID ADDRESS	ID2	ID1	ID0
SELECT 0	0	0	0
SELECT 1	0	0	1
SELECT 2	0	1	0
SELECT 3	0	1	1
SELECT 4	1	0	0
SELECT 5	1	0	1
SELECT 6	1	1	0
SELECT 7		1	1



**2.9 MB TEAC SCSI
AKA 1-4 MB
FLOPPY 3 1/2"
1/2 HEIGHT DRIVE
PACK A**

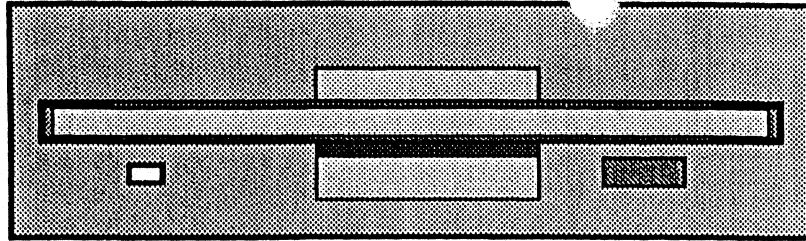
11/08/91

PART NUMBERS: NEW DRIVES

01-W2273C01 96011141 TEAC FD-235JS-502 W/O 5 1/4"
BRACKET F/W REV. J

01-W2273C02 96011215 TEAC FD-235JS-502 W/ 5 1/4" BRKT.

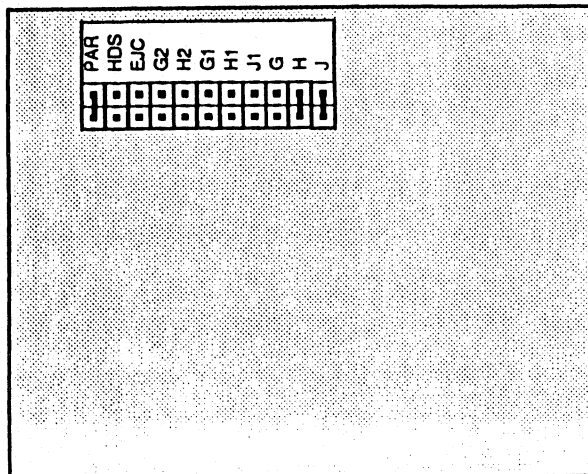
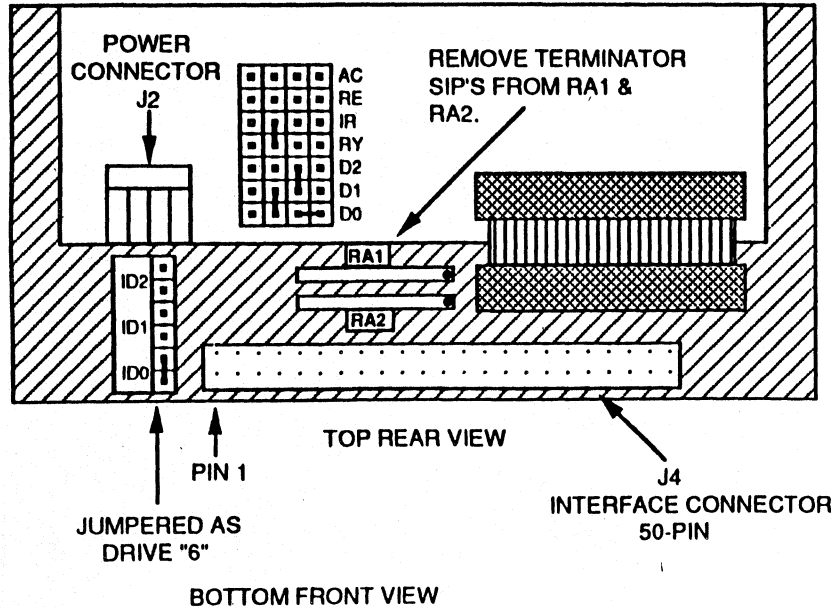
BLANK FLOPPY 99NW9809A33 66431206 3.5" 135 TPI
UNFORMATTED TOSHIBA PART # PMF-2ED



NOTE 1: SAME CONFIGURATION FOR SYS3200, 3400 & 8400's.

NOTE 2: USED IN THE FOLLOWING KITS: MVME884F-3, MVME884K-3,
MVME884F-4, MVME884K-4, MVME884F-5, MVME884K-5,
MVME884F-6 & MVME884K-6.

NOTE 3 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE
AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF
WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED.
(INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER
BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A
TRANSITION BOARD.)

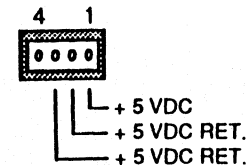


SCSI ADDRESS	CHAN 0	CHAN 1
FLOPPY 6	30	70
7	38	78

SCSI ID ADDRESS	ID2	ID1	ID0
SELECT 0	0	0	0
SELECT 1	0	0	1
SELECT 2	0	1	0
SELECT 3	0	1	1
SELECT 4	1	0	0
SELECT 5	1	0	1
SELECT 6	1	1	0
SELECT 7	1	1	1

NOMENCLATURE LIST:

- AC - OFF STATE AUTO CHUCKING
- D0-2 - DRIVE SELECT FOR DAISY CHAINING DRIVES
- ID0-3 - SDSI ID ADDRESS SELECT
- IR - DRIVE INDICATOR READY
- RE - AUTO RECALIBRATION
- RY - READY
- MON - MOTOR ON
- PAR - PARITY ENABLE
- STL - SET HEAD LOAD TIMING (NOT USED)
- LEV - DENSITY LEVEL ENABLE (USED W/ G, H & J)
- F - 1 MB DRIVE CAPACITY
- G - 1.6 MB DRIVE CAPACITY
- H - 2 MB DRIVE CAPACITY
- J - 4 MB DRIVE CAPACITY
- EJC - MEDIA EJECT



**2.9 MB TEAC SCSI
AKA 1-4 MB
FLOPPY 3 1/2"
1/2 HEIGHT DRIVE
PAGE 9B**

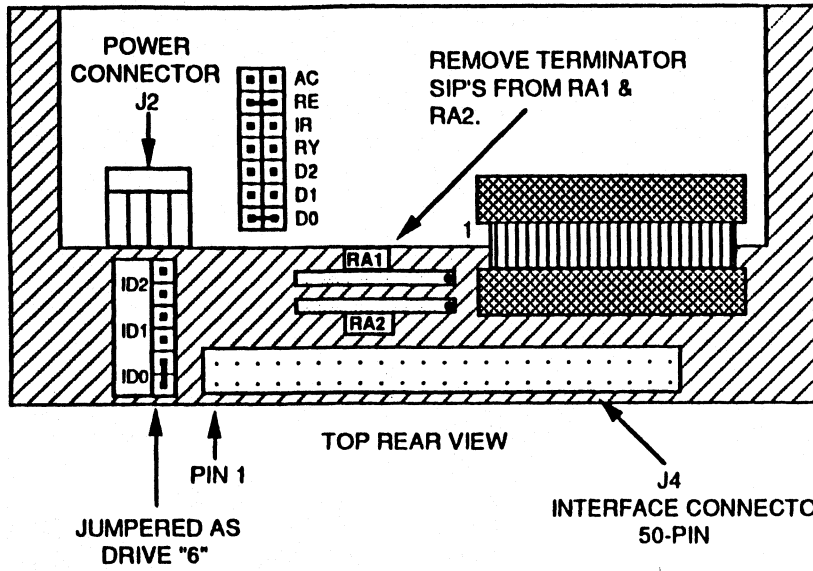
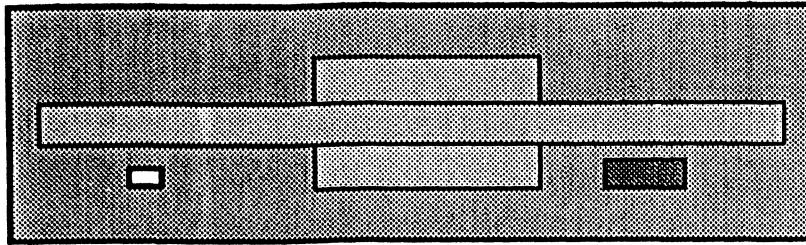
11/08/91

PART NUMBERS:

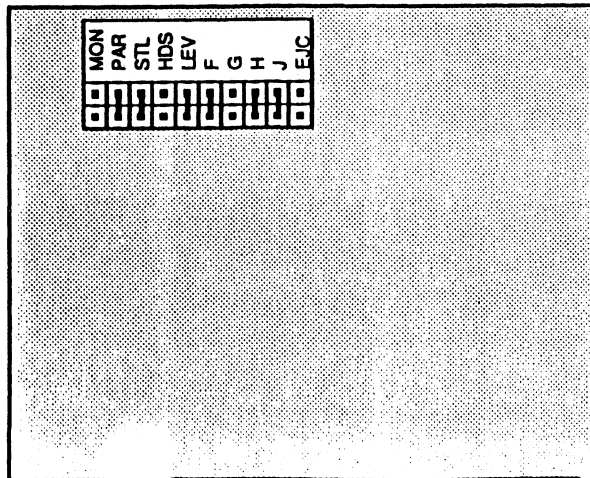
01-W2713C01 TBD TEAC FD-235J W/O 5 1/4" BRACKET
F/W REV. UNKNOWN

01-W2713C02 TBD TEAC FD-235J W/ 5 1/4" BRACKET

BLANK FLOPPY 99NW9809A33 66431206 3.5" 135 TPI
UNFORMATTED TOSHIBA PART # PMF-2ED



BOTTOM FRONT VIEW



NOTE 1 : WHERE USED AT THIS TIME IS UNKNOWN.

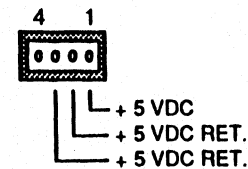
NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

SCSI ADDRESS	CHAN 0	CHAN 1
FLOPPY 6	30	70
	MVME327A	

NOMENCLATURE LIST:

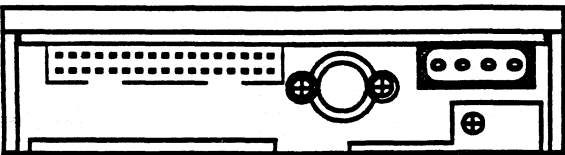
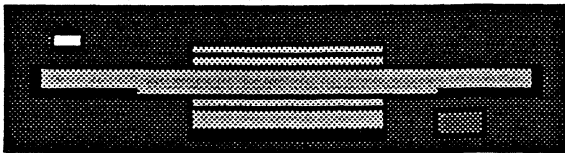
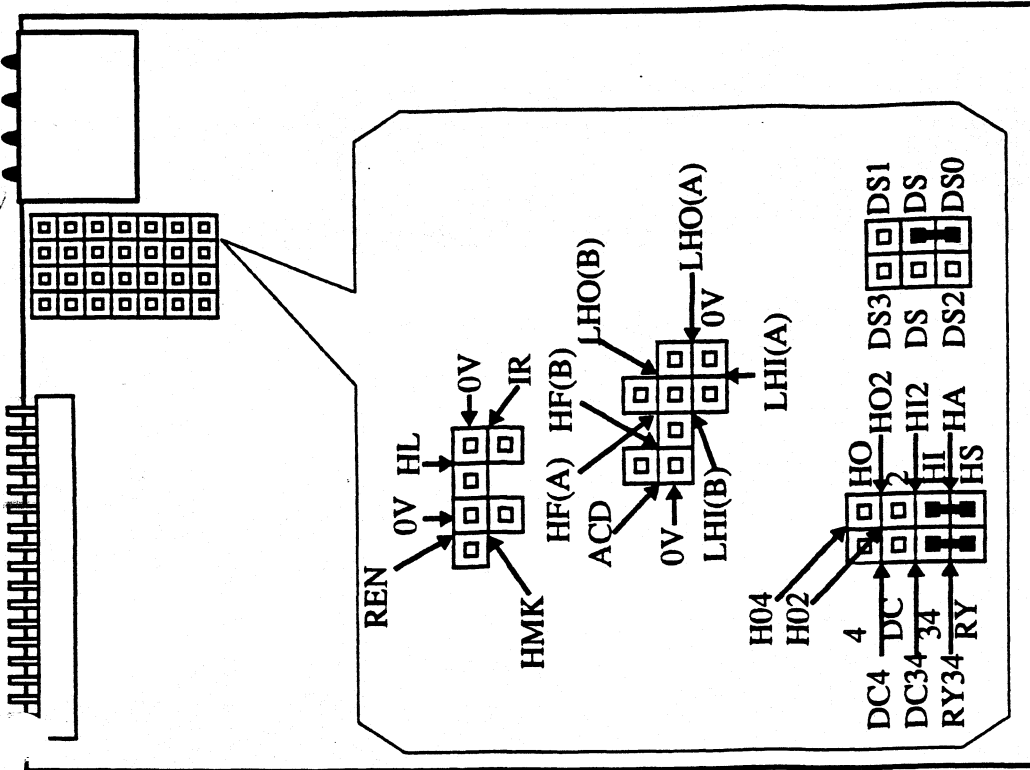
- AC - OFF STATE AUTO CHUCKING
- D0-2 - DRIVE SELECT FOR DAISY CHAINING DRIVES
- ID0-3 - SCSI ID ADDRESS SELECT
- IR - DRIVE INDICATOR READY
- RE - AUTO RECALIBRATION
- RY - READY
- MON - MOTOR ON
- PAR - PARITY ENABLE
- STL - SET HEAD LOAD TIMING (NOT USED)
- LEV - DENSITY LEVEL ENABLE (USED W/ G, H & J)
- F - 1 MB DRIVE CAPACITY
- G - 1.6 MB DRIVE CAPACITY
- H - 2 MB DRIVE CAPACITY
- J - 4 MB DRIVE CAPACITY
- EJC - MEDIA EJECT

SCSI ID ADDRESS	ID2	ID1	ID0
SELECT 0	0	0	0
SELECT 1	0	0	1
SELECT 2	0	1	0
SELECT 3	0	1	1
SELECT 4	1	0	0
SELECT 5	1	0	1
SELECT 6	1	1	0
SELECT 7		1	1



2.9 MB TEAC
AKA 1-4 MB
FLOPPY 3 1/2"
1/2 HEIGHT DRIVE
PAC 10

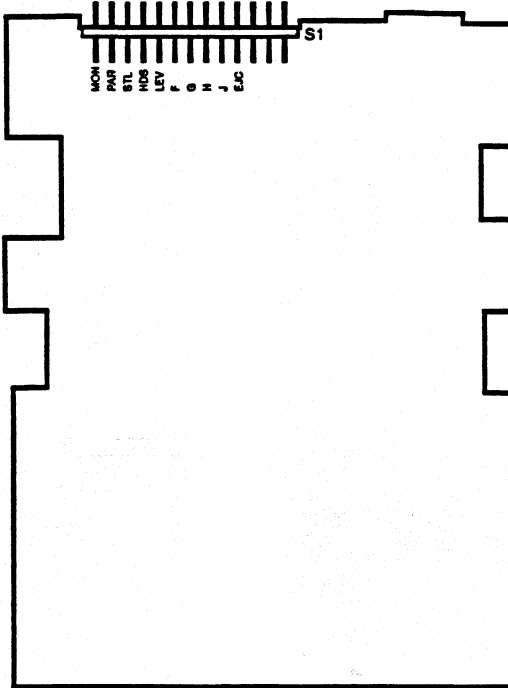
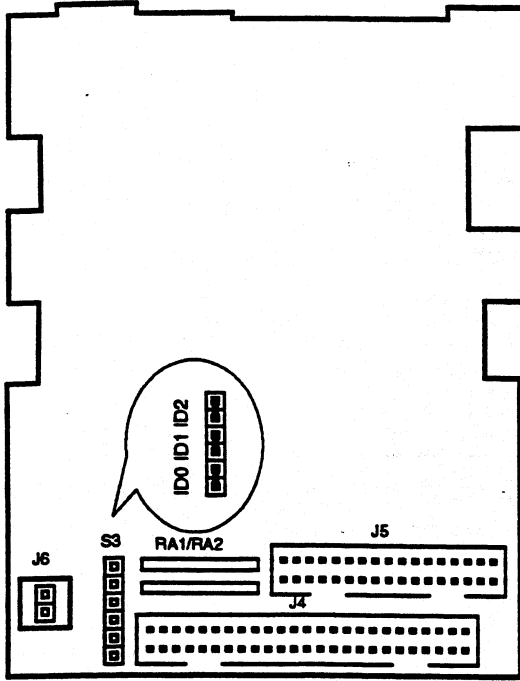
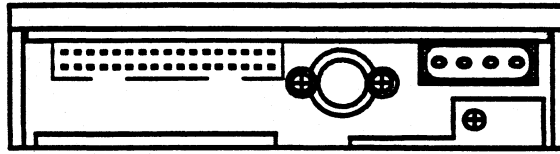
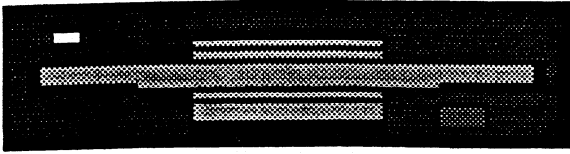
03/15/91



NOTE 1 : WITH VERSAdos, FD92 = DS2; FD93 = DS3.
THIS IS FOR USE WITH THE VME320B-1
CONTROLLER ALSO JUMPER FG, REN,
HF(A), HI2, & RY34.



STRAP	FUNCTION
DS0	DRIVE SELECT 0 input on Pin 10
DS1	DRIVE SELECT1 input on Pin 12
DS2	DRIVE SELECT2 input on Pin 14
DS3	DRIVE SELECT3 input on Pin 6
*RY34	READY output on Pin 34
*DC34	DISK CHANGE output on Pin 34
*DC2	DISK CHANGE output on Pin 2
*DC4	DISK CHANGE output on Pin 4
*HA	Density set automatically
*HI2	Density set by HD IN on Pin 2
*HO2	HD OUT output set on Pin 2
*HO4	HD OUT output set on Pin 4
*LHI	HD IN invert, LOW : 2MB mode
*LH0	HD OUT invert, LOW : HD disk
*HNK	Half mask for INDEX/READY DATA
*NKM	No mask for NDEX/READY DATA
*IR	LED on : DRIVE SELECT * Ready
*ML	Motor on : MOTOR ON + LED on
*ACD	Disable for auto-chucking
*REN	Enable for auto-reaclibration
*HF	2MB/1MB dual modes
*FG	Short between FDD frame and DC 0V

TEAC FD-235HF-3240, 3 1/2" SCSI Floppy Disk Drive



SCSI ID ADDRESS	ID2	ID1	ID0
0	ON	ON	ON
1	ON	ON	OFF
2	ON	OFF	ON
3	ON	OFF	OFF
4	OFF	ON	ON
5	OFF	ON	OFF
6	OFF	OFF	ON
7	OFF	OFF	OFF

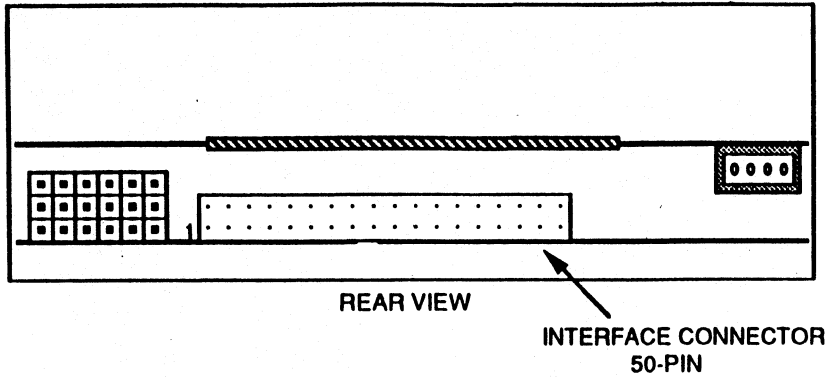
**J6
POWER CONNECTOR**

- 1  + 5 VDC
- 2  + 5 VDC RET

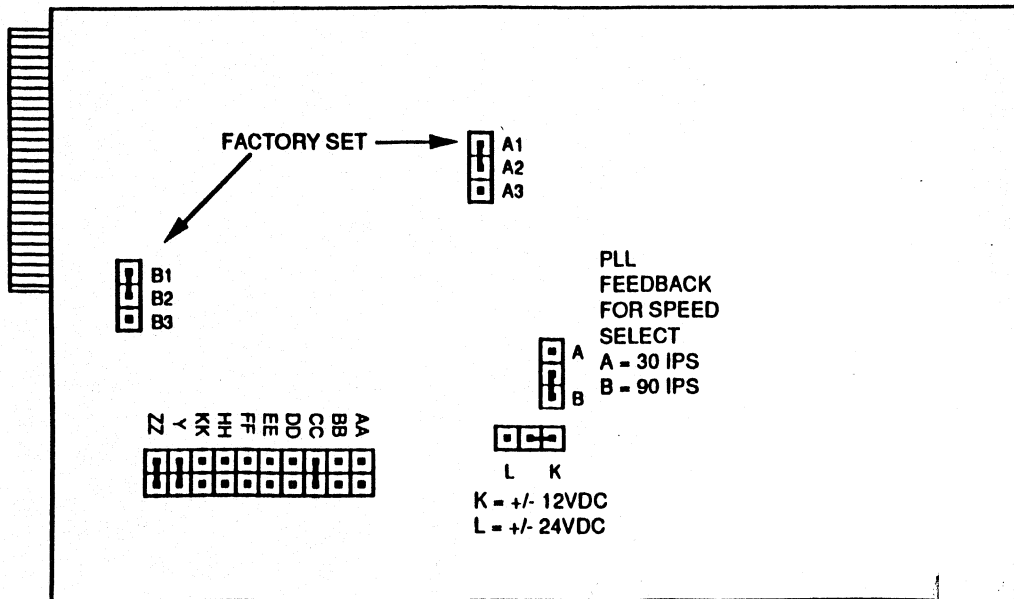
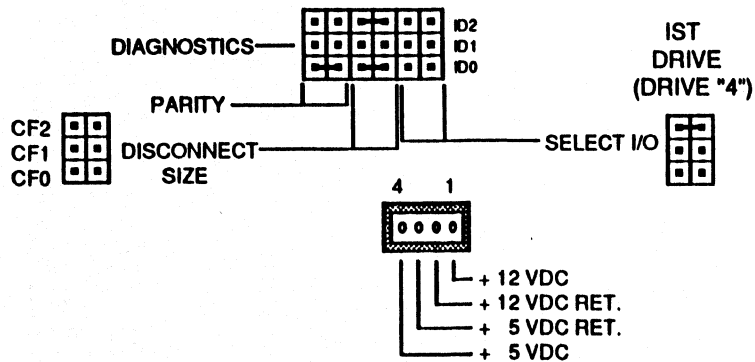
MODEL #	TYPE	DRIVE SPECIFICATIONS
FD-55/235F	F	DATA CAPACITY 1 MB
FD-55/235H	H	DATA CAPACITY 2 MB
FD-55/235G	G	DATA CAPACITY 1.6 MB
FD-55/235HF	HF	DATA CAPACITY 2/1 MB
FD-55/235GF	GH	DATA CAPACITY 1.6/1 MB
FD-135JFN/ 235J	JHF (JGH)	DATA CAPACITY 4/2/1/ MB (OR 4/1.6/1 MB)

FUJITSU FD-55/235F, G, H J SERIES.

APPENDIX C



60 MByte QIC-02 STREAMING TAPE DRIVES



PART NUMBERS:

60 MB 01-W2597B04 76435247 MODEL 5945-L2
FULL HEIGHT 5 1/4 " STREAMING TAPE

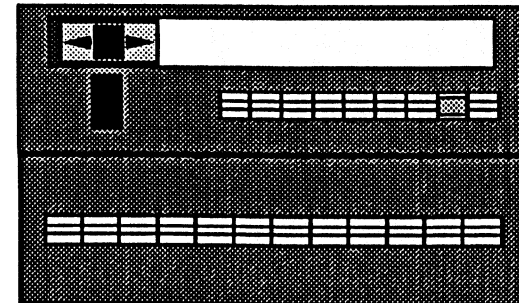
NOTE 1: QIC-02 TAPE DRIVES HAVE NO JUMPERS
INSTALLED IN LEFT MOST JUMPER BLOCK.

NOTE 2: A1/A2/A3, B1/B2/B3 ARE SET AT ARCHIVE.

NOTE 3: AA/BB = UNIT SELECT. BOTH JUMPERS OUT.
CC = FORMAT SELECT. IN = QIC-24, OUT = QIC-11.
DD = μ PROCESSOR SPEED IN = 30 IPS, OUT = 90 IPS.
EE = PARITY. IN = ENABLED, OUT = DISABLED.
FF = LOOP ON ERROR IS OUT. ARCHIVE SET.
HH = TEST CONFIGURATION. ARCHIVE SET.
KK = POWER-ON CONFIG. TEST. ARCHIVE SET.
Y = TRACK SELECT. IN = 9-TRACK, OUT = 4-TRACK.
ZZ = APPS. PROGRAM FOR EXTERNAL MEMORY IN

NOTE 4: USED IN THE FOLLOWING ASSEMBLIES: MVME834,
MVME835F-5, MVME835K-5, MVME836F-5,
MVME836K-5, MVME851F-3, MVME851K-3 &
MVME852FQ-6.

09/14/90



60 MByte QIC-02
ARCHIVE
STREAMING TAPE
PART 1

PART NUMBERS:

60MB 01-W2159C0; 96011036 2060S 22100-027

VENDOR IS ARCHIVE. 5 1/4" HALF-HEIGHT STREAMING TAPE DRIVES.

21116-007 IS ACCEPTABLE FIRMWARE

99NW9809A20 BLANK CARTRIDGE, 600', ARCHIVE P/N 80191-001

99NW9809A21 BLANK CARTRIDGE, 450', 3M P/N DC300XL/P

99NW9809A22 BLANK CARTRIDGE, (MOST COMMONLY USED ONE)
600', FSD P's 88800017 AND 96020321
600', ARCHIVE P/N 20975-001, DEI P/N 600H,
DYSAN P/N 815098-01, 3M P/N DC600A,
AND PHASE P/N 41361-01.

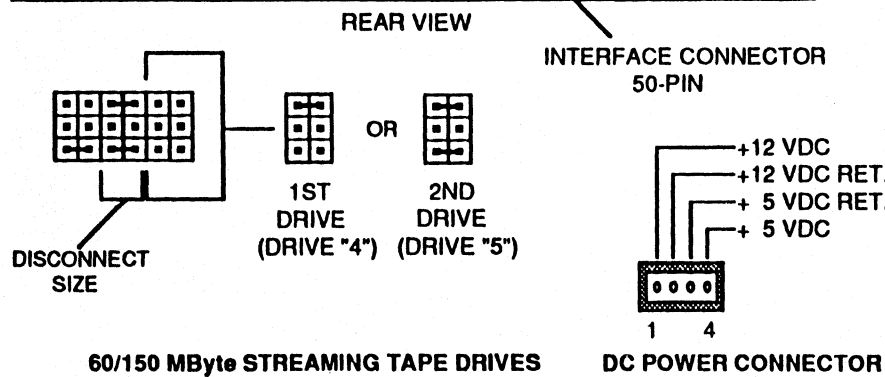
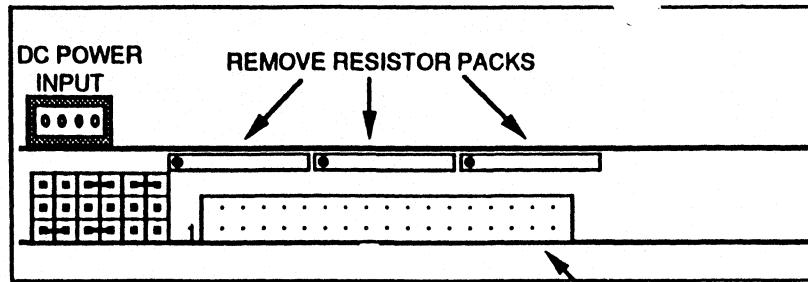
99NW9809A23 BLANK CARTRIDGE, 450', ARCHIVE P/N 20121-001
AND DYSAN P/N 815055.

99NW9809A24 BLANK CARTRIDGE, (USE A22 AS REPLACEMENT)
450', 3M P/N SDC450.

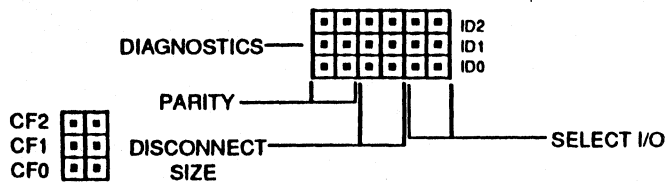
99NW9809A25 BLANK CARTRIDGE, 150', 3M P/N DC615A

150 MB BLANK CARTRIDGE, VENDOR P/N 420862 FSD P/N 96020346

03/15/91



60/150 MByte STREAMING TAPE DRIVES



NOTE 1: PARITY IS FACTORY CONFIGURED AT ARCHIVE UNIX IS NOT AFFECTED BY ITS INSTALLATION. IN OR OUT MAKES NO DIFFERENCE.

NOTE 2: DIAGNOSTICS IS NOT USED BY MOTOROLA.

NOTE 3: DISCONNECT SIZE SETS BYTE TRANSFERS. IT IS SET FOR 5 HEX (16KBYES).

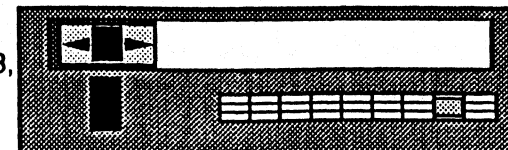
NOTE 4: INSTALL TERMINATORS ON END OF CHAIN DRIVE.

NOTE 5: SAME CONFIGURATION FOR SYS1147, 3200, 3400, 3604/08, 3640, 8400 & 8608's.

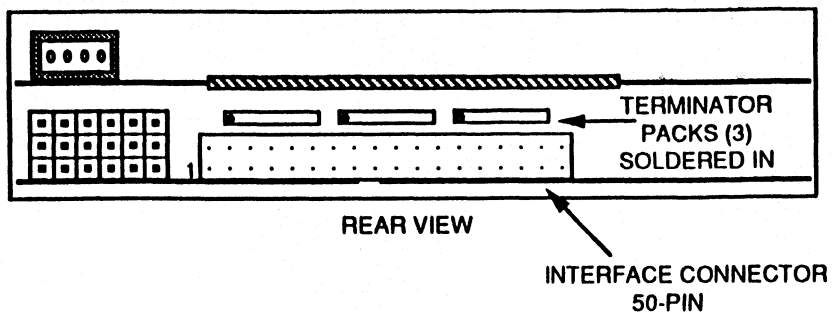
NOTE 6: USED IN THE FOLLOWING ASSEMBLIES: MVME852F-3, MVME852K-3, MVME852F-4, MVME852K-4, MVME852F-5, MVME852K-5, MVME852F-6, MVME852K-6, MVME852F-8 & MVME852K-8.

SCSI ADDRESS	CHAN 0	CHAN 1
TAPE 4	20	60
5	28	68
MVME327A		

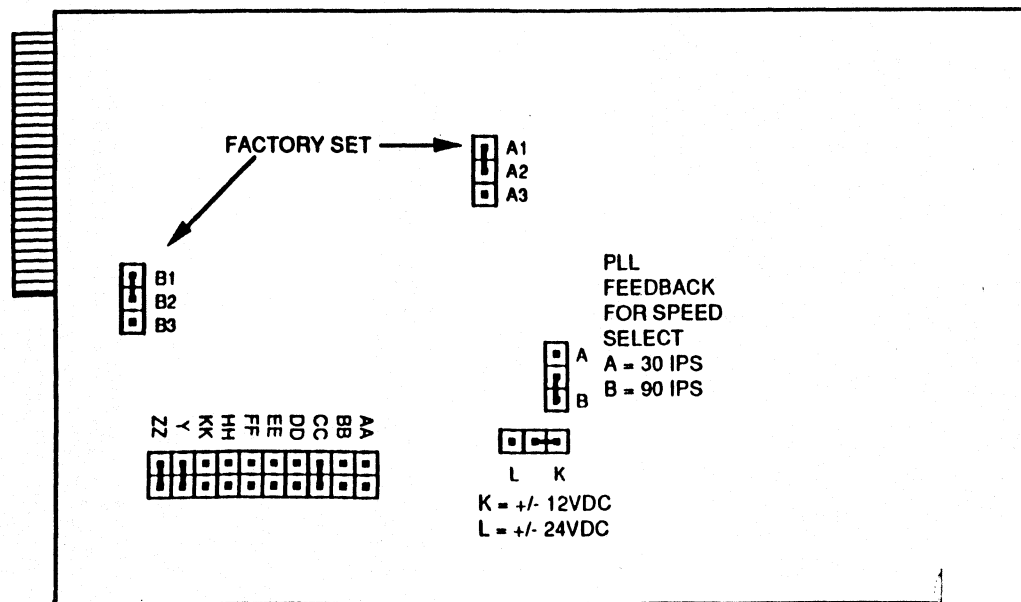
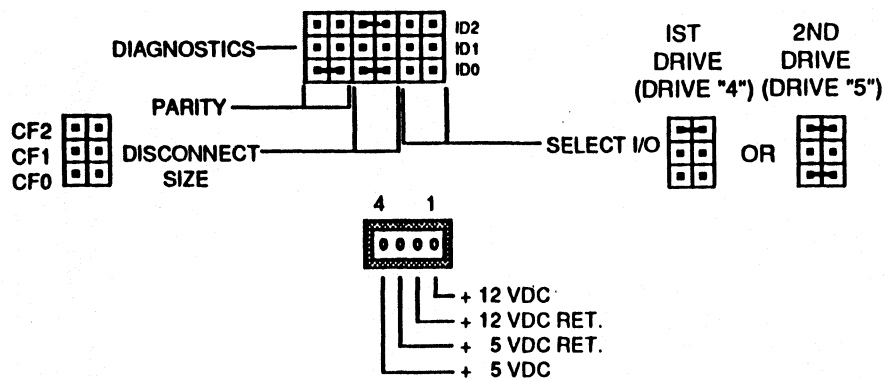
NOTE 7 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)



60 MByte
ARCHIVE SCSI
STREAMING TAPES
PAGE 2



150 MByte QIC-02 STREAMING TAPE DRIVES



PART NUMBERS:

150MB 01-W2168C01 76435589 MODEL 2150L HALF HEIGHT PART NUMBER 21241-001 OR 22200-039

NOTE 1: QIC-02 TAPE DRIVES HAVE NO JUMPERS INSTALLED IN LEFT MOST JUMPER BLOCK.

NOTE 2: A1/A2/A3, B1/B2/B3 ARE SET AT ARCHIVE.

NOTE 3: AA/BB = UNIT SELECT. BOTH JUMPERS OUT.
 CC = FORMAT SELECT. IN = QIC-24, OUT = QIC-11.
 DD = μ PROCESSOR SPEED IN = 30 IPS, OUT = 90 IPS.
 EE = PARITY. IN = ENABLED, OUT = DISABLED.
 FF = LOOP ON ERROR IS OUT. ARCHIVE SET.
 HH = TEST CONFIGURATION. ARCHIVE SET.
 KK = POWER-ON CONFIG. TEST. ARCHIVE SET.
 Y = TRACK SELECT. IN = 9-TRACK, OUT = 4-TRACK.
 ZZ = APPS. PROGRAM FOR EXTERNAL MEMORY IN

NOTE 4: SAME CONFIGURATION USED FOR SYS8608's.

NOTE 5: USED IN THE FOLLOWING ASSEMBLIES: MVME853FQ-6, MVME853KQ-6, MVME853FQ-8 & MVME853KQ-8.

NOTE 6: 21248-005 IS ACCEPTABLE FIRMWARE.



**150 MByte QIC-02
 ARCHIVE
 STREAMING TAPE
 P/ 3**

09/14/90

PART NUMBERS:

150MB 01-W2013C01 96011002 2150S 21249-603/22300-102

VENDOR IS ARCHIVE. 5 1/4" HALF-HEIGHT STREAMING TAPE DRIVES.

21247-005 IS ACCEPTABLE FIRMWARE

NOTE 1: PARITY IS FACTORY CONFIGURED AT ARCHIVE
UNIX IS NOT AFFECTED BY ITS INSTALLATION.
IN OR OUT MAKES NO DIFFERENCE.

NOTE 2: DIAGNOSTICS IS NOT USED BY MOTOROLA.

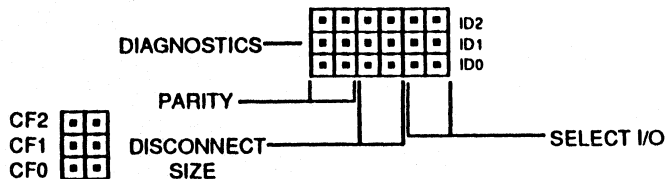
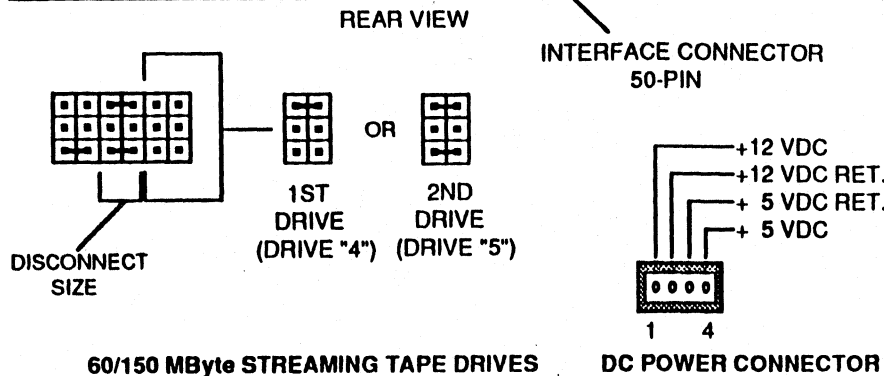
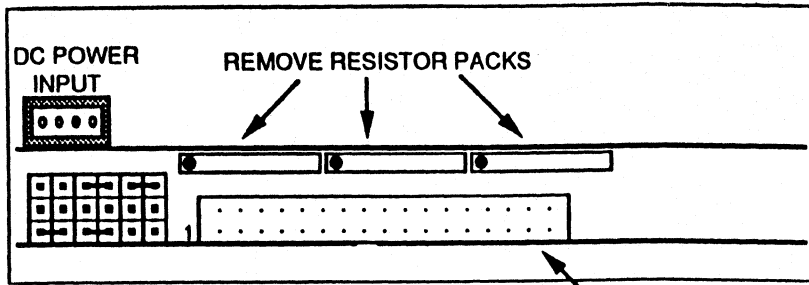
NOTE 3: DISCONNECT SIZE SETS BYTE TRANSFERS. IT
IS SET FOR 5 HEX (16KBYTES).

NOTE 4: INSTALL TERMINATORS ON END OF CHAIN DRIVE.

NOTE 5: SAME CONFIGURATION FOR SYS1147, 3200, 3400, 3604/08, 3640,
3708, 8400 & 8608's.

NOTE 6: USED IN THE FOLLOWING ASSEMBLIES: MVME853F-3, MVME853K-3,
MVME853F-4, MVME853K-4, MVME853F-5, MVME853K-5, MVME853F-6,
MVME853K-6, MVME853F-7, MVME853K-7, MVME853F-8, MVME853K-8,
MVME873FTA-5, MVME873KTA-5, MVME975FTA-5, MVME875KTA-5,
MVME876FTA-5 & MVME876KTA-5.

NOTE 7: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE
AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF
WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED.
(INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER
BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A
TRANSITION BOARD.)



99NW9809A20 BLANK CARTRIDGE, 600', ARCHIVE P/N 80191-001

99NW9809A21 BLANK CARTRIDGE, 450', 3M P/N DC300XL/P

99NW9809A22 BLANK CARTRIDGE, (MOST COMMONLY USED ONE)
600', FSD P's 88800017 AND 96020321
600', ARCHIVE P/N 20975-001, DEI P/N 600H,
DYSAN P/N 815098-01, 3M P/N DC600A,
AND PHASE P/N 41361-01.

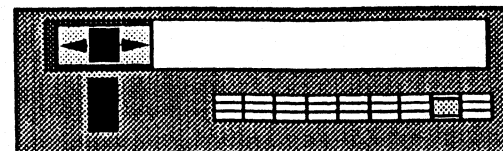
99NW9809A23 BLANK CARTRIDGE, 450', ARCHIVE P/N 20121-001
AND DYSAN P/N 815055.

99NW9809A24 BLANK CARTRIDGE, (USE A22 AS REPLACEMENT)
450', 3M P/N SDC450.

99NW9809A25 BLANK CARTRIDGE, 150', 3M P/N DC615A

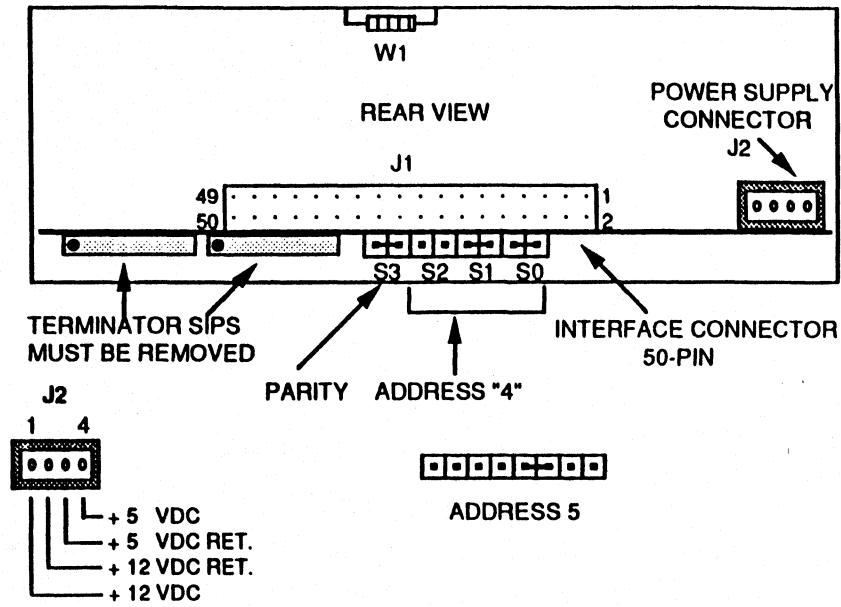
150 MB BLANK CARTRIDGE, VENDOR P/N 420862 FSD P/N 96020346

SCSI ADDRESS		CHAN 0	CHAN 1
TAPE	4	20	60
	5	28	68
		MVME327A	



150 MByte
ARCHIVE SCSI
STREAMING TAPES
PAGE 4

04/16/91



NOTE 1: WITH TEAC TAPE, REMOVE JUMPER RESISTOR W1 ON PCBA DC.

NOTE 2 : SAME CONFIGURATION USED FOR SYS1147, 3200, 3400 & 8400's.

NOTE 3 : MOTOROLA WILL REMOVE JUMPER RESISTOR "W1" ON PCBA MD TO ISOLATE SIGNAL AND CHASSIS GROUND.

NOTE 4 : USED IN THE FOLLOWING ASSEMBLIES:
 (C01) 01-W2351C01, & 01-W2351C02
 (C02) MVME855F-3, MVME855K-3, MVME855F-4, MVME855K-4, MVME855F-5, MVME855K-5, MVME855F-6 & MVME855K-5.

NOTE 5 : MEDIA CAN ONLY BE WRITTEN & READ WHEN "THIS SIDE IN" IS UP.

NOTE 6 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

PART NUMBERS:

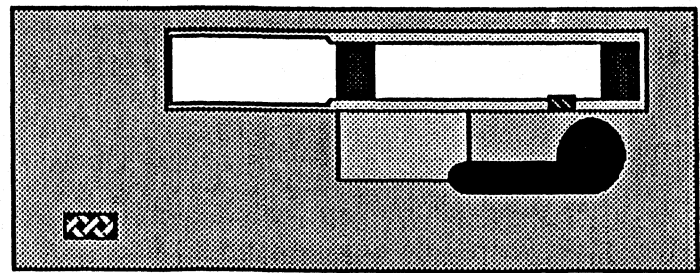
- 155MB 01-W2275C01 96011142
TEAC MODEL # MT-2ST/N50 PART # 19305070-00
3 1/2" SCSI STREAMING TAPE DRIVE W/O BRACKETS.
F/W "A" THROUGH "E" ARE ACCEPTABLE.
- 155MB 01-W2275C02 96011214
TEAC MODEL# TBD PART # TBD
3 1/2" SCSI STREAMING TAPE DRIVE W/ 5.25" BRACKETS.
SAME F/W AS C01 ABOVE.
- 99NW9809A32 66431207 BLANK CARTRIDGE, 155MB, 3 1/2"
MAXELL P/N CS-600XD, TEAC P/N CT600N

SCSI ADDRESS		CHAN 0	CHAN 1
TAPE	4	20	60
	5	28	68
		MVME327A	

SCSI ADDRESS SELECT

- ADDRESS 0
- ADDRESS 1
- ADDRESS 2
- ADDRESS 3
- ADDRESS 4
- ADDRESS 5
- ADDRESS 6

03/15/91

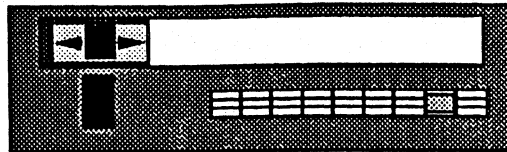
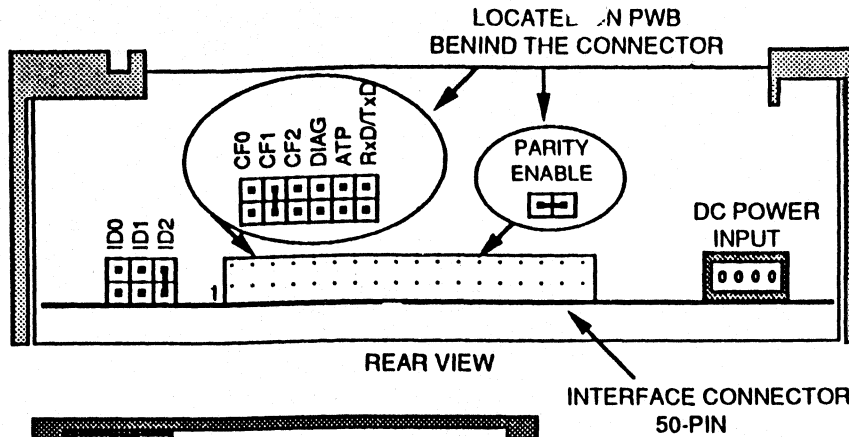


**155Mbyte
TEAC SCSI 3 1/2"
STREAMING TAPE DRIVE
PAGE 5**

PART NUMBERS:

525MB 01-W2024D01 TBD 2525S 22502-010

ARCHIVE 5 1/4" HALF-HEIGHT STREAMING TAPE DRIVE.
ACCEPTABLE FIRMWARE 007



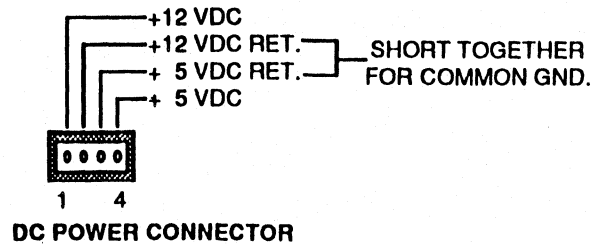
SCSI ADDRESS		CHAN 0	CHAN 1
TAPE	4	20	60
	5	28	68
MVME327A			

NOTE 1: INSTALL TERMINATORS ON END OF CHAIN DRIVE.

NOTE 2: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 3: +5VDC RETURN AND +12VDC RETURN ARE SHORTED TOGETHER FOR COMMON GROUND.

NOTE 4: USED IN MVME854(X) ASSEMBLIES.



11/19/91

PART NUMBERS:

2GB TAPE CARTRIDGE 01-W2272C01 96011089
 EXABYTE MODEL EXB-8200
 F/W REVISION: 2600

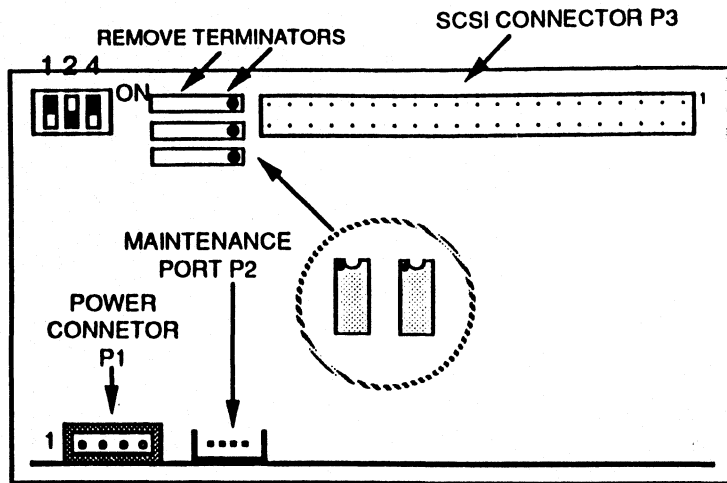
99NW9809A31 66431200 BLANK CARTRIDGE, 8MM,
 2048 MB (2.3 GB) EXABYTE P/N 180104

NOTE 1: INSTALL TERMINATORS ON END OF CHAIN DRIVE.

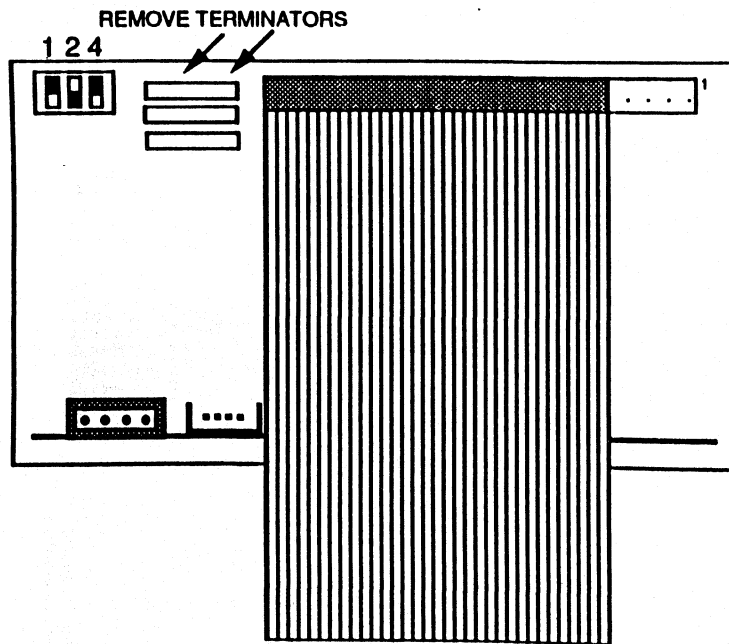
NOTE 2: USED IN SYS3200, 3400, 3708 & 8400's.

NOTE 3: USED IN THE FOLLOWING ASSEMBLIES: MVME856F-4,
 MVME856K-4, MVME856F-6, MVME856K-6, MVME856F-6,
 MVME856K-7, MVME856F-8 & MVME856K-8.

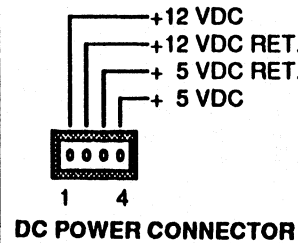
NOTE 4 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE
 CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION
 REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY
 CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE
 XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS
 ALWAYS GO THROUGH A TRANSITION BOARD.)



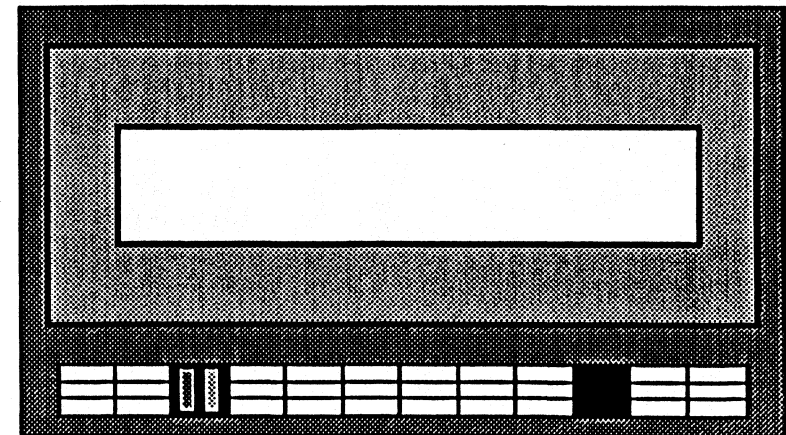
2GByte SCSI TAPE CARTRIDGE DRIVE



SCSI CABLE ADAPTER



SCSI ADDRESS	CHAN 0	CHAN 1
TAPE 4	20	60
5	28	68
MVME327A		



LED'S

UNLOAD SWITCH

**2.3 GByte
 EXABYTE
 SCSI TAPE
 CARTRIDGE DRIVE
 PAGE**

11/19/91

APPENDIX D

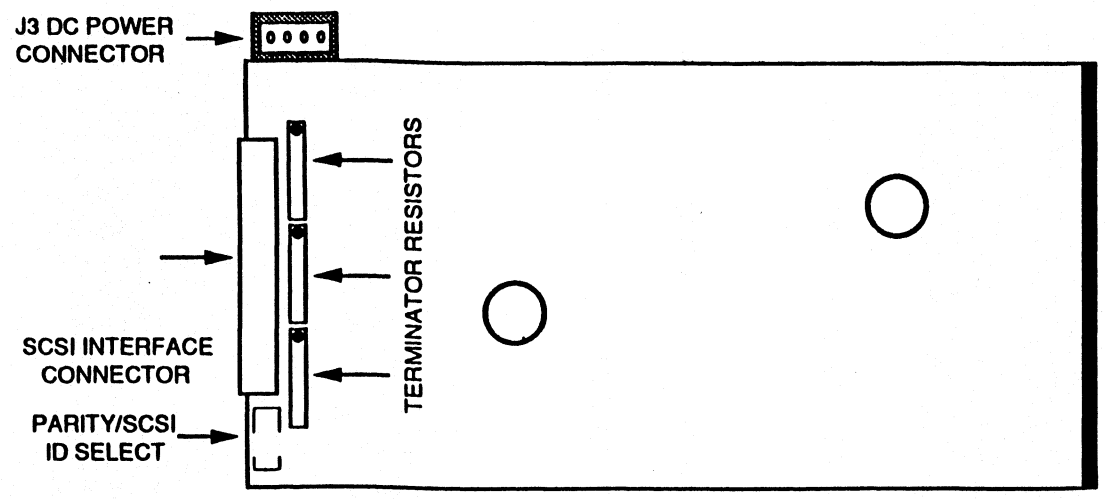
PART NUMBERS:

21MB 01-W2274C01 96011266 MODEL ST125N
W/ 5.25" MOUNTING BRACKETS

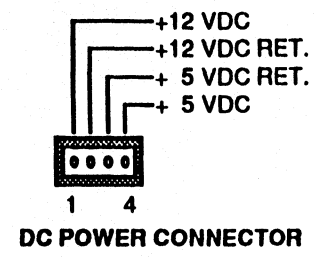
21MB 01-W2274C02 XXXXXXXX MODEL ST125N
W/O 5.25" MOUNTING BRACKETS

VENDOR IS SEAGATE. 3 1/2" HALF-HEIGHT SCSI DRIVE.
FIRMWARE REVISION 03026a

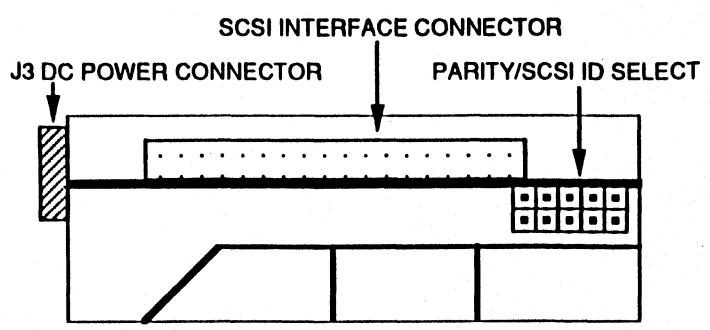
PARITY/SCSI ID SELECT
INSTALL "P" TO ENABLE PARITY



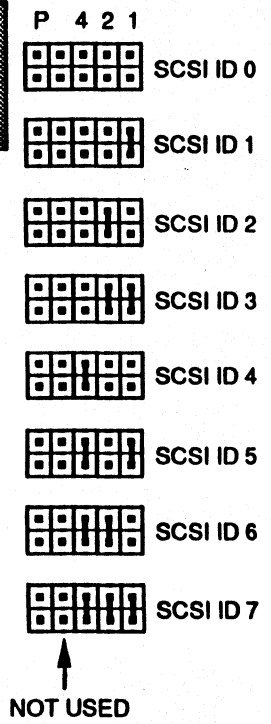
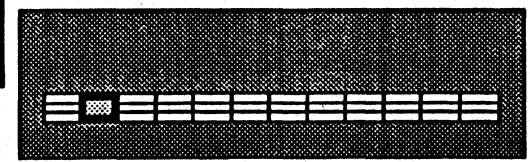
TOP VIEW



DC POWER CONNECTOR



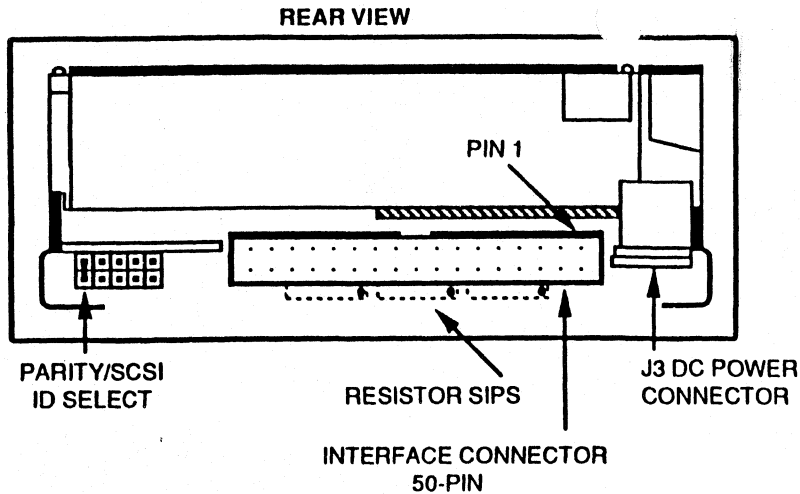
REAR VIEW



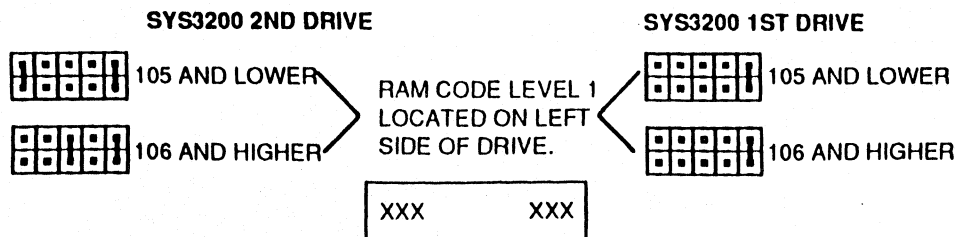
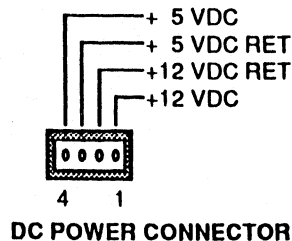
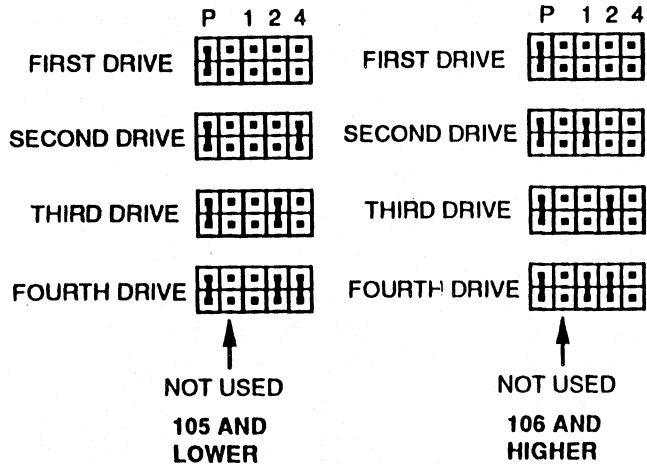
	SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
		MVME327A	

- NOTE 1: ST177N USES SCSI PROTOCOL TO OPERATE WITH MVME147 OR MVME327A VMEbus HOST ADAPTER.
- NOTE 2: NOT PRESENTLY USED IN ANY ASSEMBLY.
- NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

03/15/91



PARITY/SCSI ID SELECT
INSTALL "P" TO ENABLE PARITY



ART NUMBERS:

48MB 01-W2095C01 96011012 MODEL ST157N/M W/ 5.25" MOUNTING BRACKETS

48MB 01-W2095C02 96011140 MODEL ST157N/M W/O 5.25" MOUNTING BRACKETS
NEW DISKWARE 4846B

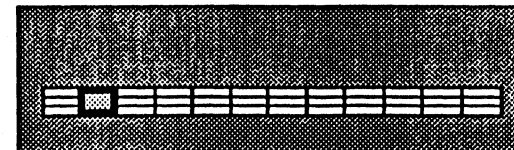
VENDOR IS SEAGATE. 5 1/4" HALF-HEIGHT WINI DRIVE.

NOTE 1: ST157N USES SCSI PROTOCOL TO OPERATE WITH MVME147 OR MVME327A VMEbus HOST ADAPTER.

NOTE 2: USED IN THE FOLLOWING ASSEMBLIES:
(C01) MVME872F-3 & MVME872K-3;
(C02) MVME862F-2 & MVME862K-2.

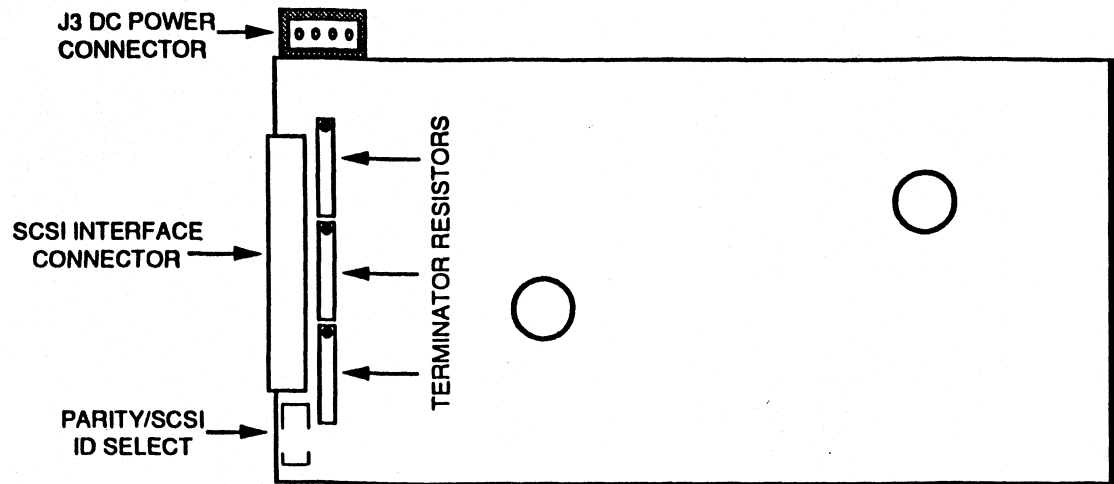
NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

03/15/91

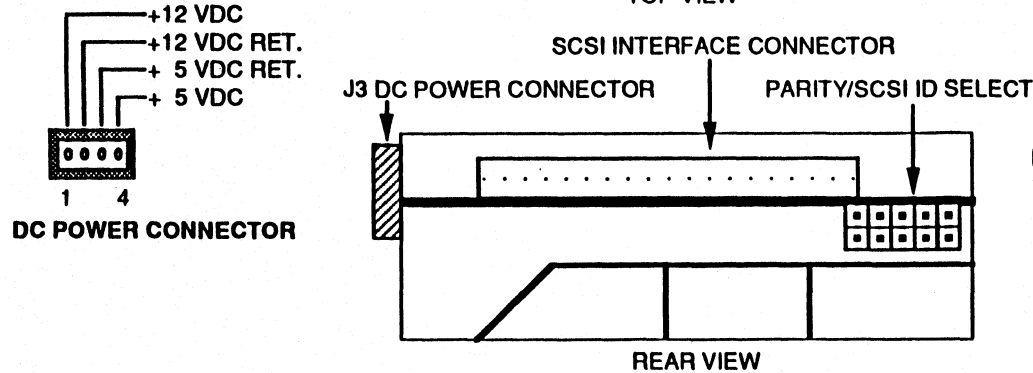


SCSI ADDRESS	CHAN 0	CHAN 1
DISK 0	00	40
1	08	48
2	10	50
3	18	58
TAPE 4	20	60
5	28	68
FLOPPY 6	30	70
MVME327A		

**48 MByte
SEAGATE
SCSI DRIVE
PAGE 2**



TOP VIEW



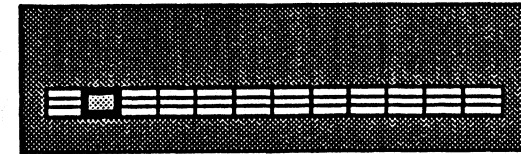
REAR VIEW

PART NUMBERS:

60MB 01-W2452C01 96011261 MODEL ST177N
W/ 5.25" MOUNTING BRACKETS

60 MB 01-W2452C02 XXXXXXXX MODEL ST177N
W/O 5.25" MOUNTING BRACKETS

VENDOR IS SEAGATE. 3 1/2" HALF-HEIGHT SCSI DRIVE.
F/W 03420a



PARITY/SCSI ID SELECT
INSTALL "P" TO ENABLE PARITY

- P 4 2 1
- SCSI ID 0
 - SCSI ID 1
 - SCSI ID 2
 - SCSI ID 3
 - SCSI ID 4
 - SCSI ID 5
 - SCSI ID 6
 - SCSI ID 7

↑
NOT USED

SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	00
	1	08
	2	10
	3	18
TAPE	4	20
	5	28
FLOPPY	6	30
		70
MVME327A		

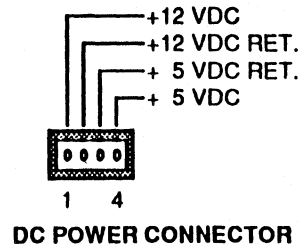
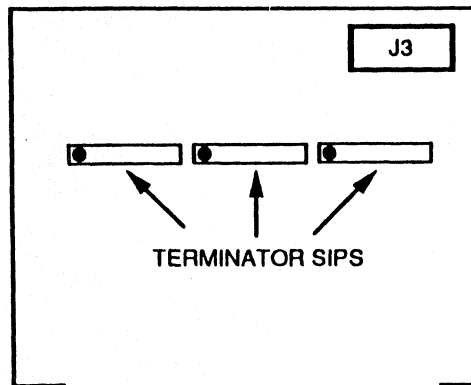
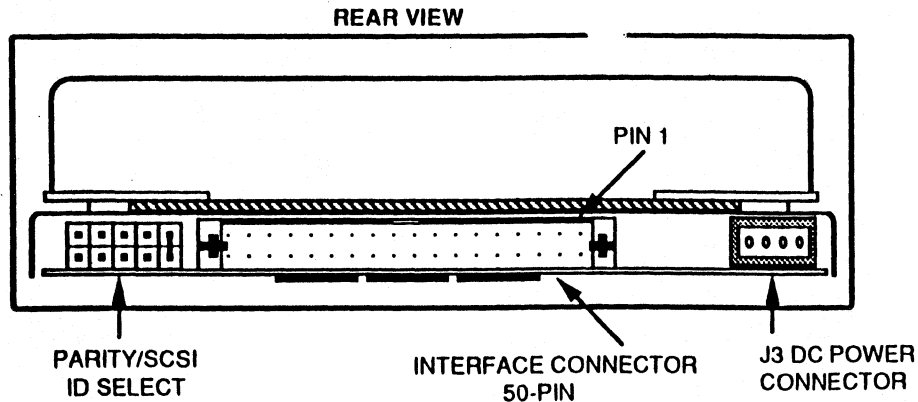
NOTE 1: ST177N USES SCSI PROTOCOL TO OPERATE WITH MVME147 OR MVME327A VMEbus HOST ADAPTER.

NOTE 2: NOT PRESENTLY USED IN ANY ASSEMBLY.

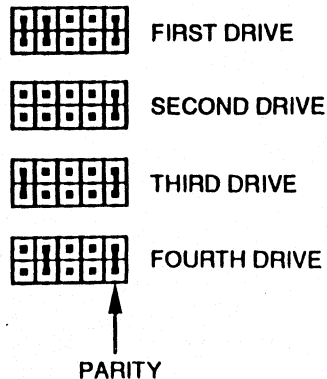
NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

03/15/91

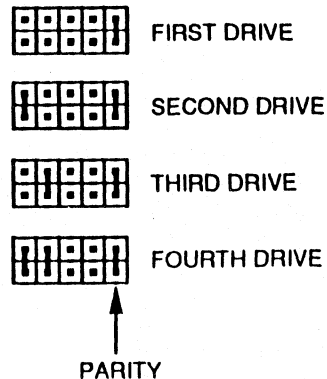
**60 MByte
SEAGATE
SCSI DRIVE**
E 3



PARITY/SCSI ID SELECT
INSTALL "P" TO ENABLE PARITY
(BEFORE AUGUST 27, 1990)



PARITY/SCSI ID SELECT
INSTALL "P" TO ENABLE PARITY
(AFTER AUGUST 27, 1990)



ART NUMBERS:

85MB 01-W2096C01 96011013/96010940 MODEL ST296N-M

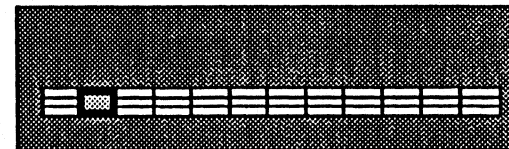
VENDOR IS SEAGATE. 5 1/4" HALF-HEIGHT TAPE DRIVE.

NOTE 1: SAME CONFIGURATION IS USED FOR SYS1147, 3304/08 & 3608's.

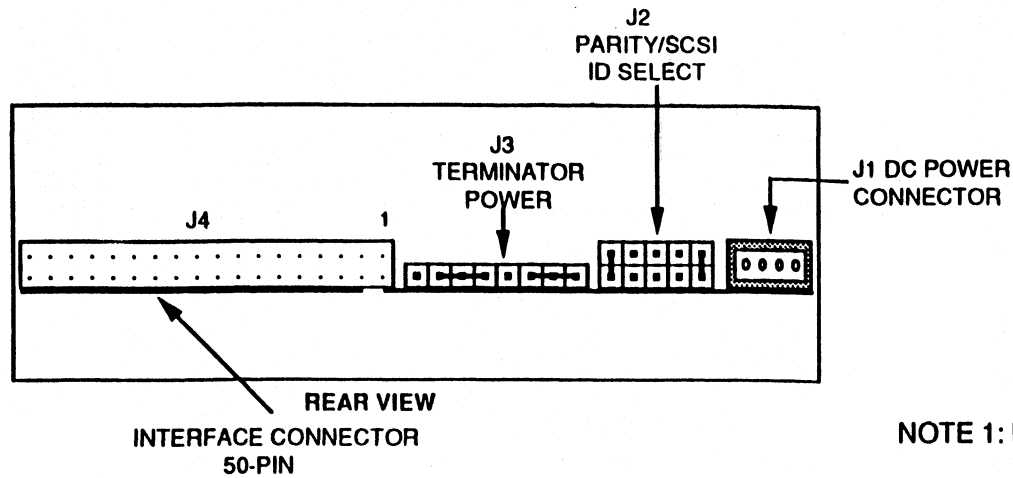
NOTE 2: USED IN THE FOLLOWING KITS: MVME873F-3, MVME873K-3, MVME873F-5, MVME873K-5, MVME873FTA-5, MVME873KTA-5, MVME873F-6 & MVME873K-6.

NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

03/15/91



SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	40
	1	48
	2	50
	3	58
TAPE	4	60
	5	68
FLOPPY 6	30	70
	MVME327A	



PART NUMBERS:

91MB 01-W2521C01 96011265/95600030 MODEL 94211-106

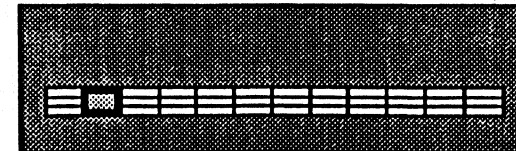
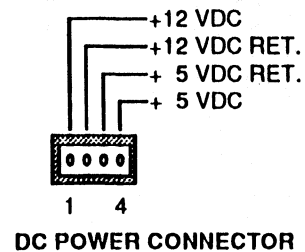
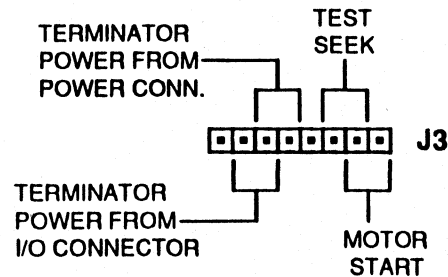
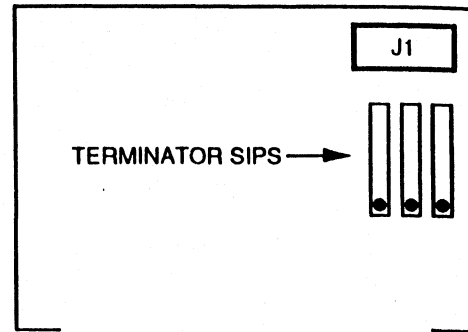
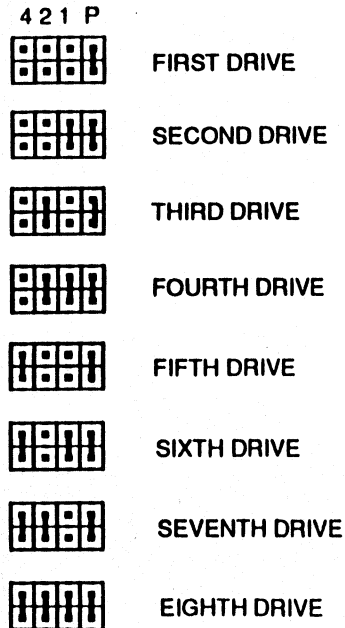
VENDOR IS CDC WREN III. 5 1/4" HALF-HEIGHT TAPE DRIVE.

NOTE 1: LEAVE TERMINATOR SIP'S IN FIRST DRIVE.
REMOVE FROM OTHER DRIVES.

NOTE 2: NOT PRESENTLY USED IN ANY ASSEMBLY.

NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF
THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION
REGARDLESS OF WHETHER IT'S INTERNALLY OR
EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS
COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL
CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION
BOARD.)

PARITY/SCSI ID SELECT
INSTALL "P" TO ENABLE PARITY



SCSI ADDRESS		CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
		MVME327A	

**91 MByte
SEAGATE
SCSI DRIVE
TYPE 5**

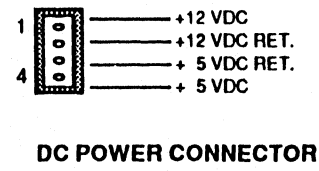
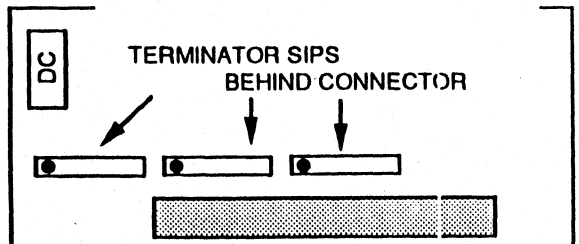
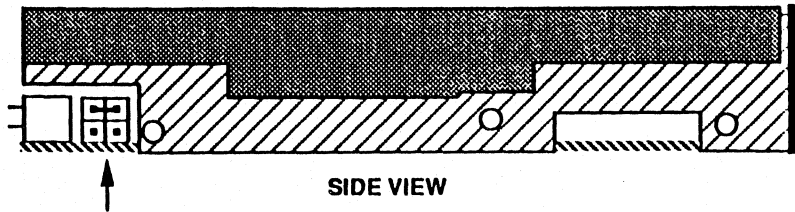
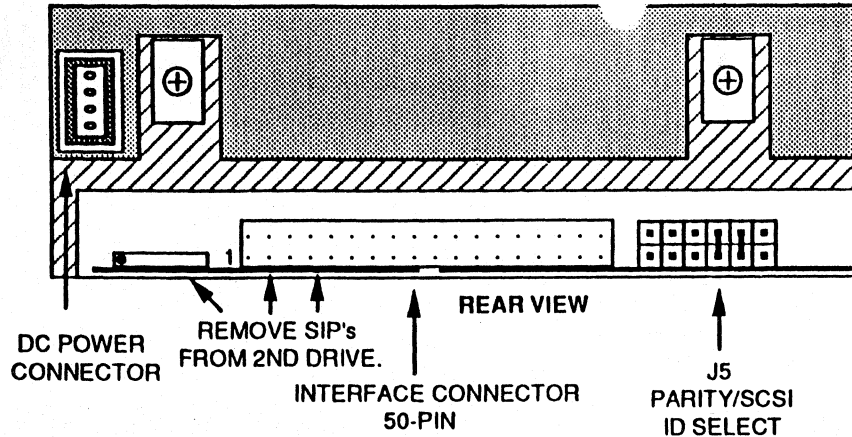
03/15/91

PART NUMBERS:

104MB 01-W2314C01 96011143 MODEL 94351-126 ST1126N
 W/O 5.25" MOUNTING BRACKETS
 P/N 75912155 F/W REQUIRED 9212.
 126MB UNFORMATTED, 104MB FORMATTED

VENDOR IS CDC 3 1/2" HALF-HEIGHT SCSI DRIVE.

104MB 01-W2314C11 96011264 MODEL 94351-126 ST1126N
 W/5.25" MOUNTING BRACKETS
 P/N TBD F/W SEE C01 ABOVE.

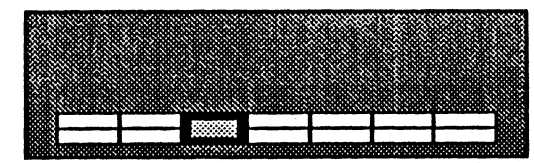
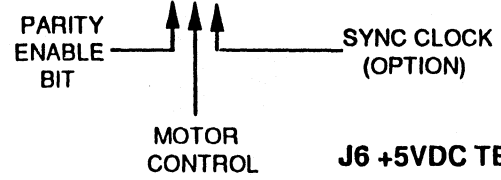
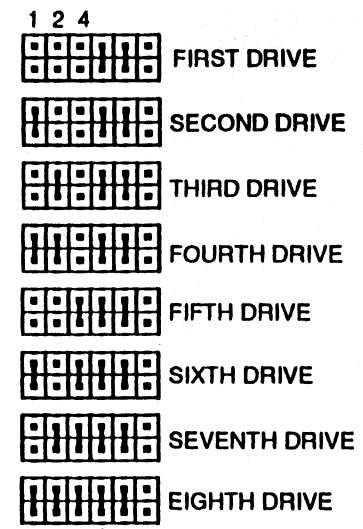


NOTE 1: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 2: SAME CONFIGURATION USED IN SYS3200, 3400 & 8400's.

NOTE 3: USED IN THE FOLLOWING ASSEMBLY: MVME863F-2 & MVME863K-2.

DRIVE/PARITY ENABLE SELECT



SCSI ADDRESS		CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
		MVME327A	

J6 +5VDC TERMINATOR POWER

- FROM POWER CONNNECTOR.
- FROM SCSI INTERFACE CABLE.
- SCSI INTERFACE CABLE. REMOVE TERMINATORS.
- POWER TO SCSI INTERFACE CABLE AND TEMINATORS.

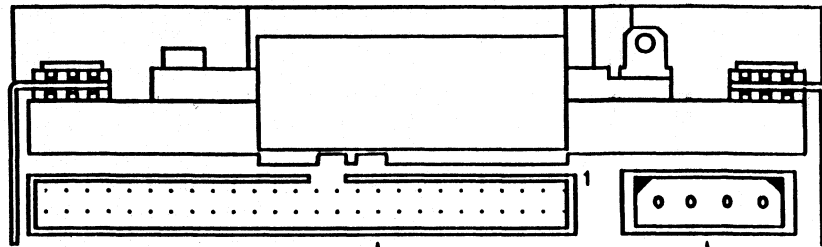
104 MByte
 CDC
 SCSI DRIVE
 PAGE 6

03/18/91

PART NUMBERS:

135MB 01-W2801C01 TBD MODEL M2613ESA P/N CG003336-001
 W/O 5.25" MOUNTING BRACKETS
 172MB UNFORMATTED F/W M606
 VENDOR IS FUJITSU 3 1/2" HALF-HEIGHT SCSI DRIVE.

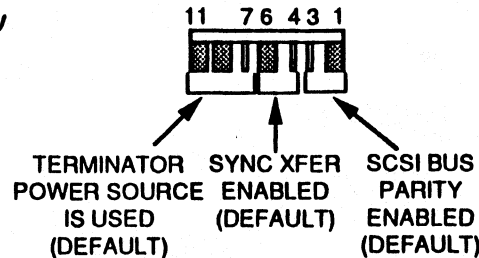
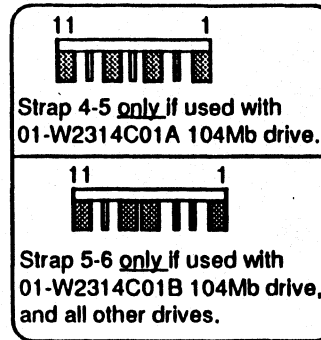
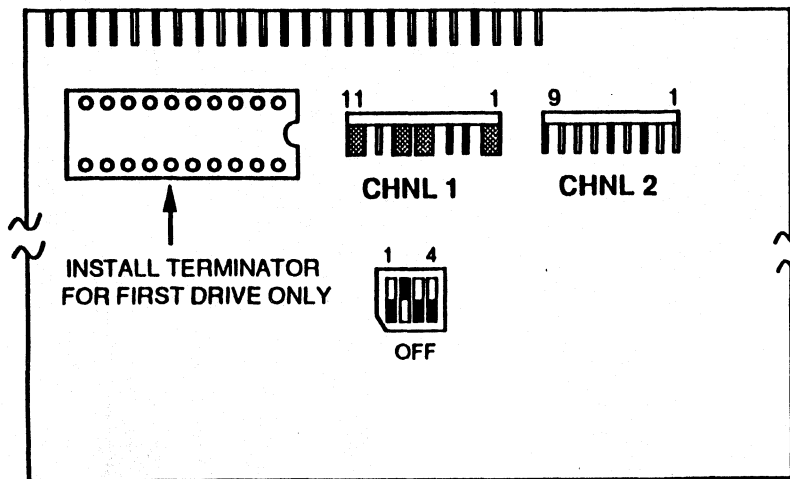
135MB 01-W2801C11 TBD MODEL M2613ESA P/N CG003363-002 FLUSH-MOUNT, W/5.25" MOUNTING BRACKETS, F/W M606
 MVME863A/864A
 W/ SYS3404/08/16's



REAR VIEW

INTERFACE CONNECTOR
50-PIN

DC POWER CONNECTOR



2-3 SCSI BUS PARITY DISABLED
 4-5 SYNCHRONOUS TRANSFER DISABLED
 7-8, 10-11 SCSI BUS TERMINATOR POWER SOURCE IS USED
 7-8, 9-10 SCSI BUS TERMINATOR POWER SOURCE IS USED

NOTE 1: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

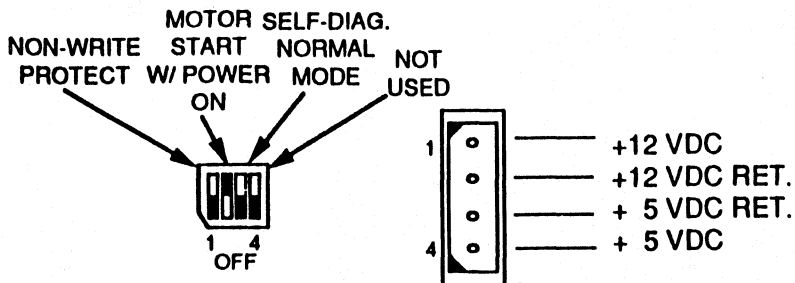
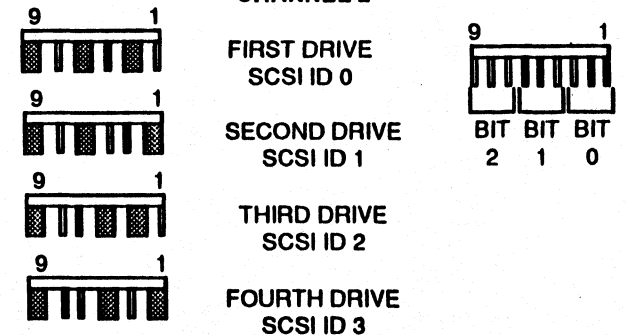
NOTE 2: SAME CONFIGURATION USED IN SYS3200, & 8400's.

NOTE 3: USED IN THE FOLLOWING ASSEMBLY: MVME863F-2 & MVME863K-2.

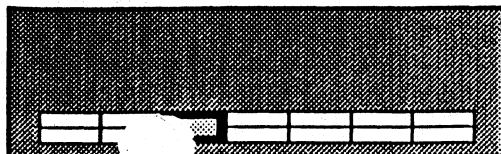
NOTE 4: S 00 REQUIRES MOTOR STARTSWITVH OFF(SW POS 2).

SCSI ADDRESS	CHAN 0	CHAN 1
DISK 0	00	40
1	08	48
2	10	50
3	18	58
MVME327A		

DRIVE ENABLE SELECT CHANNEL 2



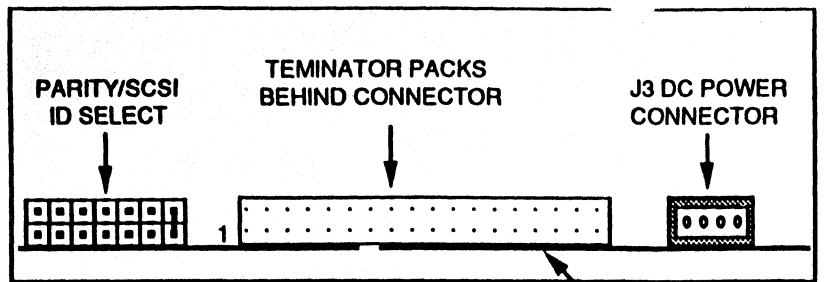
DC POWER CONNECTOR



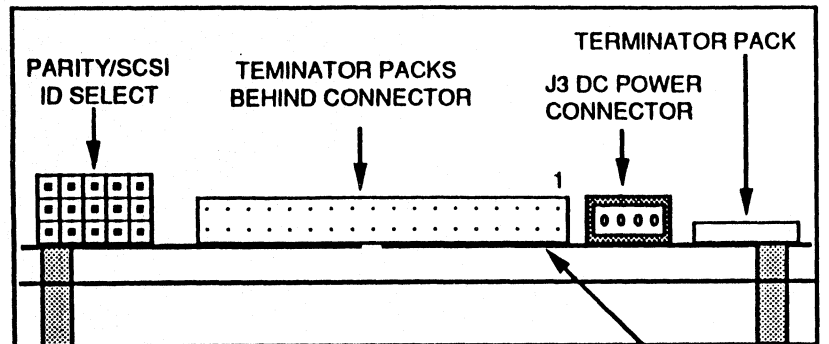
135 MByte
 FUJITSU
 SCSI DRIVE

GE 7

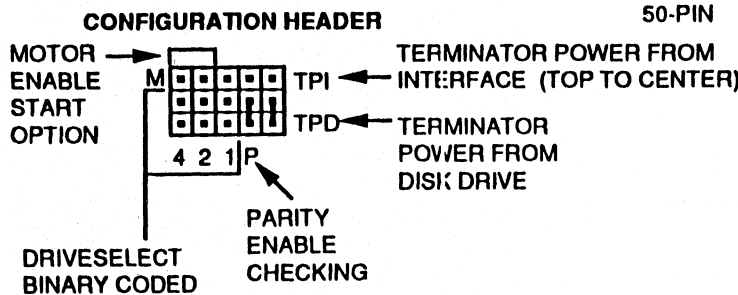
11/19/91



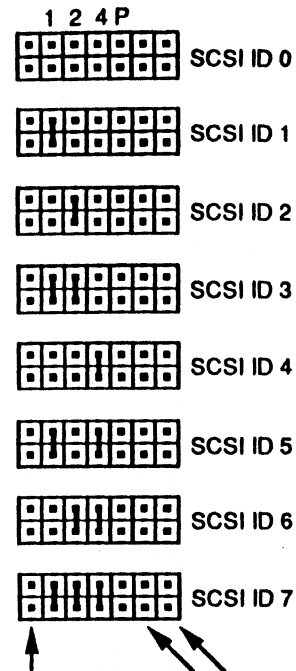
REAR VIEW OLD DRIVE
INTERFACE CONNECTOR 50-PIN



REAR VIEW NEWER DRIVES
INTERFACE CONNECTOR 50-PIN
MOTOR START OPTION
TERMINATOR POWER



PARITY/SCSI ID SELECT
INSTALL "P" TO ENABLE PARITY



SCSI ADDRESS	CHAN 0	CHAN 1
DISK 0	00	40
1	08	48
2	10	50
3	18	58
MVME327A		

PART NUMBERS:

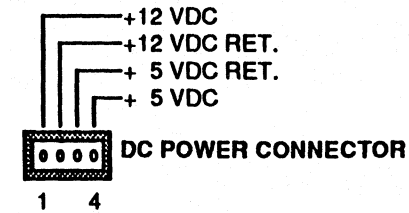
155MB SCSI 01-W2097C01 96011001
F/W REV. 6226
182MB UNFORMATTED, 155MB FORMATTED
SEAGATE WREN III MODEL 94161-155 OR 94161-182
PART # 77774611
5 1/2" FULL-HEIGHT DRIVE.

NOTE 1: MAY BE USED WITH THE MVME147(X) CPU AND MVME327A HOST ADAPTER.

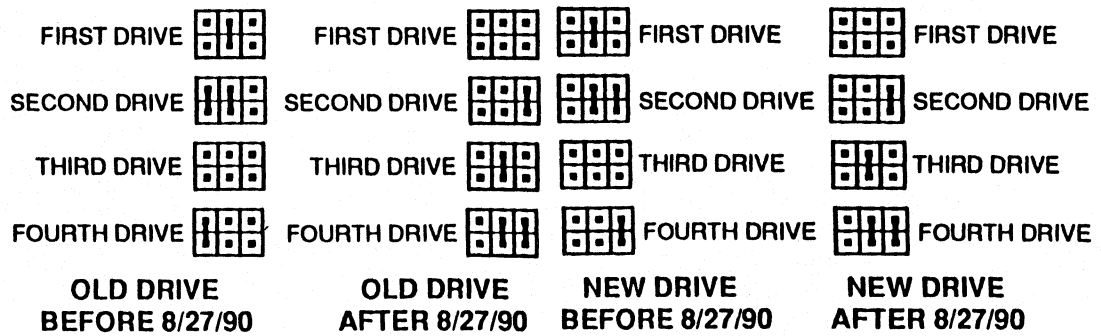
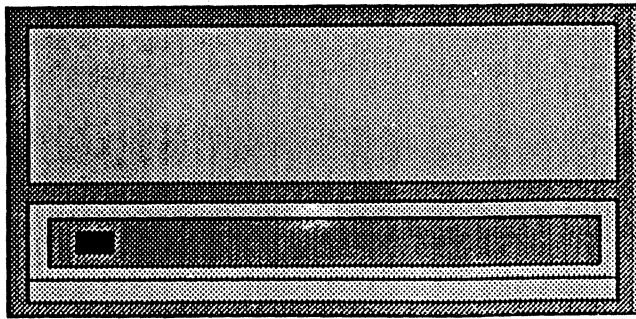
NOTE 2: EARLY VERSIONS OF THIS DRIVE HAVE INACCESSIBLE TERMINATORS. USE THESE DRIVES AT END OF CHAIN WHEN POSSIBLE.

NOTE 3: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 4: USED IN THE FOLLOWING ASSEMBLIES: MVME874, MVME874F-3, MVME874K-3, MVME874F-5, MVME874K-5, MVME874F-6, MVME874K-6, MVME874FTA-5 & MVME874KTA-5



03/19/91



**155 MByte
CDC SCSI
DRIVE
PAGE 8**

PART NUMBERS:

172MB 01-W2314C02 96011144 MODEL 94351-200S ST1201N
 W/O 5.25" MOUNTING BRACKETS
 P/N 75912188 F/W REV. 9212.
 201MB UNFORMATTED, 172MB FORMATTED

VENDOR IS CDC 3 1/2" HALF-HEIGHT SCSI DRIVE.

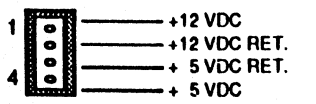
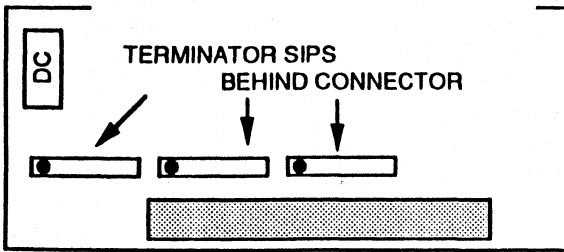
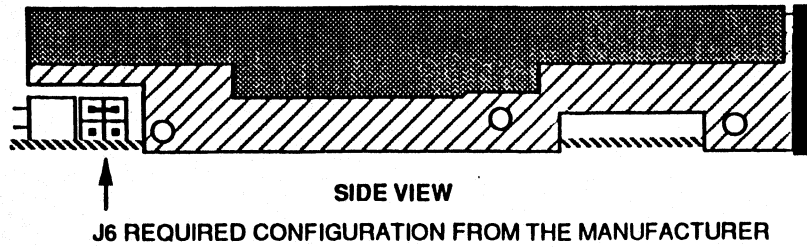
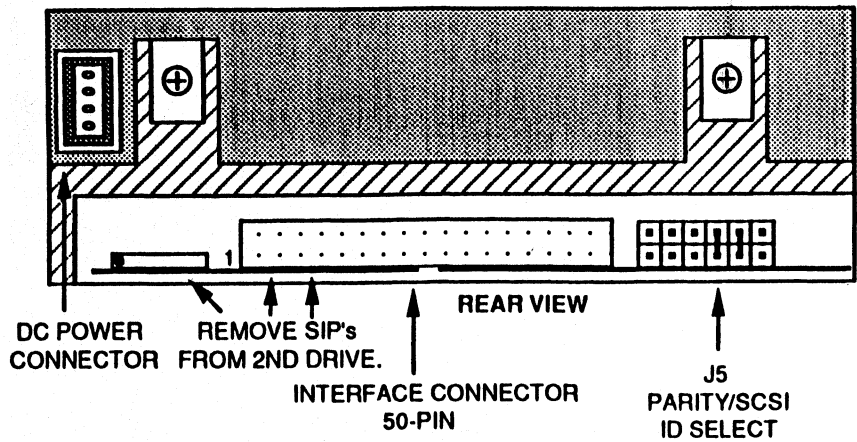
172MB 01-W2314C12 96011263 MODEL 94351-200S ST1201N
 W/5.25" MOUNTING BRACKETS
 P/N TBD F/W SEE C02 ABOVE.

NOTE 1: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

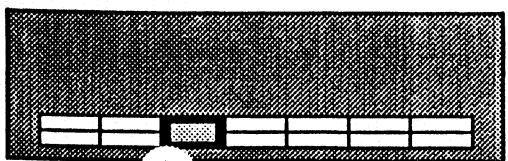
NOTE 2: SAME CONFIGURATION USED IN SYS3200, 3400 & 8400's.

NOTE 3: USED IN THE FOLLOWING ASSEMBLY: MVME864F-2 & MVME864K-2.

03/18/91

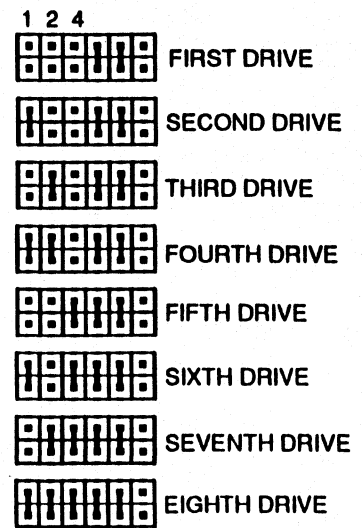


DC POWER CONNECTOR

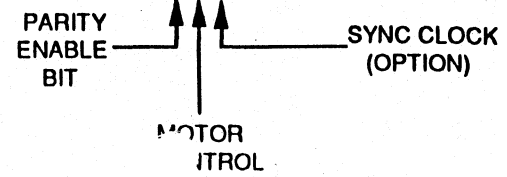
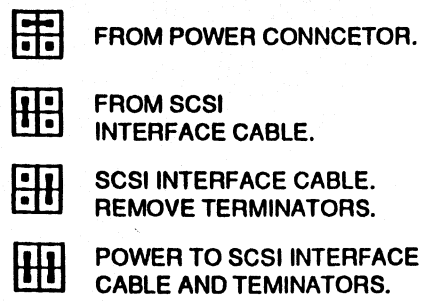


	SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
MVME32			

DRIVE/PARITY ENABLE SELECT



J6 +5VDC TERMINATOR POWER

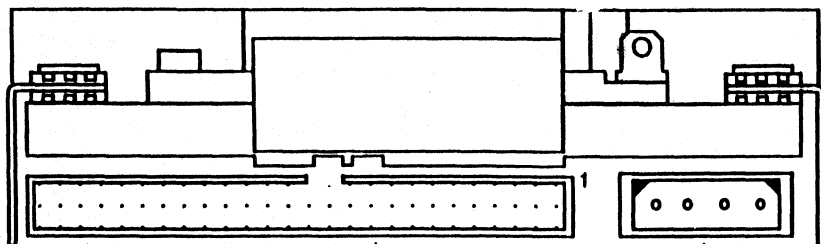


**172 MByte
 CDC
 SCSI DRIVE
 FIG 9**

PART NUMBERS:

180MB 01-W2801C02 TBD MODEL M2614ESA P/N CG003337-001
W/O 5.25" MOUNTING BRACKETS 229MB UNFORMATTED F/W
M606 VENDOR IS FUJITSU 3 1/2" HALF-HEIGHT SCSI DRIVE.

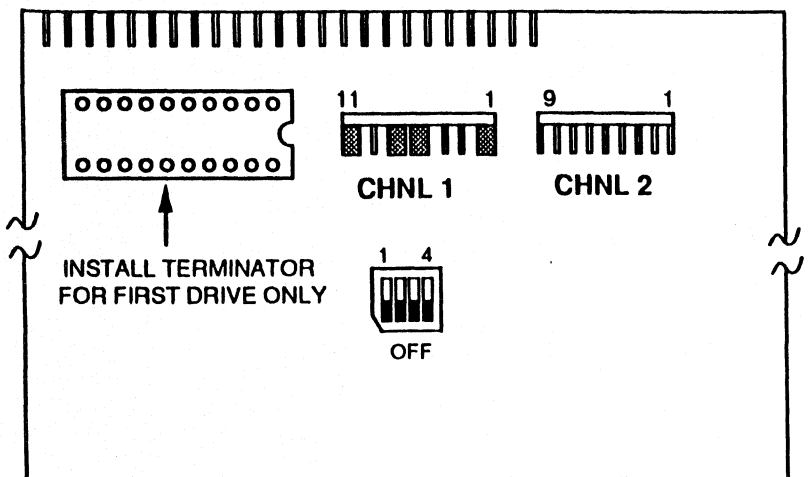
180MB 01-W2801C12 TBD MODEL M2614ESA P/N CG003364-002 FLUSH-
MOUNT, W/5.25" MOUNTING BRACKETS, F/W M606
MVME863A/864A
W/ SYS3404/08/16's



REAR VIEW

INTERFACE CONNECTOR
50-PIN

DC POWER
CONNECTOR



INSTALL TERMINATOR
FOR FIRST DRIVE ONLY

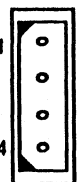
CHNL 1

CHNL 2

1 4
OFF

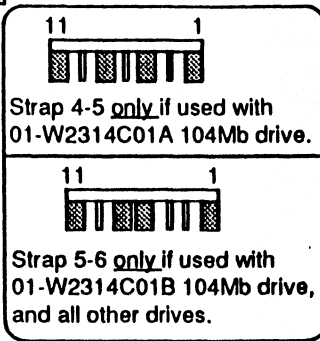
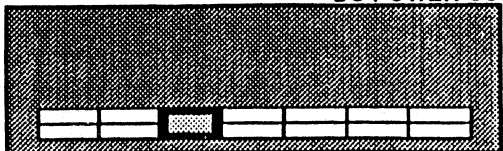
MOTOR SELF-DIAG.

NON-WRITE START NORMAL NOT
PROTECT W/ POWER MODE USED

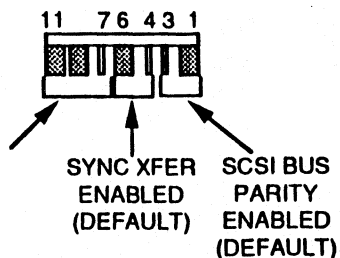


+12 VDC
+12 VDC RET.
+ 5 VDC RET.
+ 5 VDC

DC POWER CONNECTOR



CHANNEL 1



2-3 SCSI BUS PARITY DISABLED
4-5 SYNCHRONOUS TRANSFER DISABLED
7-8, 10-11 SCSI BUS TERMINATOR POWER SOURCE IS USED
7-8, 9-10 SCSI BUS TERMINATOR POWER SOURCE IS USED

NOTE 1: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

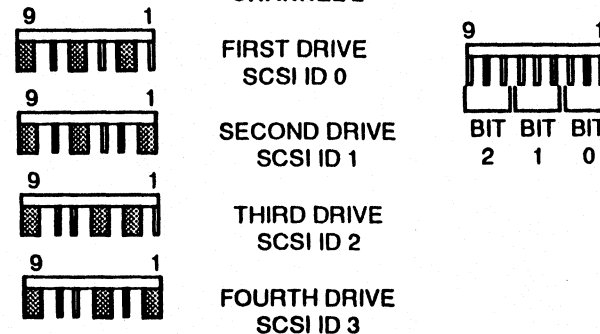
NOTE 2: SAME CONFIGURATION USED IN SYS3200, & 8400's.

NOTE 3: USED IN THE FOLLOWING ASSEMBLY: MVME863F-2 & MVME863K-2.

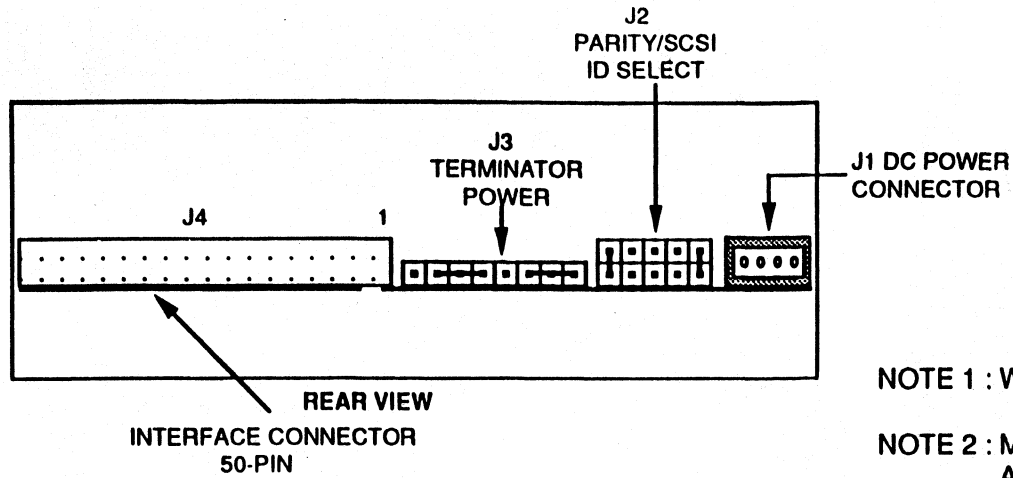
NOTE 4: SYS3400 REQUIRES MOTOR STARTSWITVH OFF(SW POS 2).

SCSI ADDRESS	CHAN 0	CHAN 1
DISK 0	00	40
1	08	48
2	10	50
3	18	58
MVME327A		

DRIVE ENABLE SELECT
CHANNEL 2



11/19/91



PART NUMBERS:

183MB WREN V 01-W2780C01 96011597
 MODEL 94221-209 P/N TBD
 209MB UNFORMATTED, 183MB FORMATTED

VENDOR IS SEAGATE. 5 1/4" HALF-HEIGHT DRIVE.

NOTE 1 : WHERE USED AT THIS TIME IS UNKNOWN.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE
 AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF
 WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED.
 (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER
 BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A
 TRANSITION BOARD.)

PARITY/SCSI ID SELECT
 INSTALL "P" TO ENABLE PARITY

4 2 1 P



FIRST DRIVE



SECOND DRIVE



THIRD DRIVE



FOURTH DRIVE



FIFTH DRIVE



SIXTH DRIVE

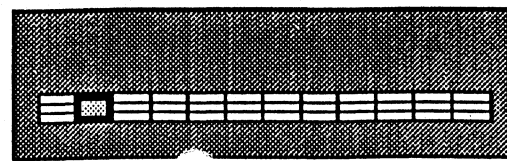
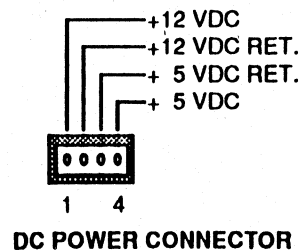
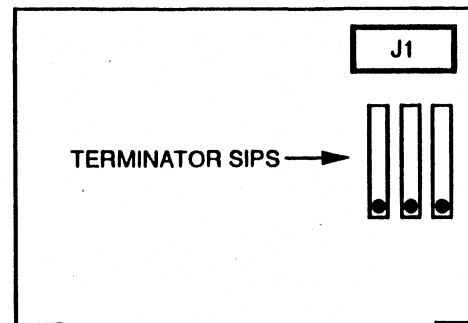


SEVENTH DRIVE

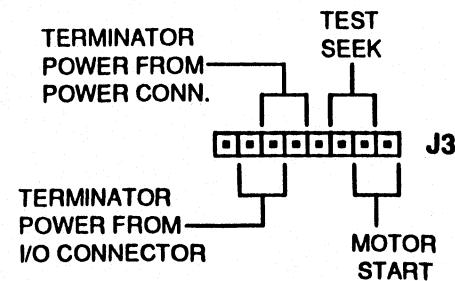


EIGHTH DRIVE

↑
PARITY



SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	40
	1	48
	2	50
	3	58
TAPE	20	60
	28	68
FLOPPY	30	70
	MVME327A	



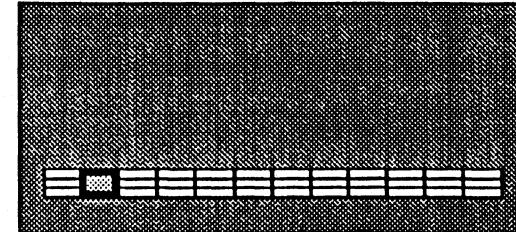
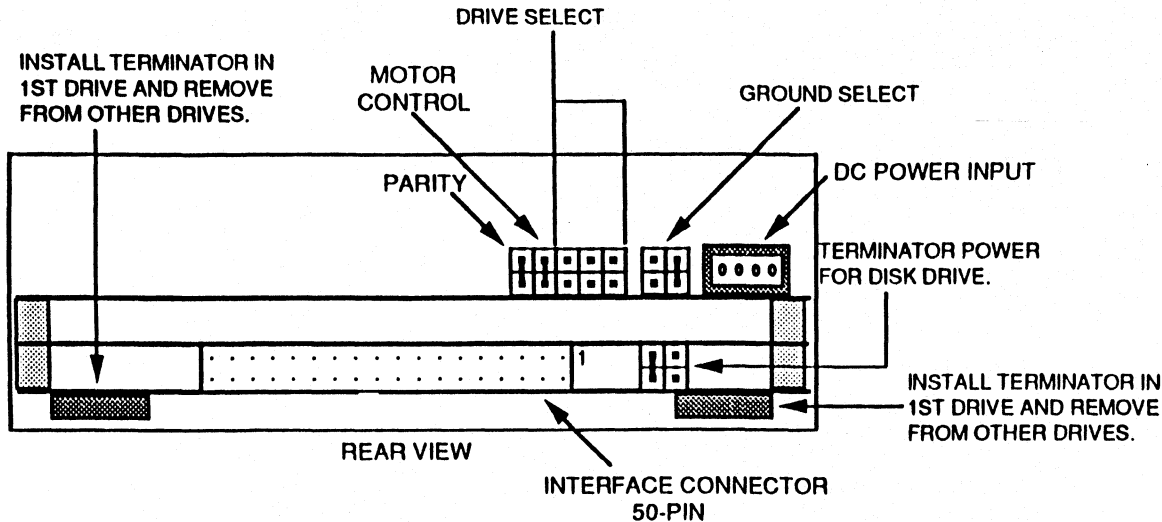
183 MByte
 SEAGATE
 SCSI DRIVE
 P/N 11

11/19/91

PART NUMBERS:

300MB SCSI 01-W2098C01 96011000
 F/W REV. MIN. = 5950; MAX. = 7975
 350MB UNFORMATTED, 307 FORMATTED

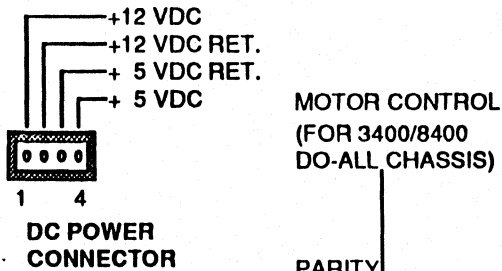
CDC WREN IV MODEL 94171-307 OR 94171-350
 PART # 77777031
 FULL-HEIGHT 5 1/4" WINCHESTER DRIVE



11/19/91

300MByte SCSI WINCHESTER DISK DRIVE

NOTE 1: 300MB SCSI WREN IV IS USED WITH MVME147 OR MVME327A.



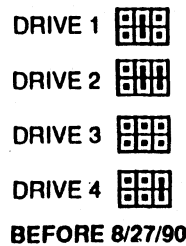
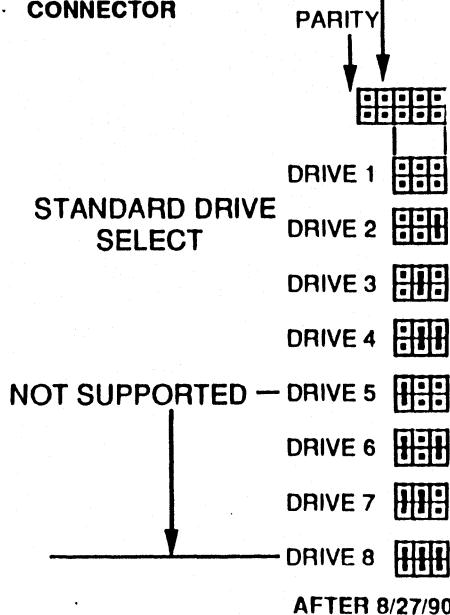
	SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
		MVME327A	

NOTE 2: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

NOTE 3: USED IN SYS1147, 3400, 3604/08, 3640, 3708, 8440, 8608, 8640, & 8840's.

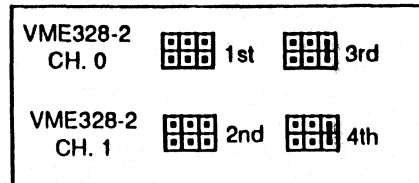
NOTE 4: USED IN THE FOLLOWING KITS: MVME875F-3, MVME875K-3, MVME875F-6, MVME875K-6, MVME875F-7, MVME875K-7, MVME875F-8, MVME875K-8, MVME875FTA-5 & MVME875KTA-5.

NOTE 5: AS USED W/ 1st VME328-1. ALSO USE ACTIVE TERMINATION BETWEEN ENDOF SCSI CABLE AND DRIVE. SEE SECTION 3 FOR CABLING OF VME328 BOARD.

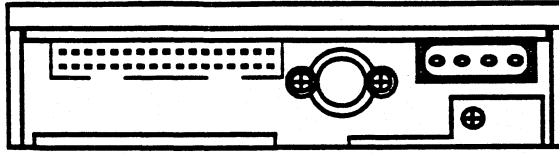
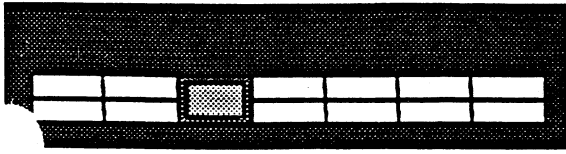


BEFORE 8/27/90

USED WITH 1st VME328-2



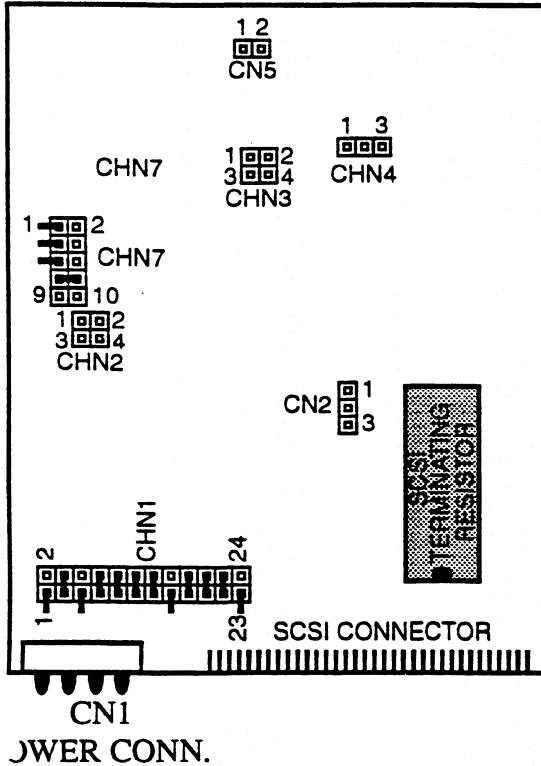
300 MByte
 CDC SCSI DRIVE
 PAGE 12



NOTE 1 : CN5 = External LED connector.

NOTE 2 : CHN2, 3 & 4 are factory set. DO NOT CHANGE.

NOTE 3 : CN6 = External SCSI address connector.



CHN1 OFFLINE SELF-DIAG. SETTINGS	5 - 6
EXECUTED (DIAGNOSTIC MODE)	SHORT
STOPPED (NORMAL OPERATING MODE)	OPEN

CHN1 UNIT ATTENTION REPORT MODE	7 - 8
For a command other than INQUIRY, REQUEST SENSE, or PRIORITY RESERVE the IDD response with the CHECK CONDITION status. (SCSI standard)	SHORT
All received commands are executed normally. (CHECK COMMAND status caused by the UNIT ATTENTION condition is not reported.)	OPEN

CHN1 RESELECTION RETRY SETTING	9 - 10
10	OPEN
(UNLIMITED)	SHORT

CHN1 SCSI BUS PARITY SETTING	13 - 14
EXECUTED	SHORT
NOT EXECUTED	OPEN

CHN1 SYNCHRONOUS MODE TRANSFER REQUEST SETTING	15 - 16
ENABLED	SHORT
DISABLED	OPEN

CHN1 JUMPER 1-2	1 - 2
0	SHORT
1	OPEN

330MB SCSI 3 1/2" DRIVE 01-W2006D02 XXXXXXXXX
FUJITSU M2622SA (CG005891-001) PICOBIRD 4 W/ M302 F/W

330MB SCSI 3 1/2" DRIVE 01-W2006D12 XXXXXXXXX
FUJITSU M2622SA (CG005892-001) PICOBIRD 4 W/ M302 F/W

520MB SCSI 3 1/2" DRIVE 01-W2006D01 XXXXXXXXX
FUJITSU M2624SA (CG005046-001) PICOBIRD 4 W/ M302 F/W

520MB SCSI 3 1/2" DRIVE 01-W2006D11 XXXXXXXXX
FUJITSU M2624SA (CG005882-001) PICOBIRD 4 W/ M302 F/W

CHN1 SCSI LEVEL SETTING					
MODE	INQUIRY DATA			INQUIRY VPD Information	3 - 4
	byte 2, bits2 to 0 (ANSI version)	byte 3, bits3 to 0 (Response data format)	byte 7 (Provided function)		
SCSI-2 MODE	'0, 1, 0' (SCSI-2)	'0, 0, 1, 0' (SCSI-2)	Indicates the function of the IDD for eac bit.	VALID	OPEN
SCSI-1/ CCS MODE	0, 0, 1' = ANSIX3.131-1986 (scsi-1)	'0, 0, 0, 1' = ANSIX3T9.2/85-52 (CCS)	ALL bits "0"	INVALID	SHORT

CHN1 LED DISPLAY REQUIREMENT SETTING	17 - 18
Lit when the IDD operates.	SHORT
Not lit when the IDD does not operate.	OPEN

CHN1 MOTOR START MODE SETTING	19 - 20
The motor is started immediately after power is turned on.	SHORT
Starting the motor is controlled with the START/XTOP command.	OPEN

CHN1 SCSI TERMINATING RESISTOR POWER SUPPLY	23 - 24	21 - 22
Power is supplied to the terminating resistor from IDD and TERM-PWR pin. power is supplied to the TERMPWR pin from the IDD.	SHORT	SHORT
The TERMPWR pin is not used. Power is supplied to the IDD terminating resistor only from the IDD.	OPEN	SHORT
Power is not supplied to the terminating resistor from the IDD. Power is supplied to the IDD terminating resistor only from TERMPWR pin.	SHORT	OPEN

CNH7 ADDRESS STRAPPING

SCSI ID ADDRESS	5 - 6	3 - 4	1 - 2
0	OPEN	OPEN	OPEN
1	OPEN	OPEN	SHORT
2	OPEN	SHORT	OPEN
3	OPEN	SHORT	SHORT
4	SHORT	OPEN	OPEN
5	SHORT	OPEN	SHORT
6	SHORT	SHORT	OPEN
7	SHORT	SHORT	SHORT

CHN7 WRITE PROTECT SETTING	7 - 8
WRITE operation is disabled	OPEN
WRITE operation is enabled	SHORT

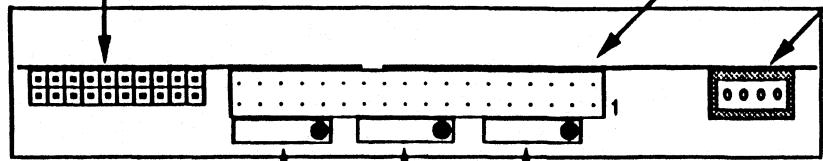
CHN7 SCSI BUS RESET SETTING	9 - 10
NORMAL operation	OPEN
FORCE SCSI bus RESET	SHORT

J2 SEE BELOW FOR CONFIGURATION

REAR VIEW

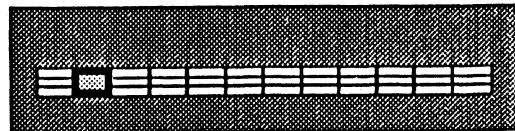
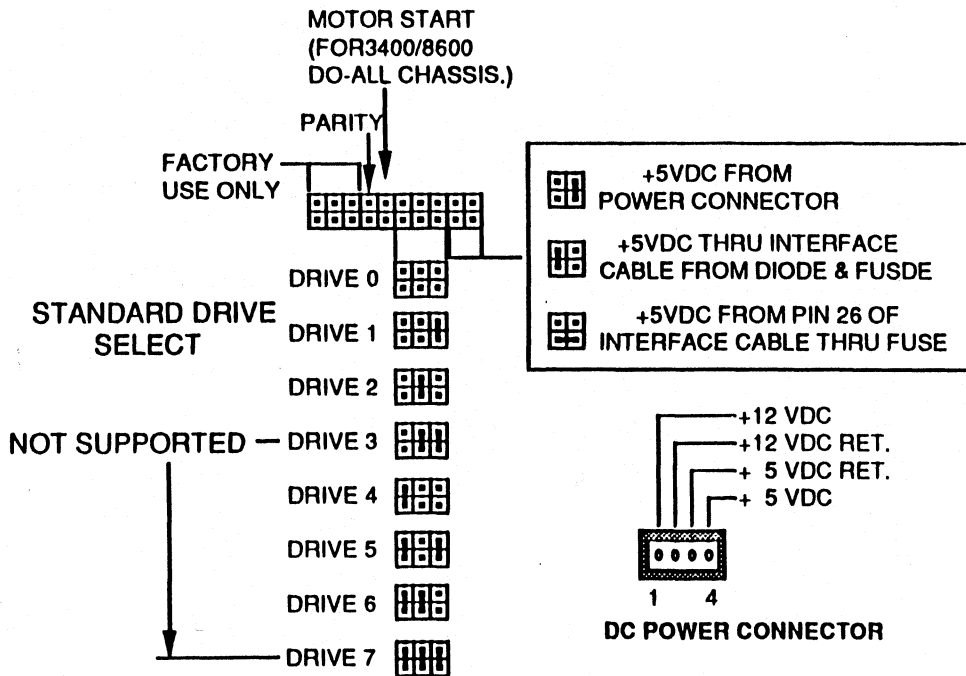
INTERFACE CONNECTOR 50-PIN J3

DC POWER INPUT



INSTALL TERMINATOR IN 1ST DRIVE AND REMOVE FROM OTHER DRIVES.

330MByte SCSI WINCHESTER DISK DRIVE



PART NUMBERS:

330MB SCSI

01-W2698C01 96011596

SEAGATE WREN VI MODEL 94241-383 PART # TBD
HALF-HEIGHT 5 1/4" WINCHESTER DRIVE

NOTE 1: 300MB SCSI WREN VI IS USED WITH ????.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.) : INSTALL TERMINATORS ON END-OF-CHAIN DRIVES.

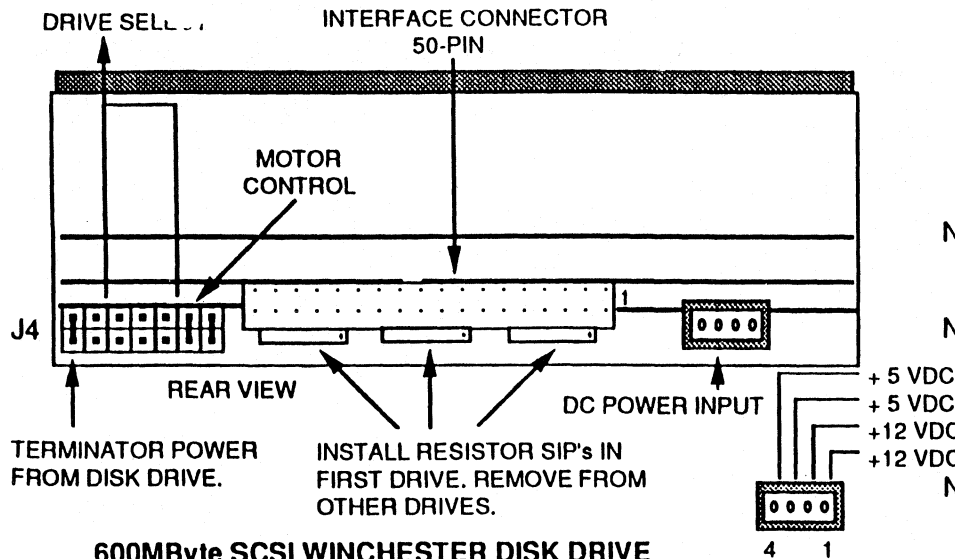
NOTE 3: USED IN SYS????.

NOTE 4: USED IN THE FOLLOWING KITS: ????

11/19/91

SCSI ADDRESS	CHAN 0	CHAN 1
DISK 0	00	40
1	08	48
2	10	50
3	18	58
TAPE 4	20	60
5	28	68
FLOPPY 6	30	70
MVME327A		

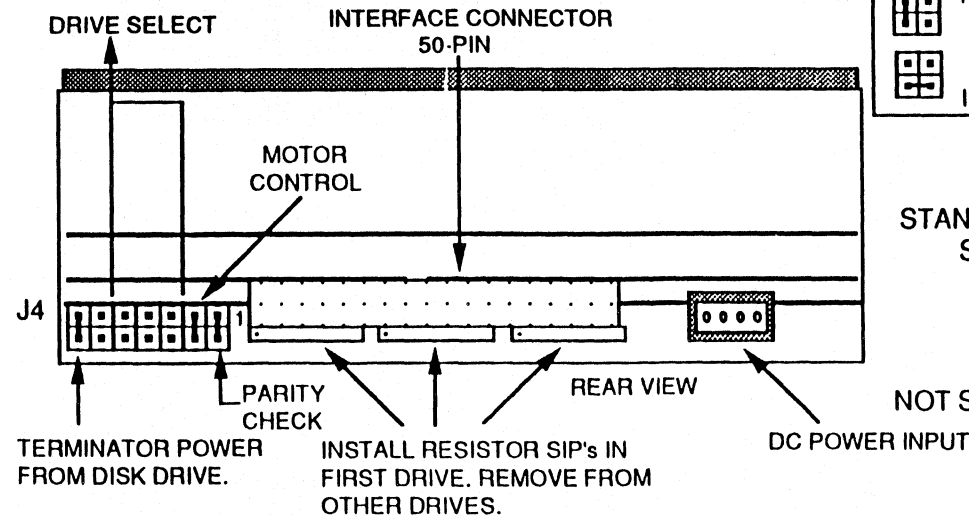
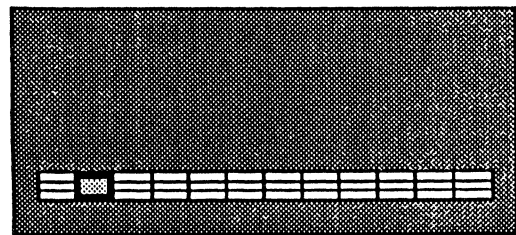
**330 MByte
SEAGATE
SCSI DRIVE
E 13**



600MByte SCSI WINCHESTER DISK DRIVE
PWA # 77776780 - START TO NOV. 89

DC POWER CONNECTOR

SCSI ADDR	CHAN 0	CHAN 1
DISK 0	00	40
1	08	48
2	10	50
3	18	58
MVME327A		



600MByte SCSI WINCHESTER DISK DRIVE PWA # 77776780 BEFORE NOV. 89; PWA # 77771215 - NOV. 89 TO NOV. 90; AND 75900620 NOV. 90 TO PRESENT.

PART NUMBERS:

600MB SCSI U1-W2264C01 96011086 F/W REV. MIN. = 5466;
 MAX. = 0293; 702MB UNF., 601MB FORMATTED
 CDC WREN V MODEL # 94181-702 PART # 7777750
 FULL HEIGHT 5 1/4" WINCHESTER DRIVE.

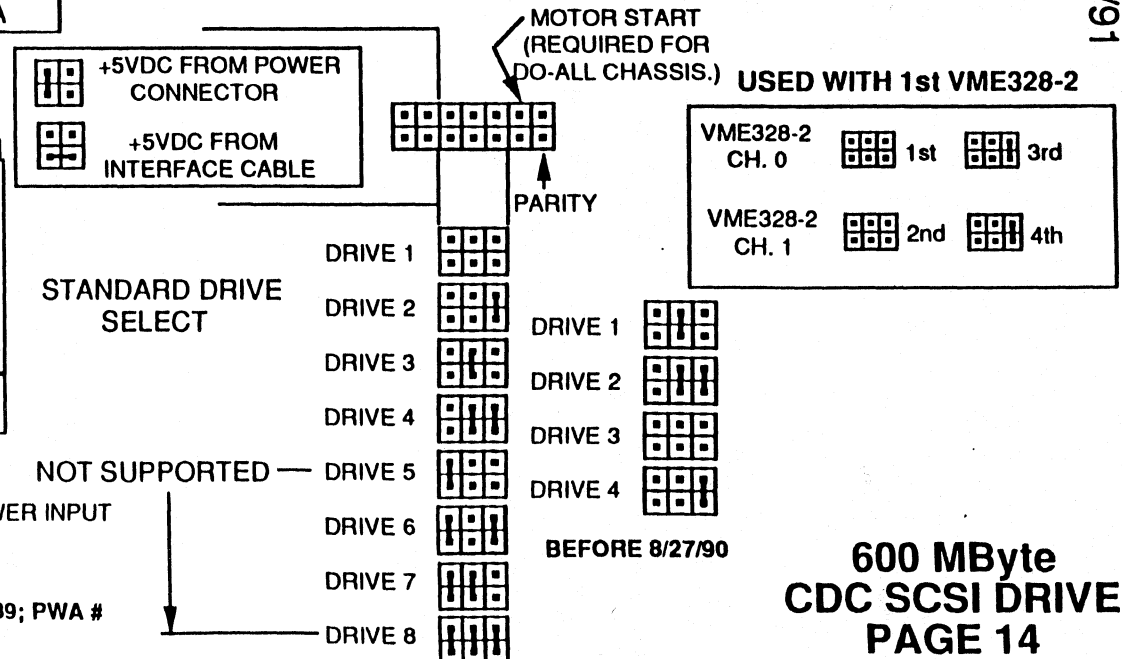
NOTE 1: 600MB SCSI WREN V IS USED W/ MVME147, 327A & 328(X).
 USED IN SYS3604/08, 3640, 3708, 8440, 8608, 8640 & 8840's.

NOTE 2: MAKE SURE TERMINATION IS ON BOTH ENDS OF THE CABLE
 REGARDLESS IF IT'S INTERNAL OR EXTERNAL. (INTERNAL
 CONFIG. COMES OFF THE XXXP2 ADAPTER. EXTERNAL CONFIG.
 ALWAYS GO THROUGH A TRANSITION BOARD.)

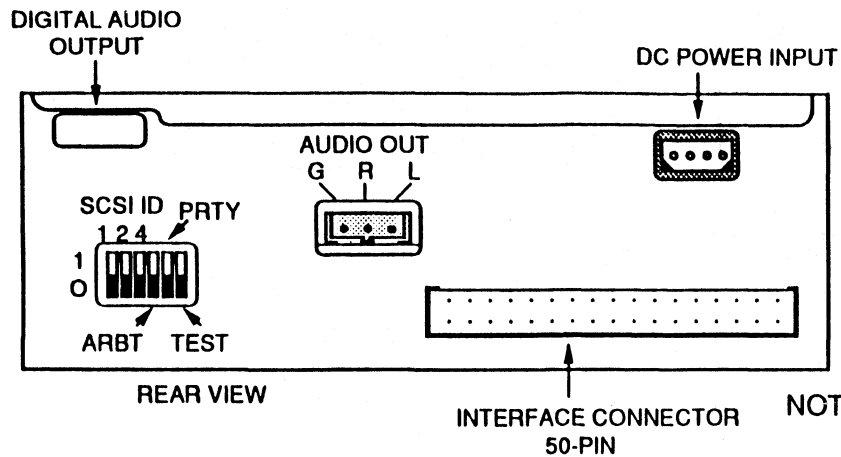
NOTE 3: USED IN THE FOLLOWING ASSEMBLIES: MVME876F-3, K-3,
 MVME876F-5, K-5, MVME876F-6, K-6, MVME876F-7, K-7,
 MVME876F-8, K-8, MVME876FTA-5 & KTA-5.

NOTE 4: TERMINATORS AND P1 CONNECTOR ON PWA # 77776780 OR
 75900620, AND PWA # 77771215 ARE REVERSED. SEE DRAWING
 DIFFERENCES ON THE LEFT TOP AND BOTTOM.

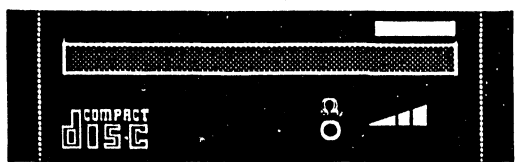
NOTE 5: AS USED W/ 1st VME328-1. ALSO USE ACTIVE TERMINATION
 BETWEEN ENDOF SCSI CABLE AND DRIVE. SEE SECTION 3
 FOR CABLING OF VME328 BOARD.



11/19/91



REAR VIEW



FRONT VIEW

PART NUMBERS:

600MB SCSI CD ROM HALF HEIGHT 5 1/4" CD ROM SCSI DRIVE.
01-W2781C01 96011678 F/W REV. G910822

TOSHIBA MODEL # XM-3301B-MR PART # TXM3301B000230

NOTE 1 : WHERE USED AT THIS TIME IS UNKNOWN.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

11/08/91

SCSI ID	SWITCH		
	LSB	MSB	
	1	2	3
0	0	0	0
1	1	0	0
2	0	1	0
3	1	1	0
4	0	0	1
5	1	0	1
6	0	1	1
7	1	1	1

FACTORY SHIP →

	SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
		MVME327A	

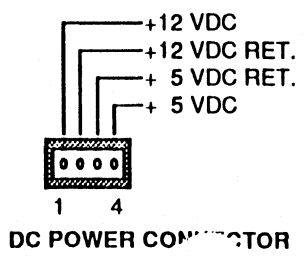
PRTY SW.	ARBT SW.	TEST SW.	FUNCTION
1*	2*	<input type="checkbox"/>	NORMAL OPERATING MODE
0	0	1	AUDIO REPRO MODE
0	1	1	FOCUS ADJ. MODE
1	0	1	TRACKING ADJ. MODE

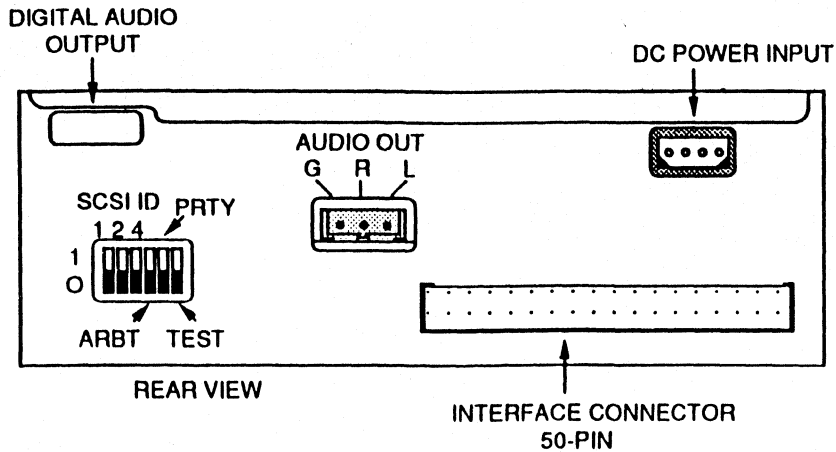
*1 = DEPENDS ON THE SETTING OF THE SCSI-ID NUMBER SHOWN ABOVE.

*2 = DEPENDS ON THE SETTING OF THE PRTY SWITCH SHOWN ABOVE.

USED TO HIGHLIGHT THE JUMPER CONFIGURATION TO BE RECEIVED FROM THE MANUFACTURER.

PARITY - 1 = PARITY CHECK
0 = NO PARITY CHECK
ARBITRATION - 1 = ARBITRATION SYSTEM
0 = NO ARBITRATION SYSTEM





PART NUMBERS:

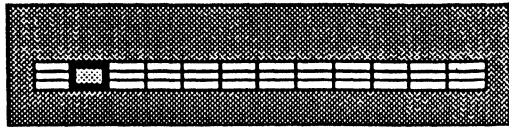
600MB SCSI CD ROM 01-W2007D01 96011677
F/W REV. ????

TOSHIBA MODEL # TMX-3201B-MR PART # N/A
HALF HEIGHT 5 1/4" CD ROM WINI DRIVE.

600 MB SCSI CD ROM 01-W2781C01 96011678
TOSHIBA MODEL # TMX-3301B

NOTE 1 : WHERE USED AT THIS TIME IS UNKNOWN.

NOTE 2 : MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)



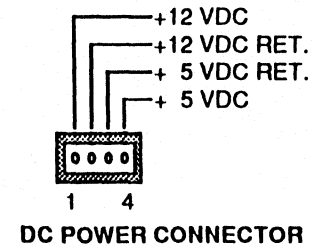
FRONT VIEW

SCSI ID	SWITCH		
	LSB	MSB	
	1	2	3
0	0	0	0
1	1	0	0
2	0	1	0
3	1	1	0
4	0	0	1
5	1	0	1
6	0	1	1
7	1	1	1

PARITY - 1 = PARITY CHECK
0 = NO PARITY CHECK

ARBITRATION - 1 = ARBITRATION SYSTEM
0 = NO ARBITRATION SYSTEM

	SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
		MVME327A	



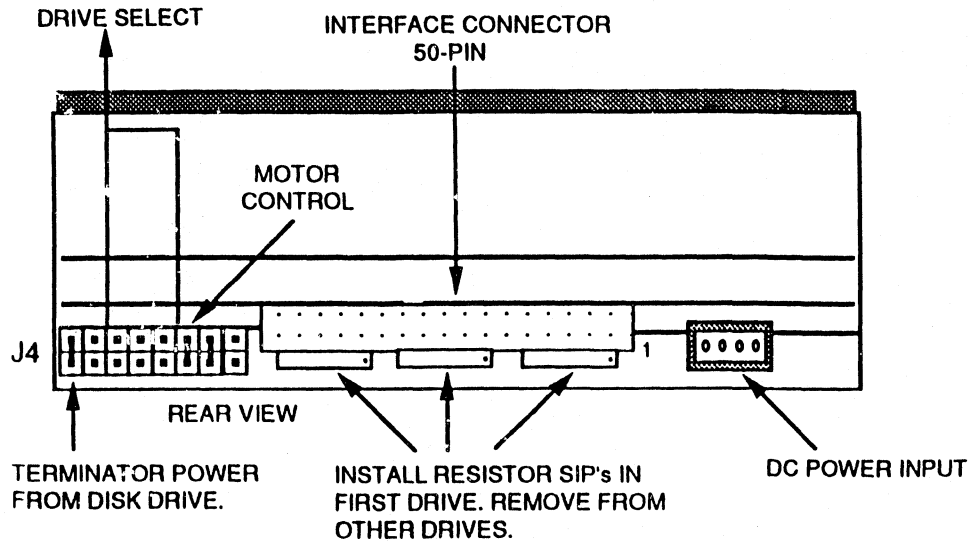
600 MByte
CD ROM SCSI DRIVE
PAGE 16

11/19/91

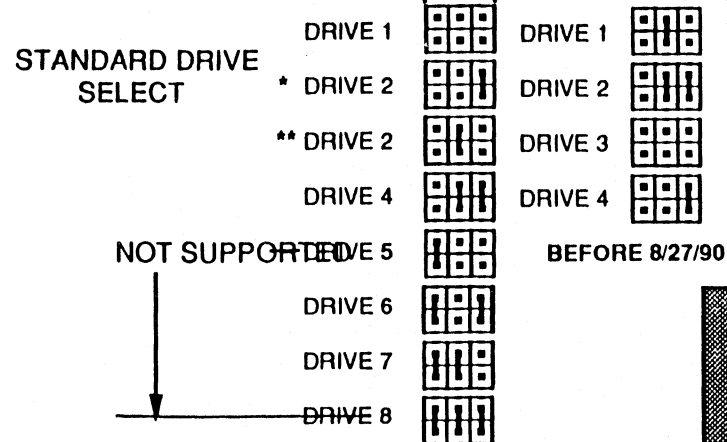
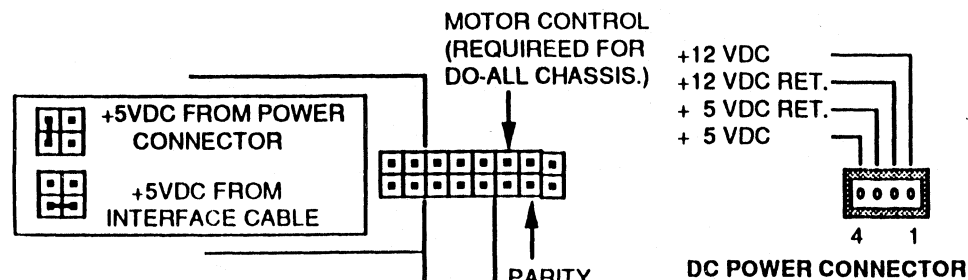
PART NUMBERS:

1.2 GB SCSI 01-W2496C01 96011196
 F/W REV. 368
 1.2GB UNFORMATTED, 1.035GB FORMATTED

CDC WREN VII MODEL # 94601-1.2G PART # 77709461
 FULL HEIGHT 5 1/4" WINCHESTER DRIVE

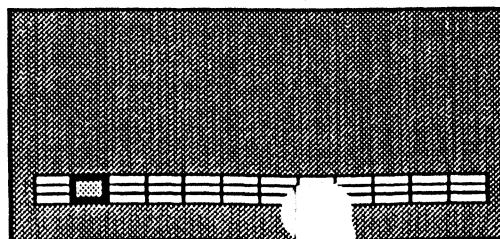
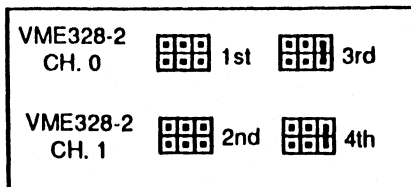


1.2 GByte SCSI WINCHESTER DISK DRIVE



* SECOND DRIVE ONLY IF 1ST DRIVE IS 5 1/4"
 ** SECOND DRIVE ONLY IF FIRST DRIVE IS 3"

USED WITH 1st VME328-2



NOTE 1: 1.2GB SCSI WREN VII IS USED WITH MVME147 OR MVME327A.

NOTE 2: USED IN SYS3604/08, 3640, 8440, 8608, 8640 & 8840's. IN 3400's THE FIRST DRIVE IS #2 AND THE SECOND DRIVE IS #1

NOTE 3: USED IN MVME877 ASSEMBLIES.

NOTE 4: MAKE SURE THERE IS A TERMINATOR ON BOTH ENDS OF THE CABLE AS SPECIFIED BY SCSI BUS SPECIFICATION REGARDLESS OF WHETHER IT'S INTERNALLY OR EXTERNALLY CONFIGURED. (INTERNAL CONFIGURATIONS COME OFF THE XXXP2 ADAPTER BOARD. EXTERNAL CONFIGURATIONS ALWAYS GO THROUGH A TRANSITION BOARD.)

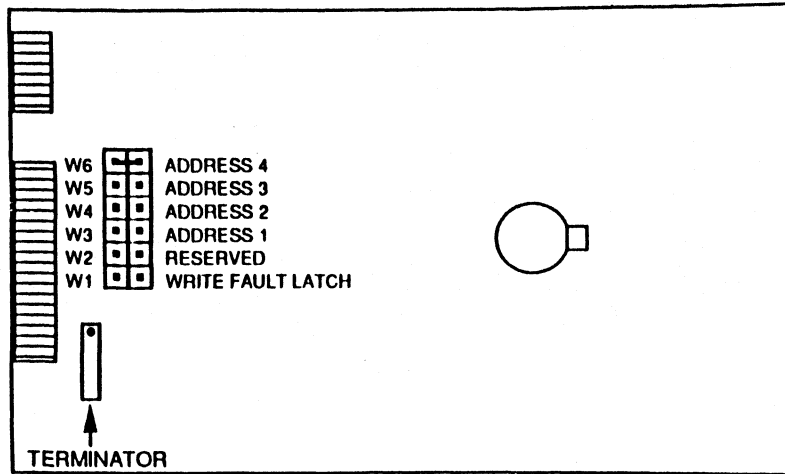
NOTE 5: AS USED W/ 1st VME328-1. ALSO USE ACTIVE TERMINATION BETWEEN ENDOF SCSI CABLE AND DRIVE. SEE SECTION 3 FOR CABLING OF VME328 BOARD.

11/19/91

	SCSI ADDRESS	CHAN 0	CHAN 1
DISK	0	00	40
	1	08	48
	2	10	50
	3	18	58
TAPE	4	20	60
	5	28	68
FLOPPY	6	30	70
		MVME327A	

1.2 GByte
 CDC
 SCSI DRIVE
 P E 16

APPENDIX E



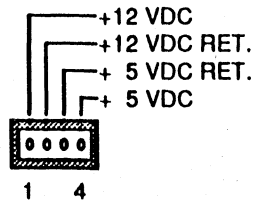
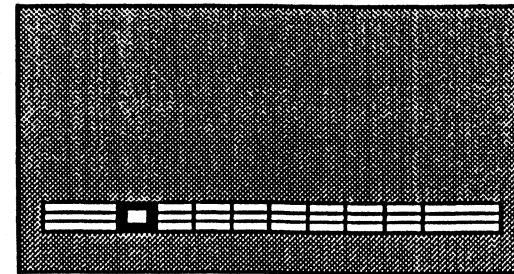
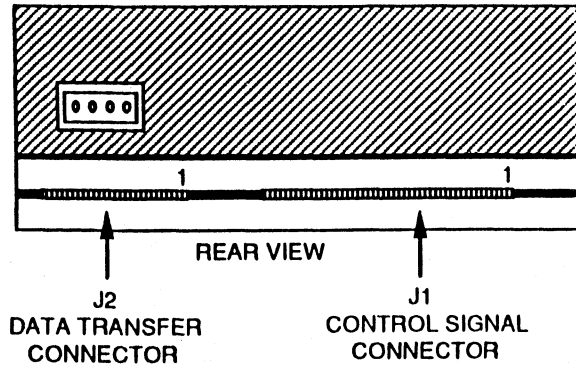
PART NUMBERS:

40 MB ST506 HARD DRIVE 01-W0306B05 76432562
(FORMATTED)
MICROPOLIS MODEL # 1304EM (51.9 MB UNFORMATTED)

50MB ST-506 HARD DRIVE 01-W0305B10 NONE
(FORMATTED)
MICROPOLIS MODEL # 1323A (53.3MB UNFORMATTED)

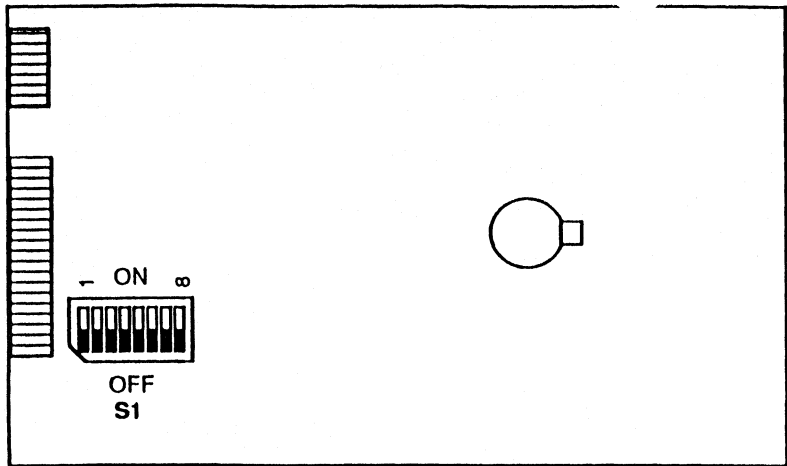
NOTE 1: USE THIS CONFIGURATION IN SYS1121's.

NOTE 2: USED IN THE FOLLOWING ASSEMBLIES: VME10's & MVME822



DC POWER CONNECTOR

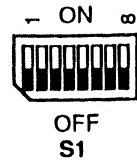
09/14/90



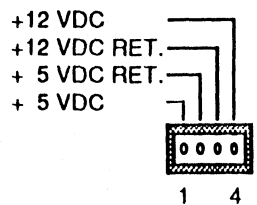
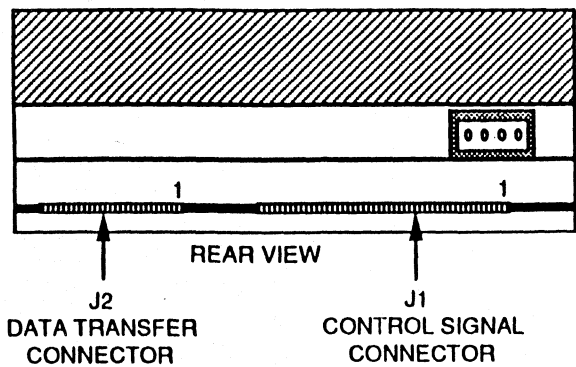
PART NUMBERS:

40 MB ST-506 HARD DRIVE 01-W0306B07 96010940
(FORMATTED)

TOSHIBA MODEL # MK54FA (60.5 MB UNFORMATTED)

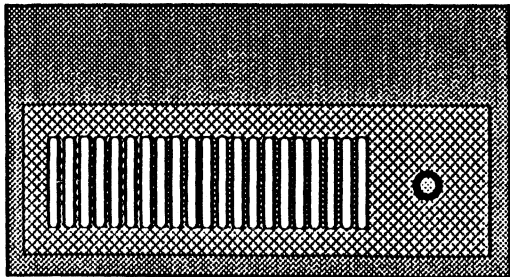


- S1-1 = DRIVE 1 SELECT
- S1-2 = DRIVE 2 SELECT
- S1-3 = DRIVE 3 SELECT
- S1-4 = DRIVE 4 SELECT
- S1-5 = RADIAL SELECT
- S1-6 = WRITE PROTECT
- S1-7 = RESERVED
- S1-8 = RESERVED

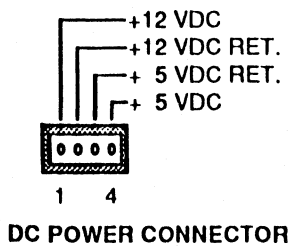
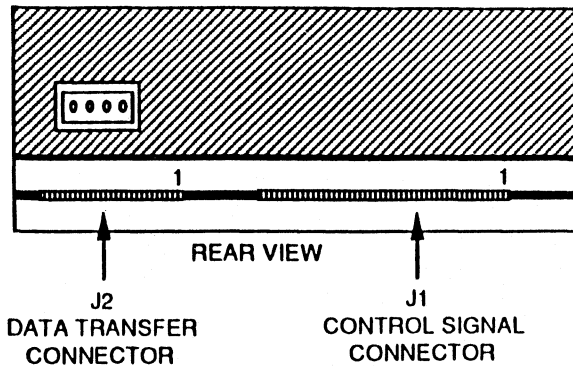
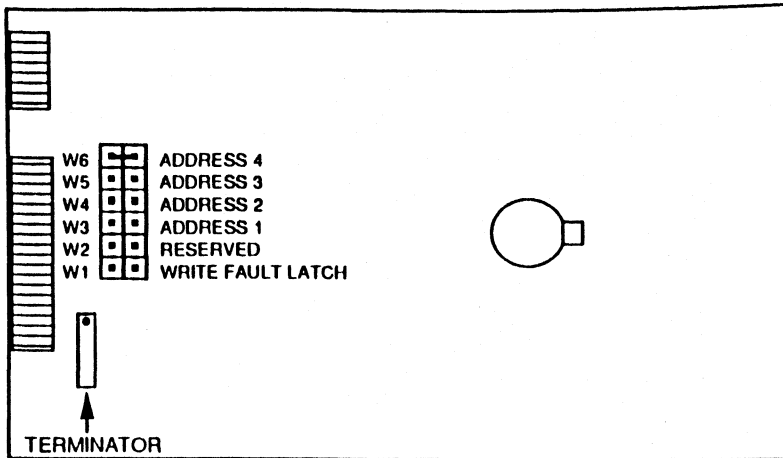


DC POWER CONNECTOR

- NOTE 1: USE THIS CONFIGURATION IN SYS1121's.
- NOTE 2: USED IN THE FOLLOWING ASSEMBLY: MVME822.
- NOTE 3: ACTIVE PART OF SWITCH IS DARKENED AREA.



09/14/90



PART NUMBERS:

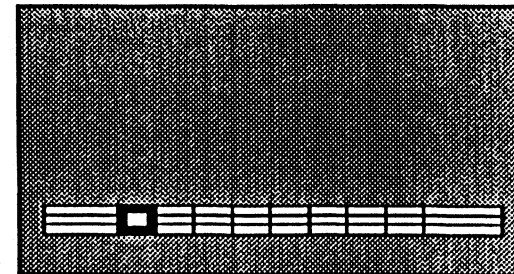
70 MB ST-506 HARD DRIVE 01-W0306B06 96010272
 (FORMATTED)
 MICROPOLIS MODEL # 1325M (85.3 MB UNFORMATTED)

70 MB ST-506 HARD DRIVE 01-W0306B09 NONE
 (FORMATTED) PART # 900525-01-5F
 -5G
 -5H
 -5J

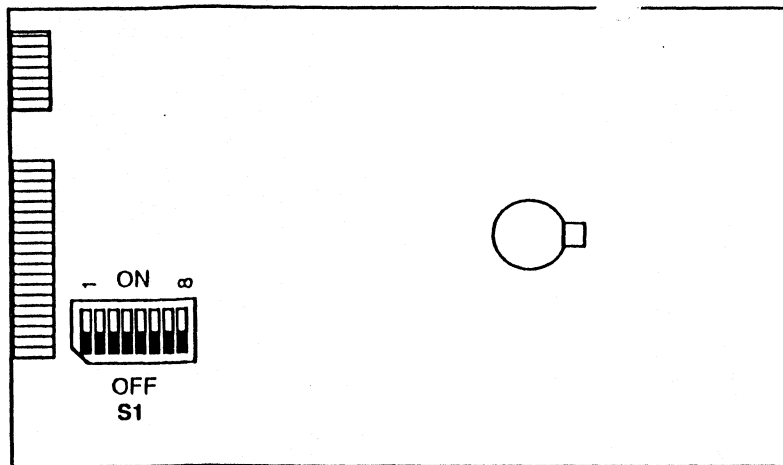
NOTE 1: USED IN THE FOLLOWING ASSEMBLIES: MVME823, MVME833, MVME834, MVME835DT, MVME835F-5, MVME835K-5, MVME841F, MVME841K, MVME841F-3 & MVME841K-3.

NOTE 2: SAME CONFIGURATION IN SYS1121, 1131, 2016, 2316, 2334 & 2616's.

09/14/90



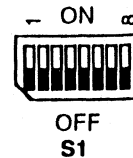
70 MByte
 MICROPOLIS
 ST-506 DRIVE
 PART # 3



PART NUMBERS:

70 MB ST-506 HARD DRIVE 01-W0306B08 96010272
(FORMATTED)

TOSHIBA MODEL # MK56A/B (86.5 MB UNFORMATTED)



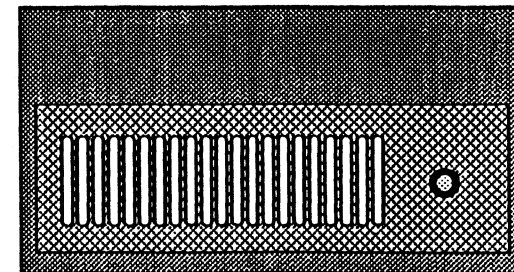
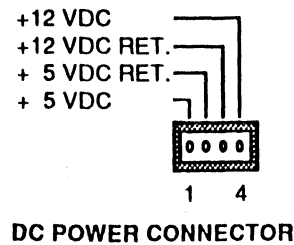
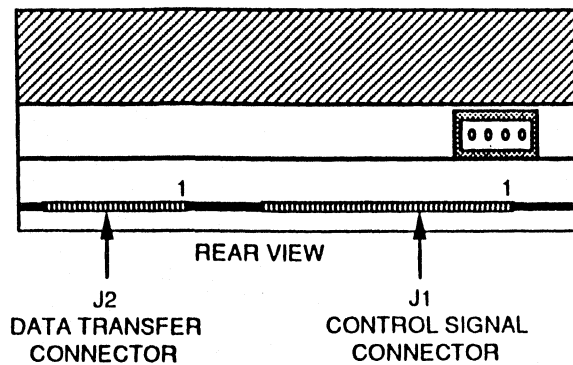
- S1-1 = DRIVE 1 SELECT
- S1-2 = DRIVE 2 SELECT
- S1-3 = DRIVE 3 SELECT
- S1-4 = DRIVE 4 SELECT
- S1-5 = RADIAL SELECT
- S1-6 = WRITE PROTECT
- S1-7 = RESERVED
- S1-8 = RESERVED

NOTE 1: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 2: USED IN THE FOLLOWING ASSEMBLIES: MVME823, MVME833, MVME834, MVME835DT, MVME835F-5, MVME835K-5, MVME841F, MVME841K, MVME841F-3 & MVME841K-3.

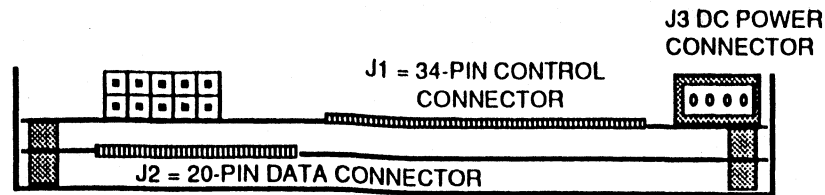
NOTE 3: SAME CONFIGURATION IN SYS1121, 1131, 2016, 2316, 2334 & 2616's.

09/14/90

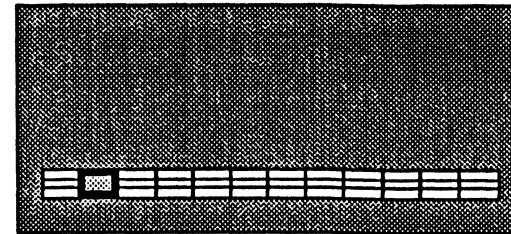
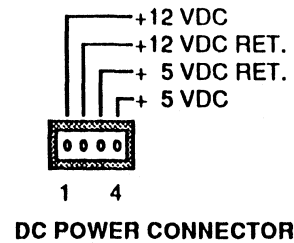
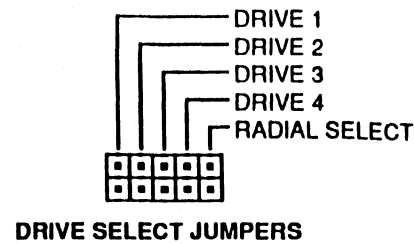


PART NUMBERS:

85MB ST-506 (UNFORMATTED) 01-W2052C01 96010940
 CDC WREN II MODEL # 94155-85 PART# 7777260
 HALF-HEIGHT 5 1/4" WINCHESTER DRIVE.



85 MByte ST506 WINCHESTER DRIVE



09/14/90

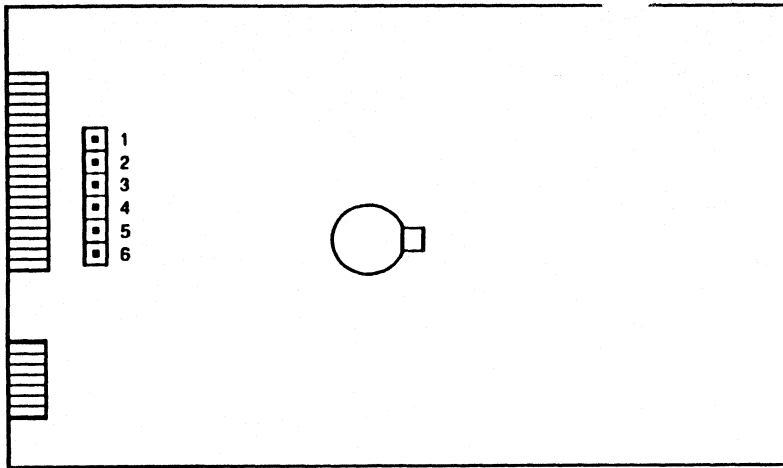
NOTE 1: DRIVE 3, DRIVE 4 SELECT AND RADIAL SELECT ARE NOTUSED IN DELTA SYSTEMS.

NOTE 2: USABLE IN DELTA SYSTEMS WITH MVME320(X) CONTROLLER. USE DEVICE DESCRIPTORS FOR MICROPOLIS 1325 TO FORMAT DRIVE.

NOTE 3: USED IN THE FOLLOWING ASSEMBLIES: MVME823, MVME833, MVME834, MVME835DT, MVME835F-5, MVME835K-5, MVME841F, MVME841K, MVME841F-3 & MVME841K-3.

NOTE 4: SAME CONFIGURATION IN SYS1121, 1131, 2016, 2316, 2334 & 2616's.

**85 MByte
 CDC
 ST-506 DRIVE
 P E 5**



85/190 MB MAXTOR DRIVE CONFIGURATION BOARD

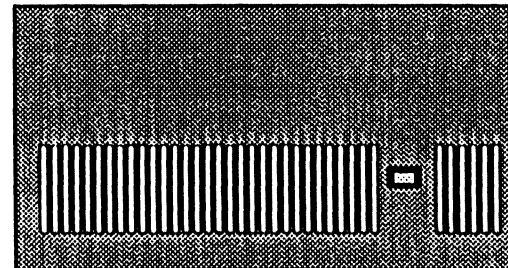
**NOTE 1: DRIVE SELECT HEADERS:
 DRIVE SELECT 0 = 1,2
 DRIVE SELECT 1 = 2,3 ETC.**

NOTE 2: PRESENTLY NOT USED IN ANY ASSEMBLIES.

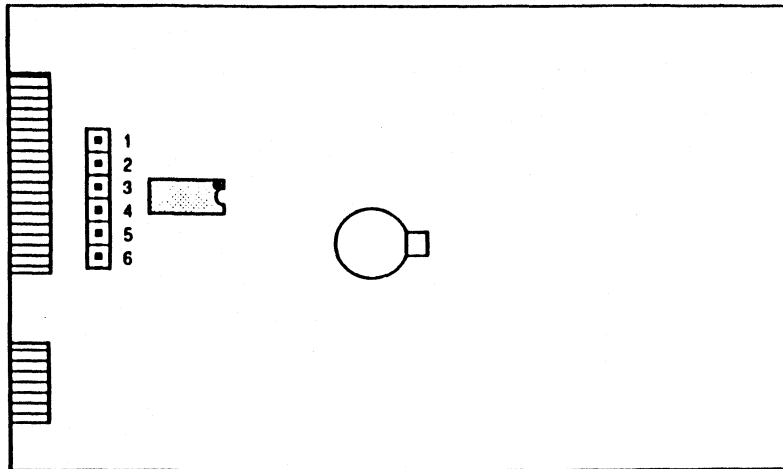
PART NUMBERS:

**85 MB FSD P/N 96010270 NUMBERS HAVE BEEN ASSIGNED.
 MAXTOR MODEL 1085 VENDOR # XT1085**

**FULL-HEIGHT 5 1/4" WINI DRIVE
 ST-506 COMPATIBLE. CAN ONLY BE PURCHASED
 THROUGH A "VAR" OR DISTRIBUTOR.**



09/14/90



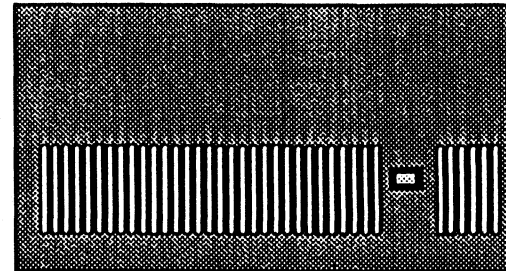
85/190 MB MAXTOR DRIVE CONFIGURATION BOARD

**NOTE 1: DRIVE SELECT HEADERS:
 DRIVE SELECT 0 = 1,2
 DRIVE SELECT 1 = 2,3 ETC.**

NOTE 2: PRESENTLY NOT USED IN ANY ASSEMBLIES.

PART NUMBERS:

**190 MB P/N FSD # 96010871
 MAXTOR MODEL 2190 VENDOR # XT 2190
 FULL-HEIGHT 5 1/4" WINI DRIVES
 ST-506 COMPATIBLE. CAN ONLY BE PURCHASED
 THROUGH A "VAR" OR DISTRIBUTOR.**



09/14/90

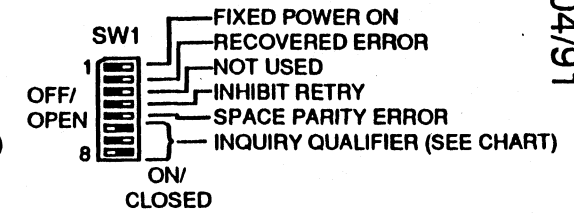
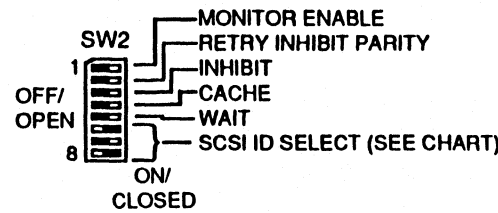
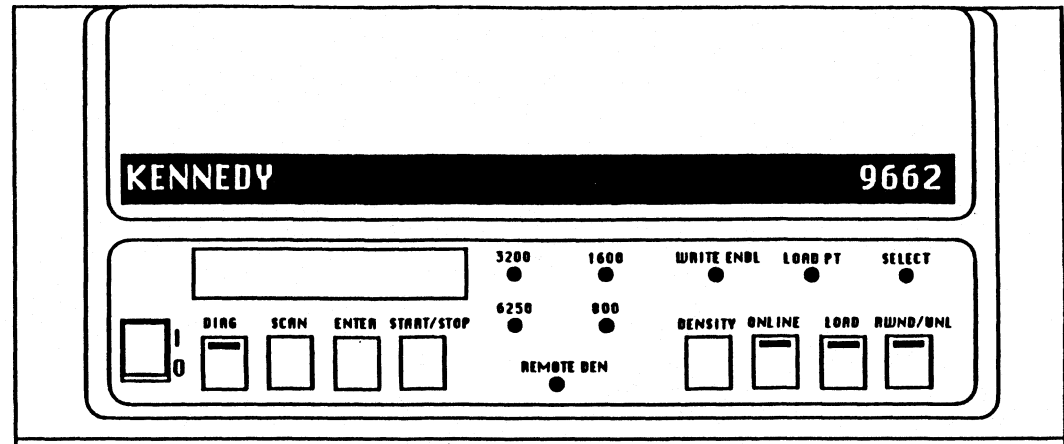
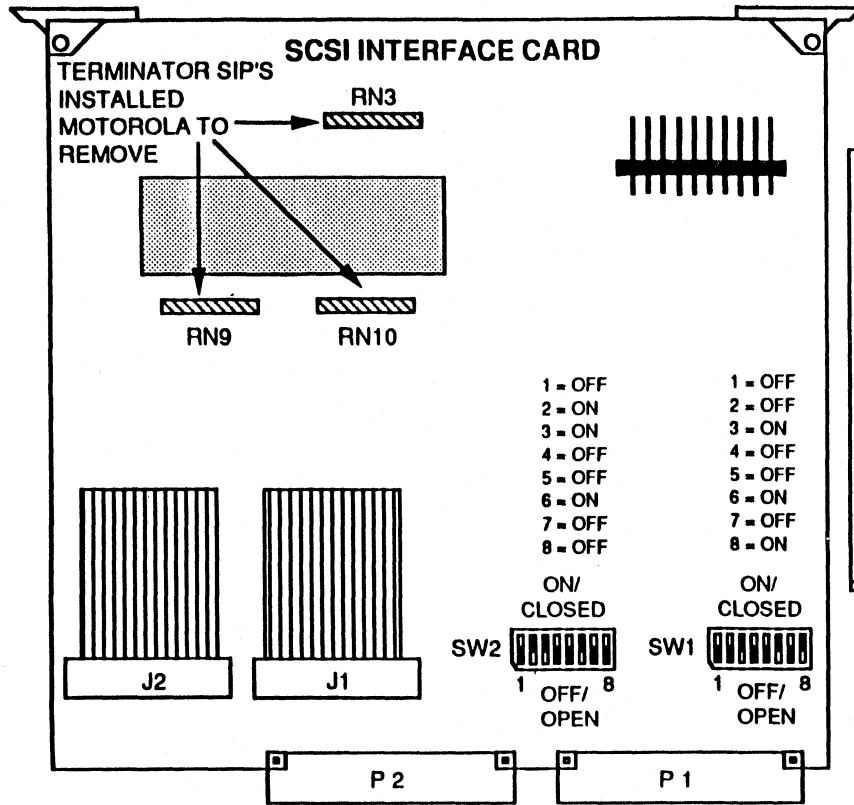
**190 MByte
 MAXTOR
 ST-506 DRIVE
 PA 7**

APPENDIX F

PART NUMBERS:

9-TRACK TAPE DRIVE 01-W2190C01 96011090

KENNEDY MODEL # U92-9662-703 SCSI
9-TRACK, QUAD DENSITY TAPE DRIVE.



03/04/91

NOTE 1: FIRST BOARD BEHIND REAR PANEL.

NOTE 2: SWITCHES S1/S2 = OPEN = OFF, CLOSED = ON.
ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 3: S1 AND S2 ARE SELECTABLE OPTIONS DEPENDENT ON CUSTOMER HARDWARE. SEE SIGNAL NOMENCLATURE FOR PROPER SETTING FOR INDIVIDUAL CUSTOMERS. SETTINGS ABOVE ARE FACTORY SHIP PER REV. E SPEC.

NOTE 4: SET FOR SCSI ADDRESS # 5.
FOR ADDR 3, SET S1-6 OFF, AND S1-7, S1-8 BOTH ON.

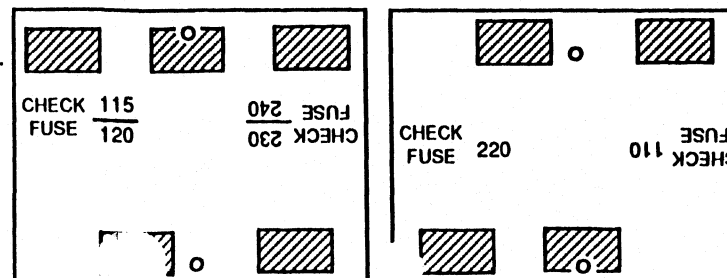
NOTE 5: SW1-2 SHOULD BE OFF TO ALLOW FOR RETRIES.

NOTE 6: USED IN THE FOLLOWING ASSEMBLIES: MVME858-1 & MVME858-2.

SCSI				SCSI
S1-8	7	6	ID	DATA BIT
OFF	OFF	OFF	0	DB0
OFF	OFF	ON	1	DB1
OFF	ON	OFF	2	DB2
OFF	ON	ON	3	DB3
ON	OFF	OFF	4	DB4
ON	OFF	ON	5	DB5
ON	ON	OFF	6	DB6
ON	ON	ON	7	DB7

			QUALIFIER	
S2-8	7	6	BIT	CODE
OFF	OFF	OFF	0	00
OFF	OFF	ON	1	01
OFF	ON	OFF	2	02
OFF	ON	ON	3	04
ON	OFF	OFF	4	08
ON	OFF	ON	5	10
ON	ON	OFF	6	20
ON	ON	ON	7	40

VOLTAGE SELECTOR CARD



**9-TRACK
KENNEDY
SCSI TAPE DRIVE
PA 1**

1 = OFF
2 = ON
3 = ON
4 = ON
5 = ON
6 = ON
7 = OFF
8 = OFF

1 = OFF
2 = ON
3 = OFF
4 = OFF
5 = OFF
6 = ON
7 = OFF
8 = ON

1 = OFF
2 = ON
3 = ON
4 = ON
5 = ON
6 = ON
7 = OFF
8 = OFF

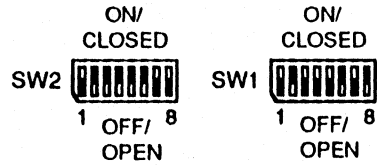
1 = OFF
2 = OFF
3 = ON
4 = OFF
5 = OFF
6 = ON
7 = OFF
8 = ON

1 = OFF
2 = ON
3 = ON
4 = ON
5 = ON
6 = ON
7 = OFF
8 = OFF

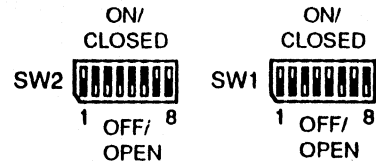
1 = OFF
2 = OFF
3 = ON
4 = OFF
5 = ON
6 = ON
7 = OFF
8 = ON

1 = OFF
2 = ON
3 = ON
4 = OFF
5 = OFF
6 = ON
7 = OFF
8 = OFF

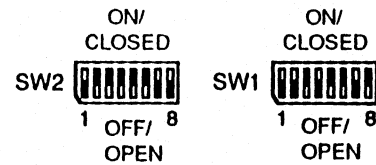
1 = OFF
2 = OFF
3 = ON
4 = OFF
5 = OFF
6 = ON
7 = OFF
8 = ON



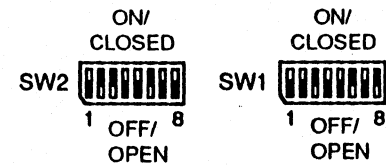
WAS



IS

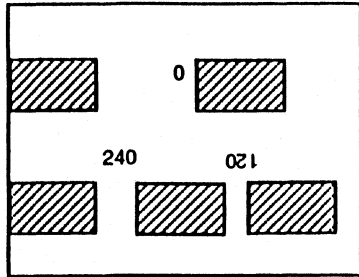


AUSTRALIA

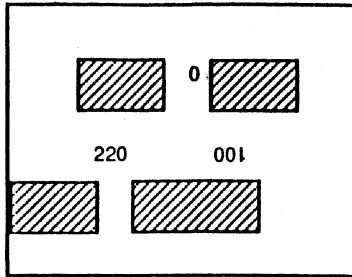


NEW SPEC.

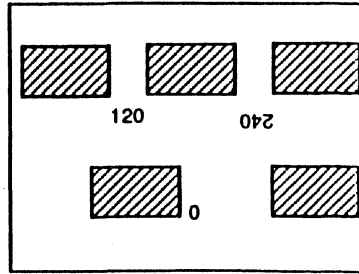
03/28/91



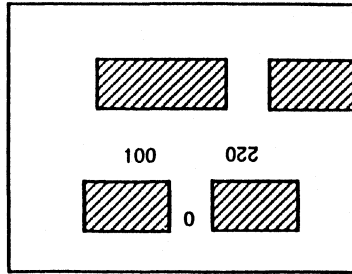
CONFIGURED FOR 240VAC



CONFIGURED FOR 220VAC



CONFIGURED FOR 120VAC



CONFIGURED FOR 100VAC

PART NUMBERS:

9-TRACK 1600 bpi 01-W5310B01 96010896
 PERTEC 1/2" TAPE DRIVE TABLE TOP
 VENDOR # FS1000

INPUT VOLTAGE	FUSE
100VAC	4A SLOW-BLOW
120VAC	4A SLOW-BLOW
220VAC	2A SLOW-BLOW
240VAC	2A SLOW-BLOW

NOTE 1: CONFIGURATION INSTRUCTIONS ARE IN THE OPERATING AND SERVICE MANUAL # 110831.

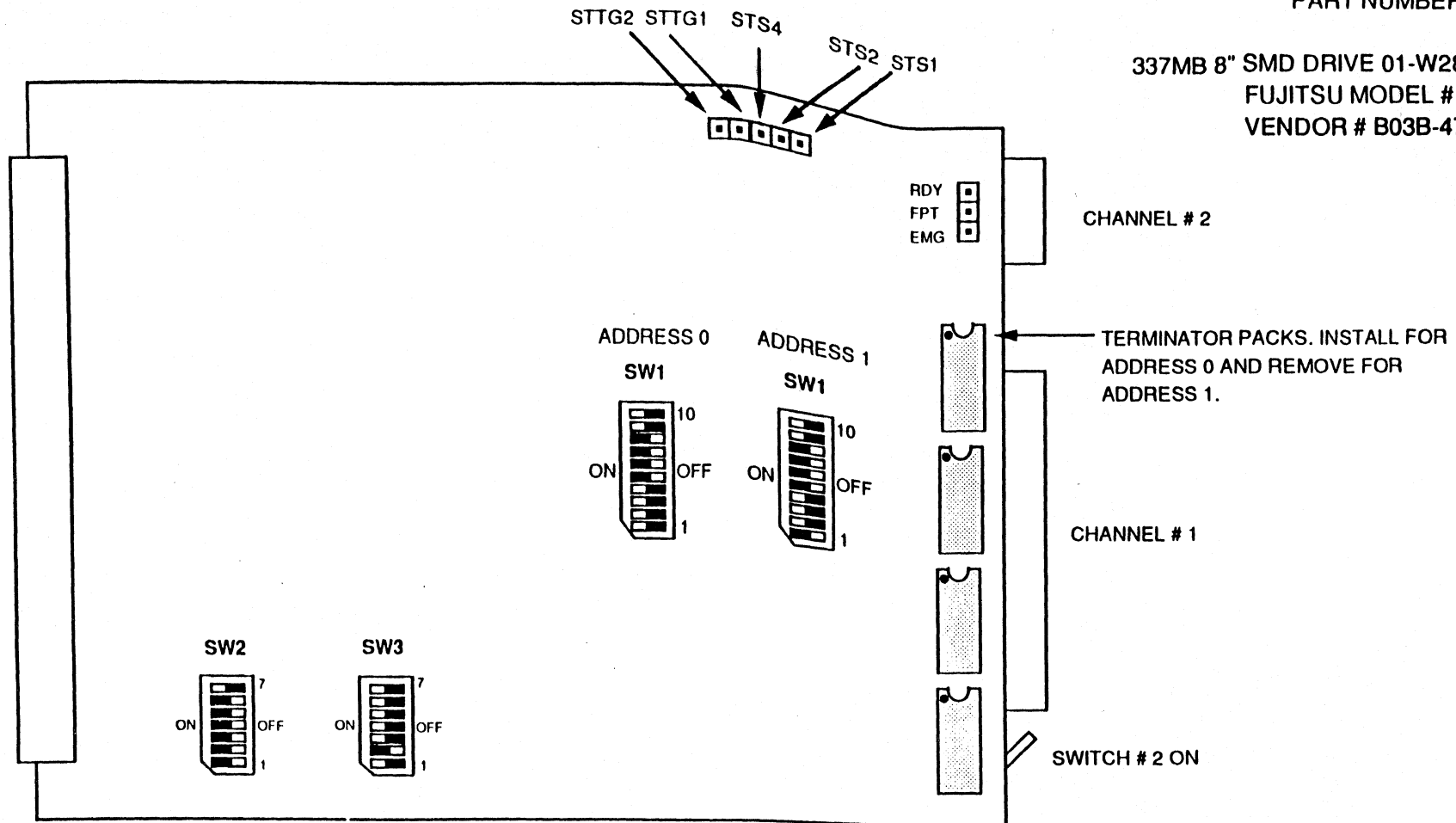
NOTE 2: USED IN THE FOLLOWING ASSEMBLIES: MVME859-1 & MVME859-2.

03/08/90

**9-TRACK TAPE
 PERTEC
 FS1000
 PA 2**

PART NUMBERS:

337MB 8" SMD DRIVE 01-W2843B01 96010818
FUJITSU MODEL # M2333K
VENDOR # B03B-4765-B003A



09/17/90

NOTE 1: THIS DRIVE ASSEMBLY HOUSES TWO DRIVES AND IS SUPPORTED BY THE DELTA SYSTEMS. THE DELTA SUPPORTS UP TO TWO OF THESE SMD DRIVES.

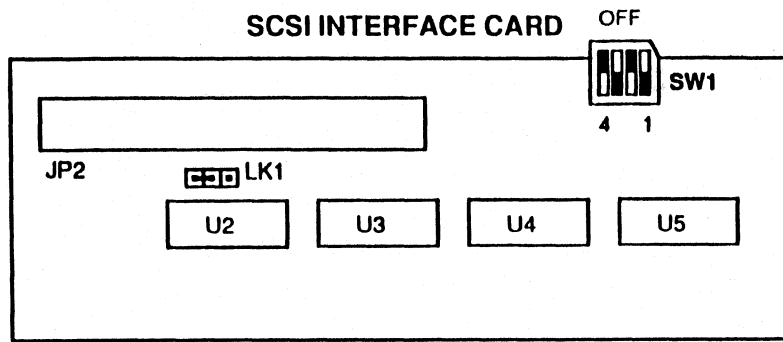
NOTE 2: SW1 IS EITHER SET UP FOR ADDRESS 0 OR ADDRESS 1, DEPENDING ON THE CUSTOMER REQUIREMENTS. SEE ABOVE SETTINGS.

NOTE 3: ACTIVE PART OF SWITCH IS DARKENED AREA.

NOTE 3: USED IN THE FOLLOWING ASSEMBLIES: SYS360D296-1, SYS360D296-2, MVME861F & MVME861K.

**SMD DRIVE
M2333
FUJITSU
PAGE 3**

SCSI INTERFACE CARD



PART NUMBERS:

9-TRACK TAPE DRIVE 01-W2671C01 TBD
M4 DATA MODEL # 9905D SCSI
9-TRACK, DUAL DENSITY TAPE DRIVE.

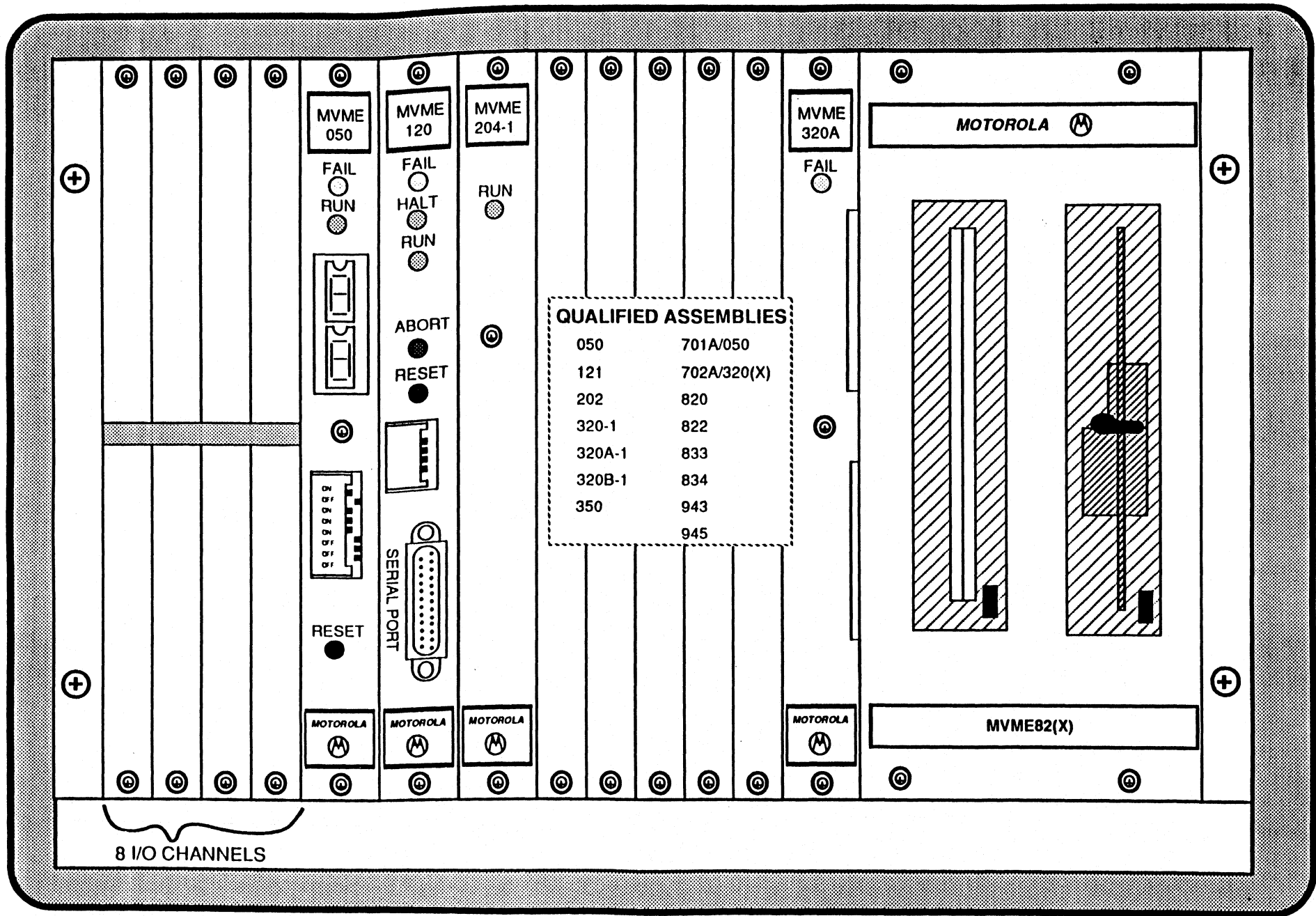
9-TRACK TAPE DRIVE 01-W2512C01 TBD
M4 DATA MODEL # 9914R SCSI
9-TRACK, QUAD DENSITY TAPE DRIVE.

NOTE 1: ACTIVE PART OF SWITCH IS DARKENED AREA.

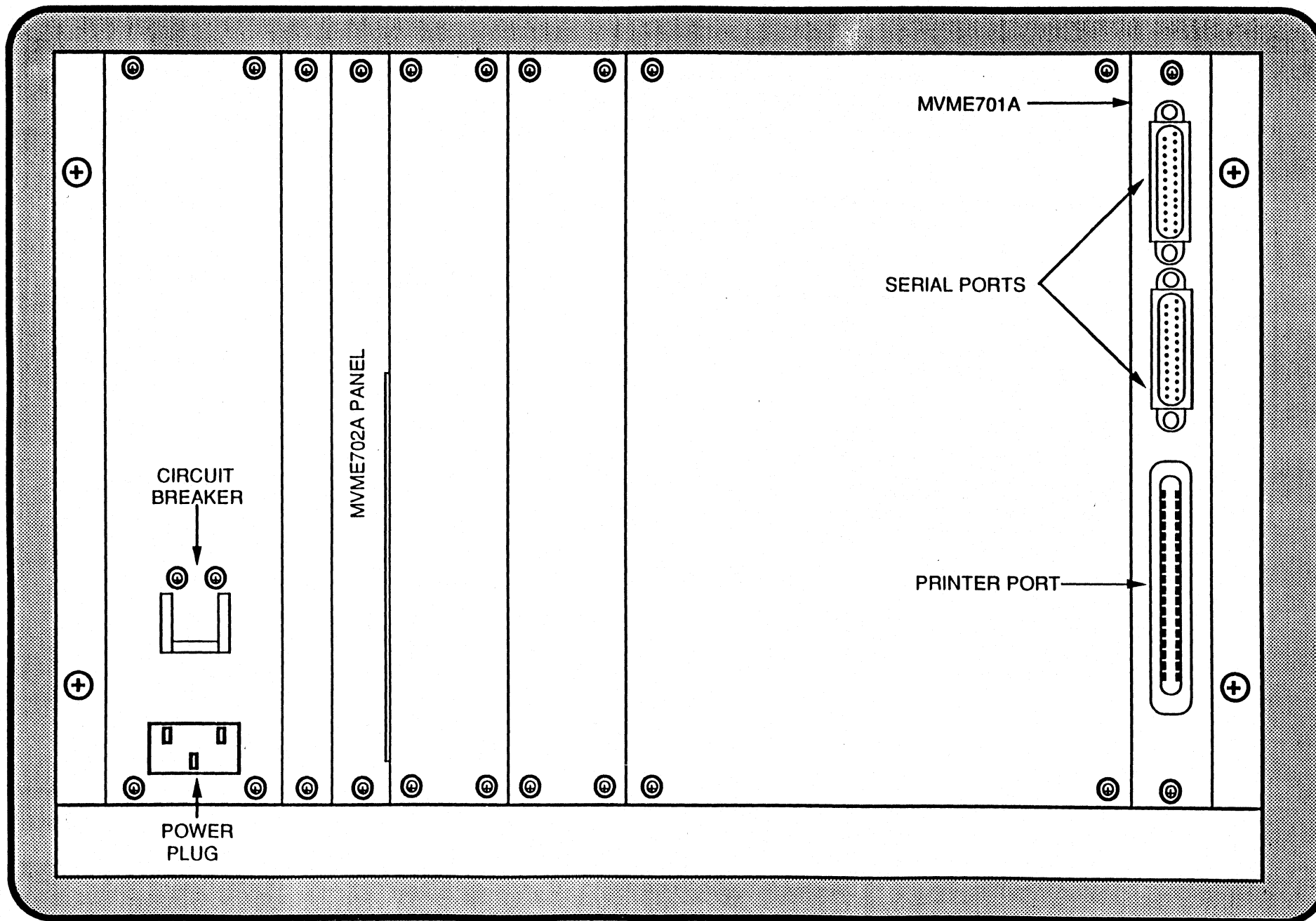
11/19/91

9-TRACK
M4 DATA
SCSI TAPE DRIVE
PAGE 1

APPENDIX G



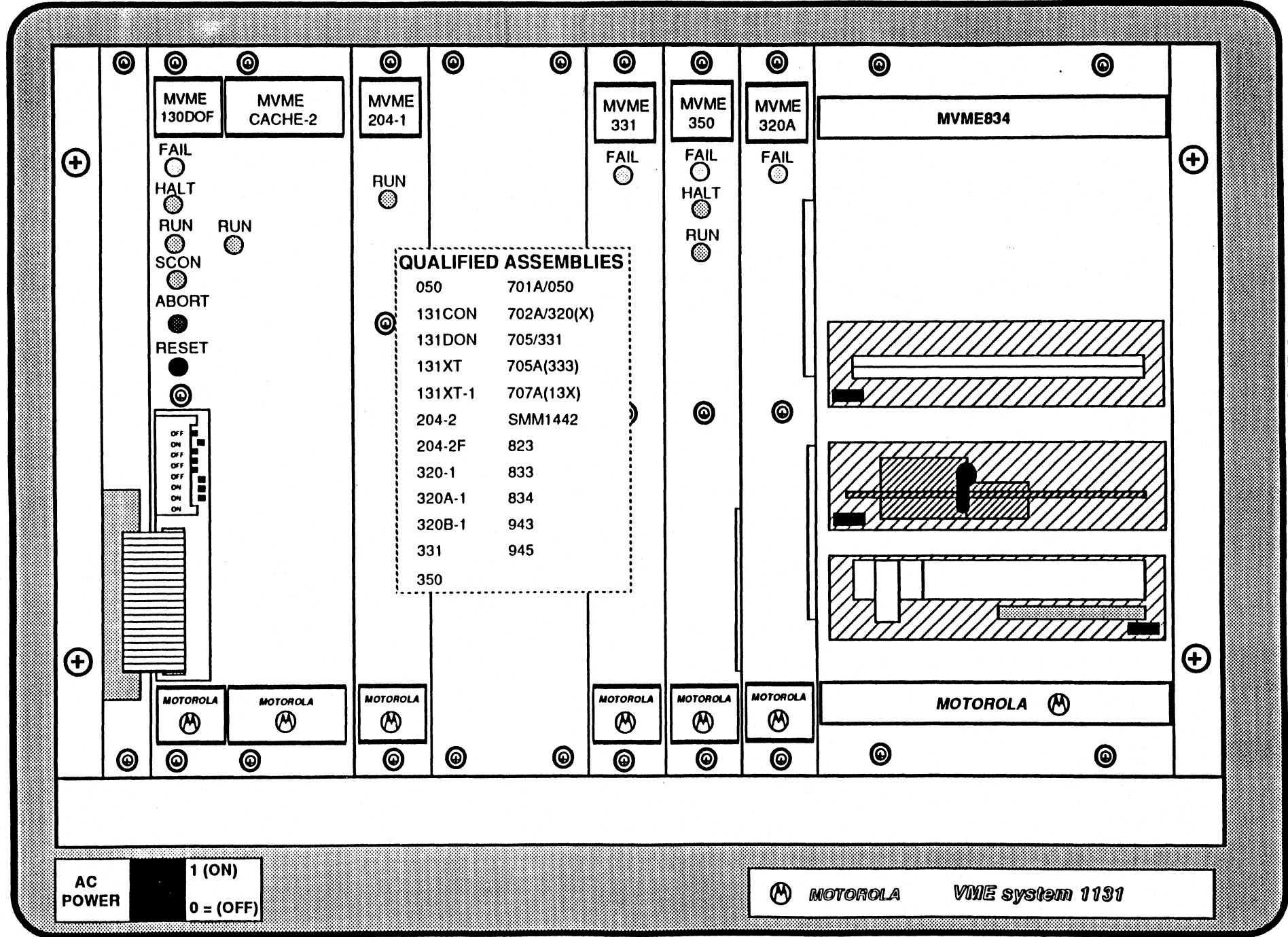
02/19/90



11/10/89

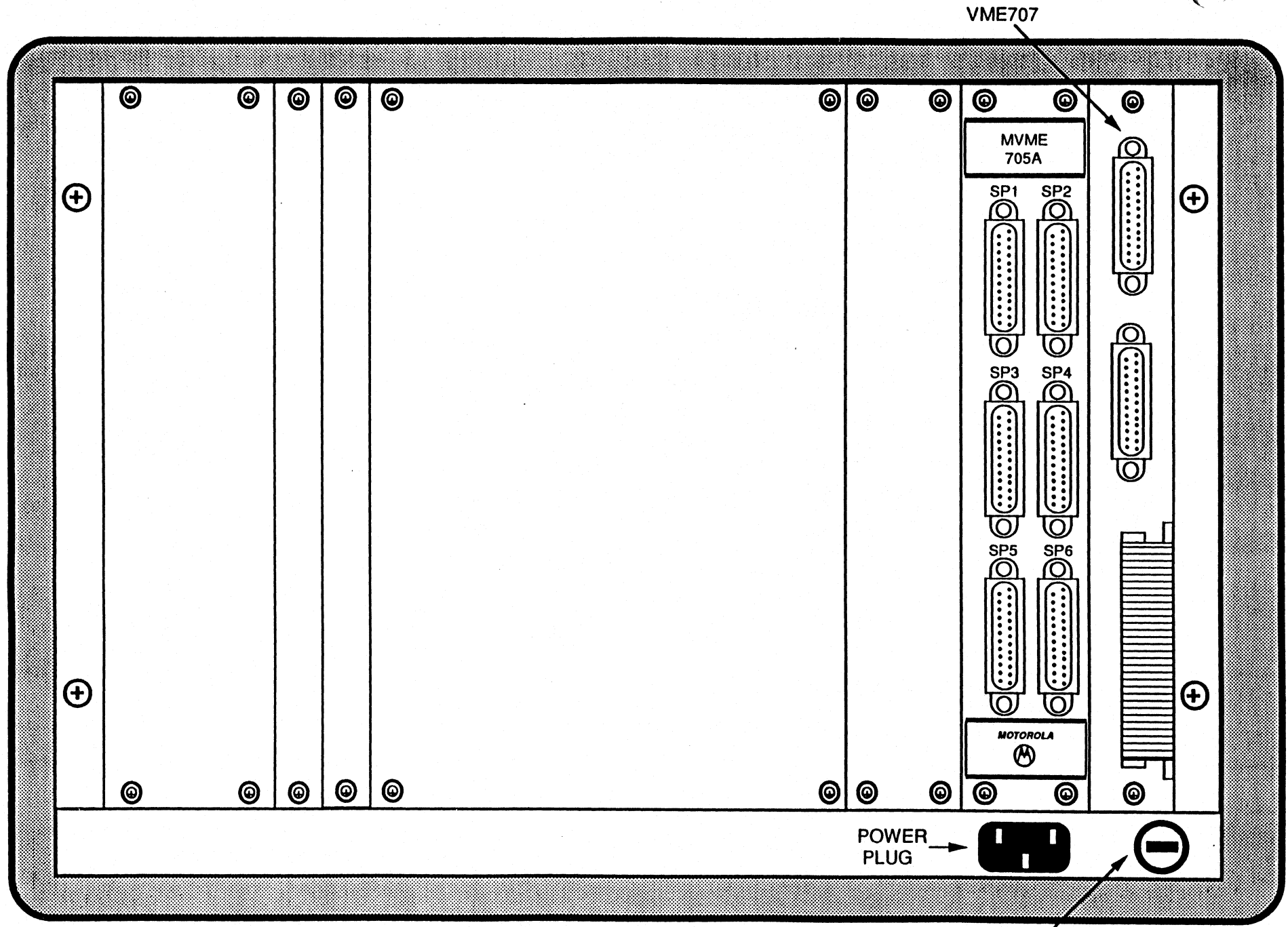
SYS1121 REAR VIEW

02/19/90



AC POWER 1 (ON)
 0 = (OFF)

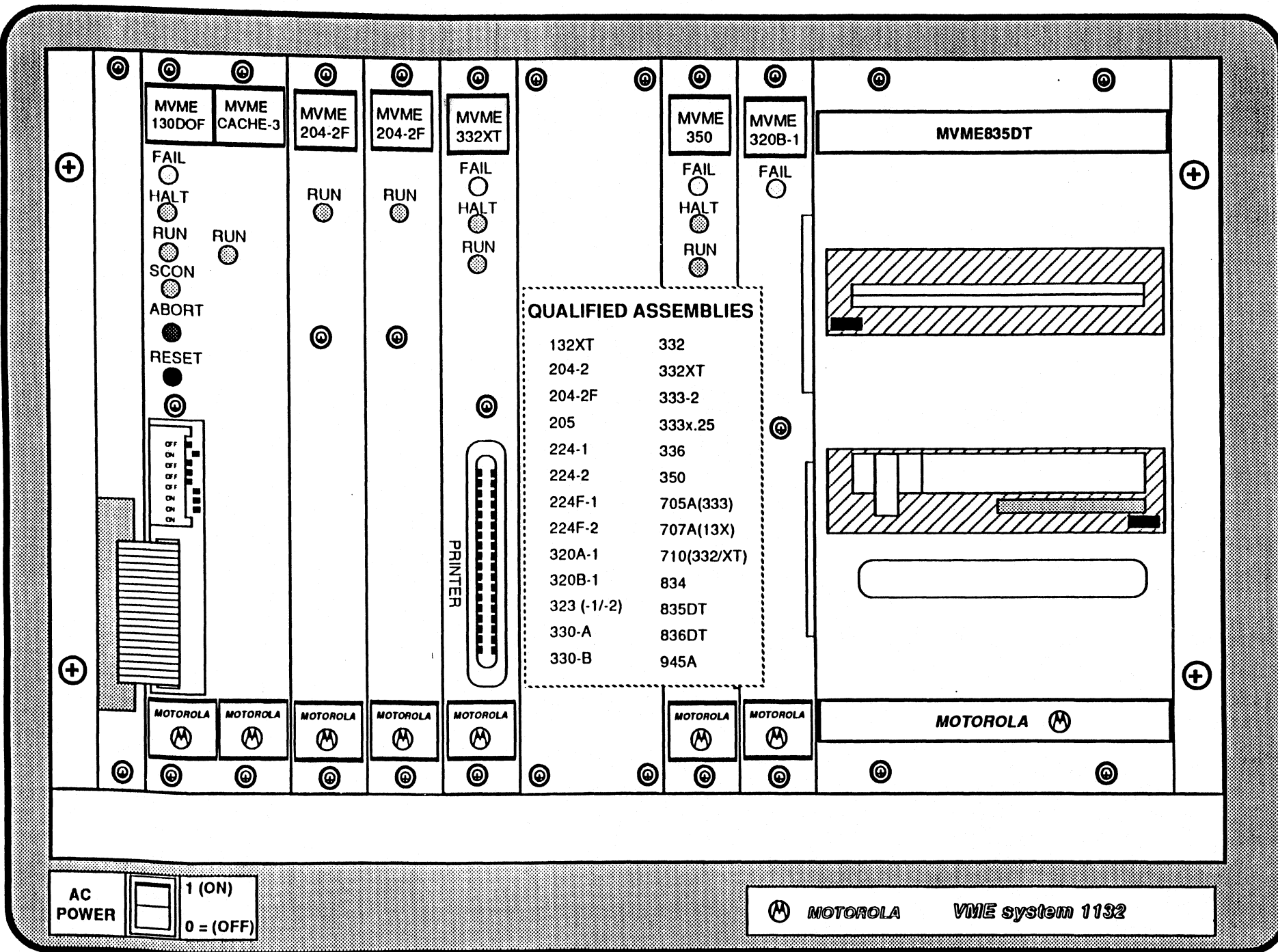
MOTOROLA VME system 1131



11/13/89

SYS1131 REAR VIEW

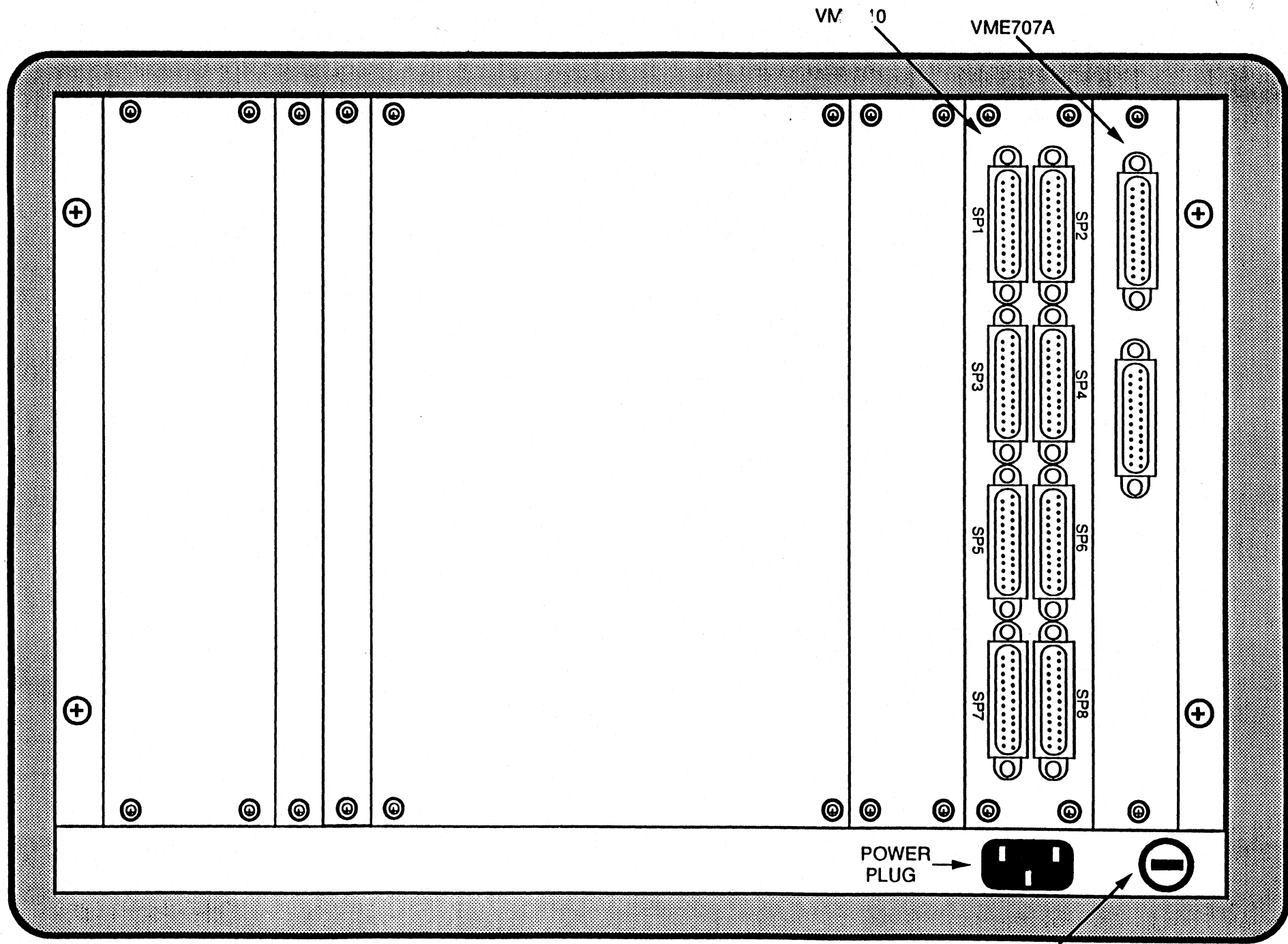
FUSE



02/16/90

QUALIFIED ASSEMBLIES

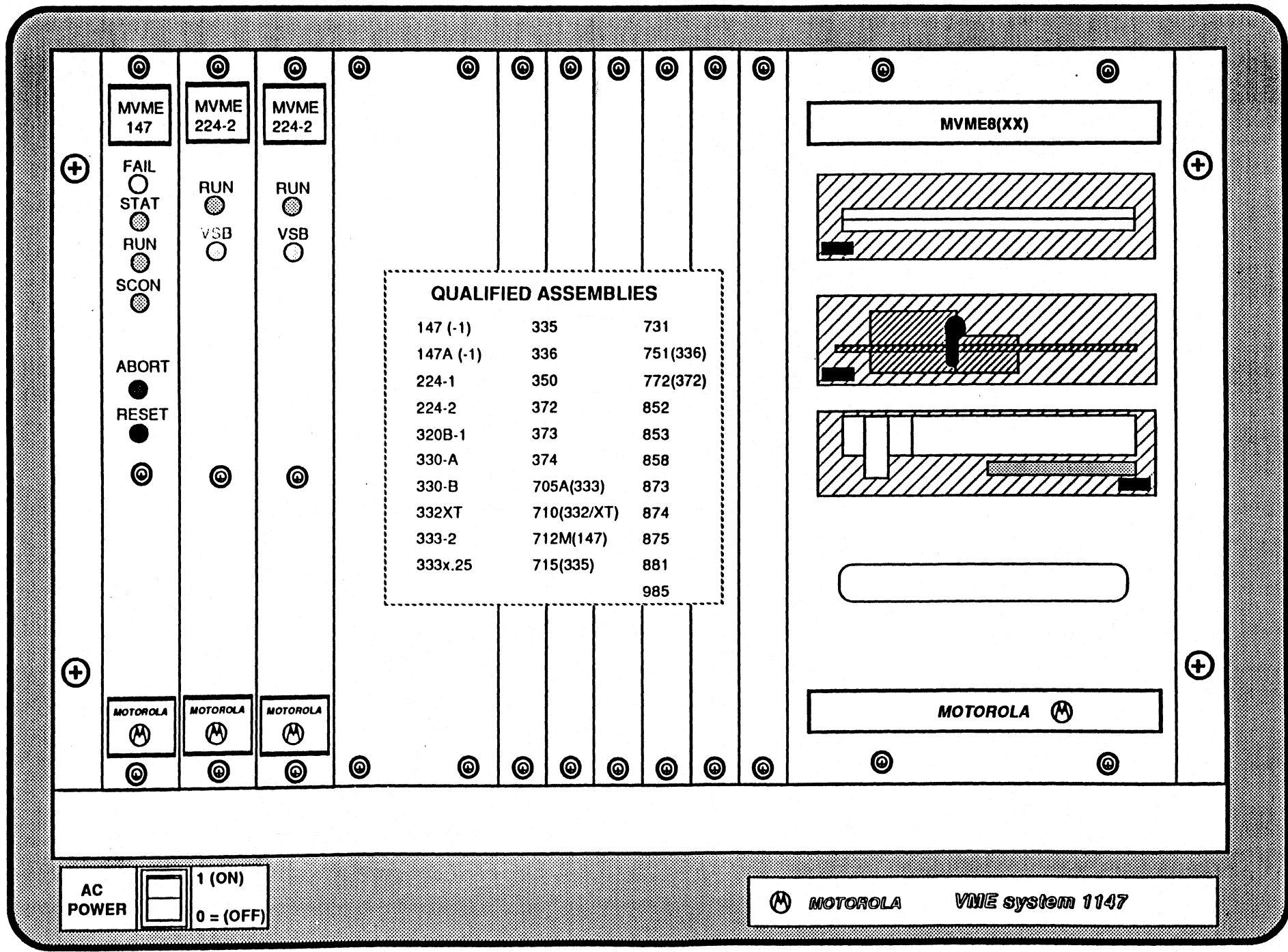
- | | |
|-------------|-------------|
| 132XT | 332 |
| 204-2 | 332XT |
| 204-2F | 333-2 |
| 205 | 333x.25 |
| 224-1 | 336 |
| 224-2 | 350 |
| 224F-1 | 705A(333) |
| 224F-2 | 707A(13X) |
| 320A-1 | 710(332/XT) |
| 320B-1 | 834 |
| 323 (-1/-2) | 835DT |
| 330-A | 836DT |
| 330-B | 945A |



11/13/89

SYS1132 REAR VIEW

FUSE



MVME
147

FAIL
STAT
RUN
SCON

ABORT
RESET

MOTOROLA

MVME
224-2

RUN
VSB

MOTOROLA

MVME
224-2

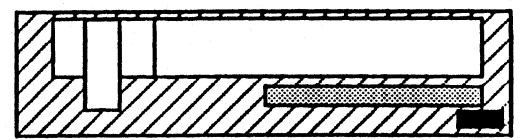
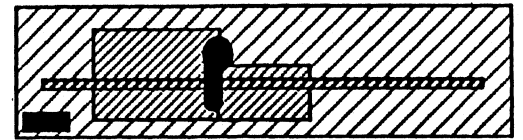
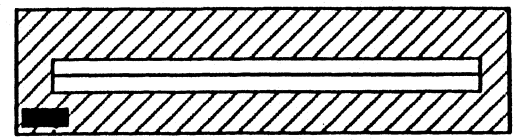
RUN
VSB

MOTOROLA

QUALIFIED ASSEMBLIES

147 (-1)	335	731
147A (-1)	336	751(336)
224-1	350	772(372)
224-2	372	852
320B-1	373	853
330-A	374	858
330-B	705A(333)	873
332XT	710(332/XT)	874
333-2	712M(147)	875
333x.25	715(335)	881
		985

MVME8(XX)



MOTOROLA

AC
POWER



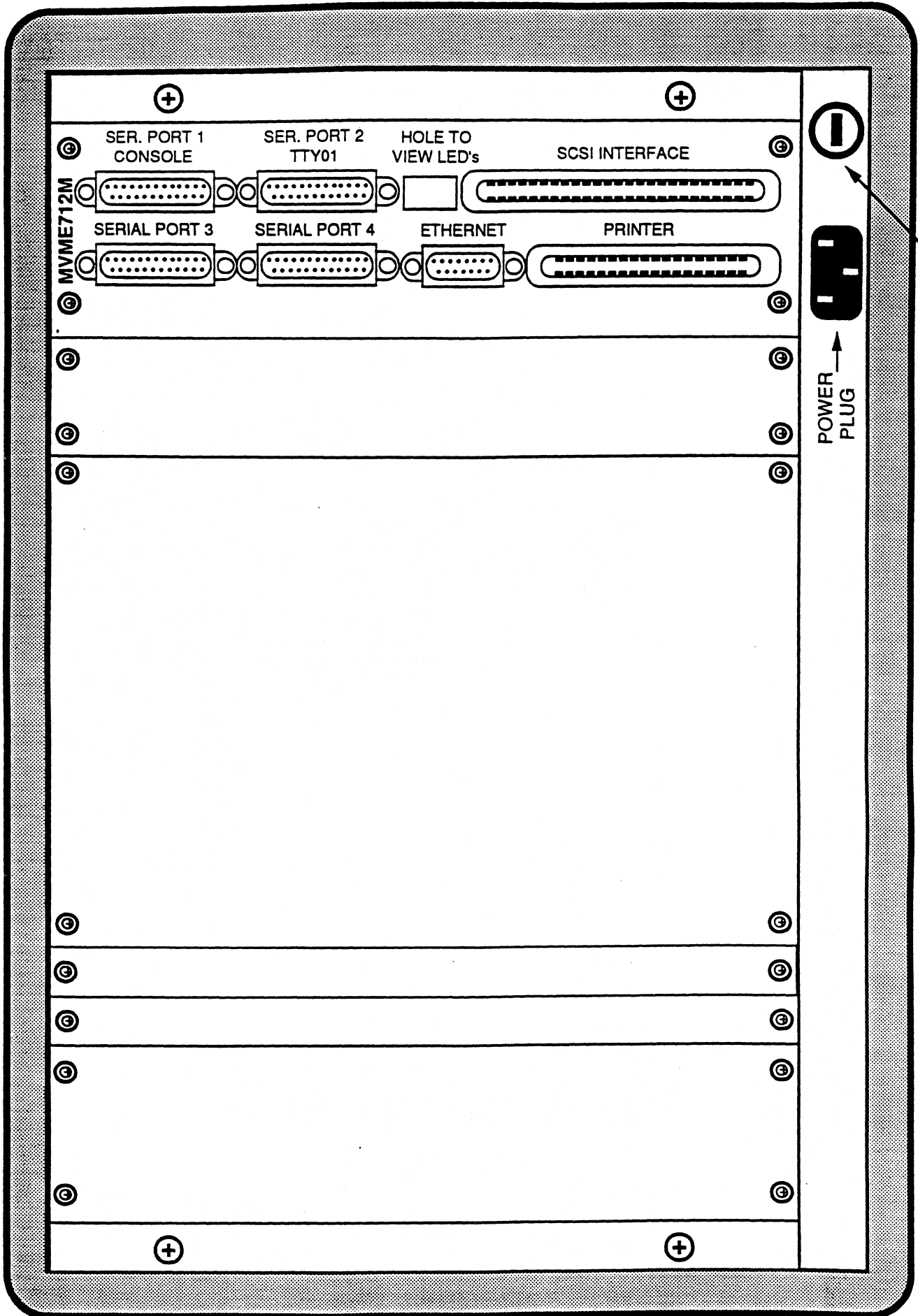
1 (ON)
0 = (OFF)

MOTOROLA

VME system 1147

02/16/90

11/14/89



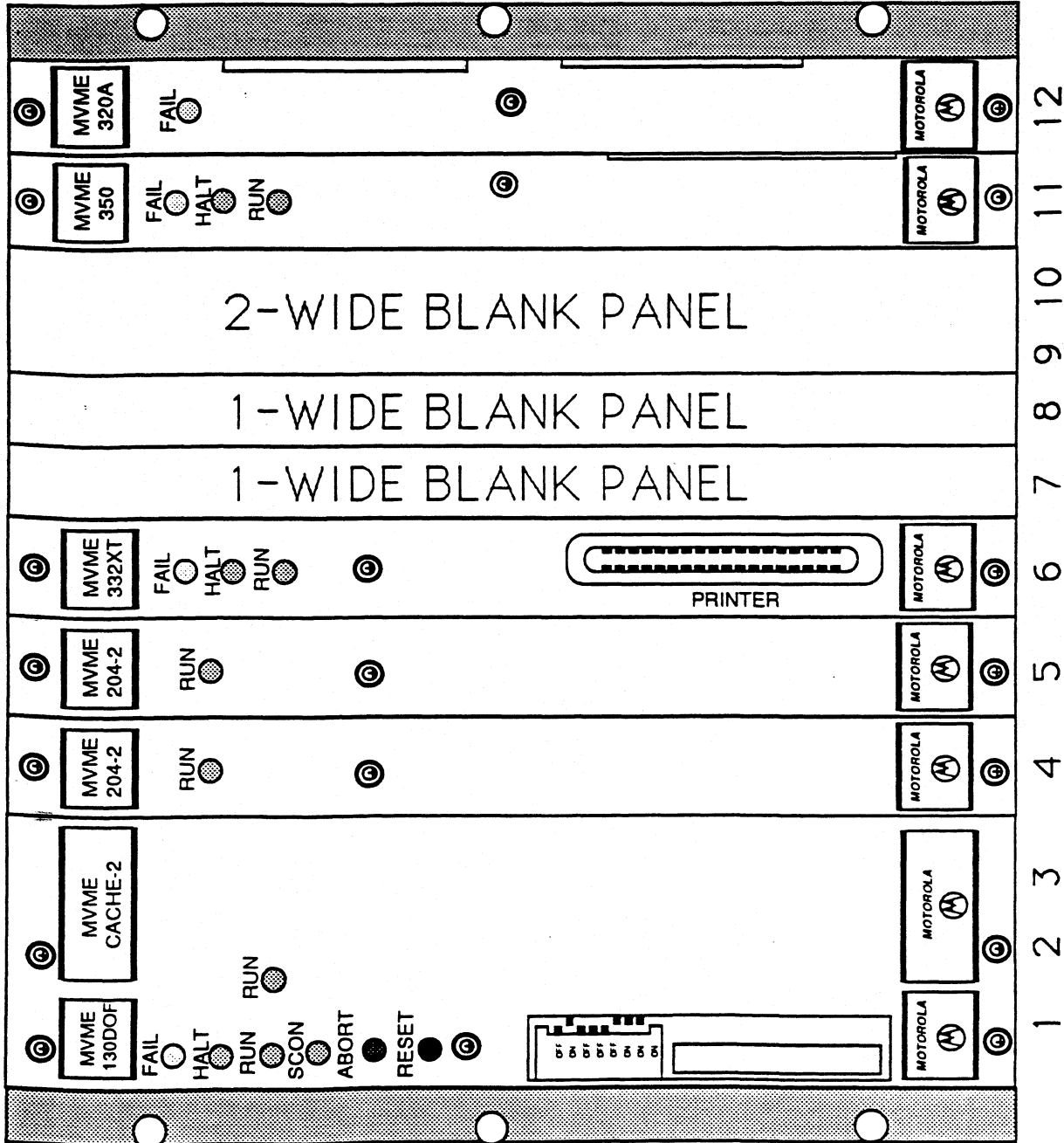
FUSE

POWER PLUG

SYS1147 REAR VIEW

02/19/90

SYS2016NY011/012 CARD CAGE VIEW.

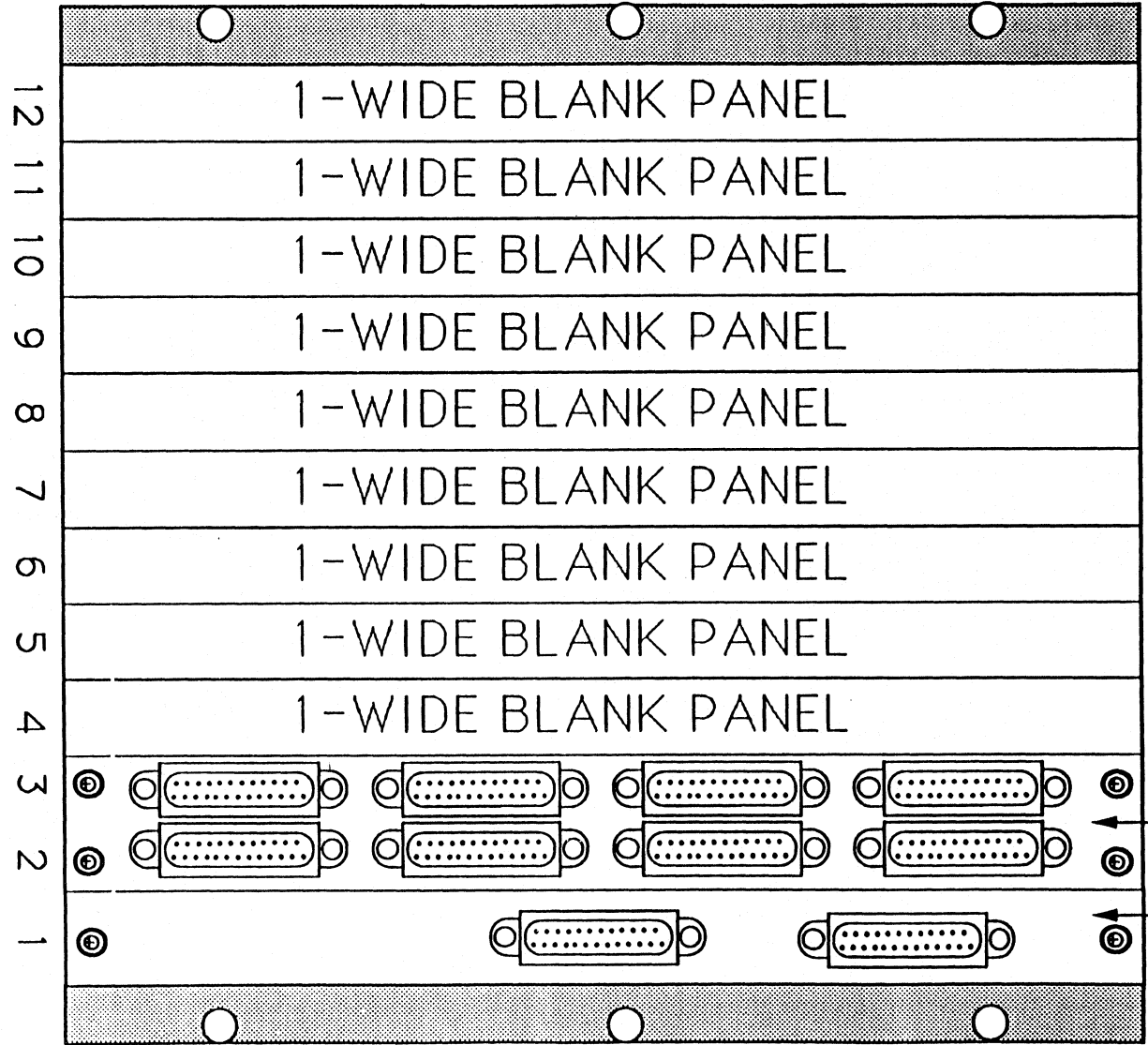


QUALIFIED ASSEMBLIES

131XT	320A	330-B	336	707(13X)	851
131DOF	320B	332	350	710(332/XT)	955
204-2	320B-1	332XT	355	710F	
204-2F	323 (-1/-2)	333-2	360	751(336)	
205	330-A	333x.25	705A(333)	841	

SYS2016NY011/012

I/O PANEL VIEW.



SYS2316NY

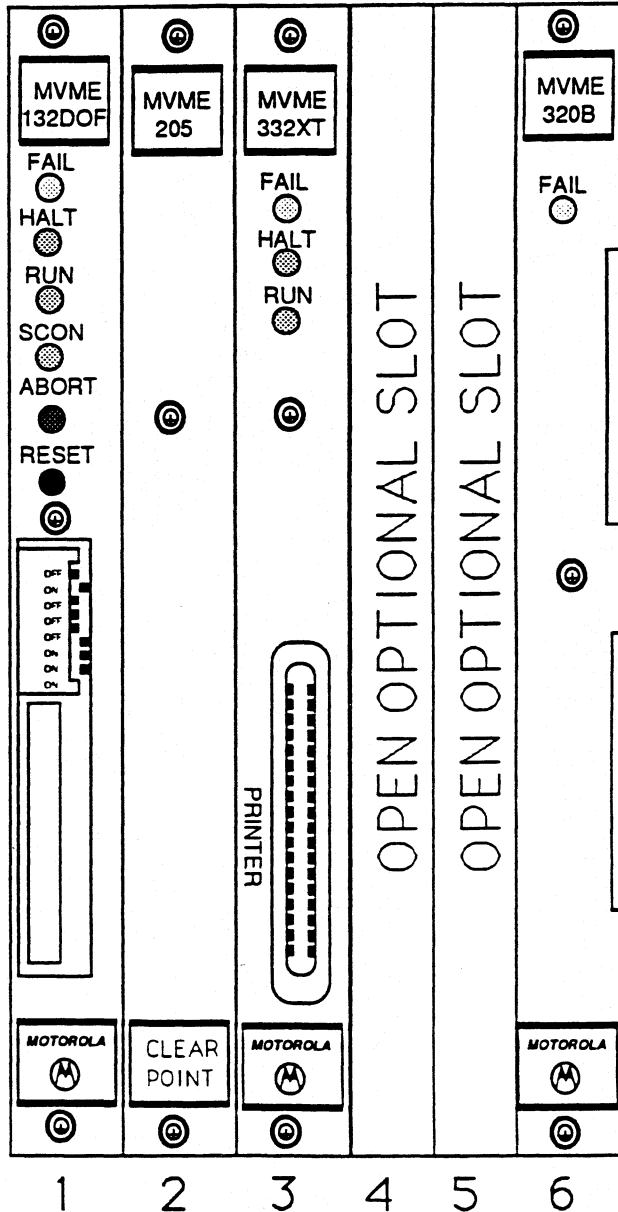
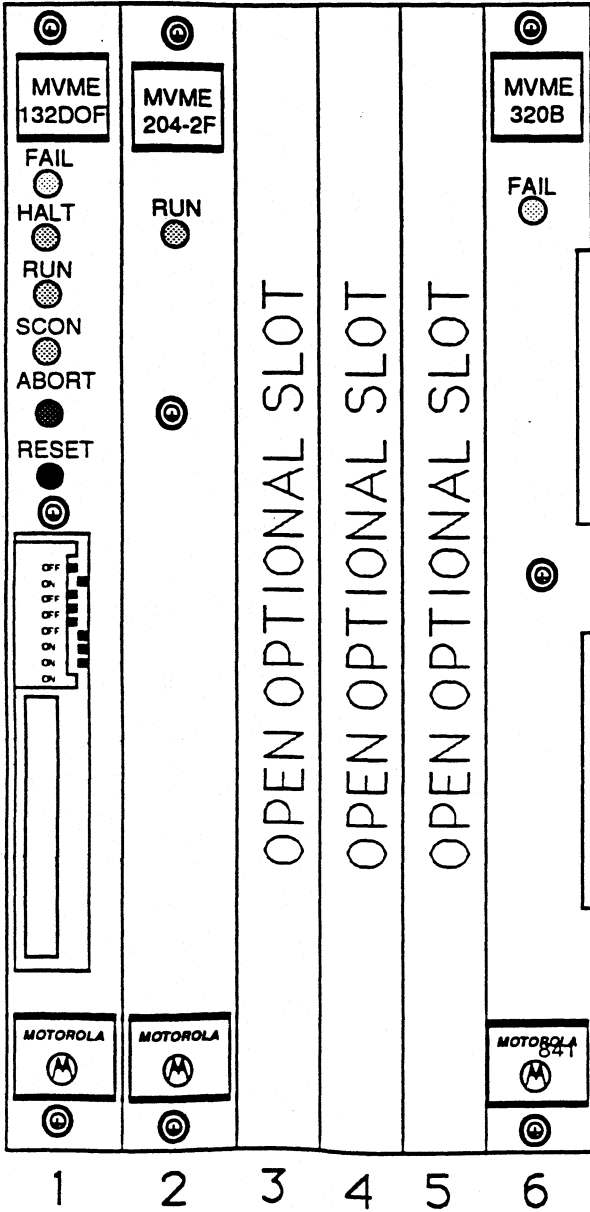
SYS2316NY

011/012

021/022

CARD CAGE VIEW.

CARD CAGE VIEW.



QUALIFIED ASSEMBLIES

132DOF	320B	332	336	710F	851
204-2F	320B-1	332XT	350	715(335)	953
205	323 (-1/-2)	333-2	705A(333)	751(336)	
224-1	330-A	333x.25	707(13X)	832	
224-2	330-B	335	710(332/XT)	842	

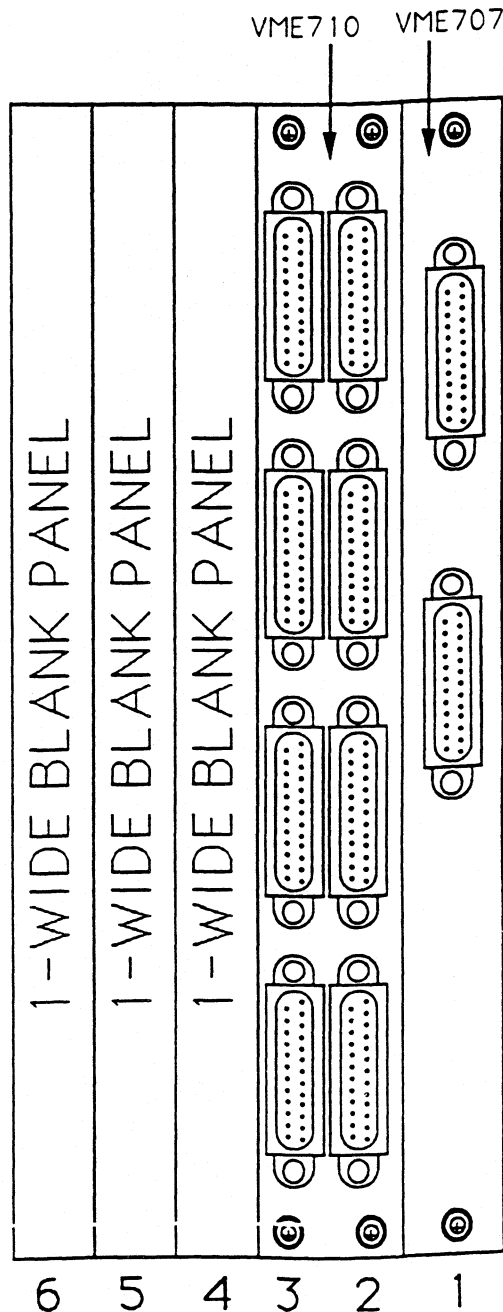
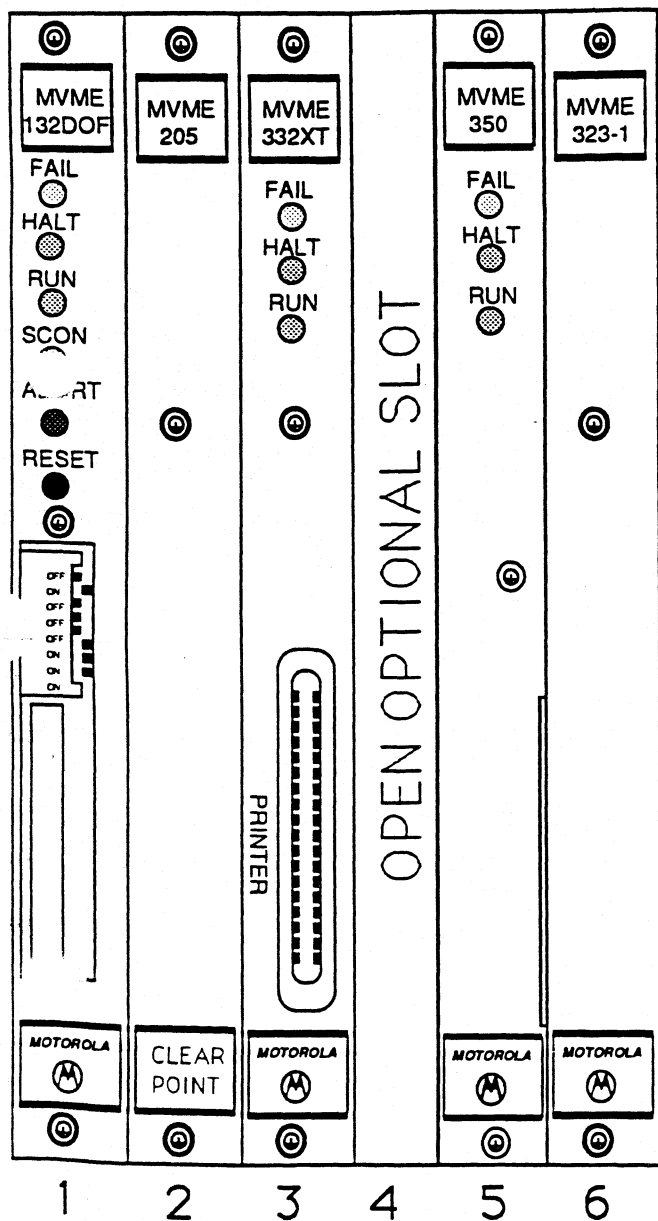
SYS2316NY

031/032

CARD CAGE VIEW.

ALL SYS2316's

I/O PANEL VIEW.



QUALIFIED ASSEMBLIES

132DOF	320B	332	336	710F	851
204-2F	320B-1	332XT	350	715(335)	953
105	323 (-1/-2)	333-2	705A(333)	751(336)	
224-1	330-A	333x.25	707(13X)	832	
224-2	330-B	335	710(332/XT)	842	

SYS2334NY

02/19/90

SYS2334NY

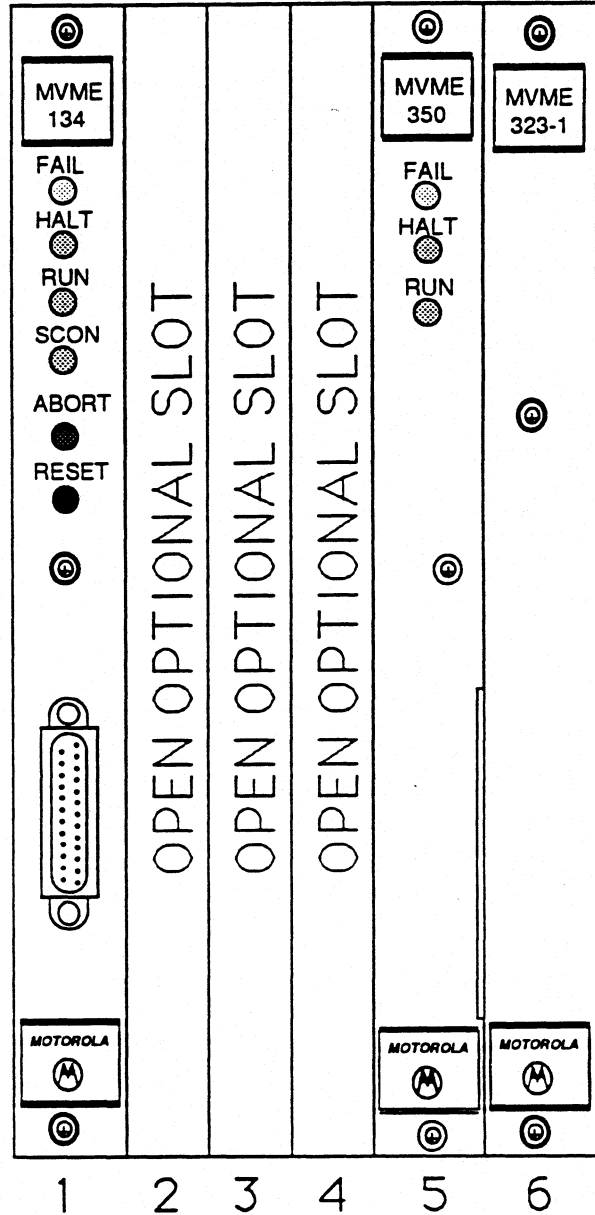
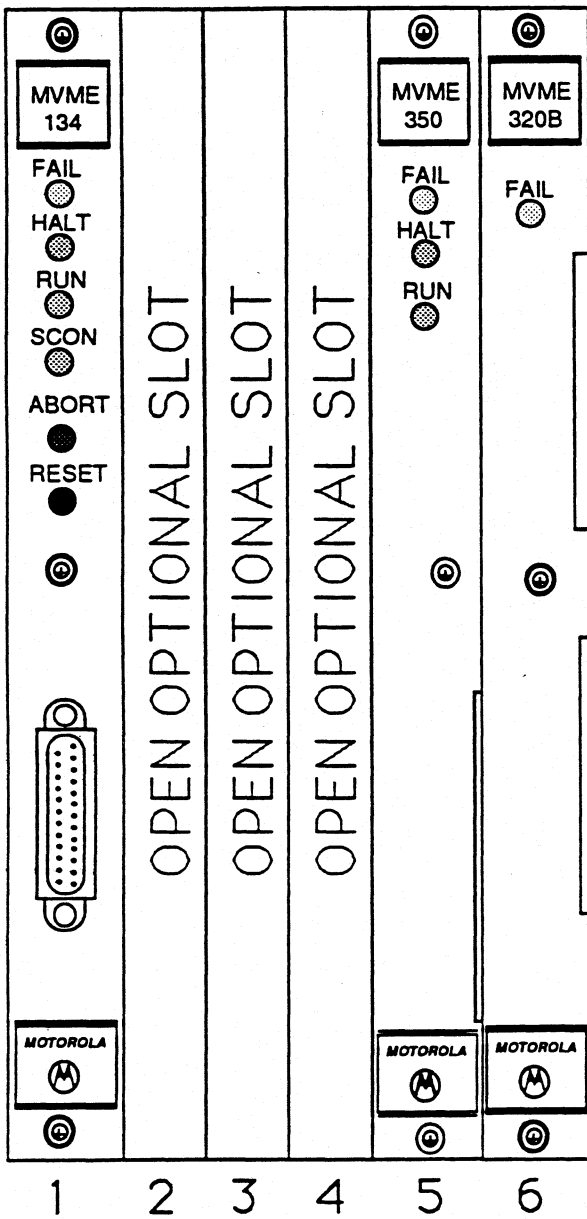
PAGE 13

011/012

021/022

CARD CAGE VIEW.

CARD CAGE VIEW.



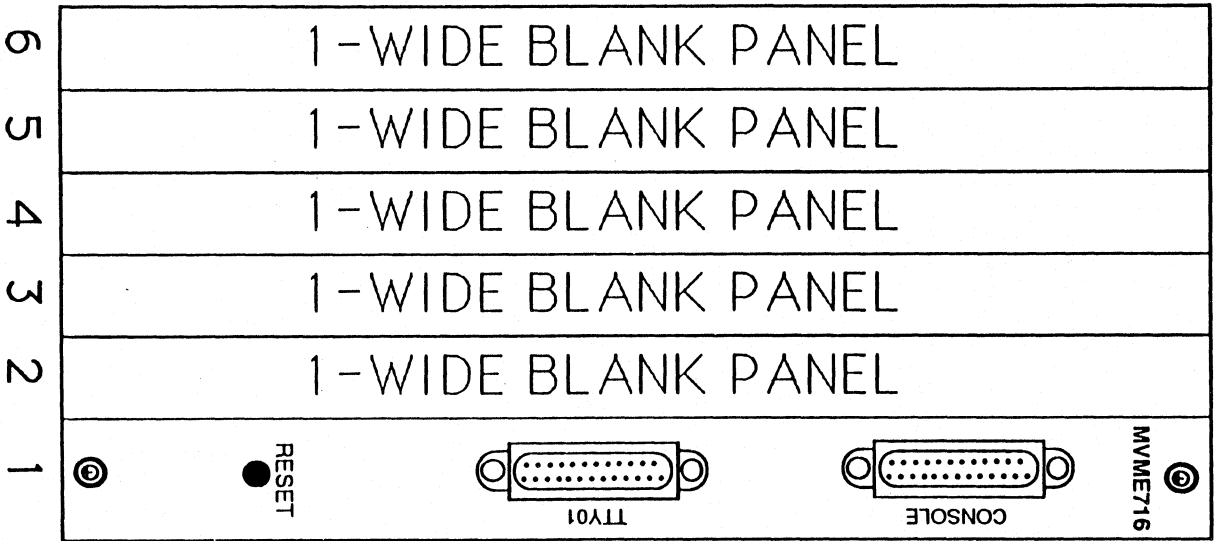
QUALIFIED ASSEMBLIES

134	323 (-1/-2)	333x.25	710(332/XT)	841
224-1	330-A	335	715(335)	842
224-2	330-B	336	716(134)	851
320B	332XT	350	731	953
320B-1	333-2	705A(333)	751(336)	

ALL

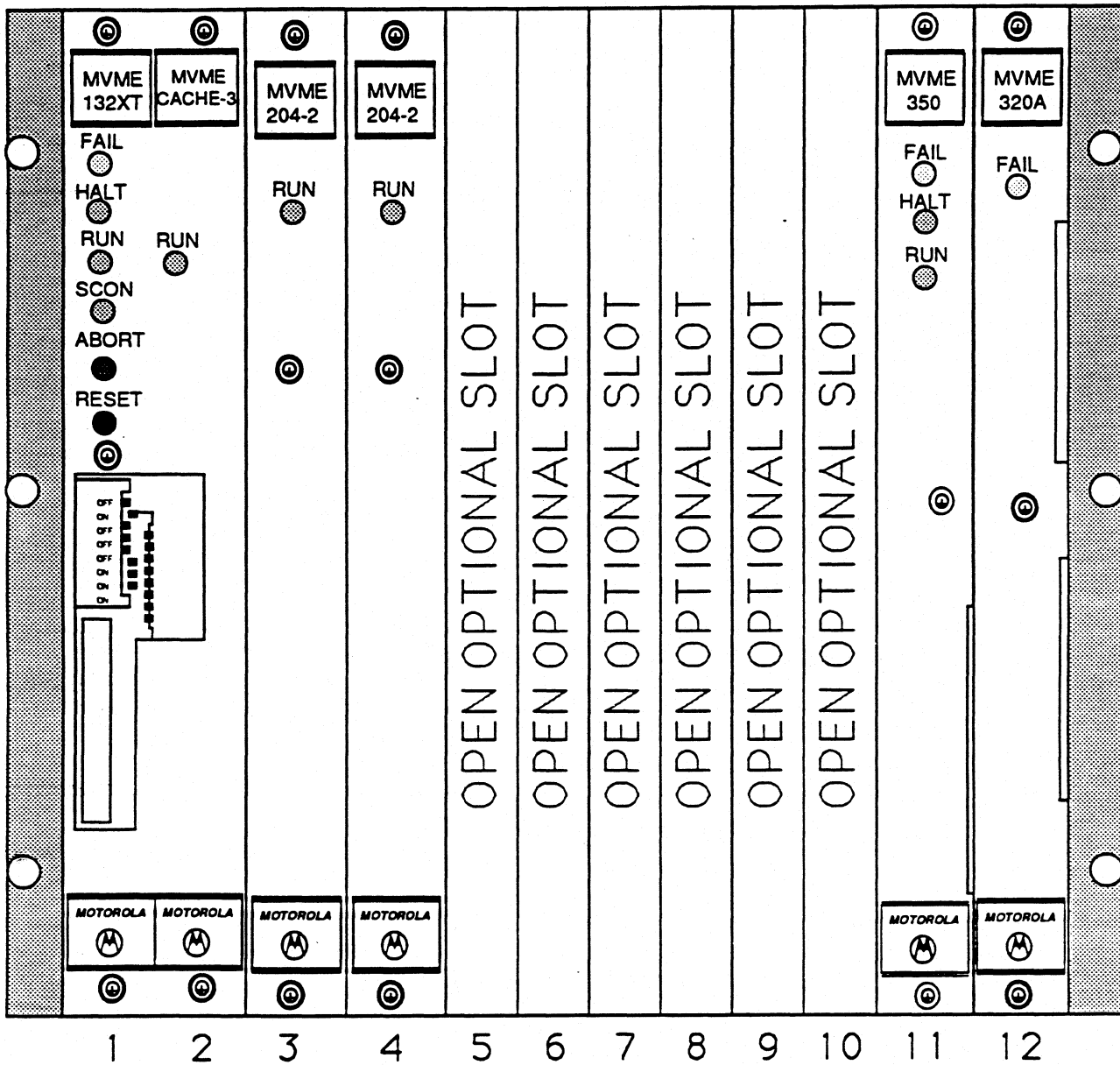
SYS2334'S

I/O PANEL VIEW.



SYS2616NY011/012

CARD CAGE VIEW.

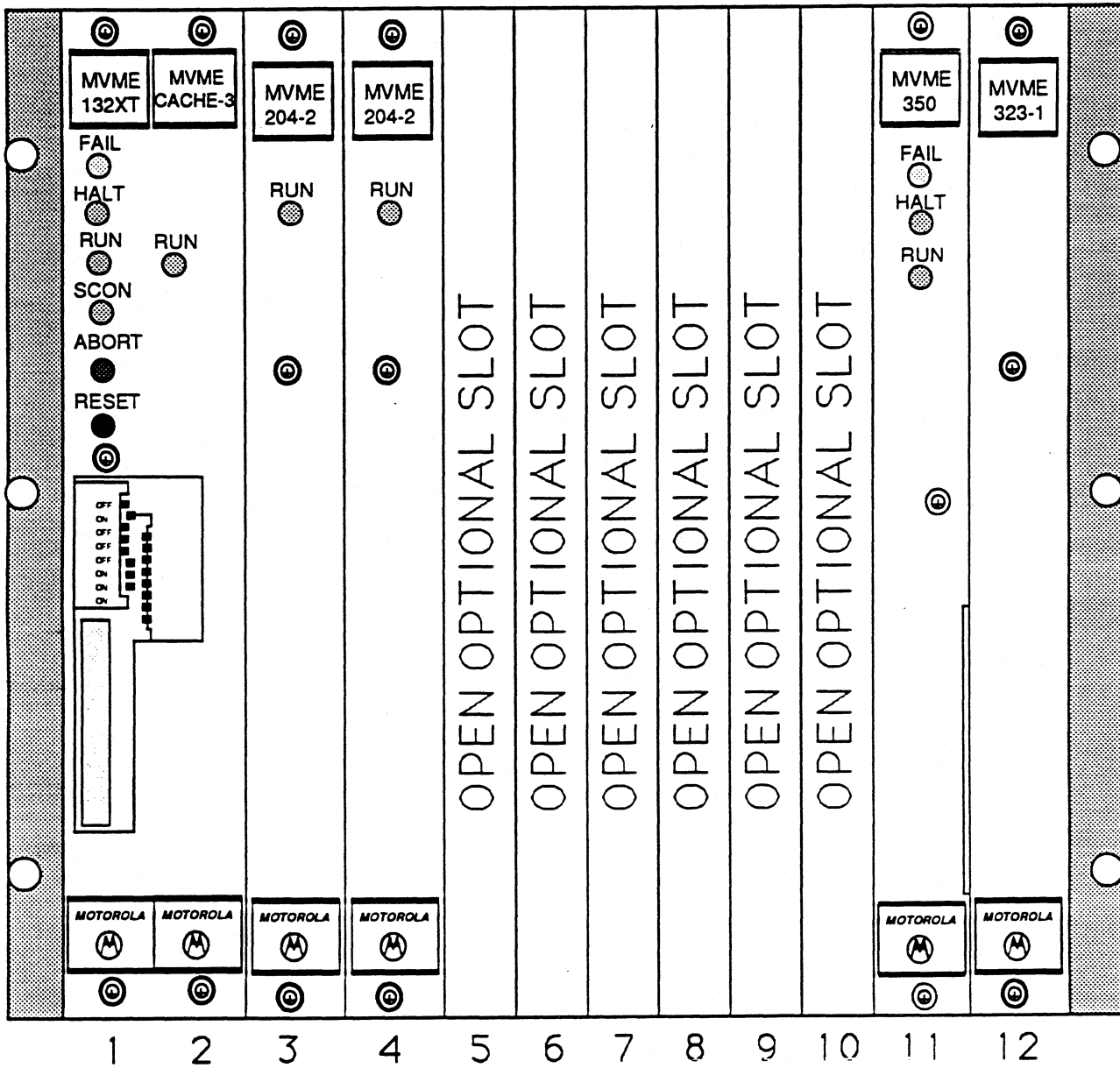


QUALIFIED ASSEMBLIES

132XT	320B	332	350	707A(13X)	841
204-2F	320B-1	332XT	355	710(332/XT)	842
205	323 (-1/-2)	333-2	360	710F	851
224-1	330-A	333x.25	702A/320(X)	714M(141/18X)	955
224-2	330-B	336	705A(333)	751(336)	

SYS26 16NY021/022

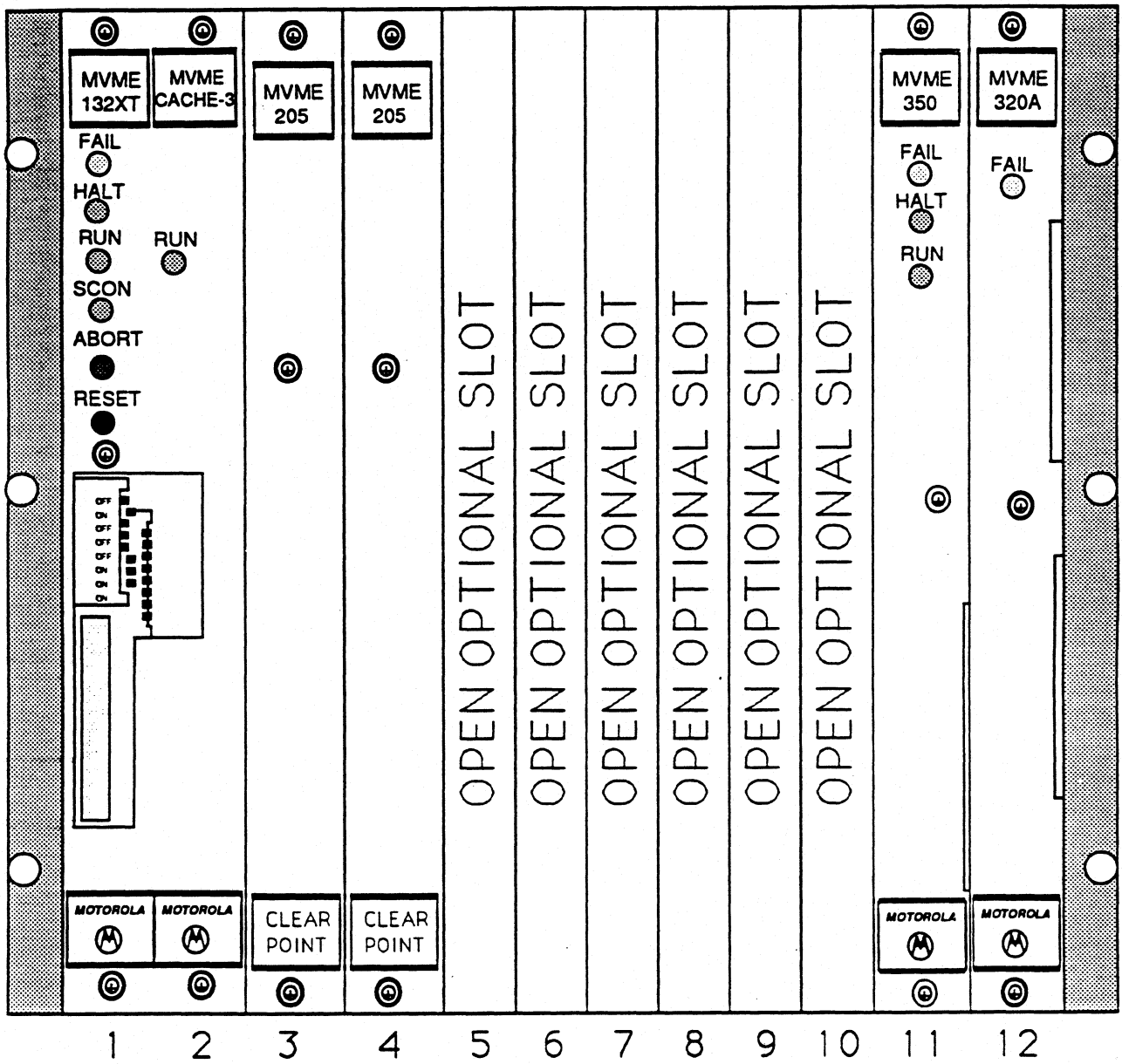
CARD CAGE VIEW.



QUALIFIED ASSEMBLIES					
132XT	320B	332	350	707A(13X)	841
204-2F	320B-1	332XT	355	710(332/XT)	842
205	323 (-1/-2)	333-2	360	710F	851
224-1	330-A	333x.25	702A/320(X)	714M(141/18X)	955
224-2	330-B	336	705A(333)	751(336)	

SYS2616NY031/032

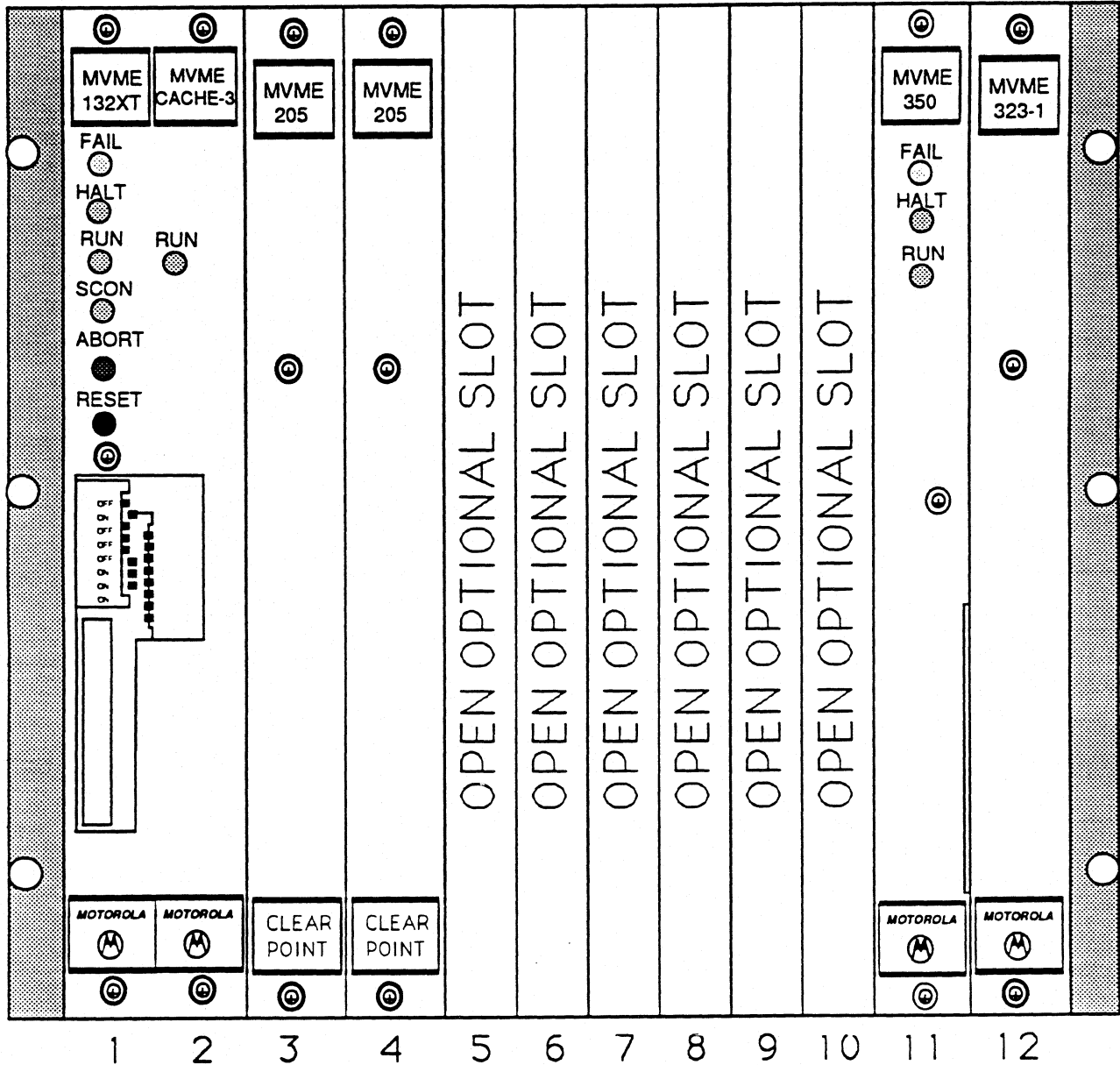
CARD CAGE VIEW.



132XT	320B	332	350	707A(13X)	841
204-2F	320B-1	332XT	355	710(332/XT)	842
205	323 (-1/-2)	333-2	360	710F	851
224-1	330-A	333x.25	702A/320(X)	714M(141/18X)	955
224-2	330-B	336	705A(333)	751(336)	

SYS2616NY041/042

CARD CAGE VIEW.

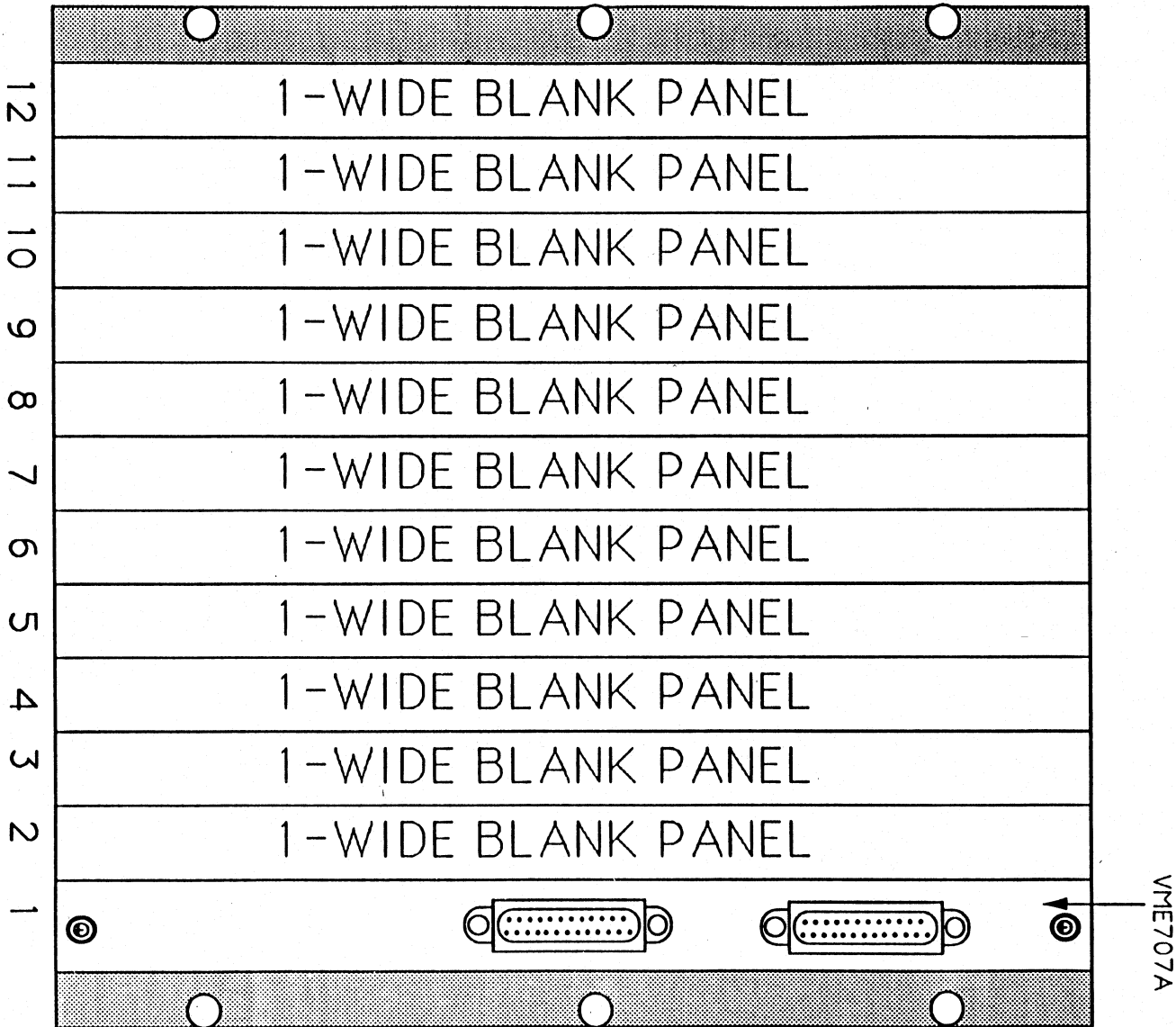


QUALIFIED ASSEMBLIES

132XT	320B	332	350	707A(13X)	841
204-2F	320B-1	332XT	355	710(332/XT)	842
205	323 (-1/-2)	333-2	360	710F	851
224-1	330-A	333x.25	702A/320(X)	714M(141/18X)	955
224-2	330-B	336	705A(333)	751(336)	

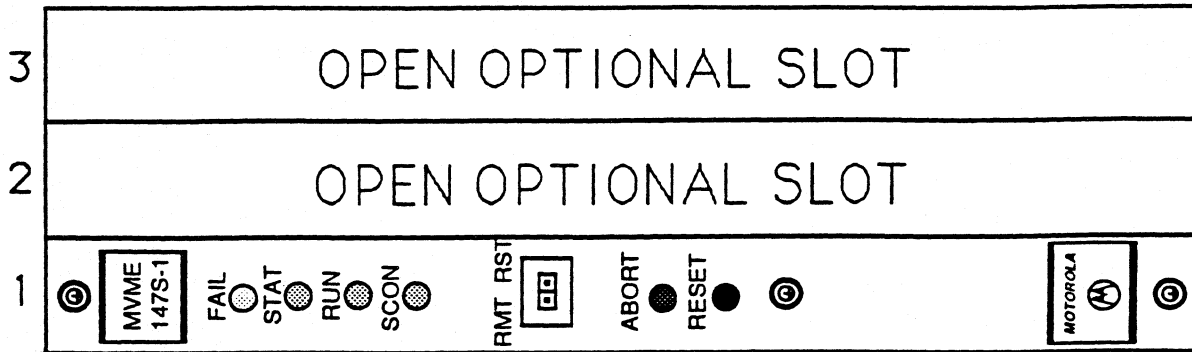
ALL SYS2616NY

I/O PANEL VIEW.

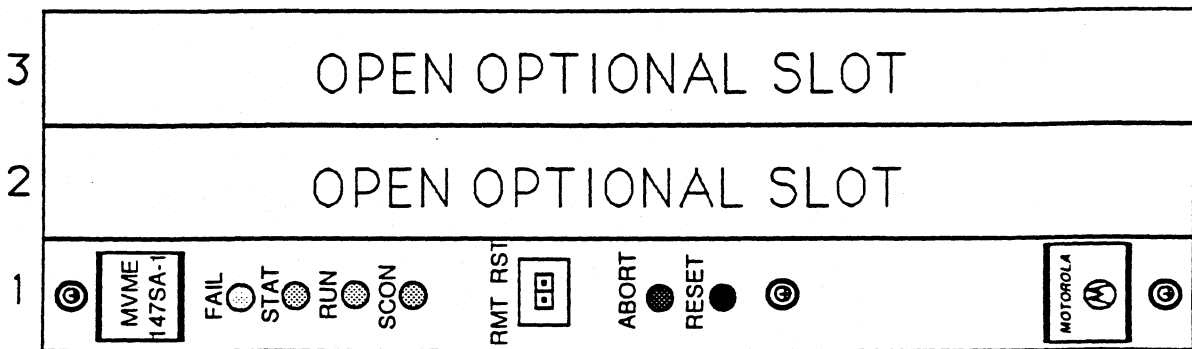


02/19/90

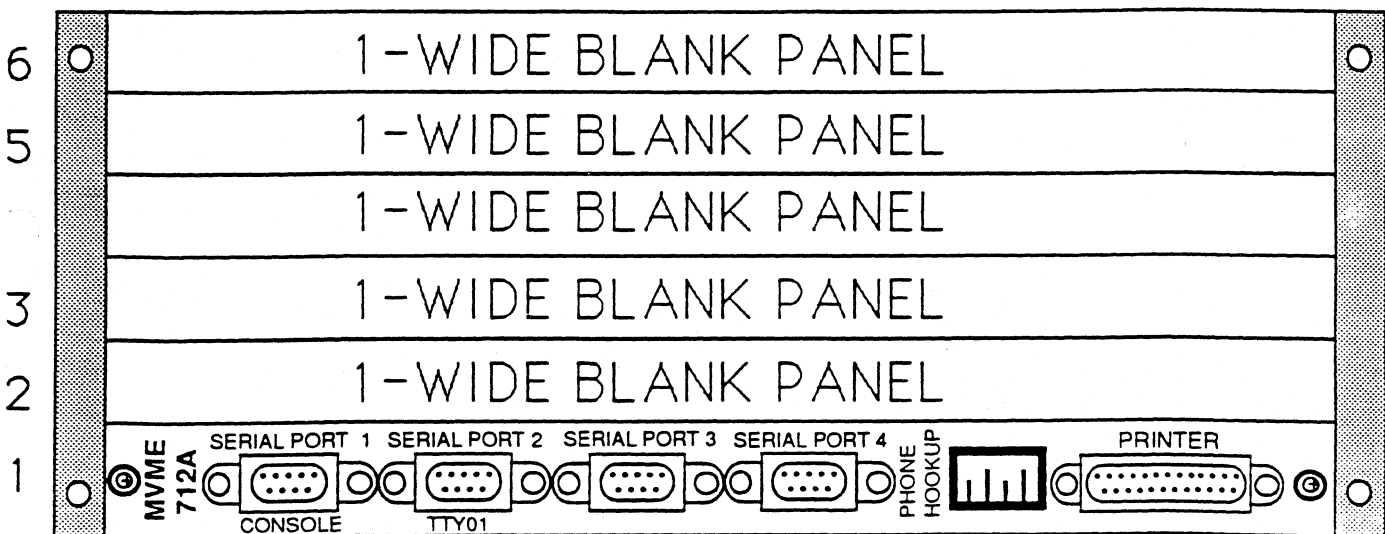
SYS3204NY 004F/043/045/103/103R/105/105R/173/175 CARD CAGE VIEW.



SYS3208NY 103/105/173/175.



ALL SYS3200 SERIES I/O PANEL VIEW.

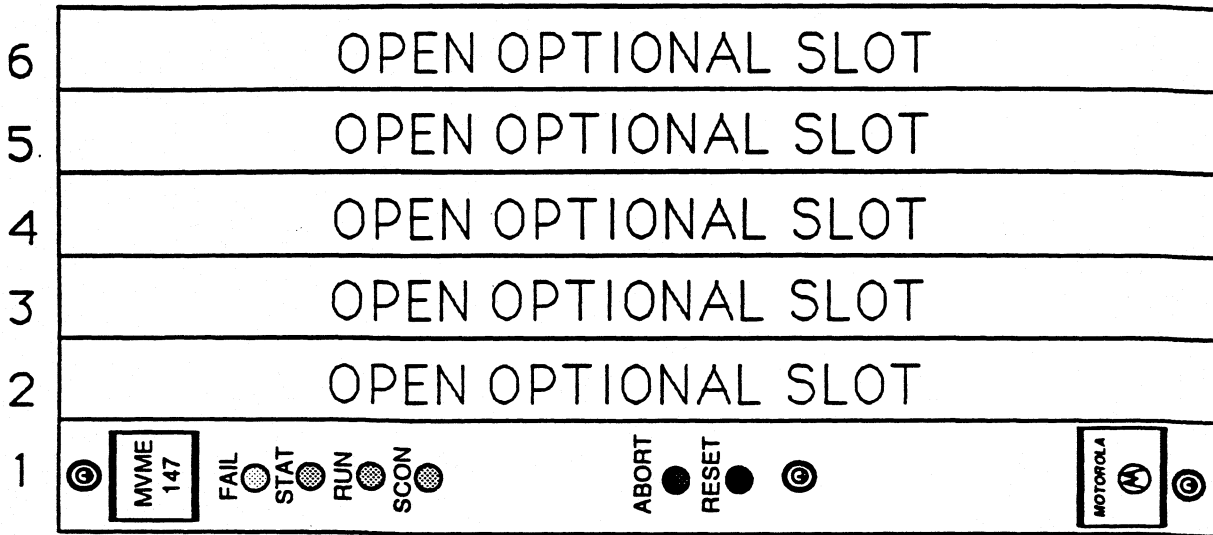


QUALIFIED ASSEMBLIES

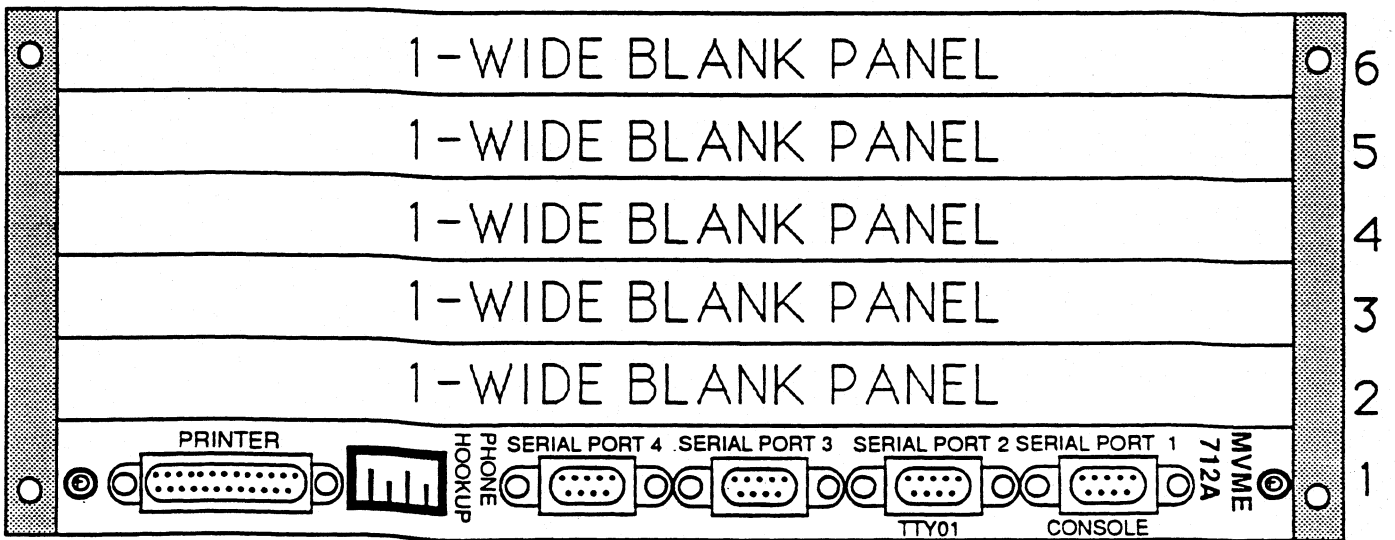
147RF	332XT	372	710(332/XT)	772(372)	855	884
147S-1	333-2	373	712M(147)	792(393)	858	952
147SA-1	333x.25	374	715(335)	792A(393)	862	
224F-1	335	393	732	822	863	
224F-2	336	705A(333)	751(336)	853	864	

02/19/90

SYS3304NY001/002/041/042/081/ 082/151/152 CARD CAGE VIEW.



SYS3304NY001/002/041/042/ 081/082/151/152 I/O PANEL VIEW.

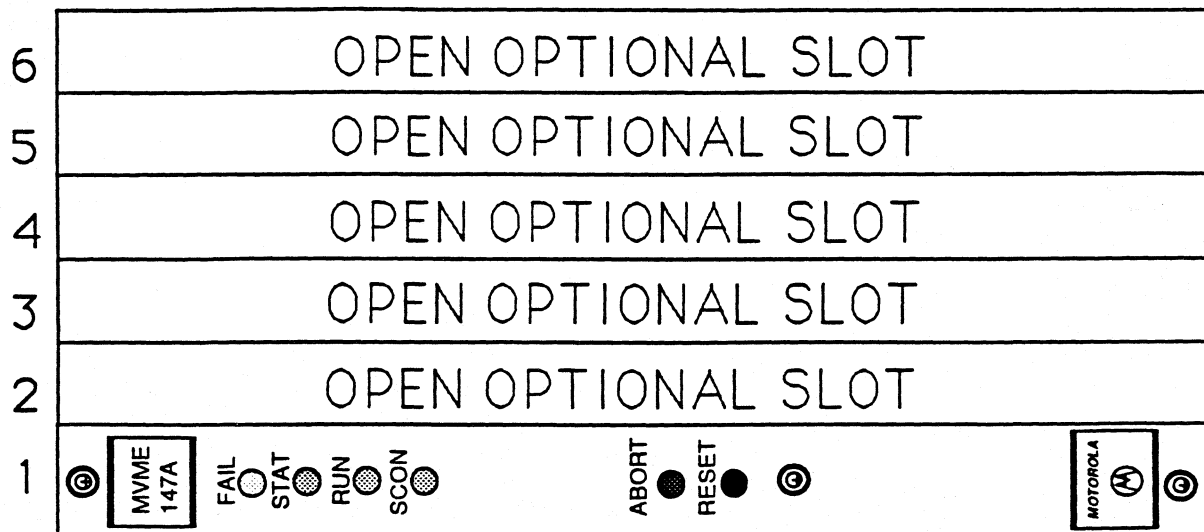


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

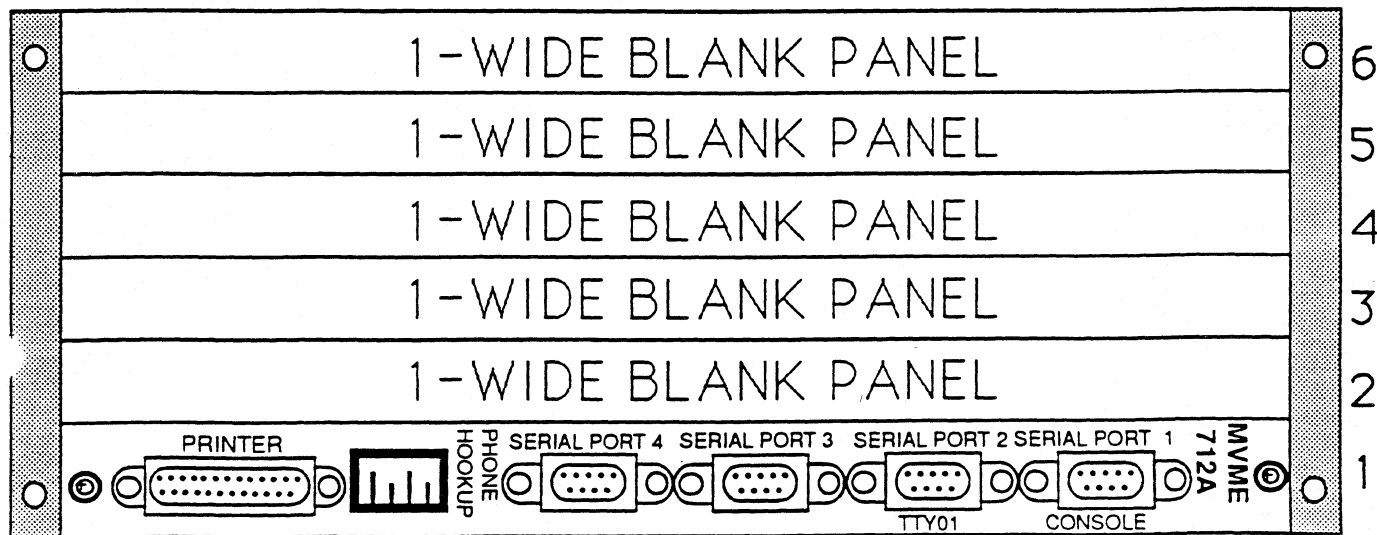
QUALIFIED ASSEMBLIES					
147(-1)	332XT	350	712M(147)	852	874
224-1	333-2	372	715(335)	853	875
224-2	333x.25	374	731	858	876
330-A	335	705A(333)	751(336)	872	881
330-B	336	710(332/XT)	772(372)	873	953A

02/19/90

SYS3308NY001/002/041/042/081/ 082/151/152/301/302 CARD CAGE VIEW.



SYS3308NY001/002/041/042/081/ 082/151/152/301/302 I/O PANEL VIEW.

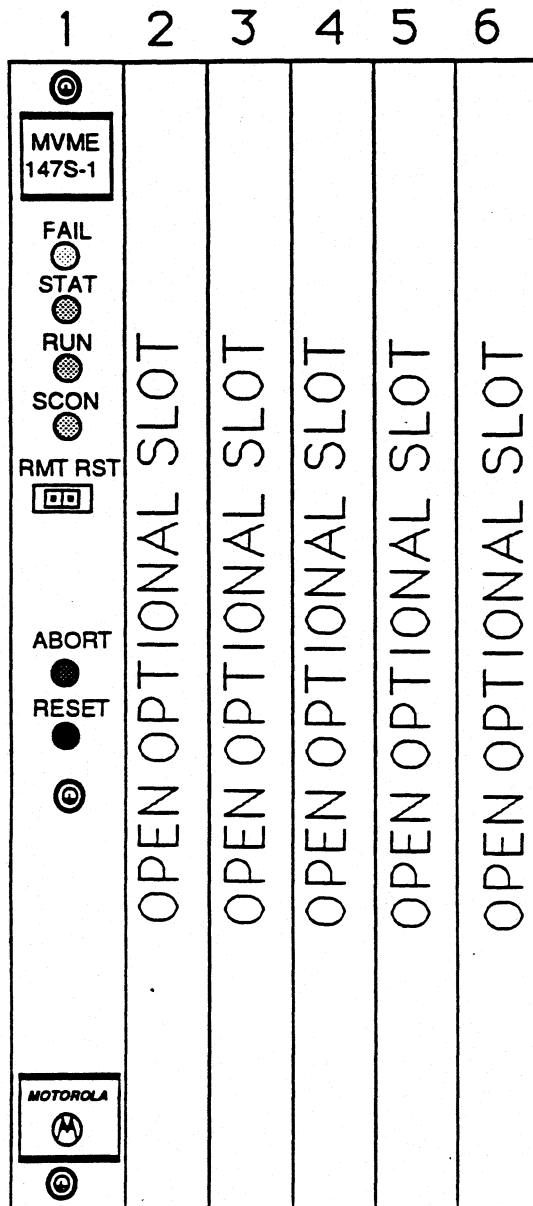


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

QUALIFIED ASSEMBLIES						
147A(-1)	332XT	350	710(332/XT)	772(372)	873	953A
224-1	333-2	372	712M(147)	852	874	
224-2	333x.25	373	715(335)	853	875	
330-A	335	374	731	858	876	
330-B	336	705A(333)	751(336)	872	881	

SYS3404NY 105/173/175

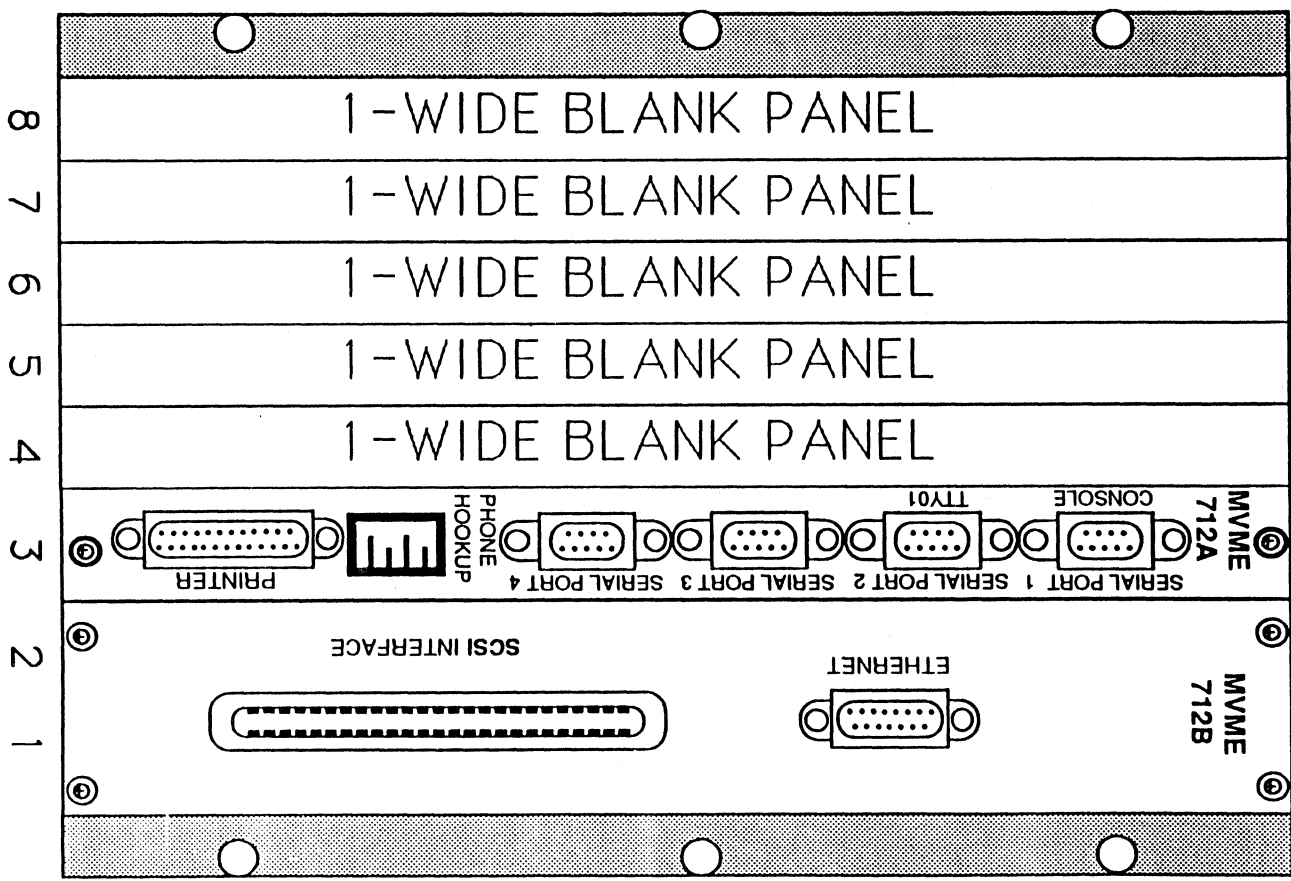
CARD CAGE VIEW.



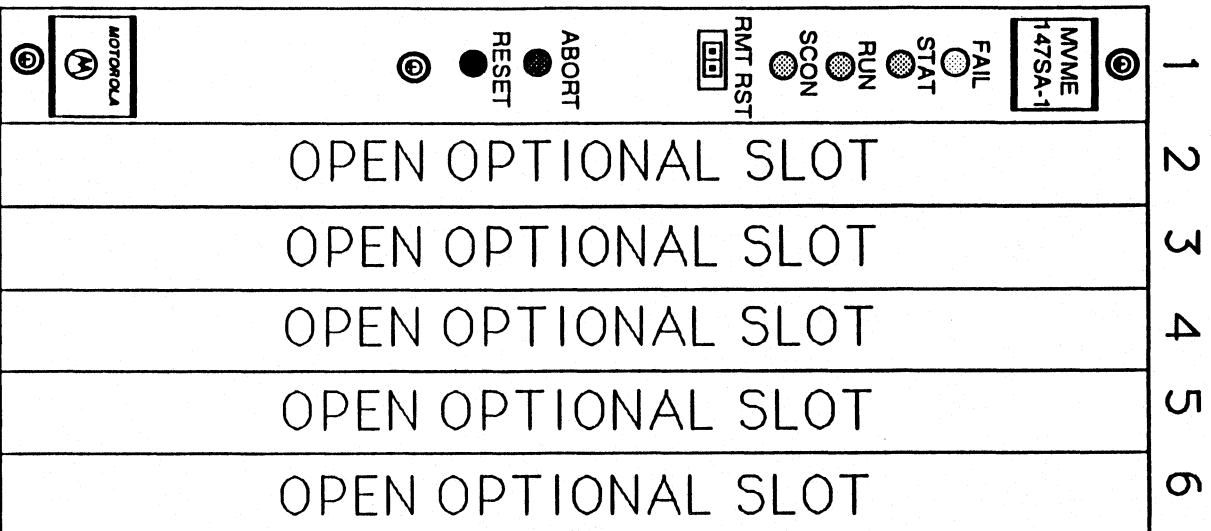
QUALIFIED ASSEMBLIES	
147S-1	751(336)
332XT	792(393)
333-2	792A(393)
333x.25	852
335	853
336	855
374	856
393	858
705A(333)	863
705B	864
710(332/XT)	875
712A(147)	876
712B(147)	884
715(335)	954
732	

ALL SYS3404/08/16'S

I/O PANEL VIEW.

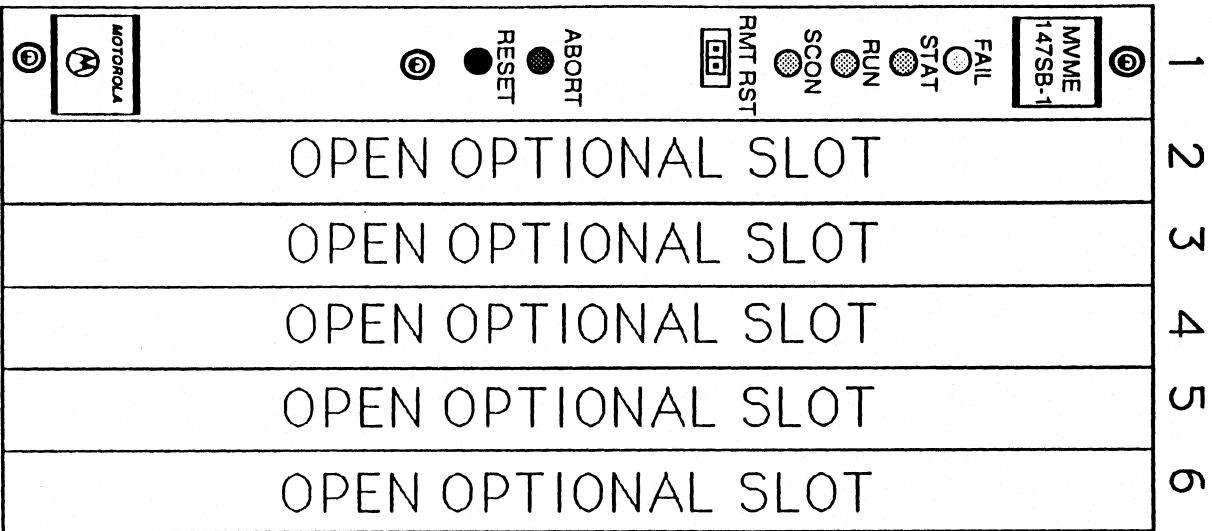


SYS3408NY 103/105/173/175/ 305/605 CARD CAGE VIEW.



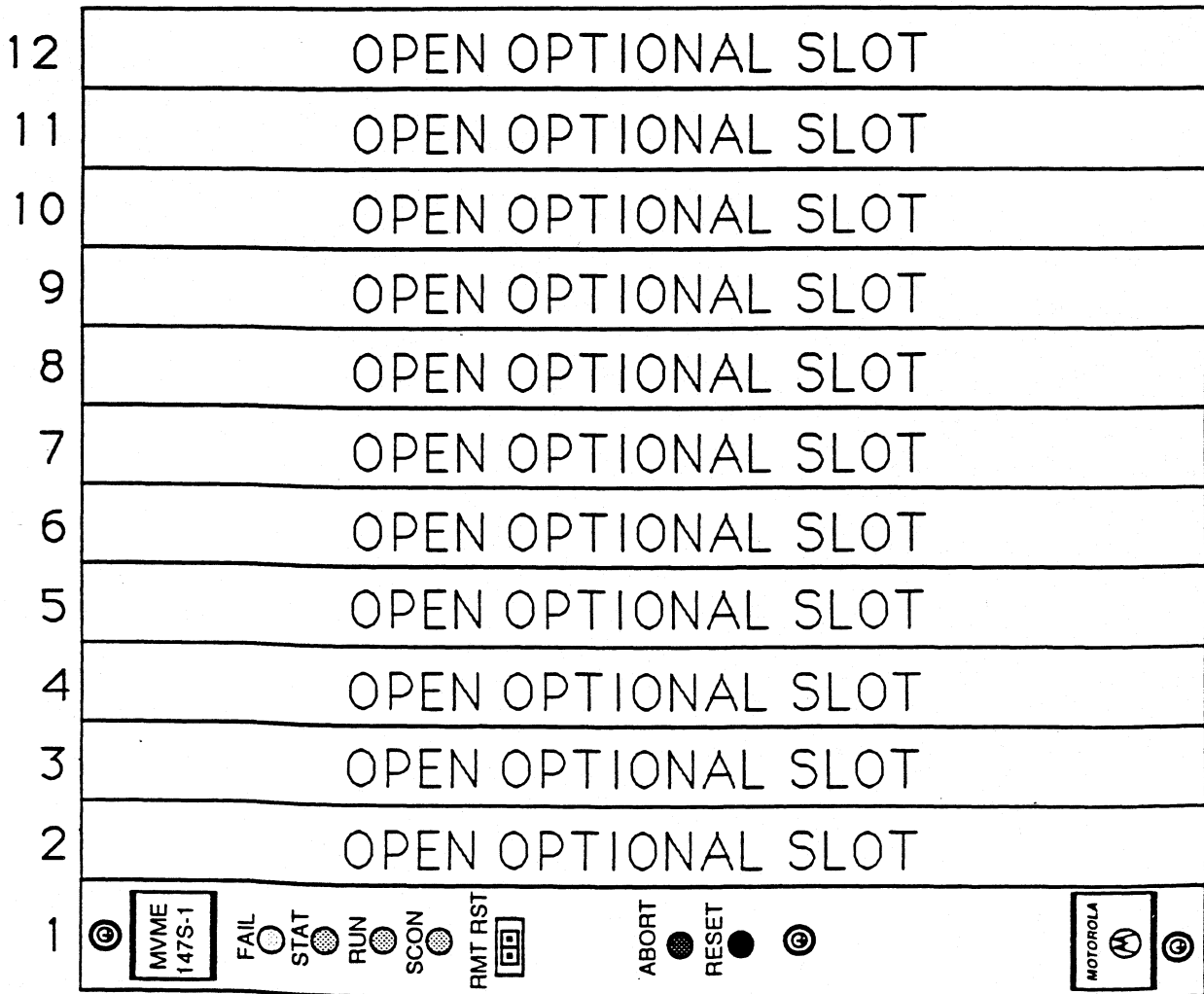
QUALIFIED ASSEMBLIES	
147SA-1	751(336)
332XT	792(393)
333-2	792A(393)
333x.25	852
335	853
336	855
374	856
393	858
705A(333)	863
705B	864
710(332XT)	875
712A(147)	876
712B(147)	884
715(335)	954
732	

SYS3416NY 103/105/173/175/ 305/605 CARD CAGE VIEW.



QUALIFIED ASSEMBLIES	
147SB-1	751 (336)
332XT	792(993)
333-2	792A(393)
333x:25	852
335	853
336	855
374	856
393	858
705A(333)	863
705B	864
710(332XT)	875
712A(147)	876
712B(147)	884
715(335)	954
732	

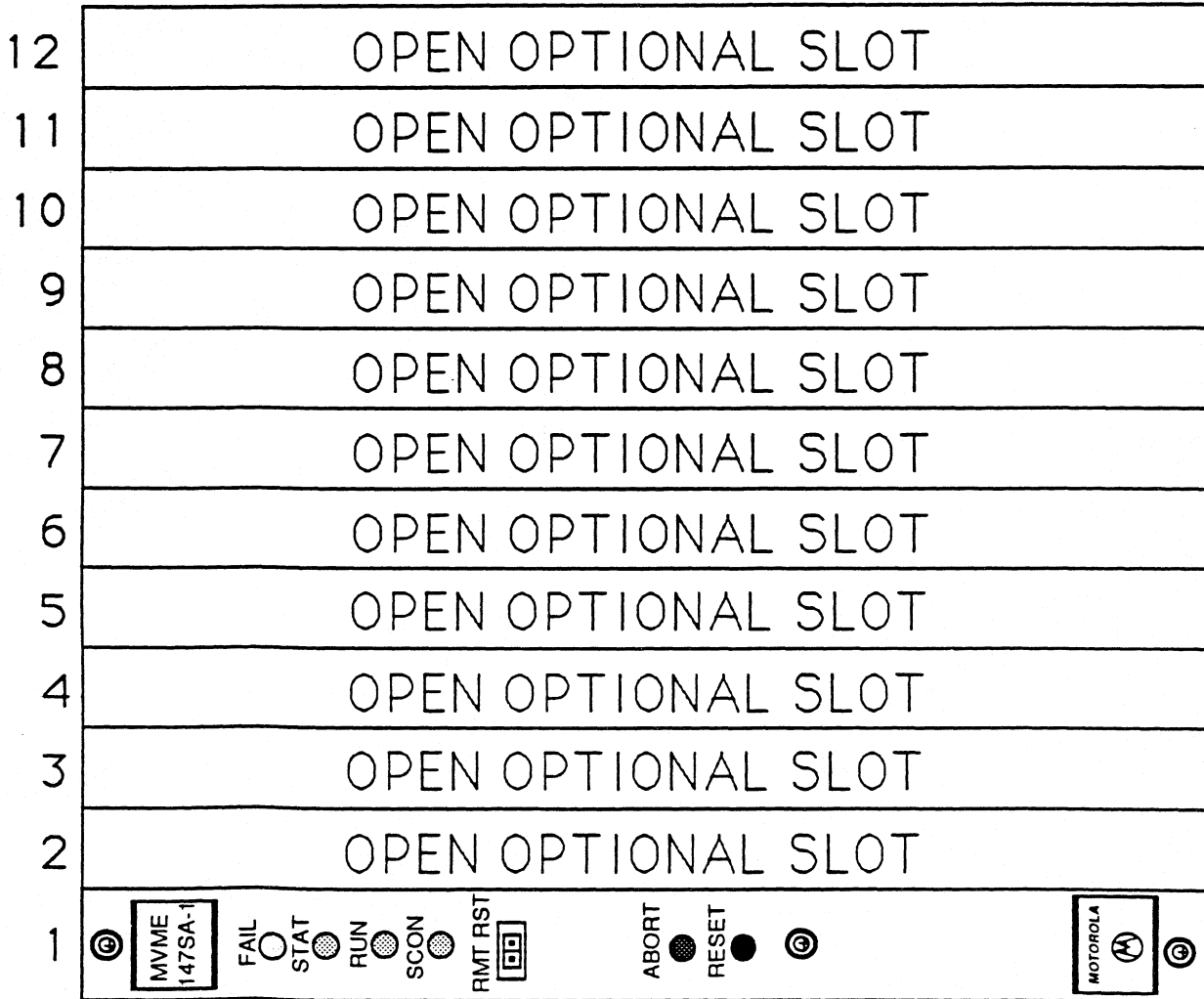
SYS3604NY001/002/081/082/ 151/152 CARD CAGE VIEW.



NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

QUALIFIED ASSEMBLIES					
147 (-1)	332XT	373	712B(147)	853	876
224-1	333-2	374	731	858	881
224-2	333x.25	705A(333)	751(336)	873	955A
330-A	336	710(332/XT)	772(372)	874	
330-B	372	712A(147)	852	875	

SYS3608NY001/002/081/082/1 51/152/301/302 CARD CAGE VIEW.

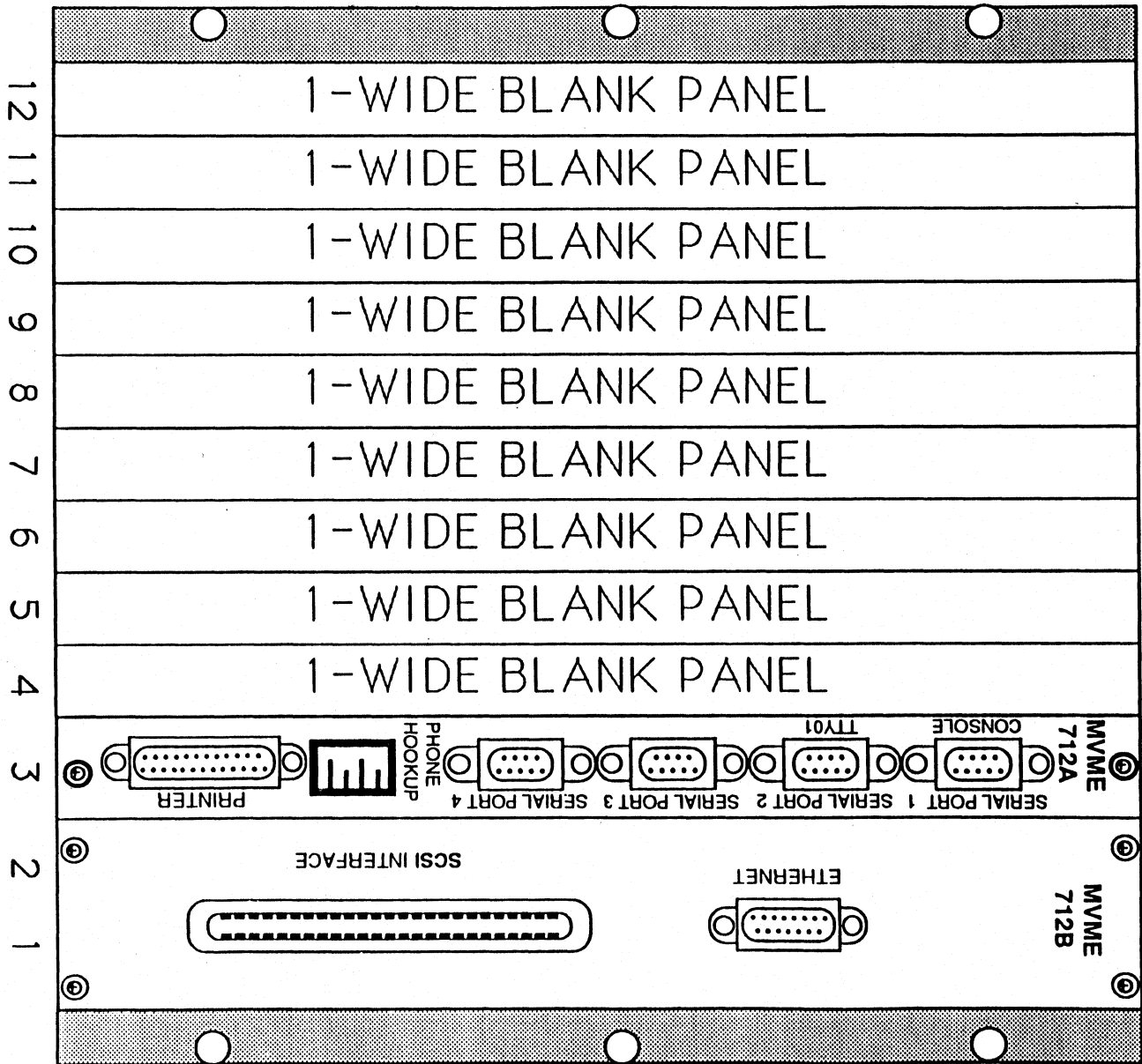


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

QUALIFIED ASSEMBLIES					
147A (-1)	332XT	373	712B(147)	853	876
224-1	333-2	374	731	858	881
224-2	333x.25	705A(333)	751(336)	873	955A
330-A	336	710(332/XT)	772(372)	874	
330-B	372	712A(147)	852	875	

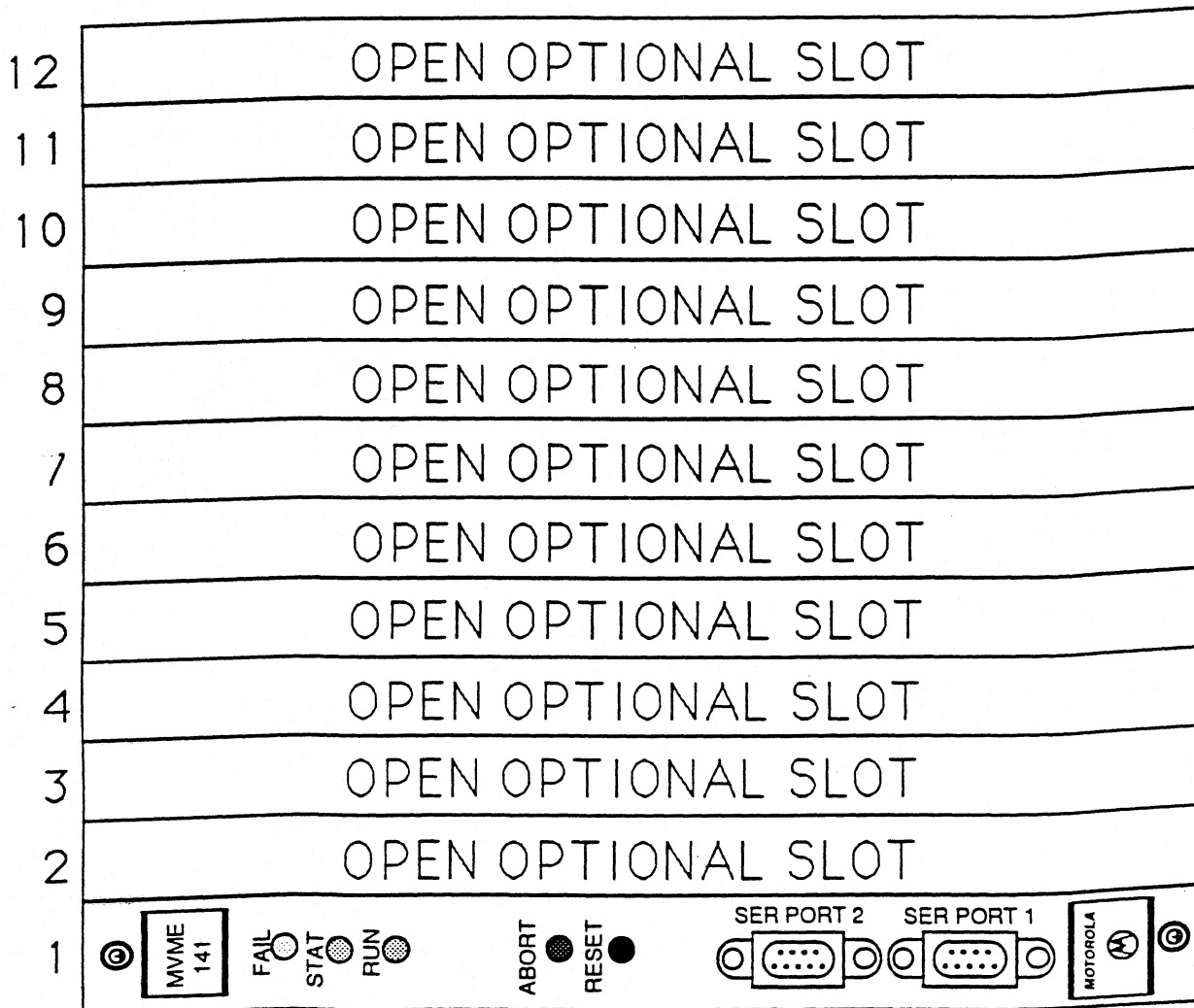
ALL SYS3604/08'S

I/O PANEL VIEW.



04/04/91

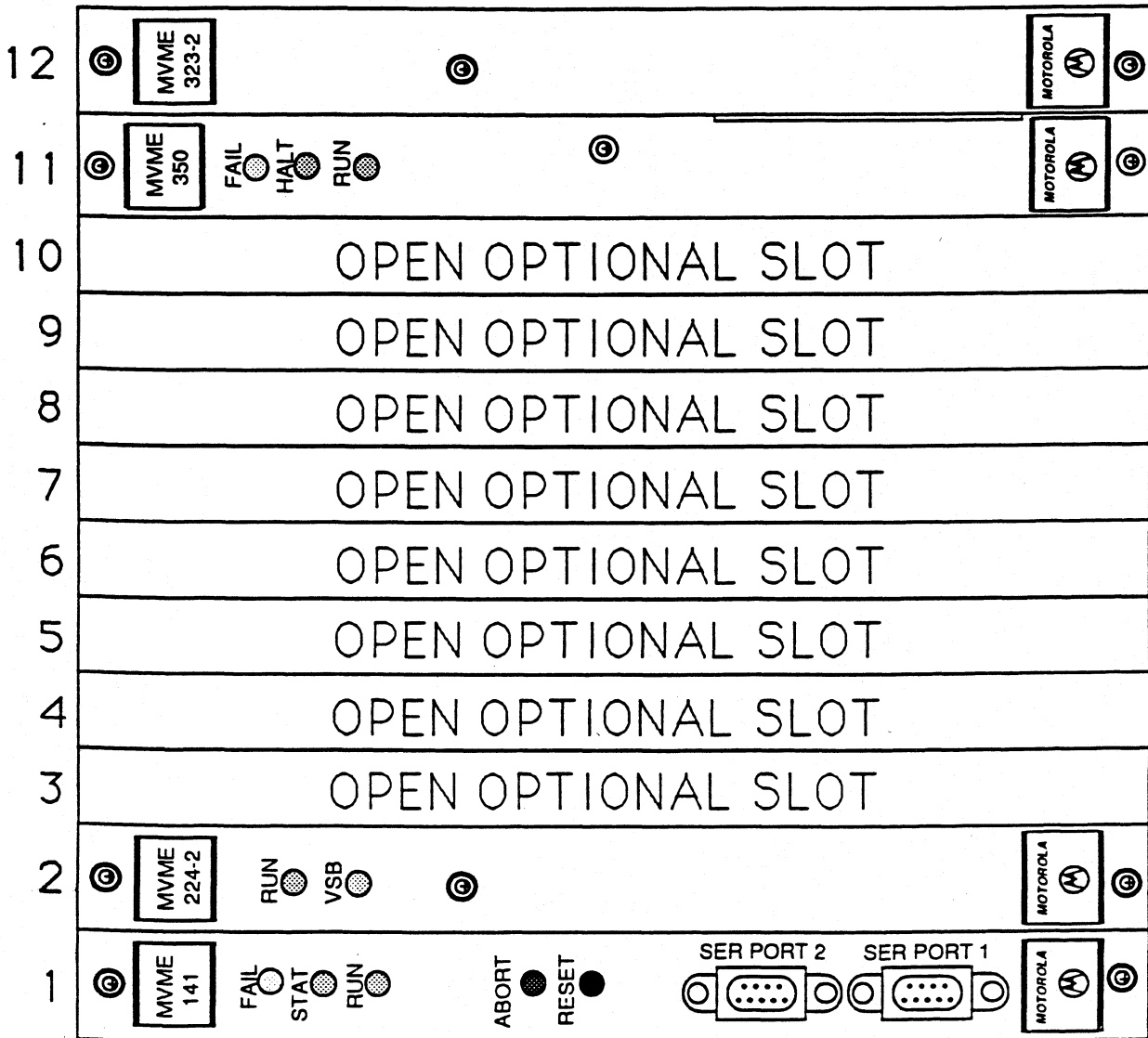
SYS3640NY001/002 CARD CAGE VIEW.



NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

QUALIFIED ASSEMBLIES								
141 (-1)	323 -2	333-2	393	714M(141/18X)	843	855	875	955
224A-1	327A	333x.25	705A	717(327A)	852	856	876	
224A-2	332XT	336	705B	732	852Q	858-1	877	
224A-3	332FPA1	350	705-1	751(336)	853	858-2	883	
230-2	332FPA2	374	710	842	853Q	874	884	

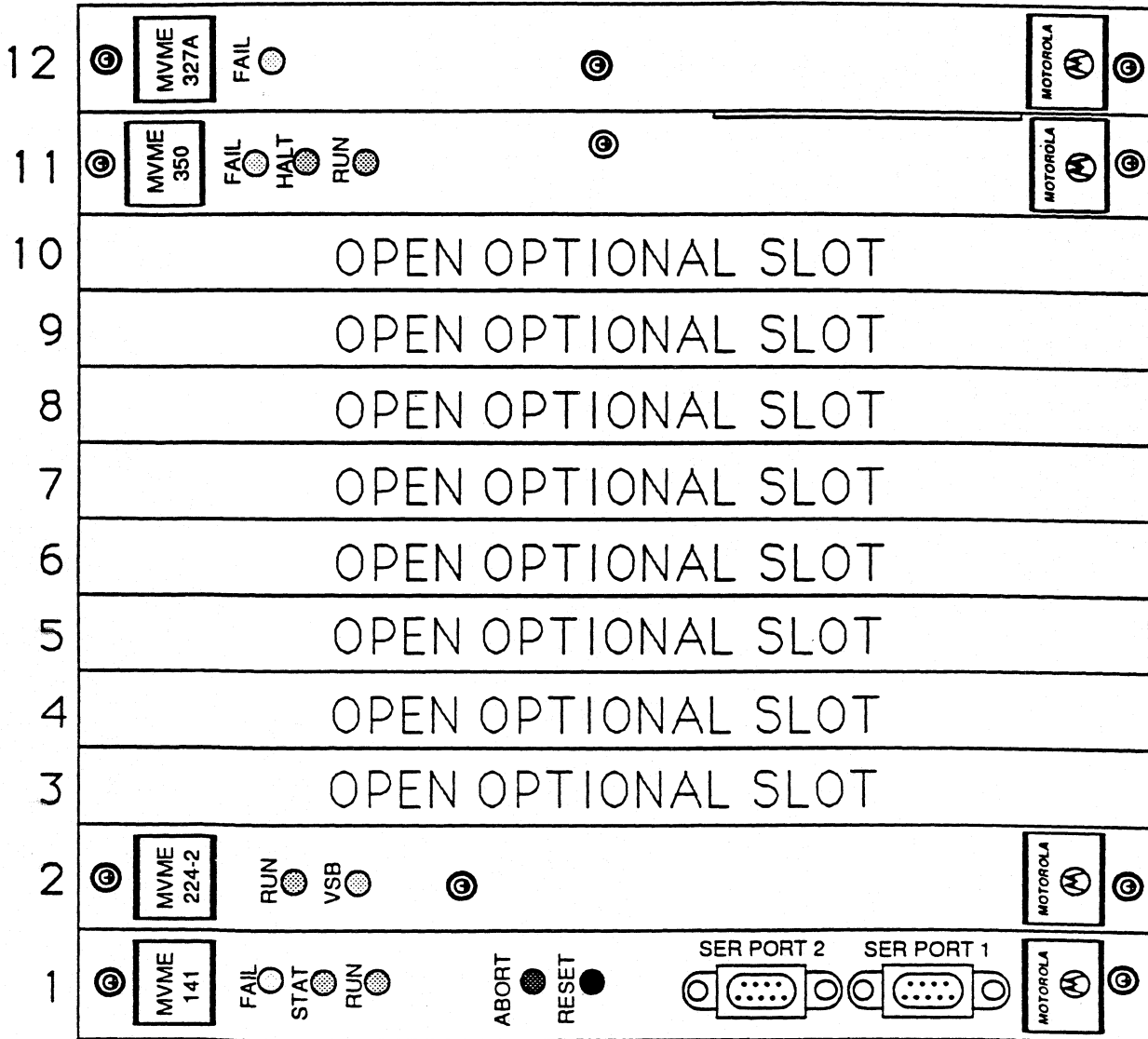
SYS3640NY021/022/ 051/052 CARD CAGE VIEW.



NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

QUALIFIED ASSEMBLIES								
141 (-1)	323 -2	333-2	393	714M(141/18X)	843	855	875	955
224A-1	327A	333x.25	705A	717(327A)	852	856	876	
224A-2	332XT	336	705B	732	852Q	858-1	877	
224A-3	332FPA1	350	705-1	751(336)	853	858-2	883	
230-2	332FPA2	374	710	842	853Q	874	884	

SYS3640NY 151/152/301/ 302/601/602 CARD CAGE VIEW.

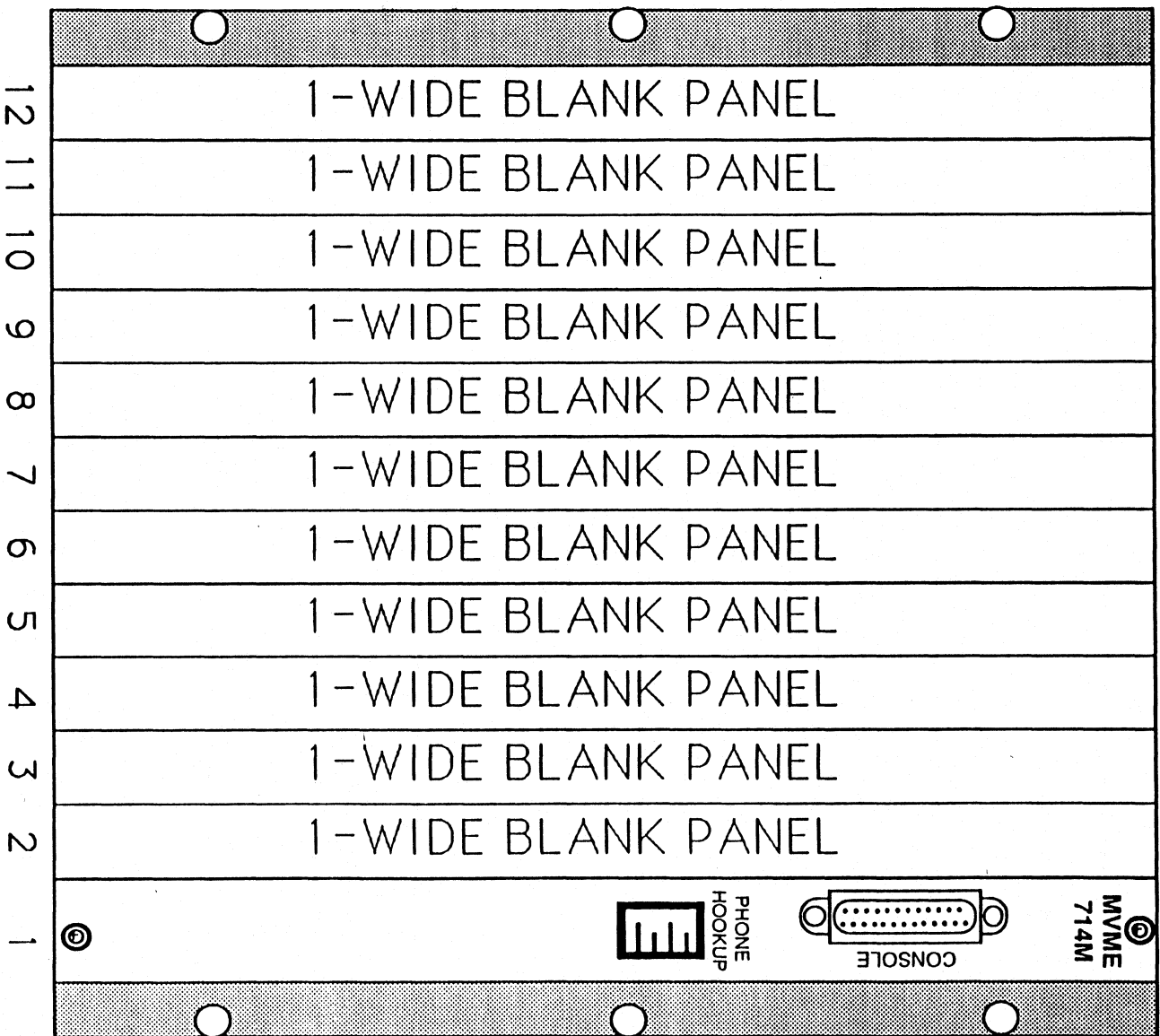


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

QUALIFIED ASSEMBLIES								
141 (-1)	323 -2	333-2	393	714M(141/18X)	843	855	875	955
224A-1	327A	333x.25	705A	717(327A)	852	856	876	
224A-2	332XT	336	705B	732	852Q	858-1	877	
224A-3	332FPA1	350	705-1	751(336)	853	858-2	883	
230-2	332FPA2	374	710	842	853Q	874	884	

SYS3640NY001/002/021/

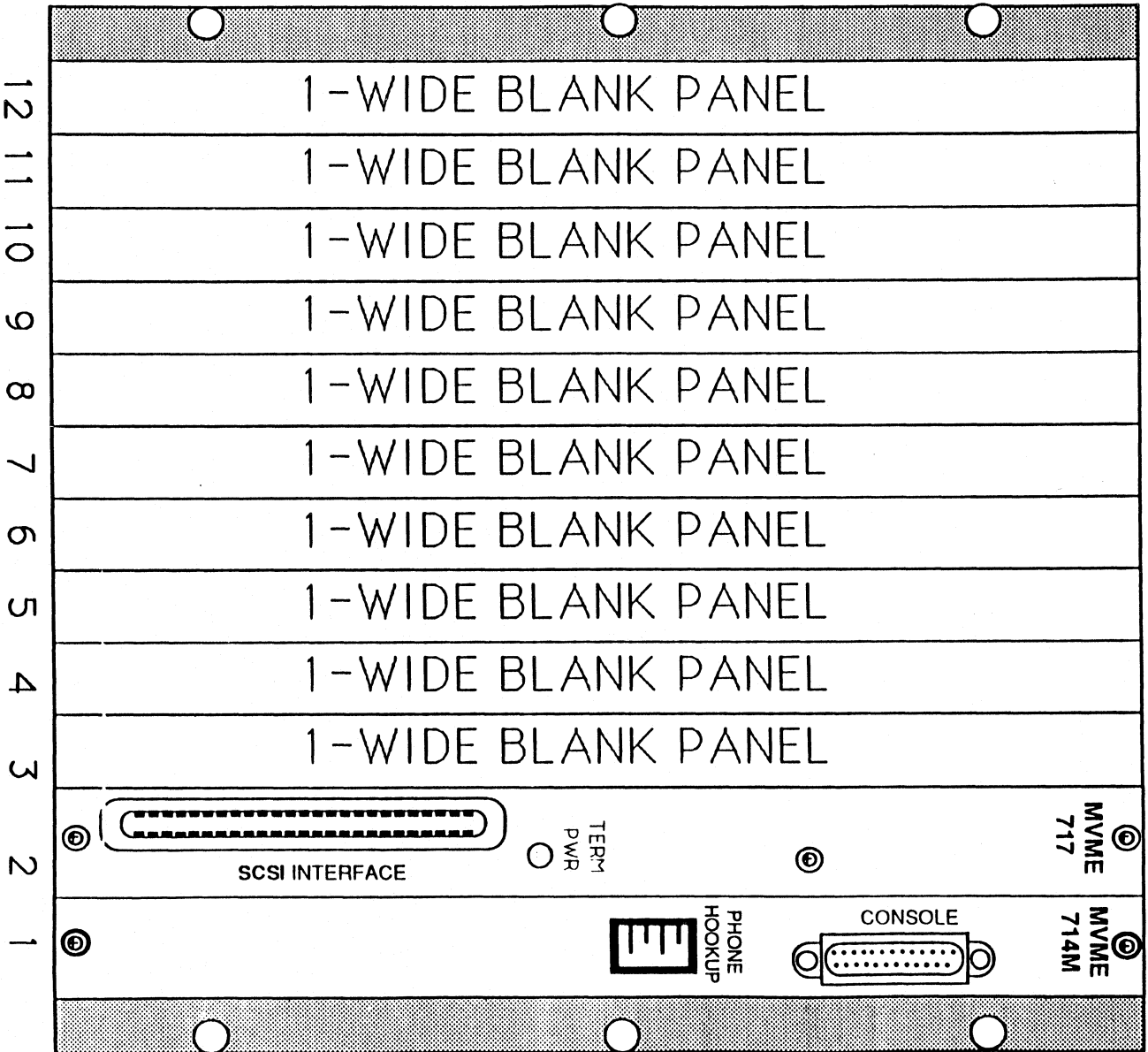
022/051/052 I/O PANEL VIEW.



11/20/89

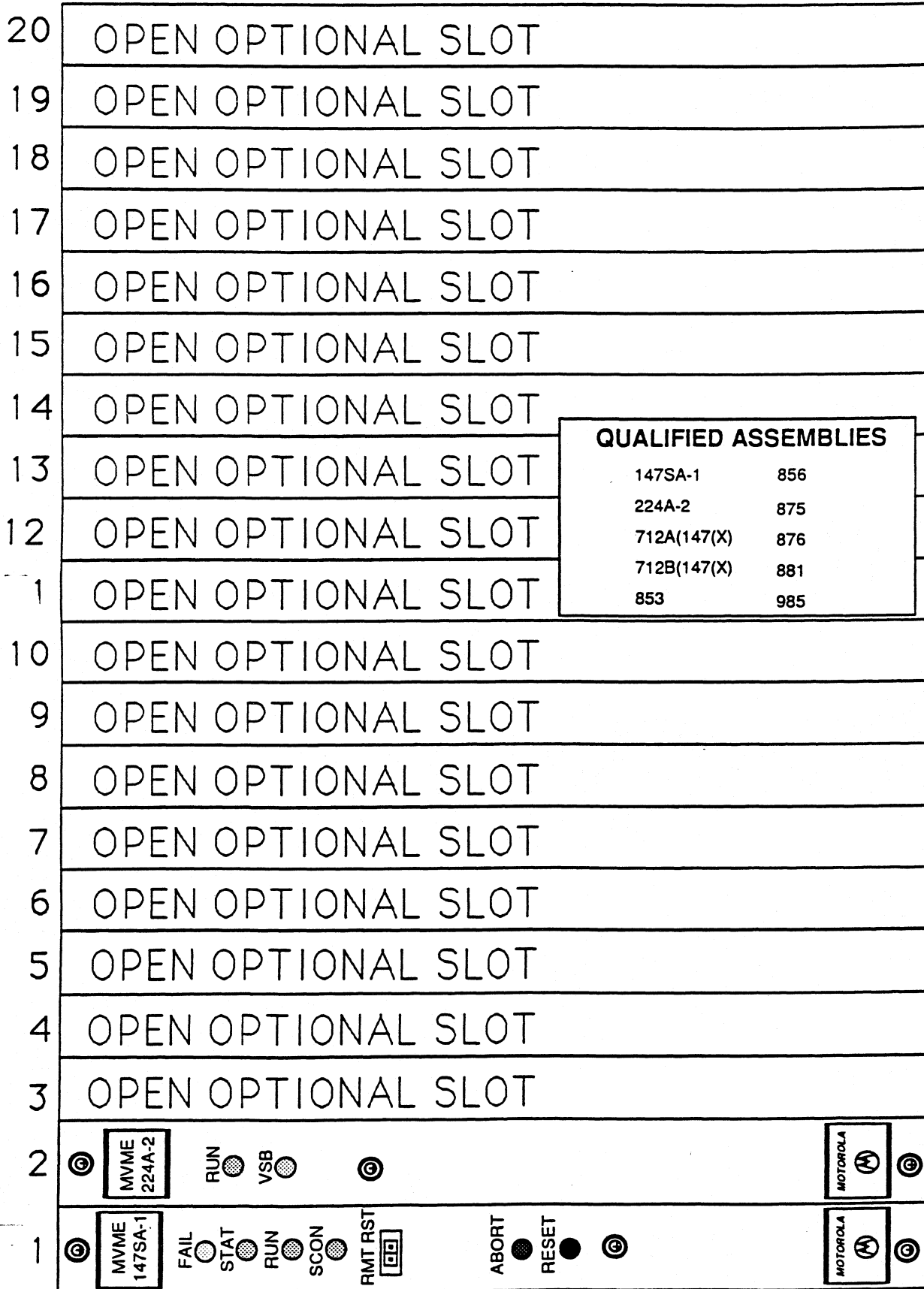
SYS3640NY 151/152/301/

302/601/602 I/O PANEL VIEW.



SYS3708NY301/601

CARD CAGE VIEW.



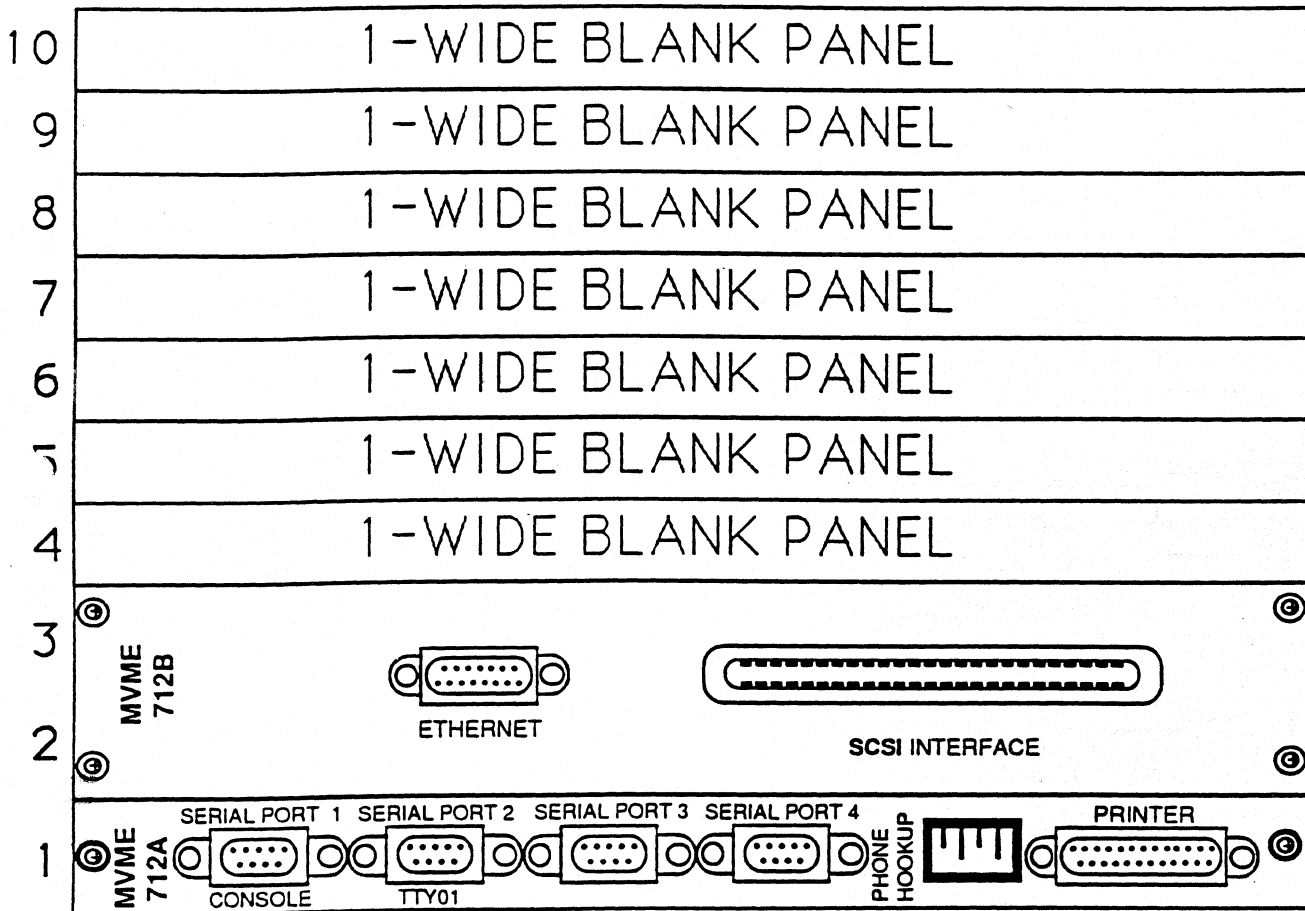
QUALIFIED ASSEMBLIES	
147SA-1	856
224A-2	875
712A(147(X))	876
712B(147(X))	881
853	985

NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

04/16/91

SYS3708NY301/601

I/O PANEL VIEW.

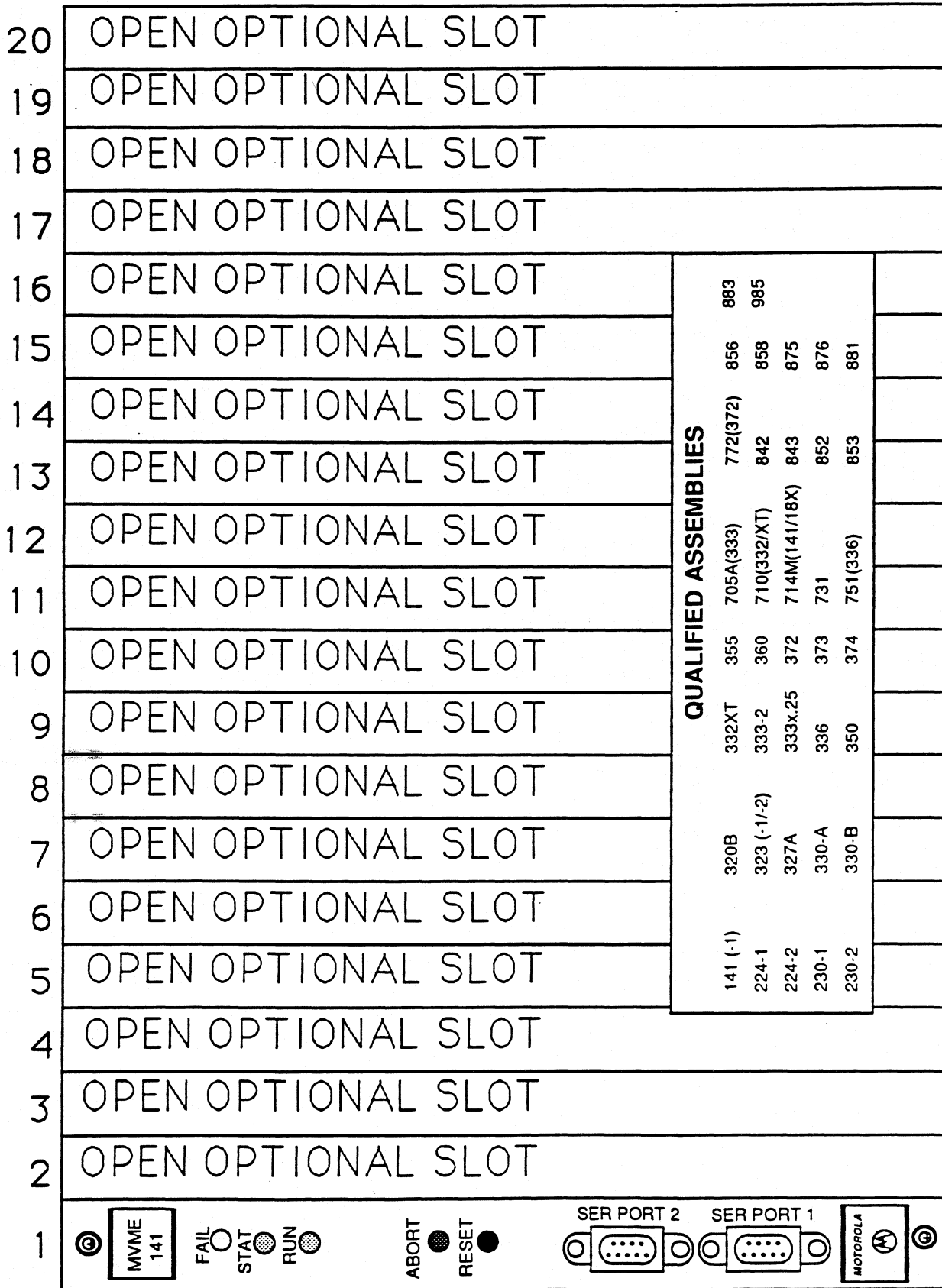


04/15/91

SYS3840NY001/002

CARD CAGE VIEW.

02/19/90



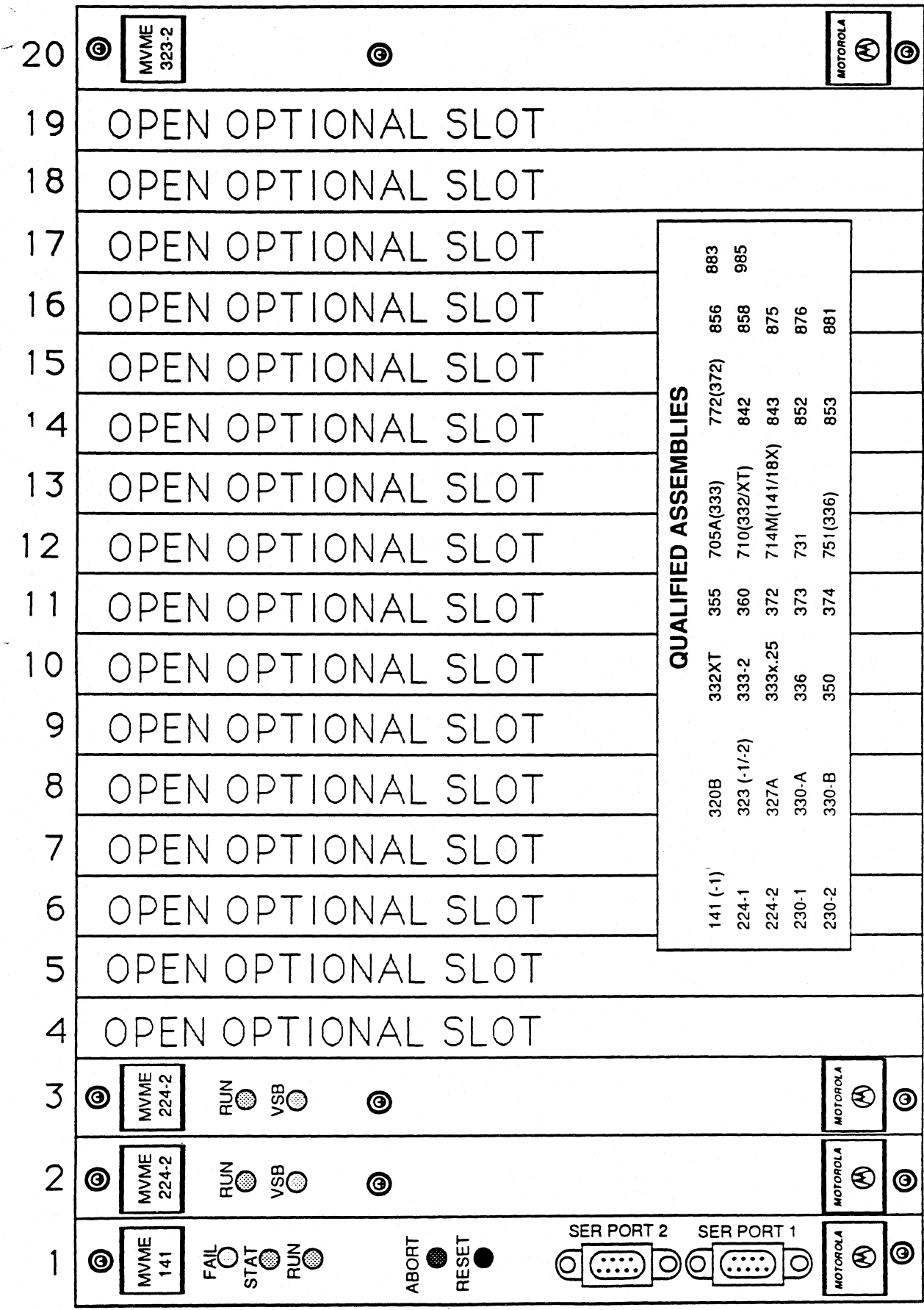
QUALIFIED ASSEMBLIES

141 (-1)	320B	355	705A(333)	772(372)	856	883
224-1	323 (-1/2)	360	710(332XT)	842	858	985
224-2	327A	372	714M(141/18X)	843	875	
230-1	330-A	373	731	852	876	
230-2	330-B	374	751(336)	853	881	

NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.




SYS3840NY021/022/ 051/052 CARD CAGE VIEW.

02/19/90



NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

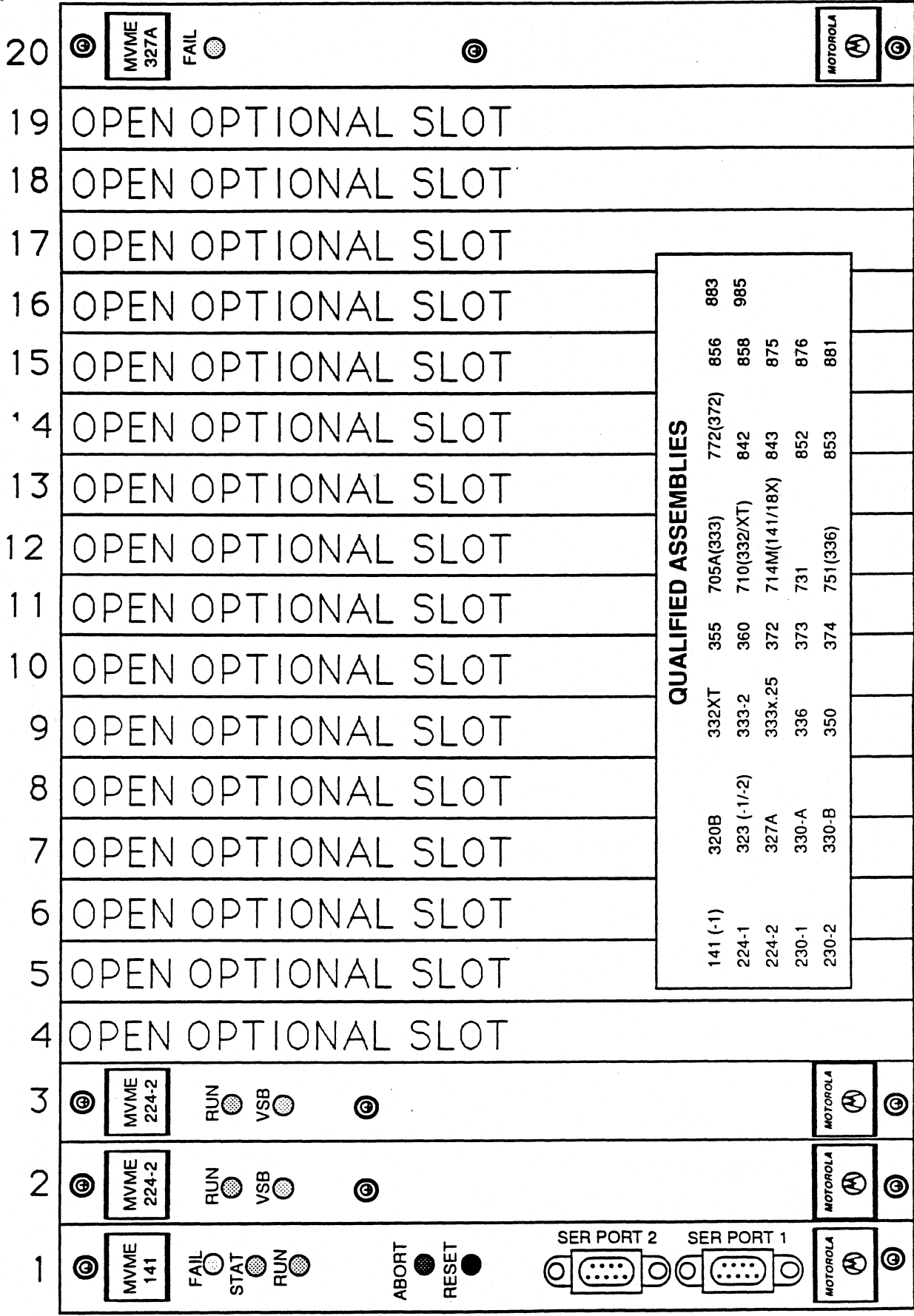
022/051/052 I/O PANEL VIEW.

20	1-WIDE BLANK PANEL
19	1-WIDE BLANK PANEL
18	1-WIDE BLANK PANEL
17	1-WIDE BLANK PANEL
16	1-WIDE BLANK PANEL
15	1-WIDE BLANK PANEL
14	1-WIDE BLANK PANEL
13	1-WIDE BLANK PANEL
12	1-WIDE BLANK PANEL
11	1-WIDE BLANK PANEL
10	1-WIDE BLANK PANEL
9	1-WIDE BLANK PANEL
8	1-WIDE BLANK PANEL
7	1-WIDE BLANK PANEL
6	1-WIDE BLANK PANEL
5	1-WIDE BLANK PANEL
4	1-WIDE BLANK PANEL
3	1-WIDE BLANK PANEL
2	1-WIDE BLANK PANEL
1	  

NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY INSTEAD OF VERTICALLY.

SYS3840NY301/302/ 601/602 CARD CAGE VIEW.

02/19/90



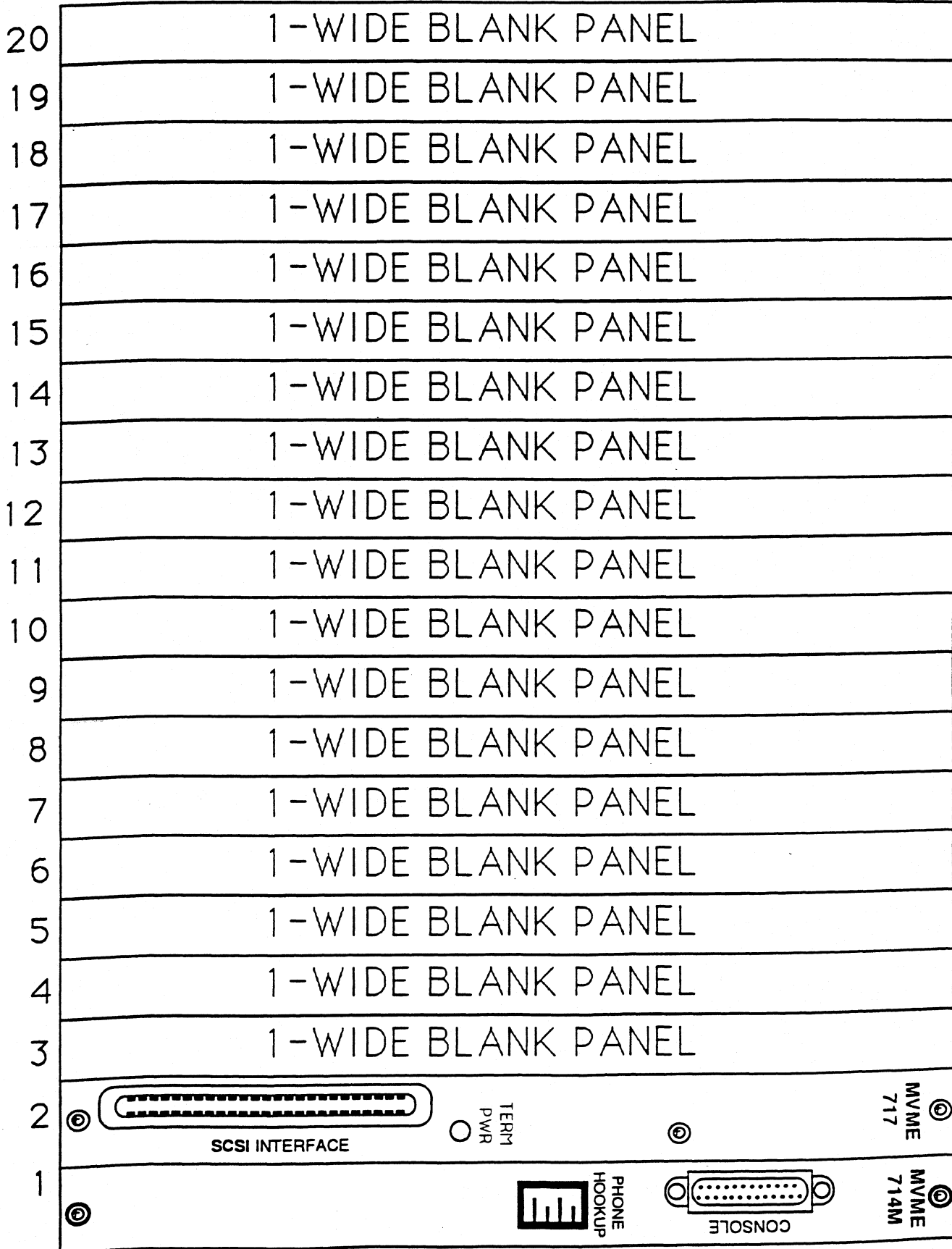
NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

SYS3840NY301/302/

11/21/89

601/602 I/O PANEL VIEW.

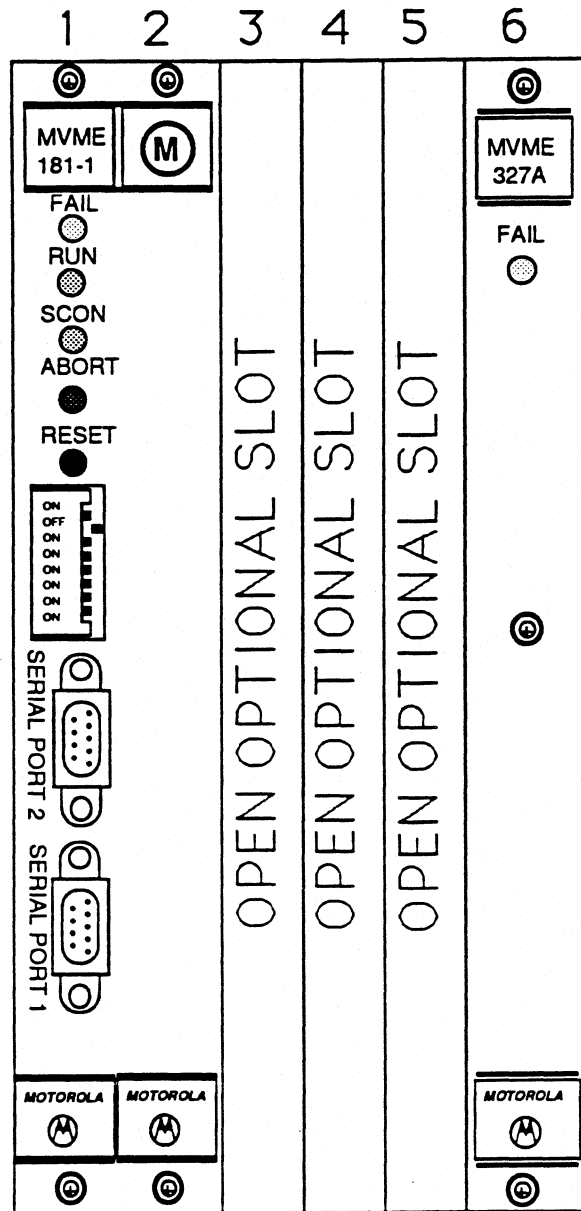
PAGE 41



NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY INSTEAD OF VERTICALLY.

SYS8408NY305/605

CARD CAGE VIEW.

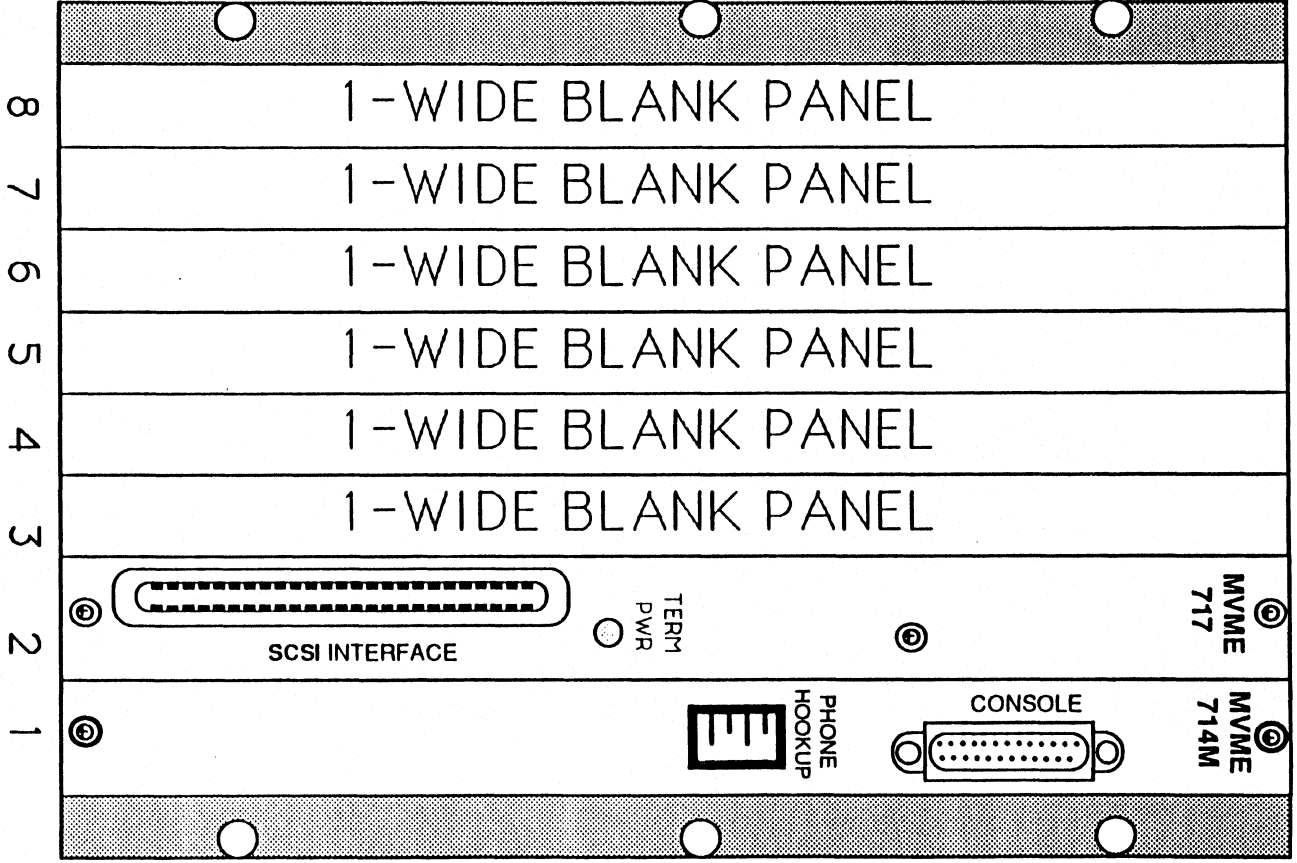


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

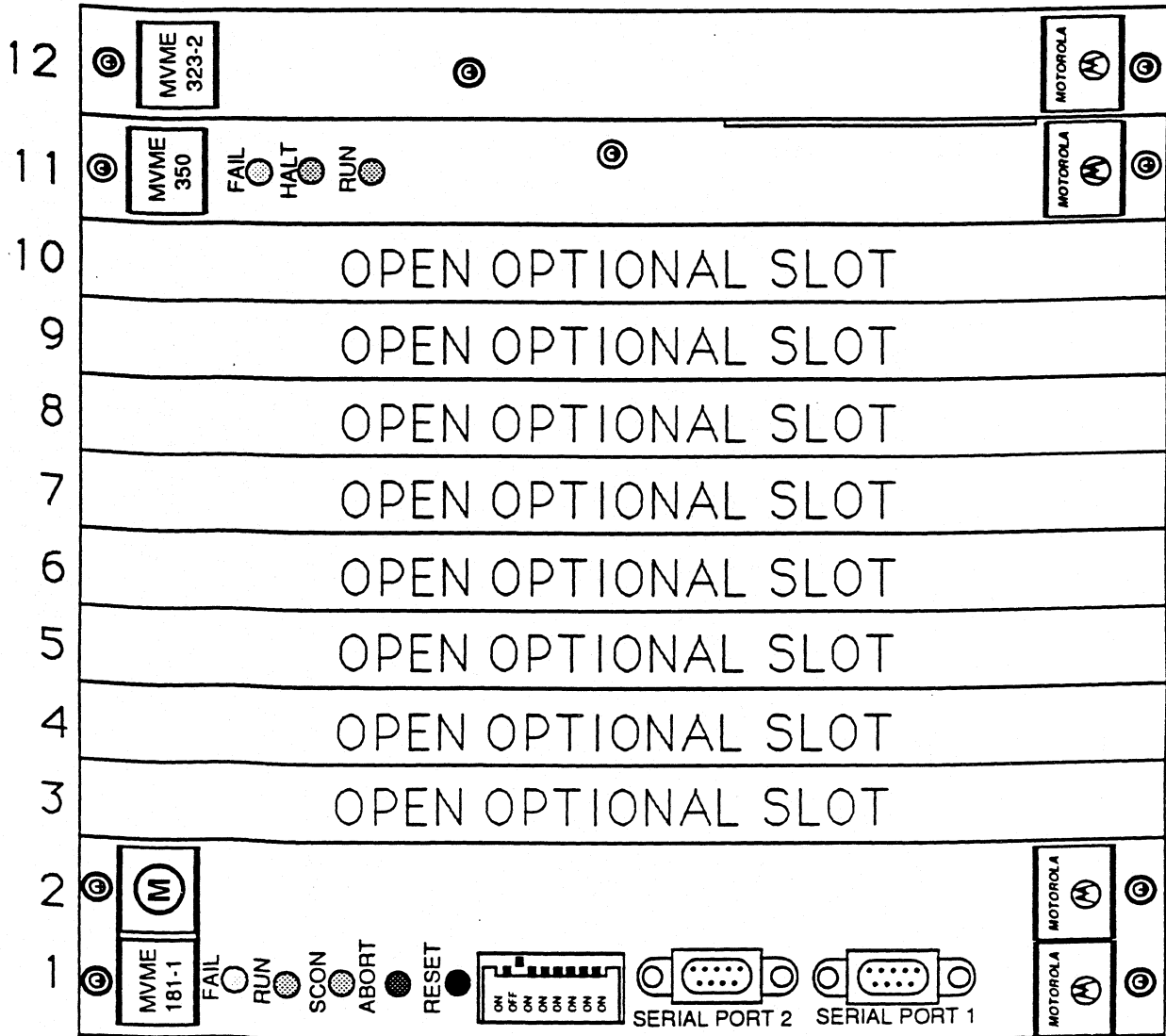
QUALIFIED ASSEMBLIES						
181-1	333-2	372	710(332/XT)	751(336)	853	876
236-2	333x.25	373	714M(141/18X)	772(372)	856	883
236-3	335	374	715(335)	792(393)	858	954
327A	336	393	717(327A)	792A(393)	864	
332XT	350	705A(333)	732	852	875	

SYS8408NY 305/605

I/O PANEL VIEW.



SYS8608NY031/032/ CARD CAGE VIEW.

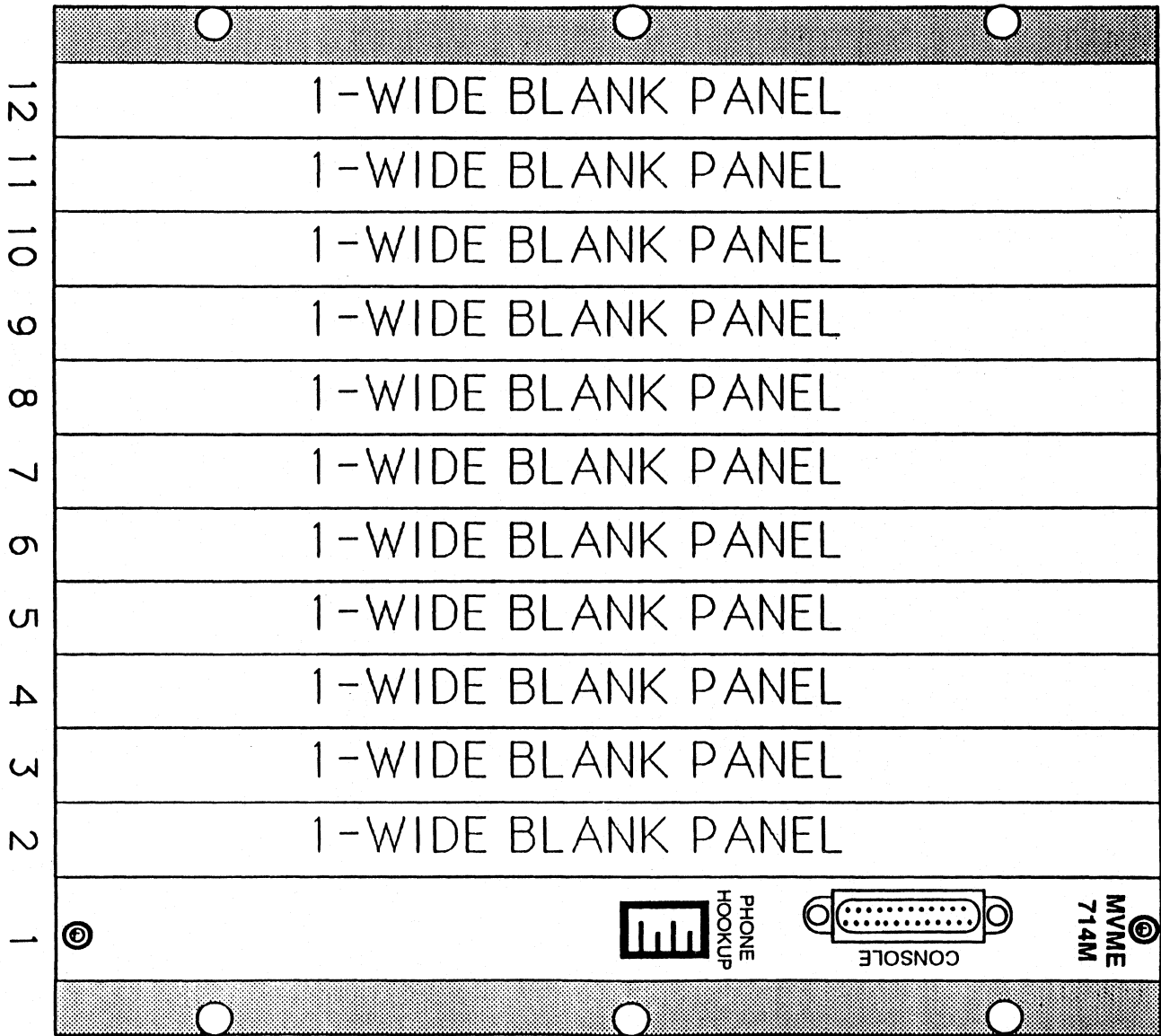


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

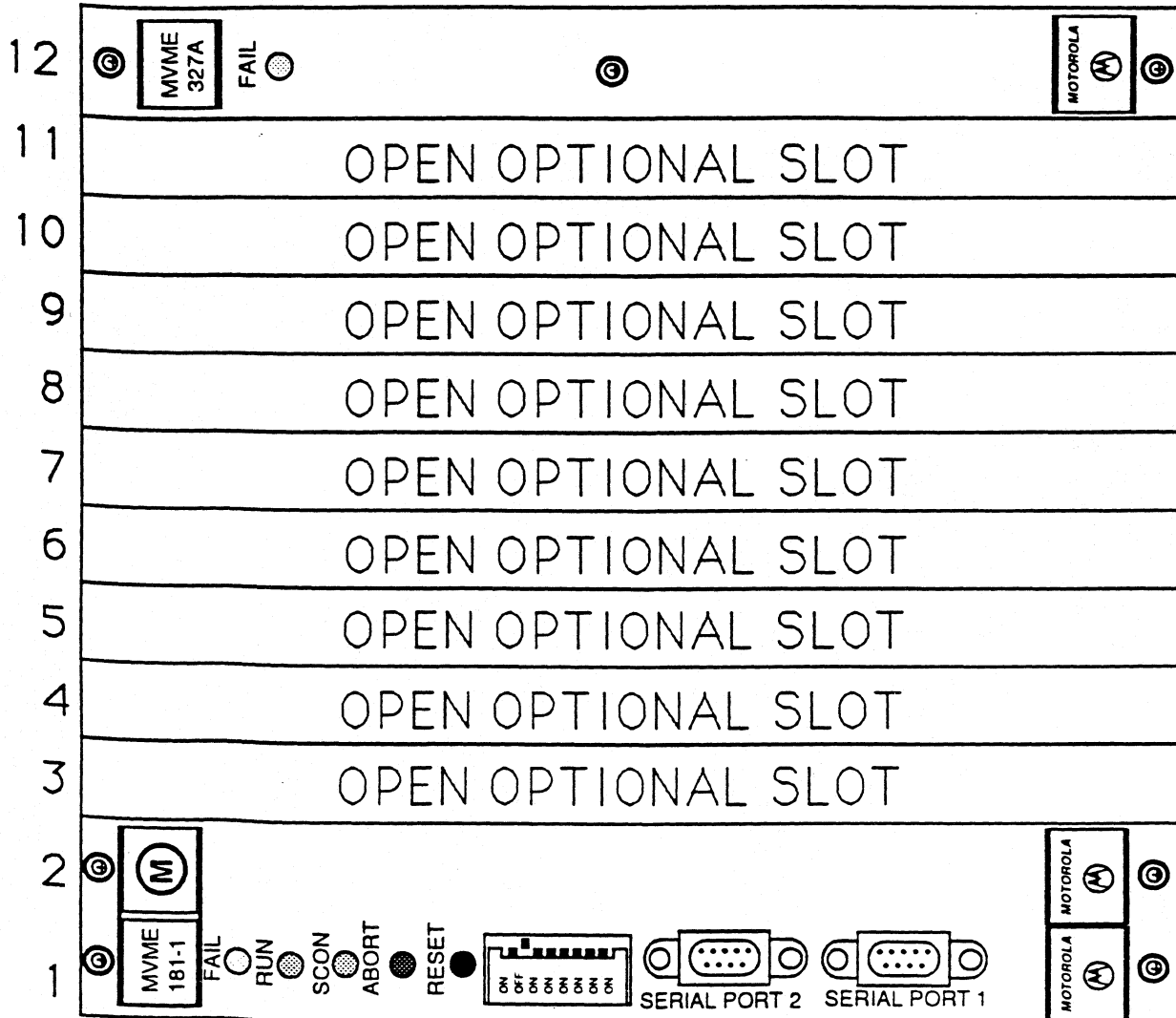
QUALIFIED ASSEMBLIES						
181-1	332XT	373	714M(141/18X)	792(393)	858	955
224-1	333-2	374	717(327A)	792A(393)	872	
224-2	333x.25	393	732	852	875	
323 (-1/-2)	336	705A(333)	751(336)	853	876	
327A	372	710(332/XT)	772(372)	856	883	

SYS8608NY031/032

I/O PANEL VIEW.



SYS8608NY301/302/ 601/602 CARD CAGE VIEW.

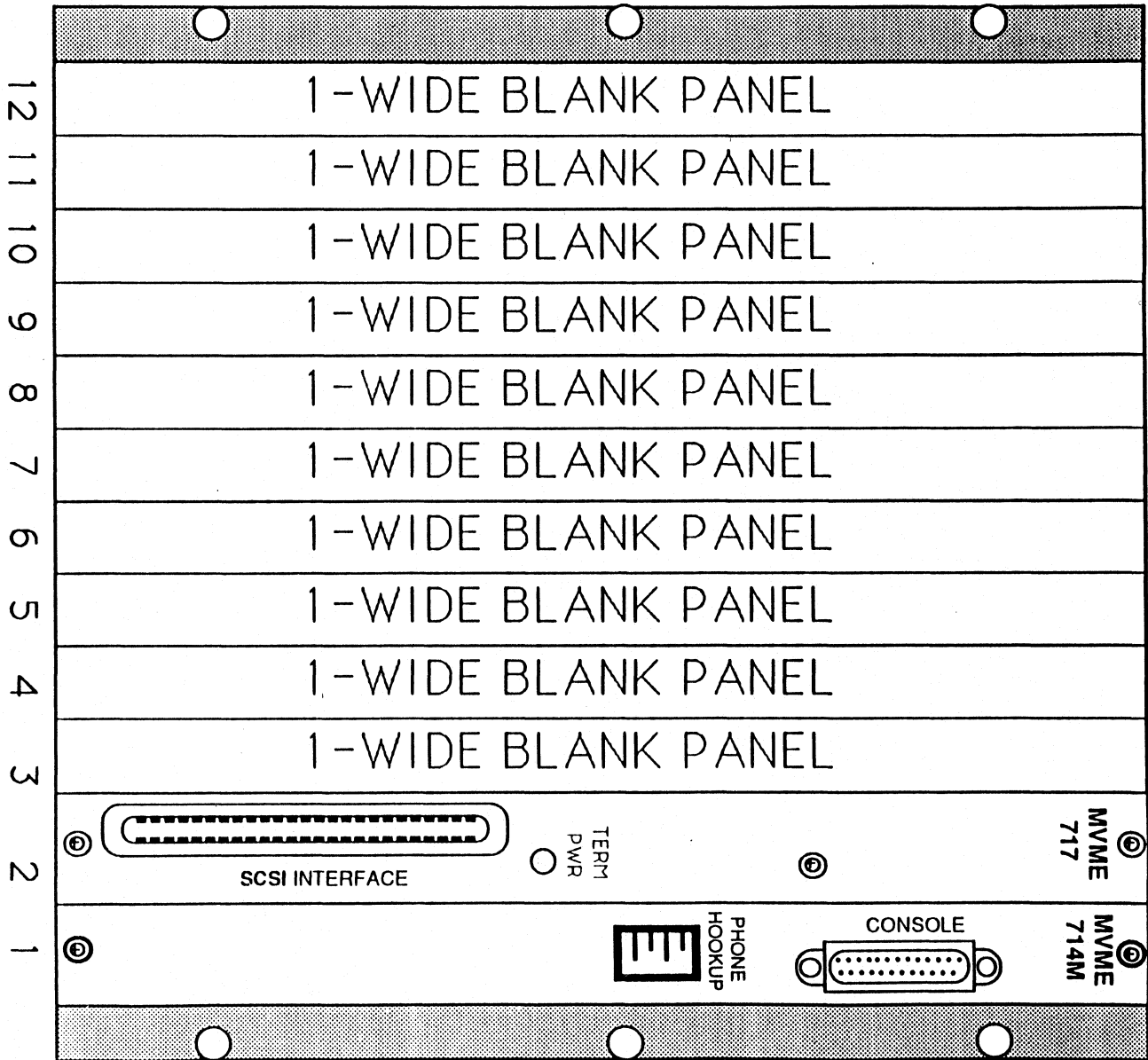


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

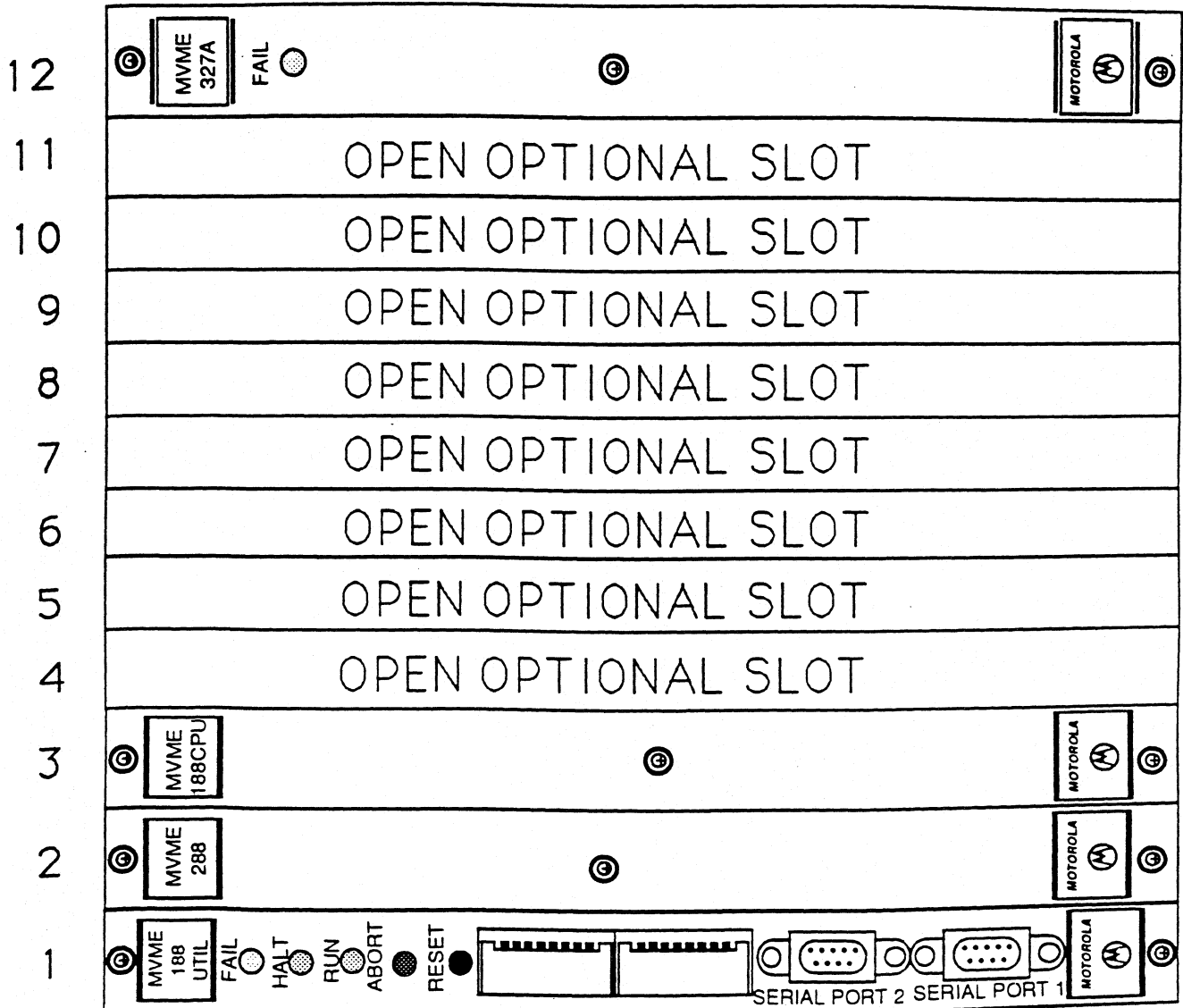
QUALIFIED ASSEMBLIES						
181-1	332XT	373	714M(141/18X)	792(393)	858	955
224-1	333-2	374	717(327A)	792A(393)	872	
224-2	333x.25	393	732	852	875	
323 (-1/-2)	336	705A(333)	751(336)	853	876	
327A	372	710(332/XT)	772(372)	856	883	

SYS8608NY301/302/

601/602 I/O PANEL VIEW.



SYS8864(X)P611/612/ 613/614 CARD CAGE VIEW.

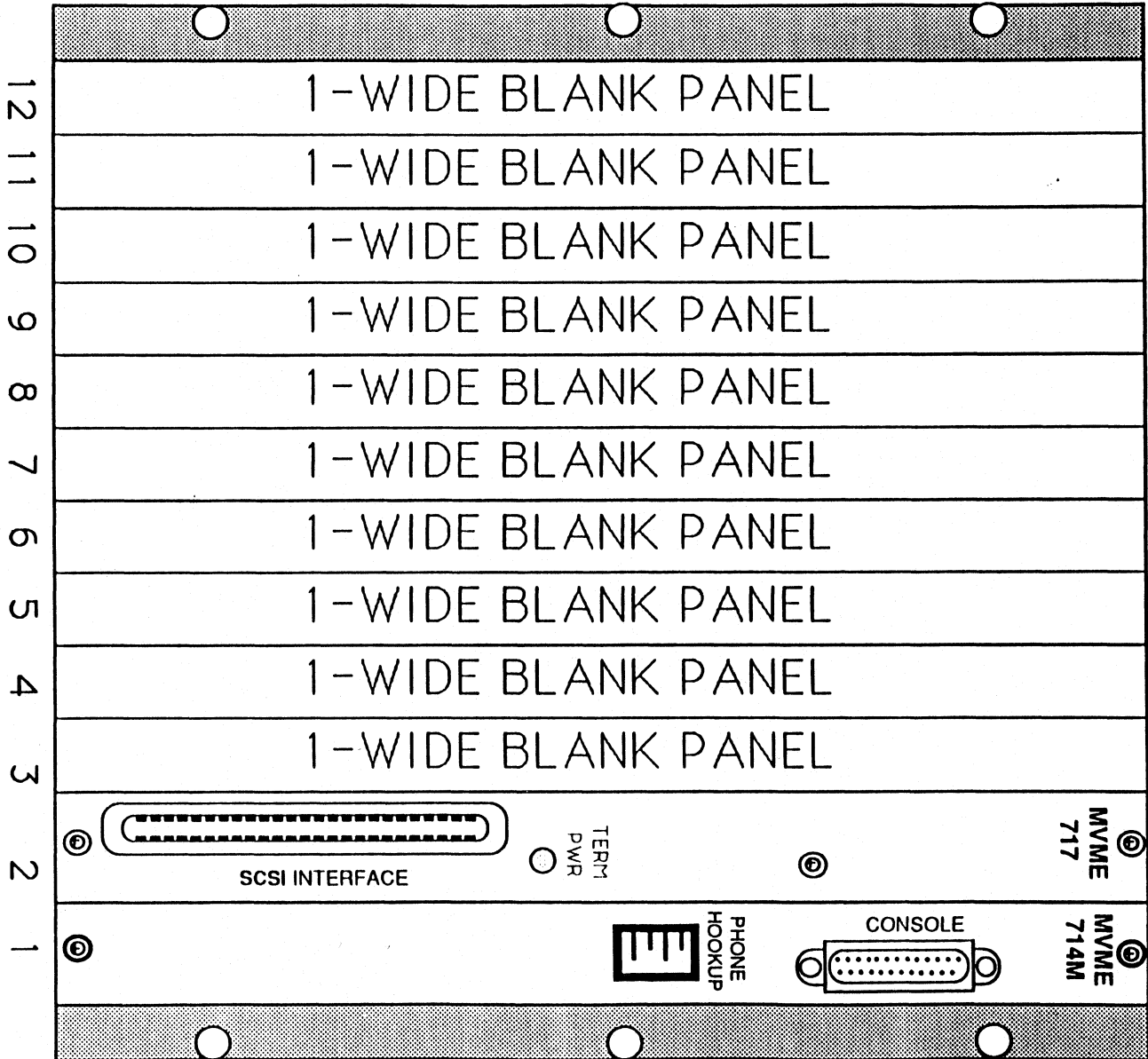


NOTE 1: FOR EASE OF DRAWING, THESE CHASSIS ARE PLACED HORIZONTALLY.

QUALIFIED ASSEMBLIES					
188SP-1	327A	372	710(332/XT)	772(372)	858
188SP-3	332XT	373	714M(141/18X)	792(393)	875
188DP-1	333-2	374	717(327A)	792A(393)	876
188QP-1	333x.25	393	732	853	883
288DF	336	705A(333)	751(336)	856	985

ALL SYS8864

I/O PANEL VIEW.



APPENDIX H

SYS1132 BACK PANEL

													SLOT PREFERENCE		
21 ←	13	12	11	10	9	8	7	6	5	4	3	2	1 ←	PRIORITY	
													1ST	VME707A	1
												1ST	VME731 (MODEM)	2	
			5TH	4TH	3RD	2ND	1ST						1ST MVE710/ VME332XT	3	
	5TH	4TH	3RD	2ND	1ST								1ST MVE710/ VME332XT	4	
	4TH	3RD	2ND	1ST									1ST MVE710/ VME332XT	5	
	3RD	2ND	1ST										1ST MVE710/ VME332XT	6	
		5TH	4TH	3RD	2ND	1ST							1ST VME705A/333X	7	
	5TH	4TH	3RD	2ND	1ST								2ND VME705A/333X	8	

11/15.3

SYS1147 BACK PANEL

												SLOT PREFERENCE	
12	11	10	9	8	7	6	5	4	3	2	1	← PRIORITY →	
											1ST	VME712M/147/A	1
											1ST	VME712A/147S/SA	
									2ND	1ST		1ST VMELAN/374	2
								2ND	1ST			2ND VMELAN/374	3
							2ND	1ST				3RD VMELAN/374	4
						2ND	1ST					4TH VMELAN/374	5
					2ND	1ST						5TH VMELAN/374	6
				2ND	1ST							6TH VMELAN/374	7
	5TH	4TH	3RD	2ND	1ST							1ST VME710/332XT	8
	4TH	3RD	2ND	1ST								2ND VME710/332XT	9
	3RD	2ND	1ST									3RD VME710/332XT	10
	2ND	1ST										4TH VME710/332XT	11
	5TH	4TH	3RD	2ND	1ST							1ST VME705A/333X	12
	4TH	3RD	2ND	1ST								2ND VME705A/333X	13
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME705B/333X	14
10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME705B/333X	15
	5TH	4TH	3RD	2ND	1ST							VME751/336	16
	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST	VME792-2/393	17

12/19/99

SYS2316 BACK PANEL

1	2	3	4	5	6	SLOT PREFERENCE	PRIORITY
1ST						VME707A/132DOF	1
	2ND		1ST			VME715P/335	2
	2ND		1ST			VME710/332XT	3
	2ND		1ST			VME705B/333	4
		4TH	3RD	2ND	1ST	VME330T/330A	5
	5TH	4TH	3RD	2ND	1ST	VME330T/330B	6
	5TH	4TH	3RD	2ND	1ST	VME332PA1	7
							8

11/15/89

SYS2334 BACK PANEL

1	2	3	4	5	6	SLOT PREFERENCE	PRIORITY
1ST						VME716/134F-3	1
	1ST		2ND			VME715P/335	2
	1ST		2ND			VME710/332XT	3
	1ST		2ND			VME705B/333	4
		4TH	3RD	2ND	1ST	VME330T/330A	5
	5TH	4TH	3RD	2ND	1ST	VME330T/330B	6
	5TH	4TH	3RD	2ND	1ST	VME332PA1	7
							8

11/16/89

SYS2616 BACK PANEL

											SLOT PREFERENCE		
12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY	
											1ST	VME330T/330A	1
										2ND	1ST	VME330T/330A	2
									3RD	2ND	1ST	VME707A	3
								4TH	3RD	2ND	1ST	VMD332PA2	4
5TH		4TH		3RD		2ND		1ST				1ST VME710/332XT	5
4TH		3RD		2ND		1ST						2ND VME710/332XT	6
3RD		2ND		1ST								3RD VME710/332XT	7
2ND		1ST										4TH VME710/332XT	8
5TH		4TH		3RD		2ND		1ST				1ST VME705A/333X	9
4TH		3RD		2ND		1ST						2ND VME705A/333X	10
5TH		4TH		3RD		2ND		1ST				VME715P	11
													12

11/17/83

SYS3204/08 BACK PANEL

1	2	3	4	5	6	SLOT PREFERENCE	PRIORITY
1ST						VME712A/147	1
	1ST					VME712B/147	2
	1ST	2ND				VME332PA1/332XT	3
	1ST		2ND			1ST VME710/332XT	4
			1ST			2ND VME710/332XT	5
	1ST	2ND	3RD	4TH	5TH	VME374T/374	6
	1ST	2ND	3RD	4TH	5TH	1ST VME705B/333	7
		1ST	2ND	3RD	4TH	2ND VME705B/333	8
	1ST	2ND	3RD	4TH	5TH	VME715P/335	9
	1ST		2ND			VME751/336	10
	1ST	2ND	3RD	4TH	5TH	VME792-2/393	11
NOTE : SLOT 1 IS THE BOTTOM SLOT.							

11/17/89

SYS3304/08 BACK PANEL

1	2	3	4	5	6	SLOT PREFERENCE	PRIORITY
1ST						VME712M/147	1
		4TH	3RD	2ND	1ST	VME332PA1	2
		2ND		1ST		VME710/332XT	3
				2ND	1ST	VMELAN/330A/B	4
				2ND	1ST	VMELAN/374	5
		2ND		1ST		VME705A/333	6
				2ND	1ST	VME705B/333X25	7
		2ND		1ST		VME715P/335	8
		4TH	3RD	2ND	1ST	VME751/336	9
				2ND	1ST	VME792-2/393	10
						FILLER PANEL	11

11/17/89

SYS3404/08/16 BACK PANEL

1	2	3	4	5	6	7	8	← SLOT PREFERENCE	PRIORITY →
1ST								VME712M/147 (W/ VME732)	1
	1ST							VME712B/147	2
							1ST	VMELAN/374	3
		1ST						1ST VME710/332XT	4
				1ST				2ND VME710/332XT	5
						1ST		3RD VME710/332XT	5
						2ND	1ST	MVMEPAX for 1ST VME332XT	6
					2ND	1ST		MVMEPAX for 1ST VME332XT	7
		1ST	2ND	3RD	4TH	5TH		1ST VME705B/333X	8
			1ST	2ND	3RD	4TH	5TH	2ND VME705B/333X	9
		1ST	2ND	3RD	4TH	5TH	6TH	VME715P/335	10
		1ST		2ND		3RD		VME751/336	11
		1ST	2ND	3RD	4TH	5TH	6TH	VME792-2/393	12
								FILLER PANEL	13

02/01/90

SYS3604/08 BACK PANEL

											SLOT PREFERENCE		
12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY	
											1ST	VME712A/147S	1
										1ST		VME712B/147S	2
									1ST			VMELAN/330A	3
								2ND	1ST			VMELAN/330B	4
								2ND	1ST			1ST VMELAN/374	5
								2ND	1ST			2ND VMELAN/374	6
								2ND	1ST			3RD VMELAN/374	7
					2ND	1ST						4TH VMELAN/374	8
				2ND	1ST							5TH VMELAN/374	9
			2ND	1ST								6TH VMELAN/374	10
		8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			VME332PA1	11
	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			VME332PA2	12
5TH		4TH		3RD		2ND		1ST				1ST VME710/332XT	13
4TH		3RD		2ND		1ST						2ND VME710/332XT	14
3RD		2ND		1ST								3RD VME710/332XT	15
2ND		1ST										4TH VME710/332XT	16
1ST												5TH VME710/332XT	17
5TH		4TH		3RD		2ND		1ST				1ST VME705A/333X	18
4TH		3RD		2ND		1ST						2ND VME705A/333X	19
10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			1ST VME705B/333X	20
9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST				2ND VME705B/333X	21
5TH		4TH		3RD		2ND		1ST				VME751/336	22
10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			VME792-2/393	23

03/09/90

SYS3640 BACK PANEL

												SLOT PREFERENCE	
12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY	
											1ST	VME714M/141	1
										1ST		VME719A	2
1ST												VME717/327A	3
										1ST		1ST VMELAN/374	4
									1ST			2ND VMELAN/374	5
								1ST				3RD VMELAN/374	6
							1ST					4TH VMELAN/374	7
						1ST						5TH VMELAN/374	8
					1ST							6TH VMELAN/374	9
				7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME332PA1	10
			8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME332PA2	11
	5TH	4TH	3RD	2ND	1ST							1ST VME710/332XT	12
	4TH	3RD	2ND	1ST								2ND VME710/332XT	13
	3RD	2ND	1ST									3RD VME710/332XT	14
	2ND	1ST										4TH VME710/332XT	15
	5TH	4TH	3RD	2ND	1ST							1ST VME705A/333X	16
	4TH	3RD	2ND	1ST								2ND VME705A/333X	17
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME705B/333X	18
10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME705B/333X	19
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME705-1/333X	20
	5TH	4TH	3RD	2ND	1ST							VME751/336	21
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME792-2/393	22
												OPEN SLOT	23

04/04/91

SYS3708 BACK PANEL

										SLOT PREFERENCE	
10	9	8	7	6	5	4	3	2	1	PRIORITY	
									1ST	VME712A	1
								1ST		VME712B	2

04/2/91

SYS3840 BACK PANEL

																			SLOT PREFERENCE		
20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY	
																			1ST	VME714M-1/141	1
																			1ST	VME731	2
1ST																				1ST VMEPAX/332XT	3
	1ST																			2ND VMEPAX/332XT	4
1ST	2ND	3RD																		VMELAN/330-A	5
1ST	2ND	3RD	4TH																	1ST 6TH VMELAN/374	6
1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH											VME717/327A	7
											4TH	3RD	2ND	1ST						1ST VME710/332XT	8
										4TH	3RD	2ND	1ST							2ND VME710/332XT	9
							4TH	3RD	2ND	1ST										3RD VME710/332XT	10
					4TH	3RD	2ND	1ST												4TH VME710/332XT	11
			4TH	3RD	2ND	1ST														5TH VME710/332XT	12
	4TH	3RD	2ND	1ST																6TH VME710/332XT	13
	3RD	2ND	1ST																	7TH VME710/332XT	14
	2ND	1ST																		8TH VME710/332XT	15
	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST											1ST VME705A/333X	16
	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST												2ND VME705A/333X	17
19TH	18TH	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME705B/333X	18
18TH	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME705B/333X	19
19TH	18TH	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME751/336	20
19TH	18TH	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME792-2/393	26

SYS8408 BACK PANEL

1	2	3	4	5	6	7	8	SLOT PREFERENCE	PRIORITY
1ST								VME714M/181	1
							1ST	VME717/327A	2
	1ST							VMELAN/374	3
	1ST							1ST VME710/332XT	4
			1ST					2ND VME710/332XT	5
					1ST			3RD VME710/332XT	6
						1ST		MVMEPA2	7
					2ND	1ST		1ST MVMEPA1	8
					1ST			2ND MVMEPA1	9
	1ST	2ND	3RD	4TH	5TH	6TH		VME715P/335	10
	1ST		2ND		3RD			VME751/336	11
	1ST	2ND	3RD	4TH	5TH	6TH		VME792-2/393	12
								FILLER PANEL	13

02/01/90

SYS8440 BACK PANEL

1	2	3	4	5	6	7	8	SLOT PREFERENCE	PRIORITY
1ST								VME714M/188	1
							1ST	VMETTRAN/328	2
							1ST	1ST VMETTRAN/374	3
					2ND			2ND VMETTRAN/374	4
			1ST 2 SLOTS FROM THE LEFT					VMETTRAN/338	5
	NEXT 2 SLOTS FROM THE LEFT							1ST VME710/332XT	6
	NEXT 2 SLOTS FROM THE LEFT							2ND VME710/332XT	7
	1ST AVAILABLE SLOT FROM THE LEFT							MVMEPA1	8
	1ST AVAILABLE SLOT FROM THE LEFT							MVMEPA2	9
	1ST 2 AVAILABLE SLOTS FROM THE LEFT							VME715A/333	10
	1ST AVAILABLE SLOT FROM THE LEFT							VME715B/333	11
	1ST AVAILABLE SLOT FROM THE LEFT							VME715-1/333	12
	1ST AVAILABLE SLOT FROM THE LEFT							VME715B/333X25	13
	1ST AVAILABLE SLOT FROM THE LEFT							VME715-1/333X25	14
	1ST 2 AVAILABLE SLOTS FROM THE LEFT							VME751/336	15

06/07/91

SYS8608 BACK PANEL

												SLOT PREFERENCE				
12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY				
												1ST	VME714M/181	1		
1ST													VME717/327A	2		
												1ST	VMELAN/374	3		
												2ND	1ST	VME332PA1	4	
												3RD	2ND	1ST	VME332PA2	5
						3RD		2ND		1ST			1ST	VME332XT	6	
				3RD		2ND		1ST					2ND	VME332XT	7	
		3RD		2ND		1ST							3RD	VME332XT	8	
		2ND		1ST									4TH	VME332XT	9	
		5TH		4TH		3RD		2ND		1ST			1ST	VME705A/333X	10	
		4TH		3RD		2ND		1ST					2ND	VME705A/333X	11	
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			1ST	VME705B/333X	12	
10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST				2ND	VME705B/333X	13	
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			VME715P/335	14		
		5TH		4TH		3RD		2ND		1ST			VME751/336	15		
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			VME792-2/393	16		

03/09, JJ

SYS8640 BACK PANEL

SLOT PREFERENCE
 PRIORITY

* SLOTS 14-17 ARE LOCATED HORIZONTALLY (NUMBERED BOTTOM TO TOP) ABOVE THE VERTICAL BACK PANEL.

17*	16*	15*	14*	13	12	11	10	9	8	7	6	5	4	3	2	1			
1ST																		VME714M	1
			1ST															1ST SCSI INTERF	2
		1ST																2ND SCSI INTERF	3
															1ST			1ST VMELAN PANEL	4
													1ST					2ND VMELAN PANEL	5
													1ST					3RD VMELAN PANEL	6
												1ST						4TH VMELAN PANEL	7
											NEXT 2 SLOTS FROM RIGHT						VMETRANS/338	8	
				1ST														1ST VME710/332XT	9
						1ST												2ND VME710/332XT	10
								1ST										3RD VME710/332XT	11
										1ST								4TH VME710/332XT	12
												1ST						5TH VME710/332XT	13
										7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME332PA1	14
										7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME332PA2	15
														2ND	1ST			2ND VME332PA2	16
				NEXT 2 SLOTS FROM LEFT												1ST VME705A/333X	17		
				NEXT 2 SLOTS FROM LEFT												2ND VME705A/333X	18		
				NEXT 2 SLOTS FROM LEFT												1ST VME705B/333X	19		
				NEXT 2 SLOTS FROM LEFT												2ND VME705B/333X	20		
				NEXT 2 SLOTS FROM LEFT												VME751/336	21		

06/27/91

																			PREFERENCE			
20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY		
																			1ST	VME714M/188	1	
1ST																				1ST	VMETRAN/328-X	2
	1ST																			2ND	VMETRAN/328-X	3
	1ST	2ND																		1ST	VMELAN/374	4
		1ST	2ND																	2ND	VMELAN/374	5
			1ST	2ND																3RD	VMELAN/374	6
				1ST	2ND															4TH	VMELAN/374	7
1ST 2 AVAILABLE SLOTS TO THE LEFT																				VMRTRAN338	8	
																			1ST	1ST	VME710/332XT	9
																1ST				2ND	VME710/332XT	10
														1ST						3RD	VME710/332XT	11
											1ST									4TH	VME710/332XT	12
										1ST										5TH	VME710/332XT	13
										1ST										6TH	VME710/332XT	14
											1ST									7TH	VME710/332XT	15
																			1ST	8TH	VME710/332XT	16
1ST AVAILABLE SLOT TO THE RIGHT																				1ST	VMEPA2/332XT	17
1ST AVAILABLE SLOT TO THE RIGHT																				2ND	VMEPA2/332XT	18
1ST AVAILABLE SLOT TO THE RIGHT																				3RD	VMEPA2/332XT	19
1ST AVAILABLE SLOT TO THE RIGHT																				1ST	VMEPA1/332XT	20
	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST											1ST	VME705A/333X	21

PAGE 17

06/06/91

SYS8840 BACK PANEL PAGE 2 OF 2

																		SLOT PREFERENCE			
20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY	
	8TH		7TH		6TH		5TH		4TH		3RD		2ND		1ST					2ND VME705A/333X	22
	18TH	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME705B/333X	23
	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME705B/333X	24
	18TH	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME705-1/333X25	25
	17TH	16TH	15TH	14TH	13TH	12TH	11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME705-1/333X25	26
	9TH		8TH		7TH		6TH		5TH		4TH		3RD		2ND		1ST			VME751/336	27

SYS8864 BACK PANEL

											SLOT PREFERENCE		
12	11	10	9	8	7	6	5	4	3	2	1	PRIORITY	
											1ST	VME714M/188	1
1ST												VME717/327A	2
										1ST		VMELAN/374	3
										2ND	1ST	VME332PA1	4
									3RD	2ND	1ST	VME332PA2	5
					3RD	2ND	1ST					1ST VME332XT	6
			3RD	2ND	1ST							2ND VME332XT	7
	3RD	2ND	1ST									3RD VME332XT	8
	2ND	1ST										4TH VME332XT	9
	5TH	4TH	3RD	2ND	1ST							1ST VME705A/333X	10
	4TH	3RD	2ND	1ST								2ND VME705A/333X	11
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME705B/333X	12
10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME705B/333X	13
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME715P/335	14
	5TH	4TH	3RD	2ND	1ST							VME751/336	15
11TH	10TH	9TH	8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME792-2/393	16

03/09/90

APPENDIX 1

SYS1132 CARD CAGE

												SLOT PREFERENCE	
1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
1ST												VME132XT	1
		1ST	2ND	3RD								1ST VME204-2F	2
			1ST	2ND	3RD							2ND VME204-2F	3
				1ST	2ND	3RD						3RD VME204-2F	4
					1ST	2ND	3RD					4TH VME204-2F	5
		1ST	2ND	3RD								1ST VME224-2	6
			1ST	2ND	3RD							2ND VME224-2	7
		1ST	2ND	3RD								1ST VME224-1	8
			1ST	2ND	3RD							2ND VME224-1	9
					1ST	2ND	3RD				1ST	VME320B-1	10
											1ST	VME323-1	11
												VME350	12
				1ST	2ND	3RD	4TH					VME330-B	13
				1ST	2ND	3RD	4TH					1ST VME332XT	14
					1ST	2ND	3RD	4TH				2ND VME332XT	15
						1ST	2ND	3RD	4TH			3RD VME332XT	16
							1ST	2ND	3RD			4TH VME332XT	17
				1ST	2ND	3RD	4TH					1ST VME333	18
					1ST	2ND	3RD	4TH				2ND VME333	19
													20

11/15/89

SYS1147 CARD CAGE

												SLOT PREFERENCE	
1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
1ST												VME147	1
1ST												VME147A	
1ST												VME147S/SA	
	1ST	2ND										1ST VME224-2	2
		1ST	2ND									2ND VME224-2	3
	1ST	2ND	3RD									1ST VME224-1	4
		1ST	2ND									2ND VME224-1	5
	1ST	2ND	3RD	4TH								1ST VME374	6
		1ST	2ND	3RD	4TH							2ND VME374	7
			1ST	2ND	3RD	4TH						3RD VME374	8
				1ST	2ND	3RD	4TH					4TH VME374	9
					1ST	2ND	3RD	4TH				5TH VME374	10
						1ST	2ND	3RD	4TH			6TH VME374	11
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	1ST VME332XT	12
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	2ND VME332XT	13
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	3RD VME332XT	14
				1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	4TH VME332XT	15
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	1ST VME333	16
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	2ND VME333	17
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	VME333X25	18
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	VME336	19
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	VME393	20

12/19/89

SYS2316 CARD CAGE

						SLOT PREFERENCE	
1	2	3	4	5	6	PRIORITY	
1ST						VME132DOF	1
	1ST					VME204-2F	2
	1ST					VME205	3
					1ST	VME320B	4
				2ND	1ST	VME323	5
			3RD	2ND	1ST	VME350	6
		3RD	2ND	1ST		VME335	7
		3RD	2ND	1ST		VME332	8
		3RD	2ND	1ST		VME332XT	9
		3RD	2ND	1ST		VME333	10
		3RD	2ND	1ST		VME330-A OR VME330-B	11
							12

11/15/89

SYS2334 CARD CAGE

						SLOT PREFERENCE	
1	2	3	4	5	6	PRIORITY	
1ST						VME134F-3	1
				2ND	1ST	VME320B	2
					1ST	VME323	3
			3RD	2ND	1ST	VME350	4
		3RD	2ND	1ST		VME335	5
		3RD	2ND	1ST		VME332XT	6
		3RD	2ND	1ST		VME333	7
		3RD	2ND	1ST		VME330-A OR VME330-B	8
							9

11/16/89

SYS2616 CARD CAGE

SLOT
PREFERENCE

1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
1ST												VME132DOF	1
1ST												VME132XT	2
	1ST	2ND	3RD									1ST VME205	3
		1ST	2ND	3RD								2ND VME205	4
			1ST	2ND	3RD							3RD VME205	5
	1ST	2ND	3RD									1ST VME204-2F	6
		1ST	2ND	3RD								2ND VME204-2F	7
			1ST	2ND	3RD							3RD VME204-2F	8
				1ST	2ND	3RD						4TH VME204-2F	9
										2ND	1ST	1ST VME320B	10
									2ND	1ST		2ND VME320B	11
											1ST	VME323	12
										1ST		VME350	13
								3RD	2ND	1ST		1ST VME360	14
							3RD	2ND	1ST			2ND VME360	15
						4TH	3RD	2ND	1ST			VME355	16
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH			VME330-B	17
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH			VME330-A	18
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH			1ST VME332	19
			1ST	2ND	3RD	4TH	5TH	6TH	7TH			2ND VME332	20
				1ST	2ND	3RD	4TH	5TH	6TH			3RD VME332	21
					1ST	2ND	3RD	4TH	5TH			4TH VME332	22
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH			1ST VME332XT	23
			1ST	2ND	3RD	4TH	5TH	6TH	7TH			2ND VME332XT	24
				1ST	2ND	3RD	4TH	5TH	6TH			3RD VME332XT	25
					1ST	2ND	3RD	4TH	5TH			4TH VME332XT	26
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH			1ST VME333	27
			1ST	2ND	3RD	4TH	5TH	6TH	7TH			2ND VME333	28
													29

11/17/89

SYS3204/08 CARD CAGE

1	2	3	SLOT PREFERENCE	PRIORITY
1ST			VME147	1
	1ST	2ND	1ST VME224-2	2
		1ST	2ND VME224-2	3
	1ST	2ND	1ST VME224-1	4
		1ST	2ND VME224-1	5
	1ST	2ND	1ST VME332XT	6
		1ST	2ND VME332XT	7
	1ST	2ND	VME374	8
	1ST	2ND	1ST VME333	9
		1ST	2ND VME333	10
	1ST	2ND	VME333X25	11
	1ST	2ND	VME335	12
	1ST	2ND	VME336	13
	1ST	2ND	VME393	14
NOTE: SLOT 1 IS THE BOTTOM SLOT.				

11/17/89

SYS3304/08 CARD CAGE

						SLOT PREFERENCE	
1	2	3	4	5	6	PRIORITY	
1ST						VME147	1
1ST						VME147A	1
1ST						VME147-1	1
1ST						VME147A-1	1
	1ST	2ND				1ST VME224-2	2
		1ST	2ND			2ND VME224-2	3
	1ST	2ND				1ST VME224-1	4
		1ST	2ND			2ND VME224-1	5
			1ST	2ND		VME374	6
		3RD	2ND	1ST		VME330-A	7
		3RD	2ND	1ST		VME330-B	8
		3RD	2ND	1ST		1ST VME332XT	9
			2ND	1ST		2ND VME332XT	10
		1ST	2ND	3RD		1ST VME333	11
			1ST	2ND		2ND VME333	12
		1ST	2ND	3RD		VME333X25	13
		3RD	2ND	1ST		VME335	14
		1ST	2ND	3RD		VME336	15
		1ST	2ND	3RD		VME393	16
						FILLER PANEL	17

11/17/89

SYS3404/08/16 CARD CAGE

						SLOT PREFERENCE	
1	2	3	4	5	6	PRIORITY	
1ST						VME147	1
	1ST					1ST VME224-2	2
		1ST				2ND VME224-2	3
	1ST	2ND				1ST VME224-1	4
	1ST					2ND VME224-1	5
	1ST	2ND	3RD			VME374	6
	1ST	2ND	3RD	4TH		1ST VME332XT	7
		1ST	2ND	3RD	4TH	2ND VME332XT	8
			1ST	2ND	3RD	3RD VME332XT	9
	1ST	2ND	3RD	4TH	5TH	VME333	10
	1ST	2ND	3RD	4TH	5TH	VME333X25	11
	1ST	2ND	3RD	4TH	5TH	VME335	12
	1ST	2ND	3RD	4TH	5TH	VME336	13
	1ST	2ND	3RD	4TH	5TH	VME393	14

02/02/96

SYS3604/08 CARD CAGE

												SLOT PREFERENCE	
1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
1ST												VME147S/A-1	1
	1ST											1ST VME224-2	2
		1ST										2ND VME224-2	3
	1ST	2ND										1ST VME224-1	4
		1ST										2ND VME224-1	5
	1ST	2ND	3RD									VME330-A	6
	1ST	2ND	3RD	4TH								1ST VME374	7
		1ST	2ND	3RD	4TH							2ND VME374	8
			1ST	2ND	3RD	4TH						3RD VME374	9
				1ST	2ND	3RD	4TH					4TH VME374	10
					1ST	2ND	3RD	4TH				5TH VME374	11
						1ST	2ND	3RD	4TH			6TH VME374	12
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH		VME330-B	13
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	1ST VME332XT	14
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	2ND VME332XT	15
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	3RD VME332XT	16
				1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	4TH VME332XT	17
					1ST	2ND	3RD	4TH	5TH	6TH	7TH	5TH VME332XT	18
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	1ST VME333	19
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	2ND VME333	20
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	VME333X25	21
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	VME336	22
	1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	VME393	23

03/09/90

SYS3640 CARD CAGE

SYS3640 CARD CAGE												SLOT PREFERENCE	
1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
1ST												VME141-2	1
	1ST											1ST VME224A-3/-2/-1	2/7/12
		1ST										2ND VME224A-3/-2/-1	3/8/13
			1ST									3RD VME224A-3/-2/-1	4/7/14
				1ST								4TH VME224A-3/-2/-1	5/10/15
					1ST							5TH VME224A-3/-2/-1	6/11/16
	1ST											1ST VME230-2	17
		1ST										2ND VME230-2	18
			1ST									3RD VME230-2	19
				1ST								4TH VME230-2	20
					1ST							5TH VME230-2	21
											1ST	VME323-2	22
											1ST	VME350	23
											1ST	VME327A	24
		1ST	2ND	3RD	4TH							1ST VME374	25
			1ST	2ND	3RD	4TH						2ND VME374	26
				1ST	2ND	3RD	4TH					3RD VME374	27
					1ST	2ND	3RD	4TH				4TH VME374	28
						1ST	2ND	3RD	4TH			5TH VME374	29
							1ST	2ND	3RD	4TH		6TH VME374	30
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		1ST VME332XT	31
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH		2ND VME332XT	32
				1ST	2ND	3RD	4TH	5TH	6TH	7TH		3RD VME332XT	33
					1ST	2ND	3RD	4TH	5TH	6TH		4TH VME332XT	34
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		1ST VME333	35
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH		2ND VME333	36
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		VME333X25	37
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		VME336	38
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		VME393	39

04/04, 1

SYS3708 CARD CAGE

																			SLOT PREFERENCE		
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	PRIORITY	
1ST																				VME147SA-1	1
	1ST																			VME224A-2	2

YS3840 CARD CAGE PAGE 1 OF 2

																			SLOT PREFERENCE		
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	PRIORITY	
1ST																				VME141-2	1
	1ST																			1ST VME224-2	2
		1ST																		2ND VME224-2	3
			1ST																	3RD VME224-2	4
				1ST																4TH VME224-2	5
					1ST															5TH VME224-2	6
	1ST	2ND	3RD	4TH	5TH															1ST VME224-1	7
		1ST	2ND	3RD	4TH															2ND VME224-1	8
			1ST	2ND	3RD															3RD VME224-1	9
				1ST	2ND															4TH VME224-1	10
					1ST															5TH VME224-1	11
																			1ST	VME323-2	12
																		1ST		VME350	13
																		1ST		VME327A	14
																	2ND	1ST		1ST VME374	15
																2ND	1ST			2ND VME374	16

SYS3840 CARD CAGE PAGE 2 OF 2

																			SLOT PREFERENCE		
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	PRIORITY	
															2ND	1ST				3RD VME374	17
															2ND	1ST				4TH VME374	18
														2ND	1ST					5TH VME374	19
												2ND	1ST							6TH VME374	20
											8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME330-A	21
						1ST	2ND	3RD	4TH											1ST VME332XT	22
							1ST	2ND	3RD	4TH										2ND VME332XT	23
								1ST	2ND	3RD	4TH									3RD VME332XT	24
									1ST	2ND	3RD	4TH								4TH VME332XT	25
										1ST	2ND	3RD	4TH							5TH VME332XT	26
											1ST	2ND	3RD	4TH						6TH VME332XT	27
												1ST	2ND	3RD	4TH					7TH VME332XT	28
													1ST	2ND	3RD	4TH				8TH VME332XT	29
						1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	12TH			1ST VME333	30
							1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	12TH		2ND VME333	31
						1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	12TH	13TH	14TH	VME333X25	32
						1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	12TH	13TH	14TH	VME336	33
						1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	12TH	13TH	14TH	VME393	34

SYS8408 CARD CAGE

						SLOT PREFERENCE	
1	2	3	4	5	6	PRIORITY	
1ST						VME181-1	1
		1ST				1ST VME236-3	2
			1ST			2ND VME236-3	3
		1ST	2ND			1ST VME236-2	4
			1ST	2ND		2ND VME236-2	5
				1ST		3RD VME236-2	4
					1ST	VME327A	5
		1ST	2ND	3RD		VME374	6
		1ST	2ND	3RD		1ST VME332XT	7
			1ST	2ND		2ND VME332XT	8
				1ST		3RD VME332XT	9
		1ST	2ND	3RD		VME335	12
		1ST	2ND	3RD		VME336	13
		1ST	2ND	3RD		VME393	14

02/02/

SYS8440 CARD CAGE

						SLOT PREFERENCE	
1	2	3	4	5	6	PRIORITY	
1ST						VME188	1
					1ST	VME228-X	2
				1ST		1ST VME374	3
			1ST			2ND VME374	4
			2ND	1ST		SMM/VME338	5
			2ND	1ST		1ST VME332XT	6
			1ST			2ND VME332XT	7
			2ND	1ST		VME333	8
			2ND	1ST		VME333X25	9
			2ND	1ST		VME336	10

06/07/91

SYS8604/08 CARD CAGE

												SLOT PREFERENCE	
1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
1ST												VME181-1	1
		1ST										1ST VME236-3	2
			1ST									2ND VME236-3	3
		1ST										1ST VME236-2	4
			1ST									2ND VME236-2	5
				1ST								3RD VME236-2	6
					1ST							4TH VME236-2	7
											1ST	VME323-2	8
										1ST		VME350	9
											1ST	VME327A	10
		1ST	2ND	3RD	4TH	5TH						VME374	11
		1ST	2ND	3RD	4TH	5TH	6TH					1ST VME332XT	12
			1ST	2ND	3RD	4TH	5TH	6TH				2ND VME332XT	13
				1ST	2ND	3RD	4TH	5TH	6TH			3RD VME332XT	14
					1ST	2ND	3RD	4TH	5TH	6TH		4TH VME332XT	15
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		1ST VME333	16
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH		2ND VME333	17
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		VME333X25	18
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		VME335	19
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		VME336	20
		1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH		VME393	21

06/60'00

SYS8640 CARD CAGE

												SLOT PREFERENCE	
1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
1ST												VME188	1
1ST												VME188 W/ 2ND 288	1
1ST												VME188 W/ 3RD 288	1
1ST												VME188 W/ 4TH 288	1
											1ST	1ST VME328-X	2
											1ST	2ND VME328-X	3
									2ND	1ST		1ST VME374	4
								2ND	1ST			2ND VME374	5
							2ND	1ST				2RD VME374	6
						2ND	1ST					4TH VME374	7
					6TH	5TH	4TH	3RD	2ND	1ST		1ST SMM/VME338	8
				6TH	5TH	4TH	3RD	2ND	1ST			2ND SMM/VME338	9
			6TH	5TH	4TH	3RD	2ND	1ST				3RD SMM/VME338	10
			5TH	4TH	3RD	2ND	1ST					4TH SMM/VME338	11
			8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME332XT	12
			7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME332XT	13
			6TH	5TH	4TH	3RD	2ND	1ST				3RD VME332XT	14
			5TH	4TH	3RD	2ND	1ST					4TH VME332XT	15
			4TH	3RD	2ND	1ST						5TH VME332XT	16
			8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		1ST VME333-2	17
			7TH	6TH	5TH	4TH	3RD	2ND	1ST			2ND VME333-2	18
			8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME333X25	19
			8TH	7TH	6TH	5TH	4TH	3RD	2ND	1ST		VME336	20

06/25/91

3YS8840 CARD CAGE PAGE 1 OF 2

																			SLOT PREFERENCE		
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	PRIORITY	
1ST 6-SLOTS RESERVED																				VME188	1
																			1ST	1ST VME328-X	2
																		1ST		2ND VME328-X	3
																	2ND	1ST		1ST VME374	4
																2ND	1ST			2ND VME374	5
															2ND	1ST				3RD VME374	6
															2ND	1ST				4TH VME374	7
													6TH	5TH	4TH	3RD	2ND	1ST		1ST SMM/VME338	8
												6TH	5TH	4TH	3RD	2ND	1ST			2ND SMM/VME338	9
											6TH	5TH	4TH	3RD	2ND	1ST				3RD SMM/VME338	10
										6TH	5TH	4TH	3RD	2ND	1ST					4TH SMM/VME338	11
							1ST													1ST VME332XT	12
								1ST												2ND VME332XT	13
									1ST											3RD VME332XT	14
										1ST										4TH VME332XT	15
											1ST									5TH VME332XT	16

SYS8840 CARD CAGE PAGE 2 OF 2

																			SLOT PREFERENCE		
1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	PRIORITY	
											1ST									6TH VME332XT	17
												1ST								7TH VME332XT	18
													1ST							8TH VME332XT	19
						1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH						1ST VME333	20
							1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH					2ND VME333	21
						1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH				VME333X25	22
						1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH	9TH	10TH	11TH	12TH			VME336	23

SYS8864 CARD CAGE

												SLOT PREFERENCE	
1	2	3	4	5	6	7	8	9	10	11	12	PRIORITY	
3 SLOTS MINIMUM			3 SLOTS MAXIMUM									VME188	1
											1ST	VME327A	2
			1ST	2ND	3RD	4TH						VME374	3
			1ST	2ND	3RD	4TH	5TH					1ST VME332XT	4
				1ST	2ND	3RD	4TH	5TH				2ND VME332XT	5
					1ST	2ND	3RD	4TH	5TH			3RD VME332XT	6
						1ST	2ND	3RD	4TH	5TH		4TH VME332XT	7
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH		1ST VME333	8
				1ST	2ND	3RD	4TH	5TH	6TH	7TH		2ND VME333	9
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH		VME333X25	10
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH		VME336	11
			1ST	2ND	3RD	4TH	5TH	6TH	7TH	8TH		VME393	12

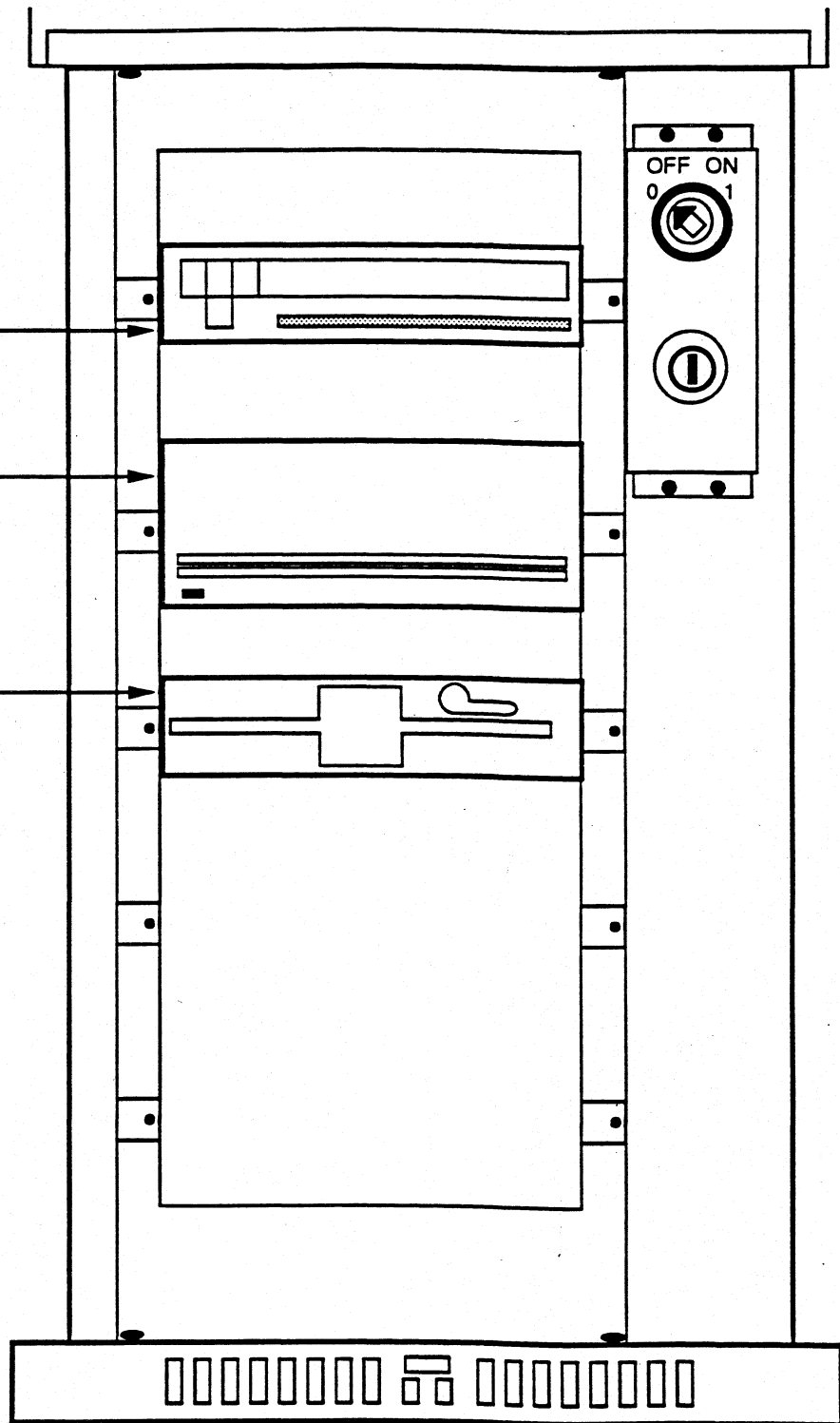
03/09/90

APPENDIX J

4,0
STREAMING
TAPE

HARD DRIVE
ST-506 = 0,0
ESDI = 8,0

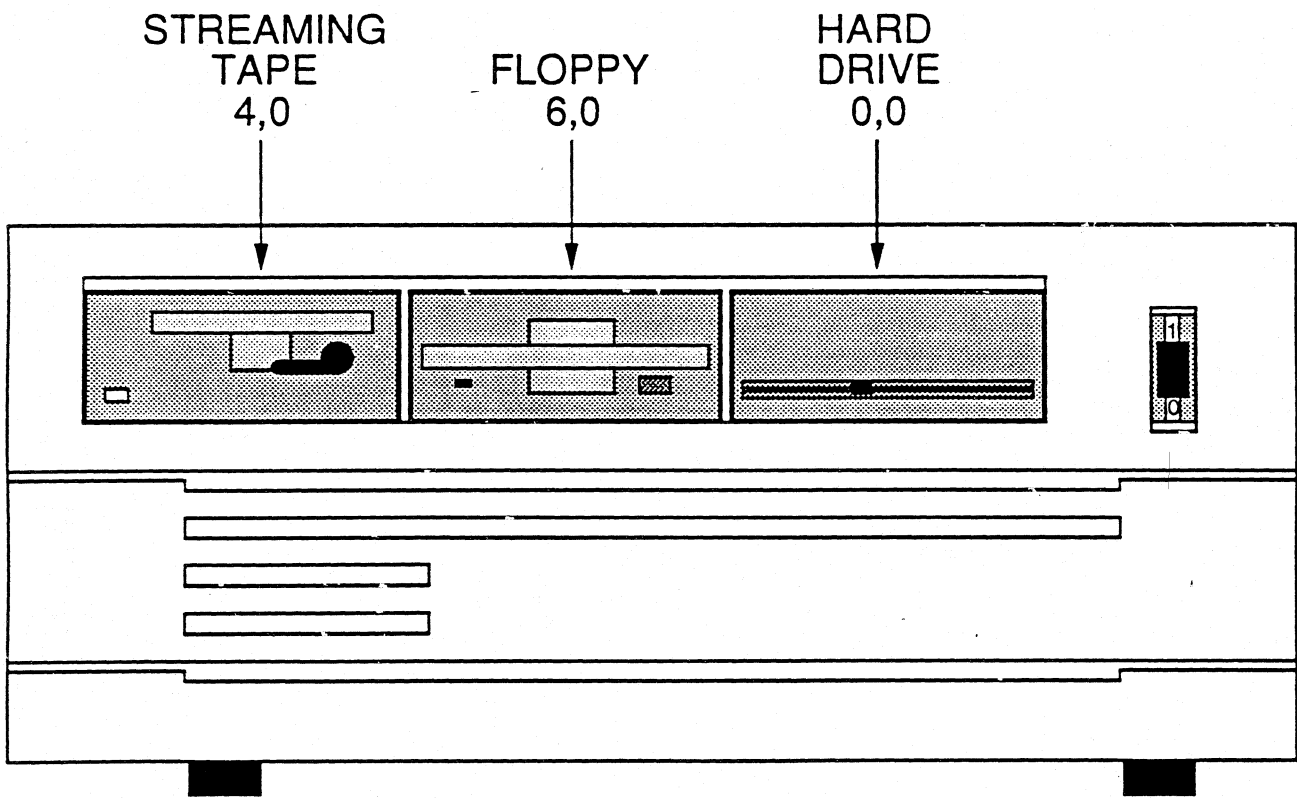
0,2
FLOPPY



DRIVE PLACEMENT FOR VME320A/
323/350 12-SLOT CHASSIS.

NOTE 1: THIS CONFIGURATION USED IN
SYS2016, 2316, 2334, 2616's.

11/15/89



11/17/89

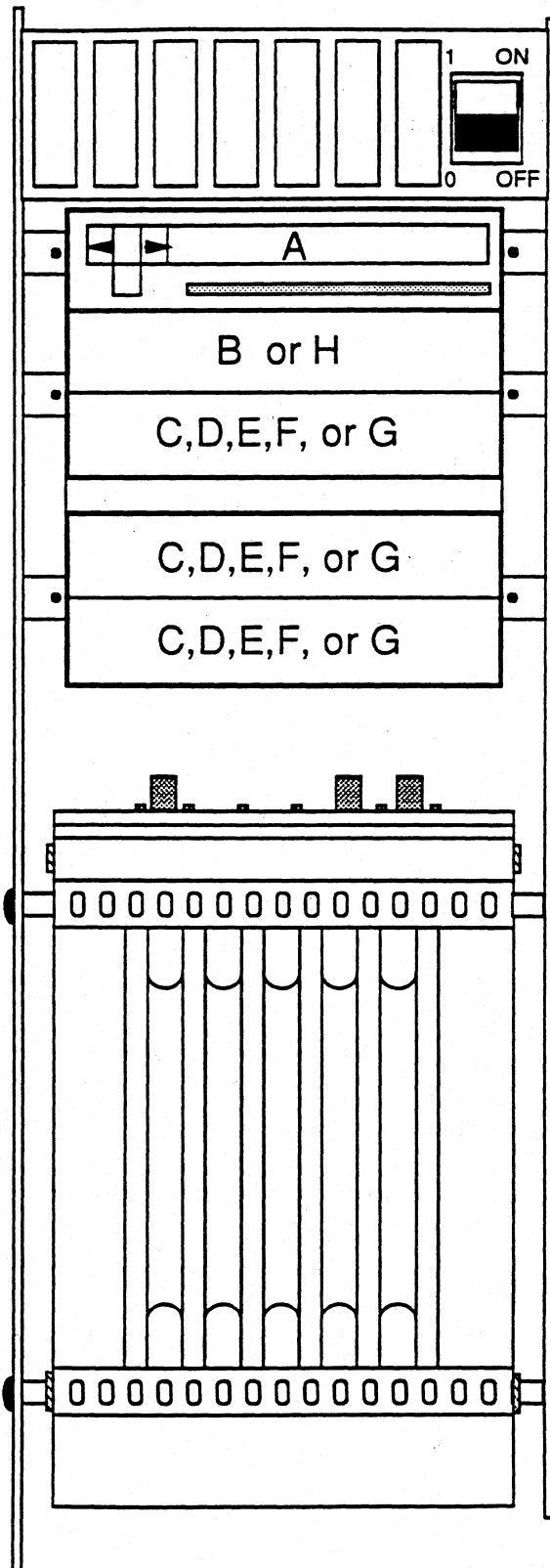
**DRIVE PLACEMENT FOR VME147(X)
SCSI INTERFACES TO 3-SLOT CHASSIS.**

NOTE 1: THIS CONFIGURATION USED IN SYS3200's.

SCSI DRIVES
A VME852 60MB STREAMING TAPE
B VME853 150MB STREAMING TAPE
C VME872 48MB WINCHESTER DRIVE
D VME873 85MB WINCHESTER DRIVE
E VME874 150MB WINCHESTER DRIVE
F VME875 300MB WINCHESTER DRIVE
G VME876 600MB WINCHESTER DRIVE
FLOPPY DRIVES
H VME881 1.2MB FLOPPY/SCSI CONT.

DRIVE SELECT

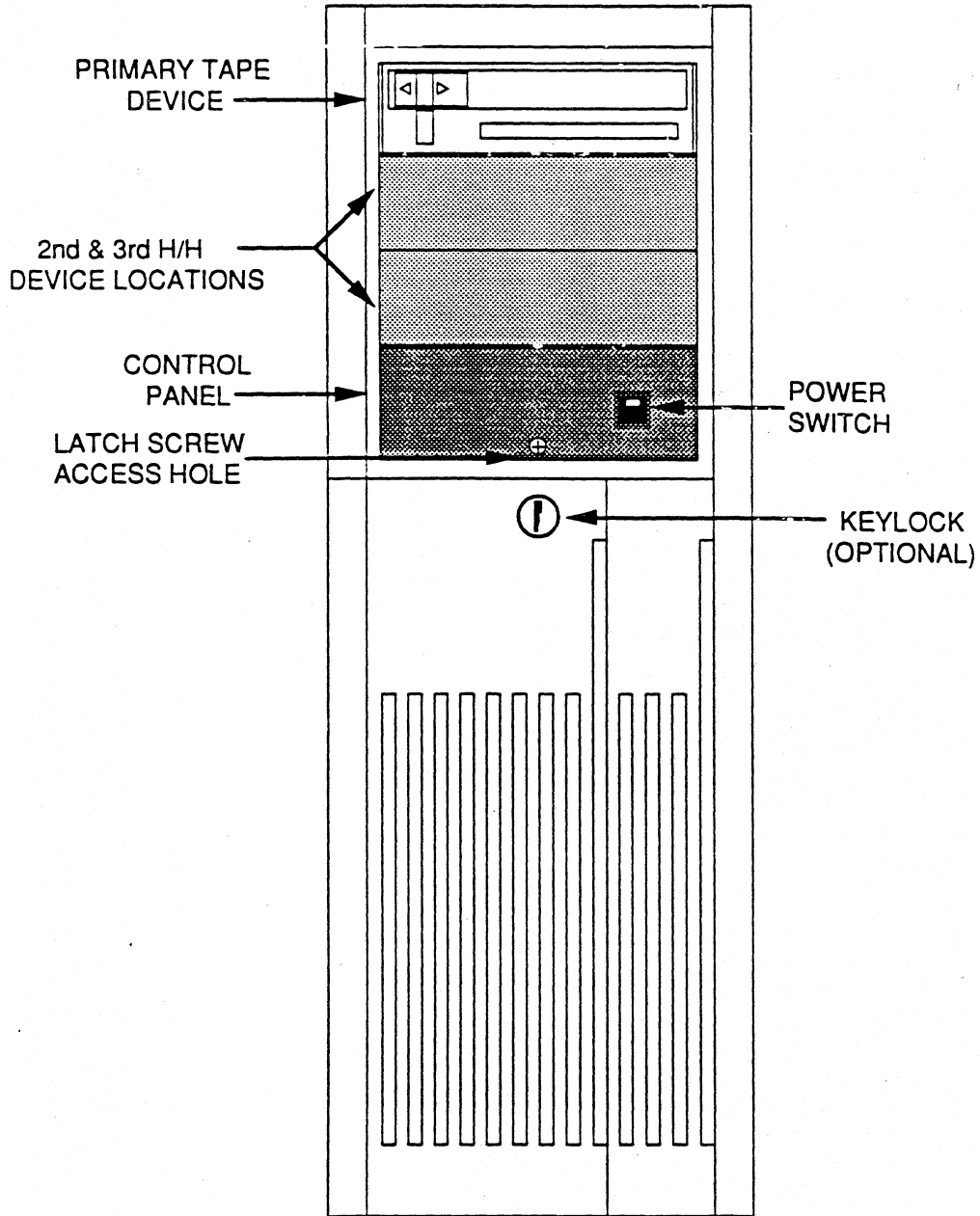
- A = 4,0
- B = 5,0
- C = 3,0
- D = 3,0
- E = 0,0
- F = 2,0
- G = 2,0
- H = 6,0



SYS3304, 3308, SCSI
DRIVE PLACEMENT

3/14/90

Figure 2-1. Model 3400/8400 Front View with Front Panel in Place.

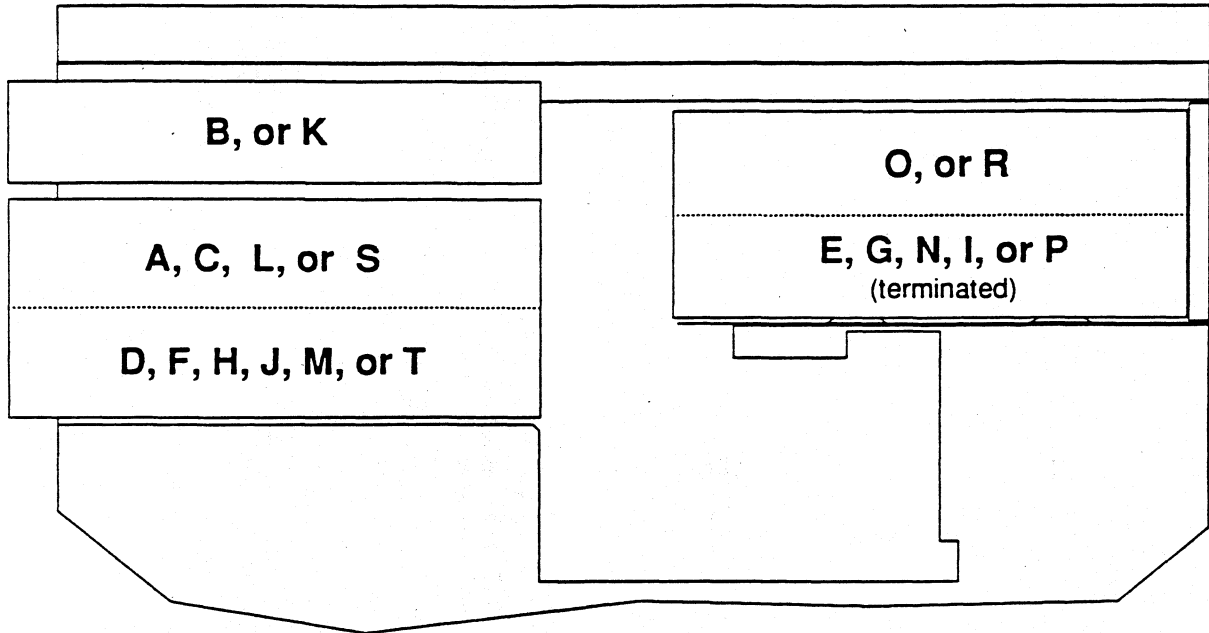


**SYS3400 FRONT VIEW
W/ FRONT PANEL IN PLACE**

04/03/91

SYS3400 SERIES DRIVE PLACEMENT

(SIDE VIEW)



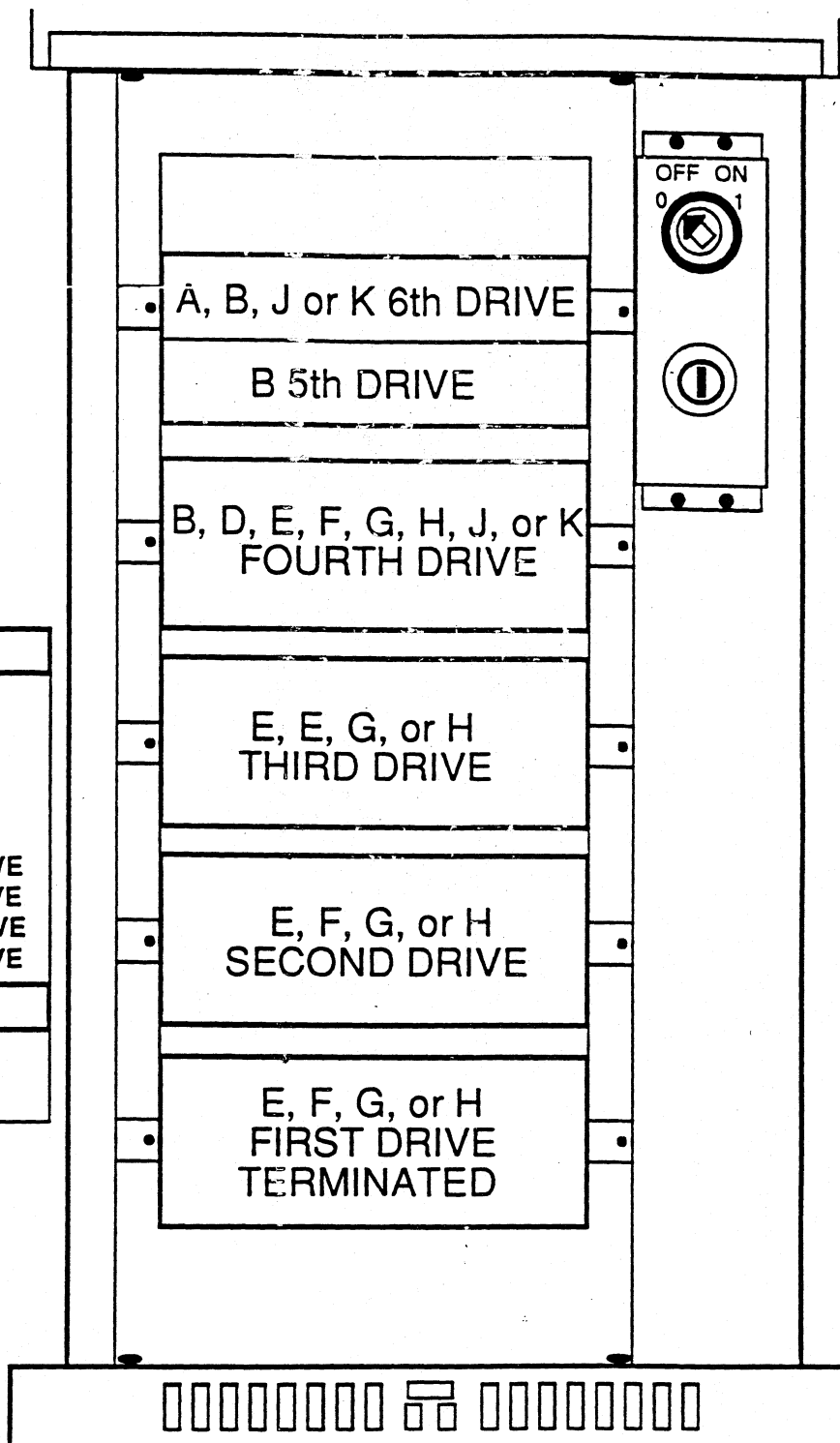
02/04/91

1st Choice	2nd Choice	3rd Choice	<u>5 1/4" DRIVES</u>	<u>ADDRESS</u>		<u>MAX # DRIVES</u>
				<u>1st</u>	<u>2nd</u>	
A			MVME852 - 60 Mbyte Streaming Tape Drive	5		1
B	C		MVME853 - 150 Mbyte Streaming Tape Drive	4	5	2
D			MVME856 - 2 Gbyte Cartridge Tape Drive	5		1
E	F		MVME875 - 300 Mbyte Winchester Disk Drive	0	1	2
G	H		MVME876 - 600 Mbyte Winchester Disk Drive	0	1	2
I	J		MVME877 - 1.2GByte Winchester Disk Drive			2
			<u>3 1/2" DRIVES</u>			
K	L	M	MVME855 - 155 Mbyte Streaming Tape Drive	4	5	
N	O		MVME863/A - 104/135 Mbyte Win. Disk Drive	0	1	
P	R		MVME864/A - 172/180 Mbyte Win. Disk Drive	0	1	
S	T		MVME884 - 2.9 Mbyte Diskette Drive (SCSI)	6		

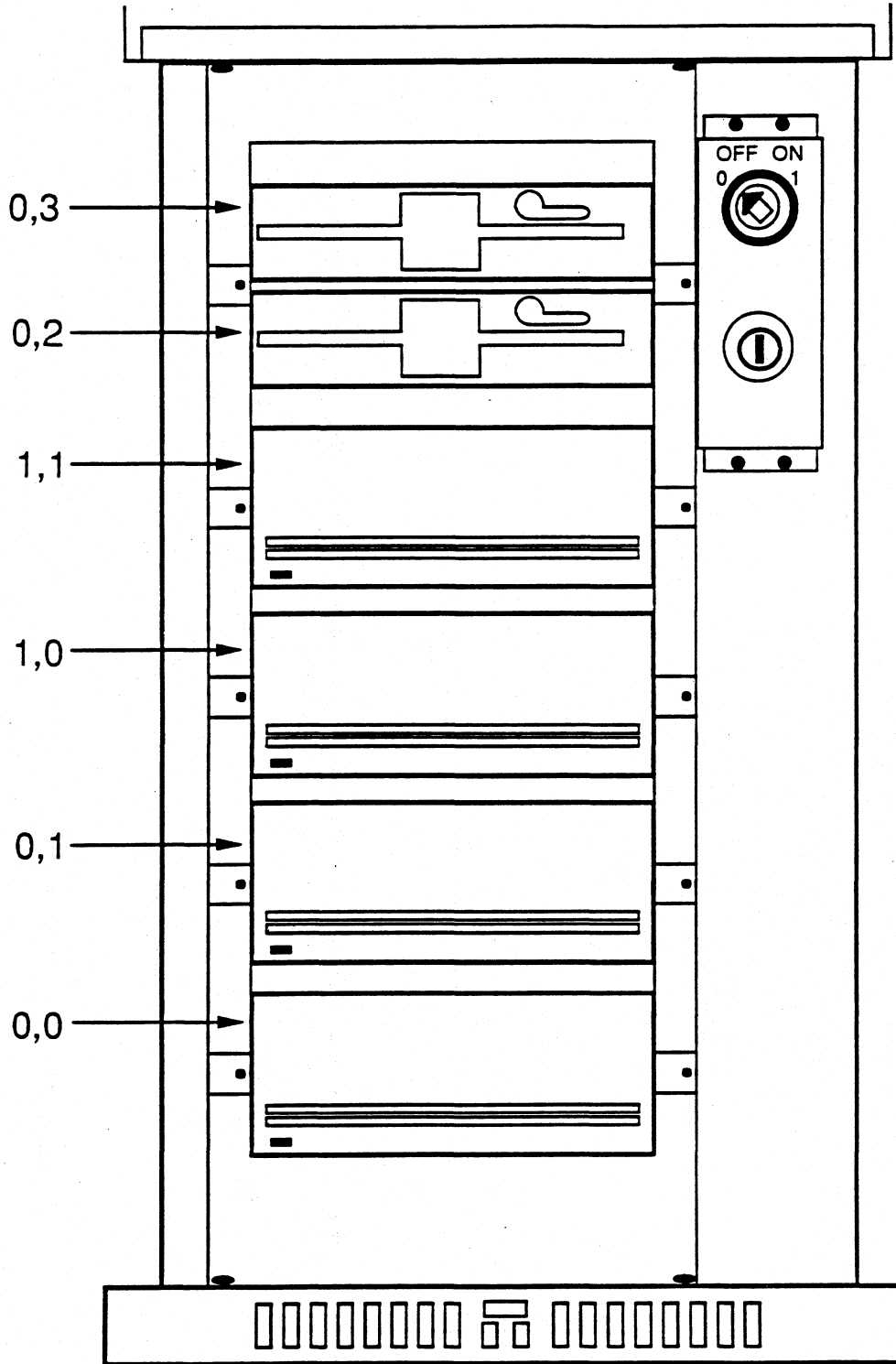
* 300 OR 600 MB DRIVES IN LOCATION D2/E2 MUST BE ADDRESSED AS ADDRESS 2 IF TWO 3.5 INCH DRIVES ARE IN THE PRIMARY LOCATION.

04/04/91

SCSI DRIVES
A VME852 60MB STREAMING TAPE
B VME853 150MB STREAMING TAPE
C VME855 155MB STREAMING TAPE
D VME856 2GB CARTRIDGE TAPE
E VME874 150MB WINCHESTER DRIVE
F VME875 300MB WINCHESTER DRIVE
G VME876 600MB WINCHESTER DRIVE
H VME877 1.2GB WINCHESTER DRIVE
FLOPPY DRIVES
I VME883 1.2MB FLOPPY SA-450
J VME884 2.9MB FLOPPY SCSI



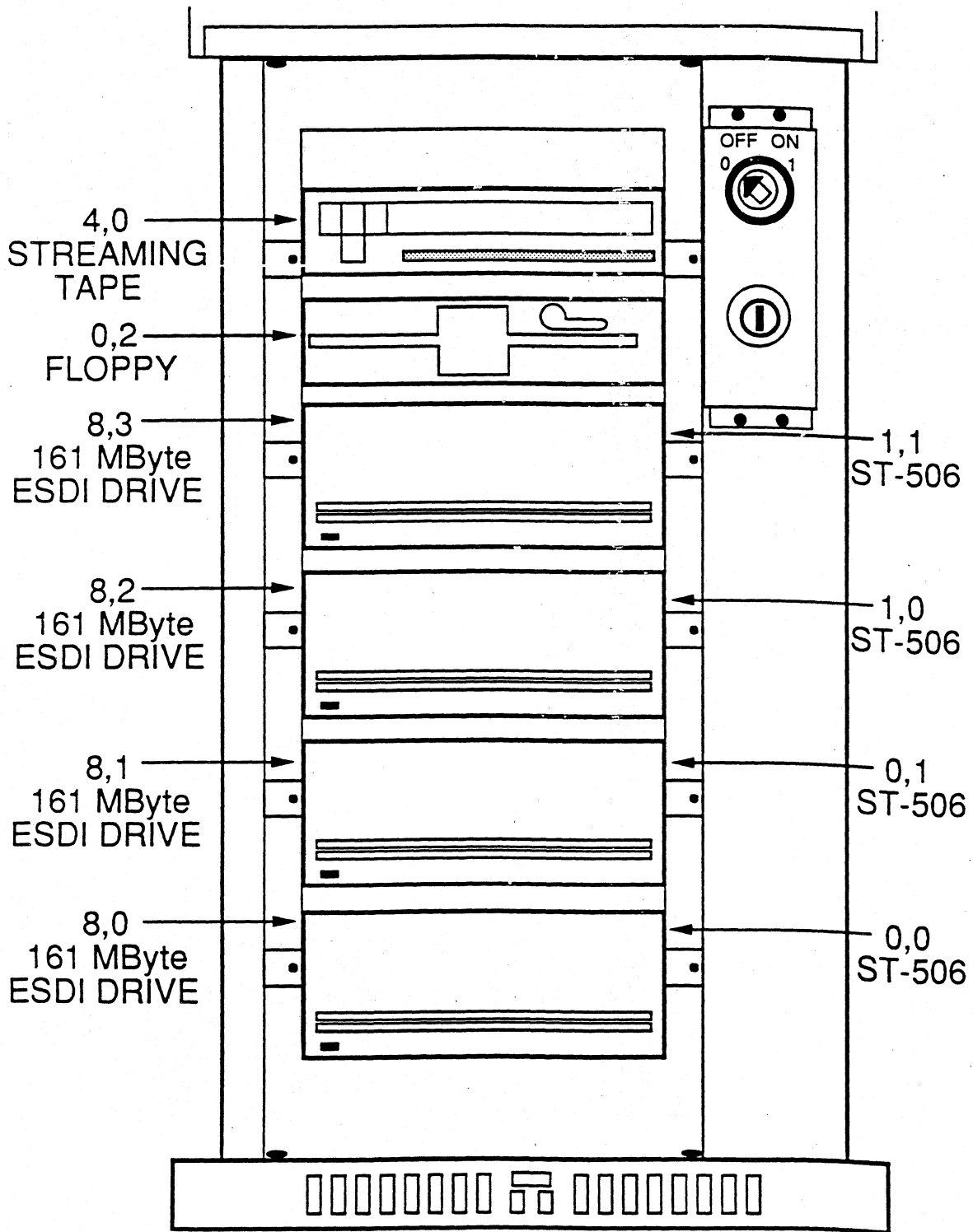
SYS3604, 3608, 3640 SCSI & SA-450 DRIVE PLACEMENT



03/09/90

**DRIVE PLACEMENT FOR VME320A/B
12-SLOT CHASSIS**

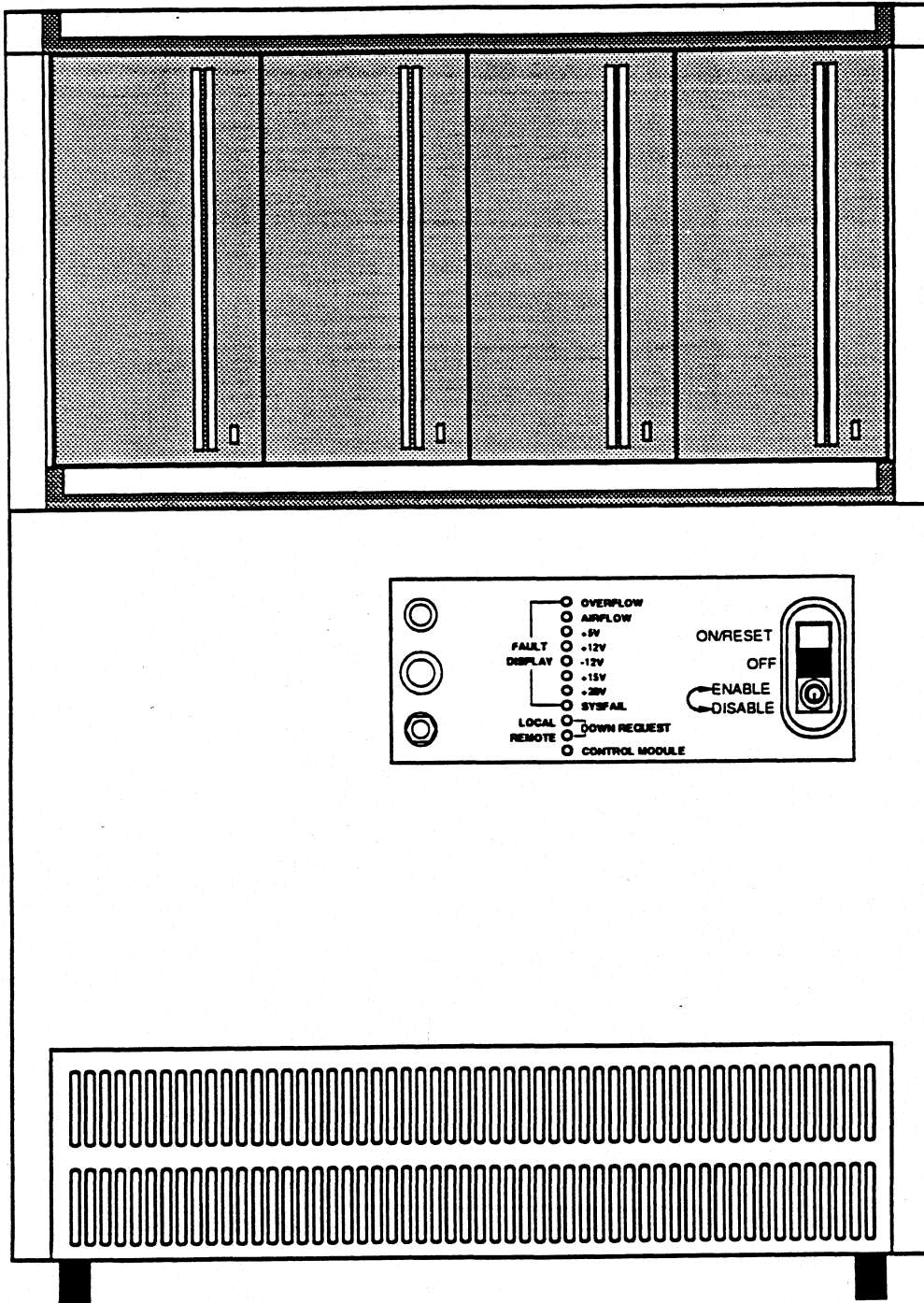
NOTE 1: THIS CONFIGURATION USED IN
SYS3604, 3608, & 3640's.



03/09/90

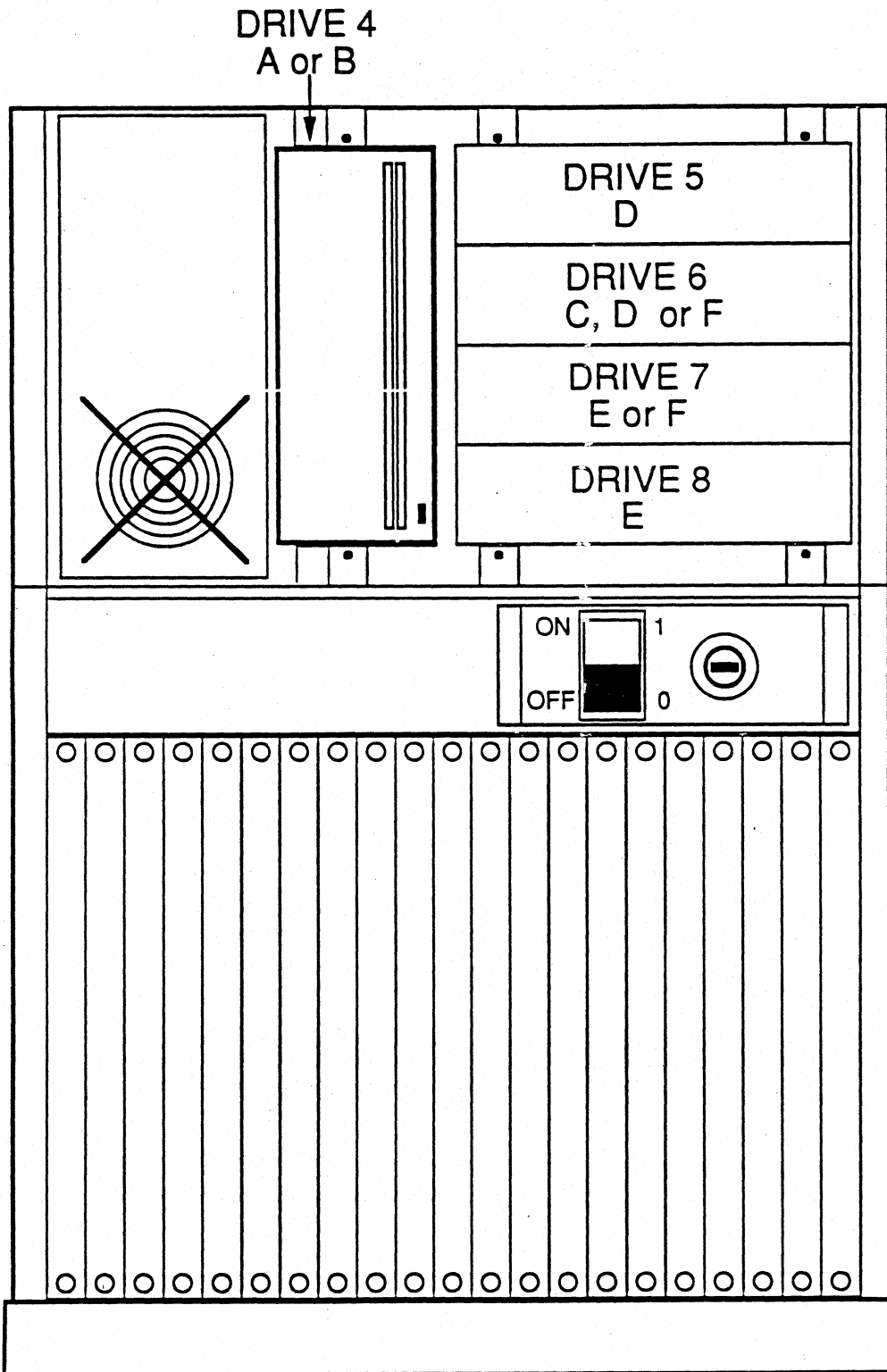
DRIVE PLACEMENT FOR VME 320(X),
VME323-2 & VME350 12-SLOT CHASSIS.

NOTE 1: THIS CONFIGURATION USED IN
SYS3604, 3608, & 3640's.



04/16/91

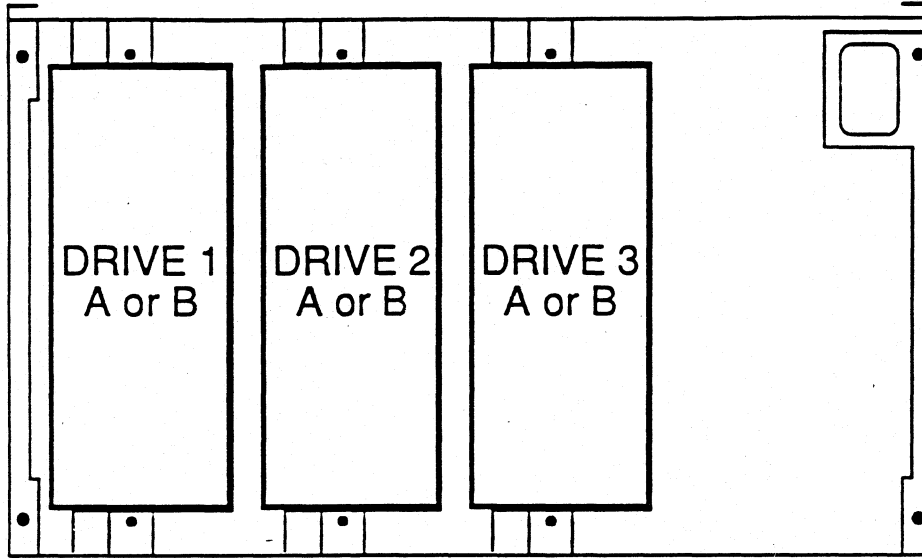
SYS3708 CHASSIS FRONT VIEW



03/09/90

SYS3840 CHASSIS FRONT VIEW

FOR SYS3840's



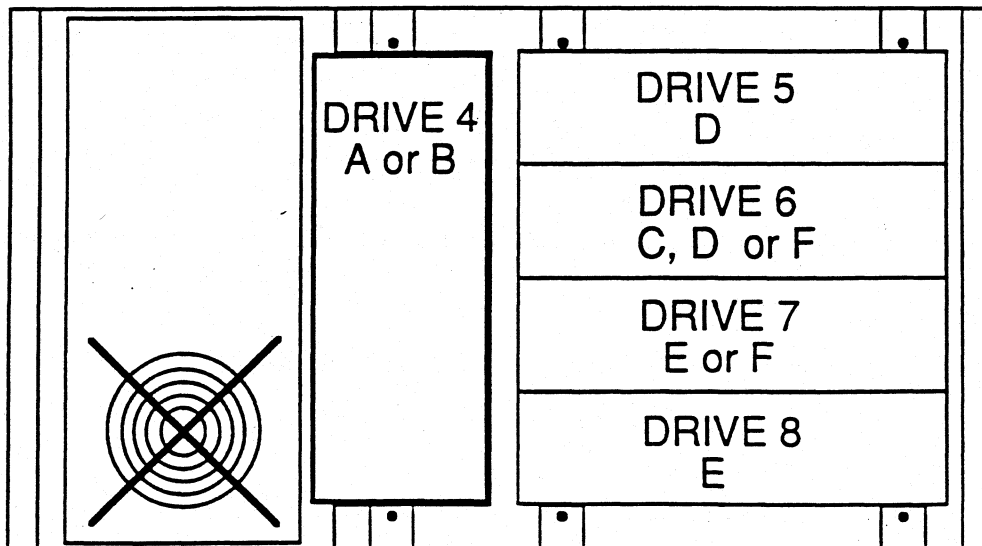
VME327A/VME883

A VME875 300MB WINCHESTER DRIVE
B VME876 600MB WINCHESTER DRIVE

C VME852 60MB STREAMING TAPE
D VME853 150MB STREAMING TAPE
E VME856 2GB CARTRIDGE TAPE

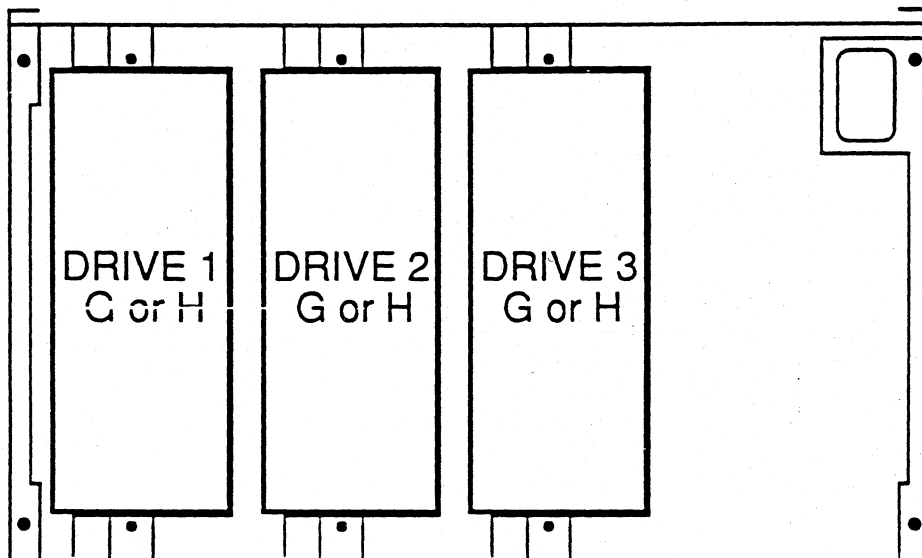
F VME883 1.2MB FLOPPY(TERMINATED)

03/09/90



SCSI/SA-405 DRIVE PLACEMENT

FOR SYS3840's

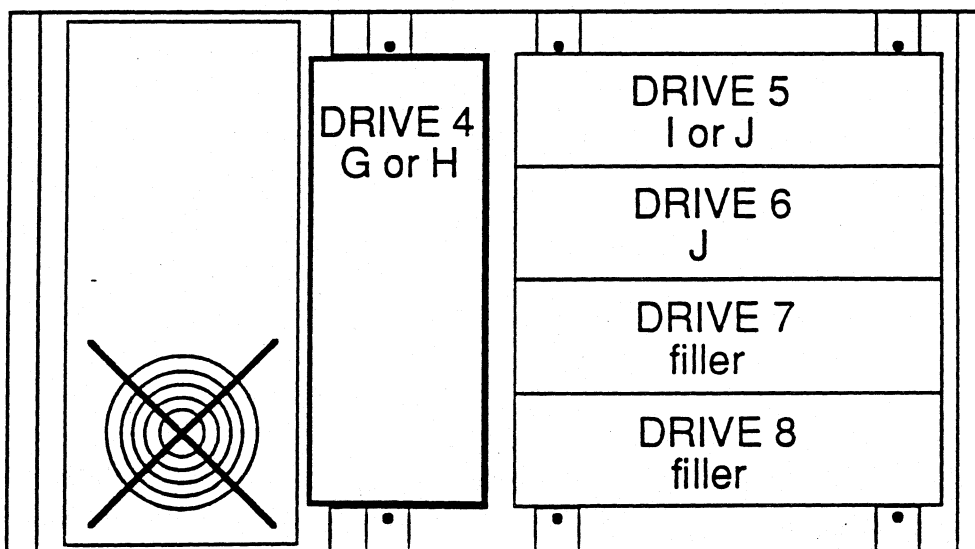


VME323/VME85(X)Q

G VME842 161MB WINCHESTER DRIVE
H VME843 390MB WINCHESTER DRIVE

I VME853Q 150MB QIC-02 TAPE
J VME852Q 60MB QIC-02 TAPE

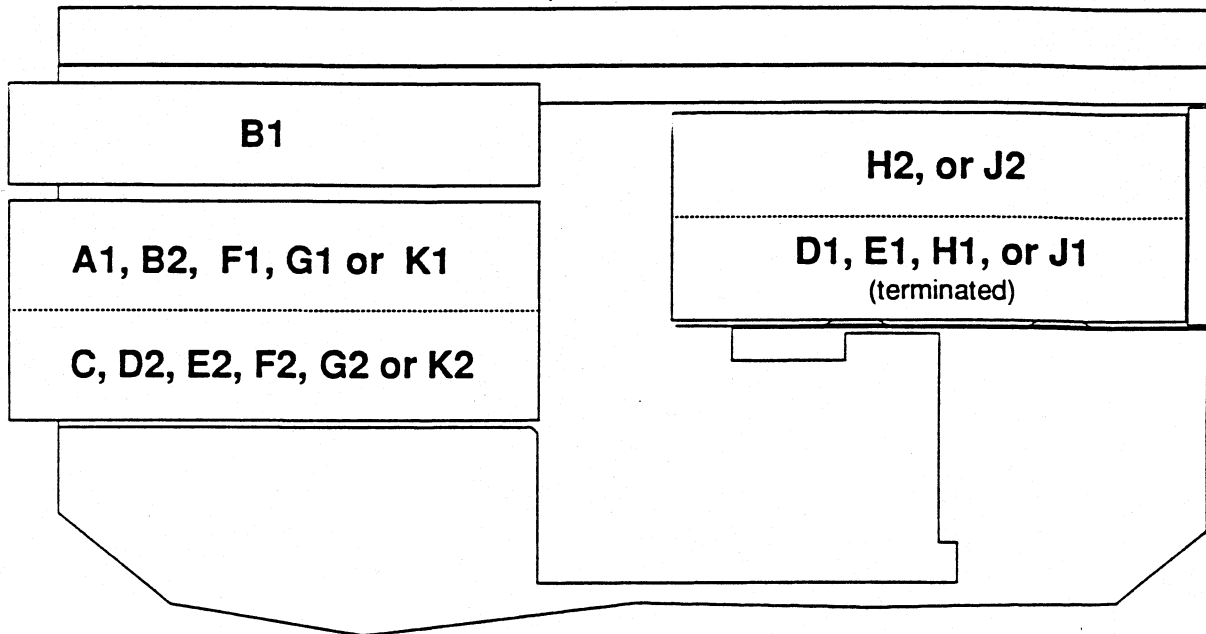
03/05/90



ESDI/QIC-02 DRIVE PLACEMENT

SYS8400 SERIES DRIVE PLACEMENT

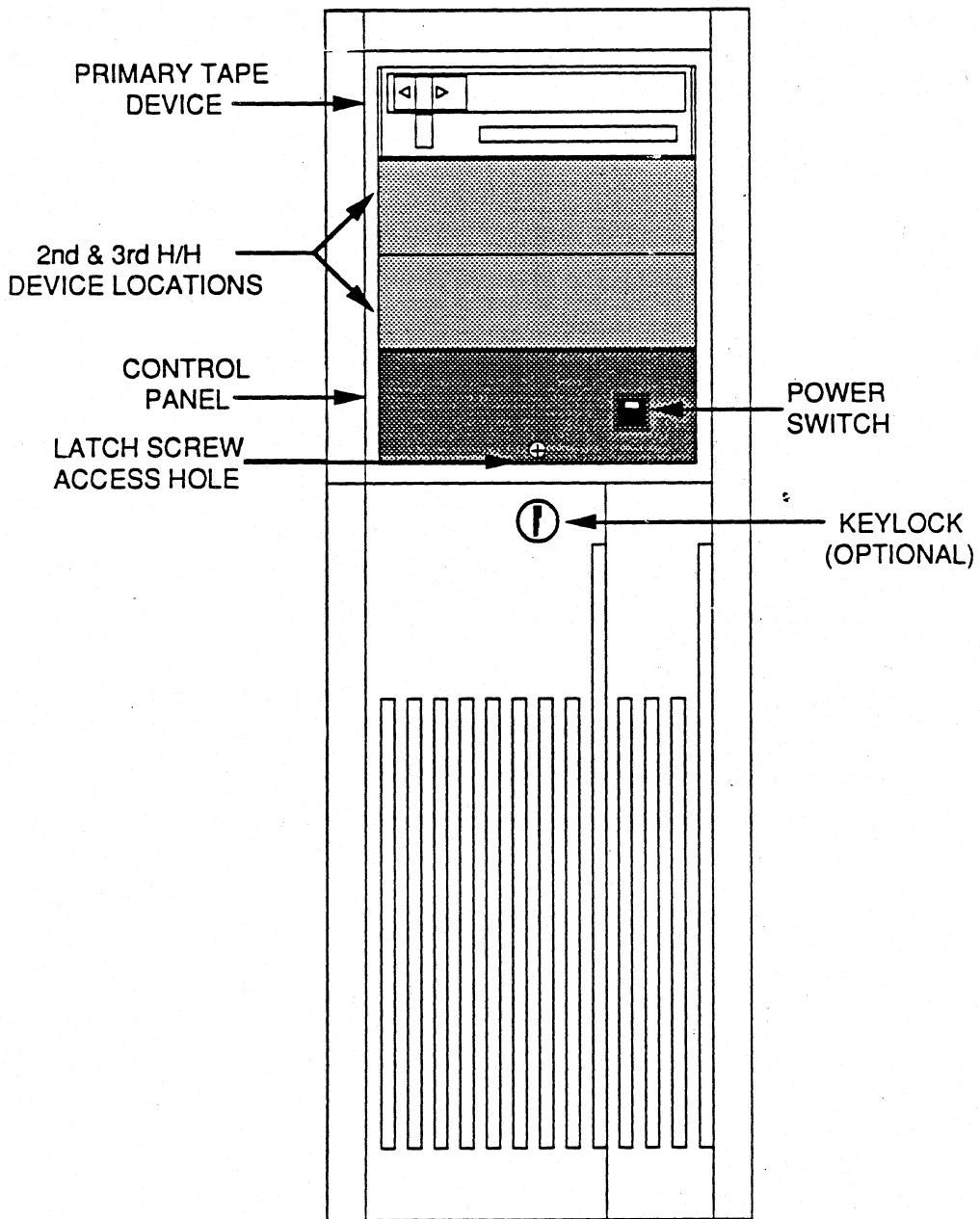
(SIDE VIEW)



1st Choice	2nd Choice	<u>5 1/4" DRIVES</u>	<u>ADDRESS</u>	
			<u>1st</u>	<u>2nd</u>
A1		MVME852 - 60 Mbyte Streaming Tape Drive	5	
B1	B2	MVME853 - 150 Mbyte Streaming Tape Drive	4	5
C		MVME856 - 2 Gbyte Cartridge Tape Drive	5	
D1	D2*	MVME875 - 300 Mbyte Winchester Disk Drive	0	1
E1	E2*	MVME876 - 600 Mbyte Winchester Disk Drive	0	1
F1	F2	MVME883 - 1.2 Mbyte Diskette Drive (SA-405)		

02/02/90

* 300 OR 600 MB DRIVES IN LOCATION D2/E2 MUST BE ADDRESSED AS ADDRESS 2 IF TWO 3.5 INCH DRIVES ARE IN THE PRIMARY LOCATION.

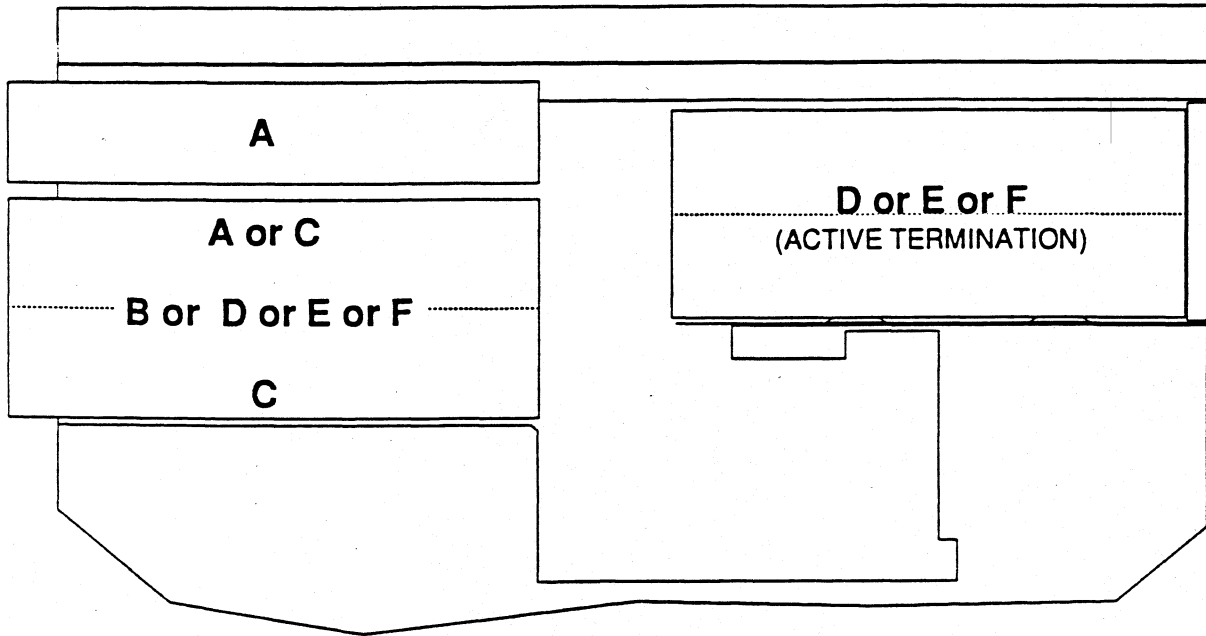


**SYS8400 FRONT VIEW
W/ FRONT PANEL IN PLACE**

04/05/91

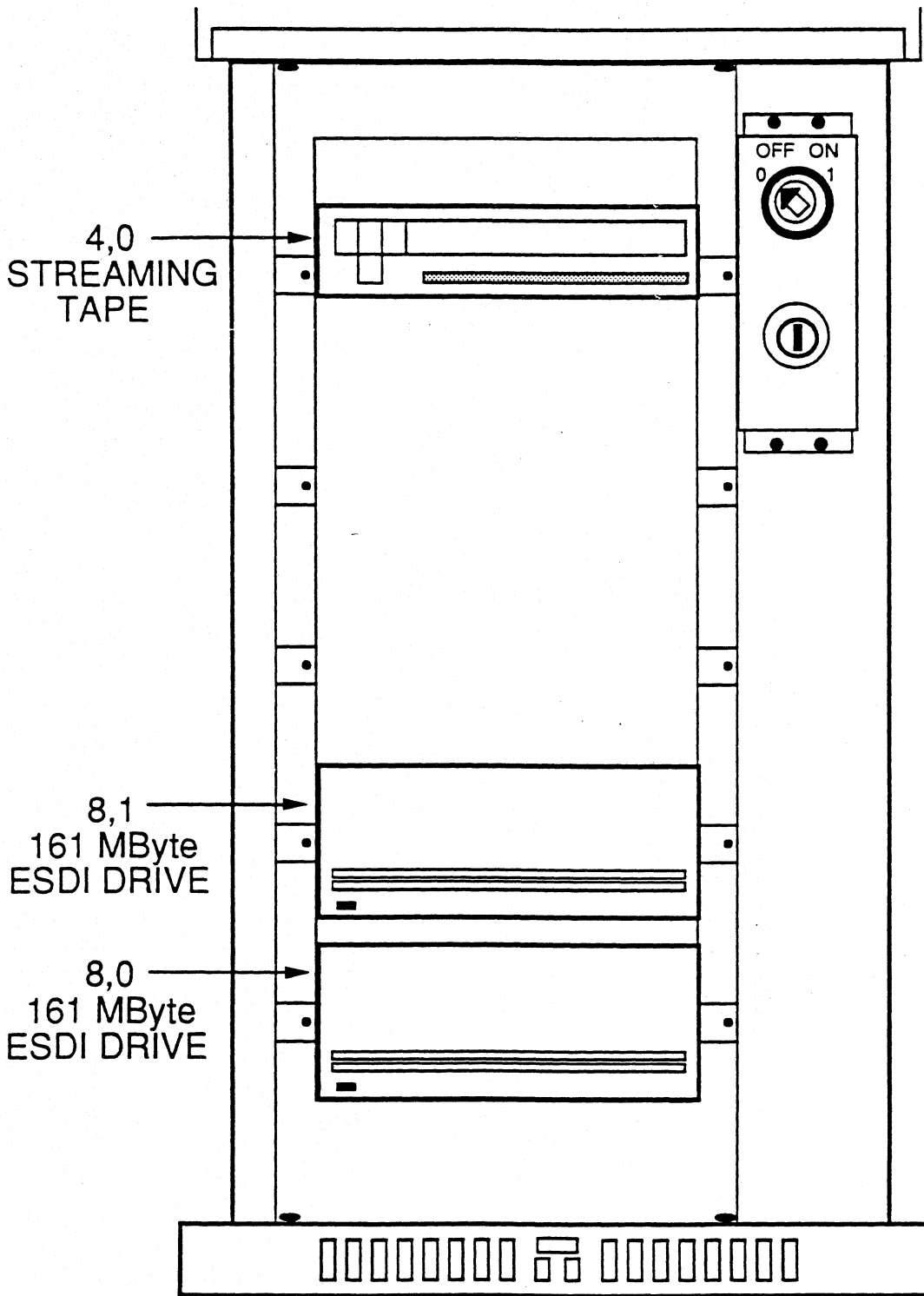
SYS8440 SERIES DRIVE PLACEMENT

(SIDE VIEW)



POSITION	
A	MVME853 - 150 Mbyte Streaming Tape Drive
B	MVME856 - 2 Gbyte Cartridge Tape Drive
C	MVME881A - 1.2MByte Diskette Drive
D	MVME875 - 300 Mbyte Winchester Disk Drive
E	MVME876 - 600 Mbyte Winchester Disk Drive
F	MVME877 - 1.0 Gbyte Winchester Disk Drive

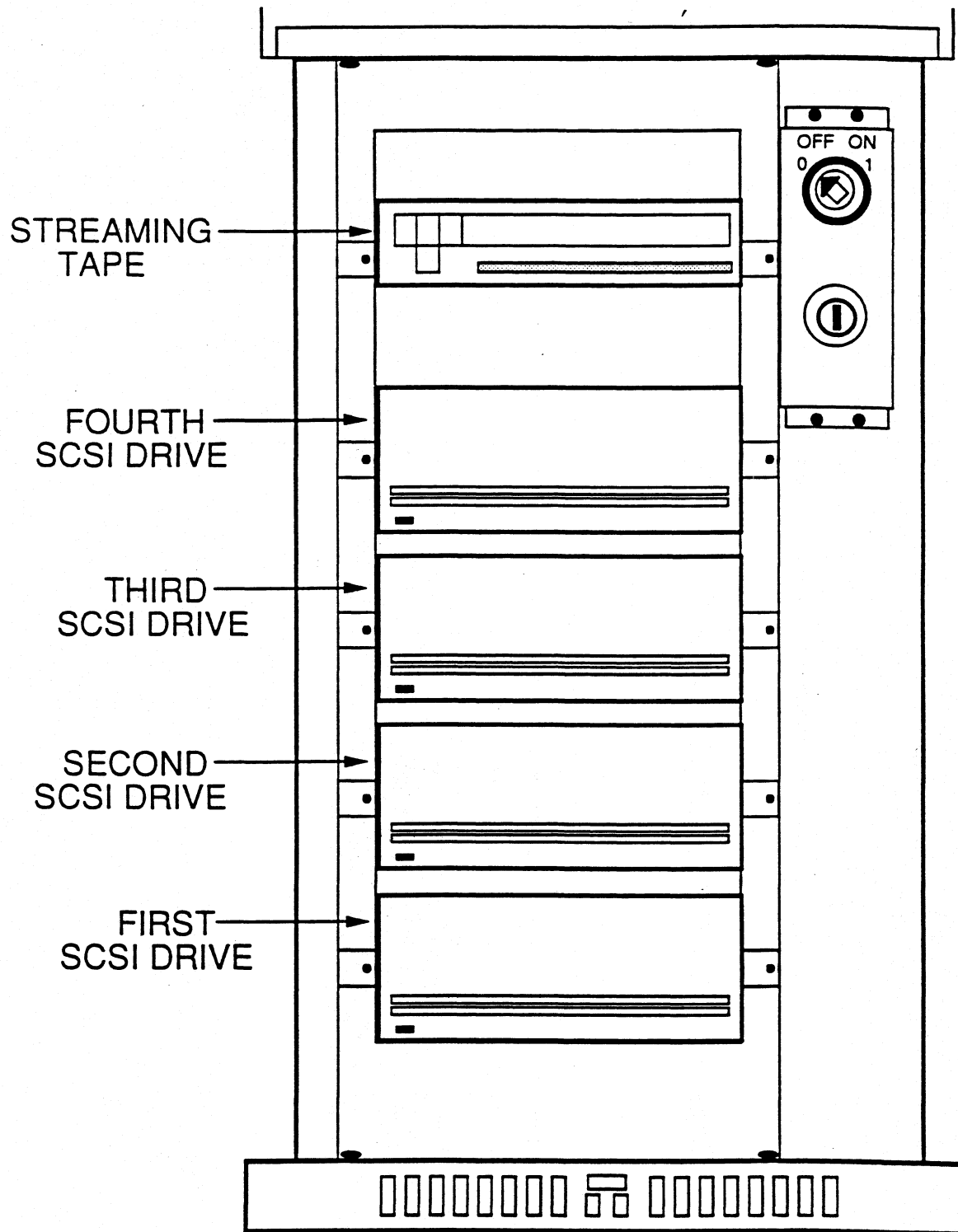
07/01/91



03/09/90

DRIVE PLACEMENT FOR VME323-2
& VME350 12-SLOT CHASSIS

NOTE 1: THIS CONFIGURATION USED IN
SYS8608 AND 8664's.



DRIVE PLACEMENT FOR VME327A
12-SLOT CHASSIS

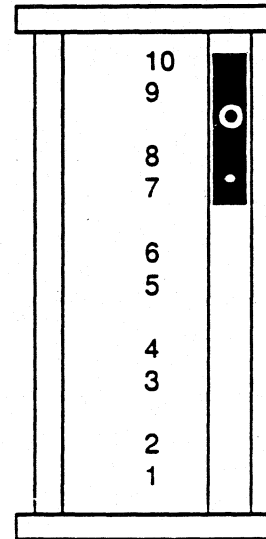
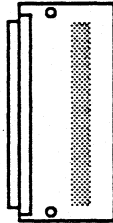
NOTE : THIS CONFIGURATION USED IN
SYS8608, AND 8864's.

06/09/90

SYS8640's DRIVE PLACEMENT FOR VME328-1

NOTE 1: LOCATE DRIVES AS SHOWN BELOW.

NOTE 2: CONNECT INDICATED DRIVES TO P4 OF 328P2.



PRIORITY	LOCATION DRIVE (ADDRESS LABEL)	1	2	3	4	5	6	7	8	9	10
		1	1st 150MB Tape - 853(4)								
2	2nd Tape --										
	150MB - 853 (5) or										1st
	2.3Gb - 856 (5)							1st			
3	1.2MB Floppy - 881A (6)								2nd		1st
4	1st Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)	1st									
5	2nd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)			1st							
6	3rd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)					1st					
7	4th Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)							1st			

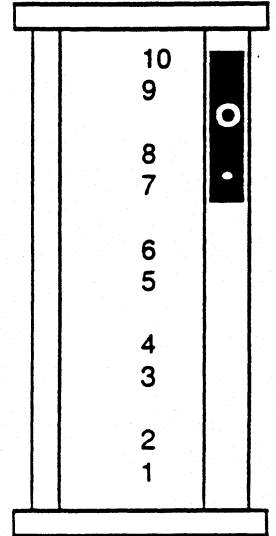
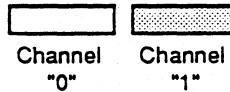
07/01/91

SYS8640's DRIVE PLACEMENT FOR VME328-2

NOTE 1: LOCATE DRIVES AS SHOWN BELOW.

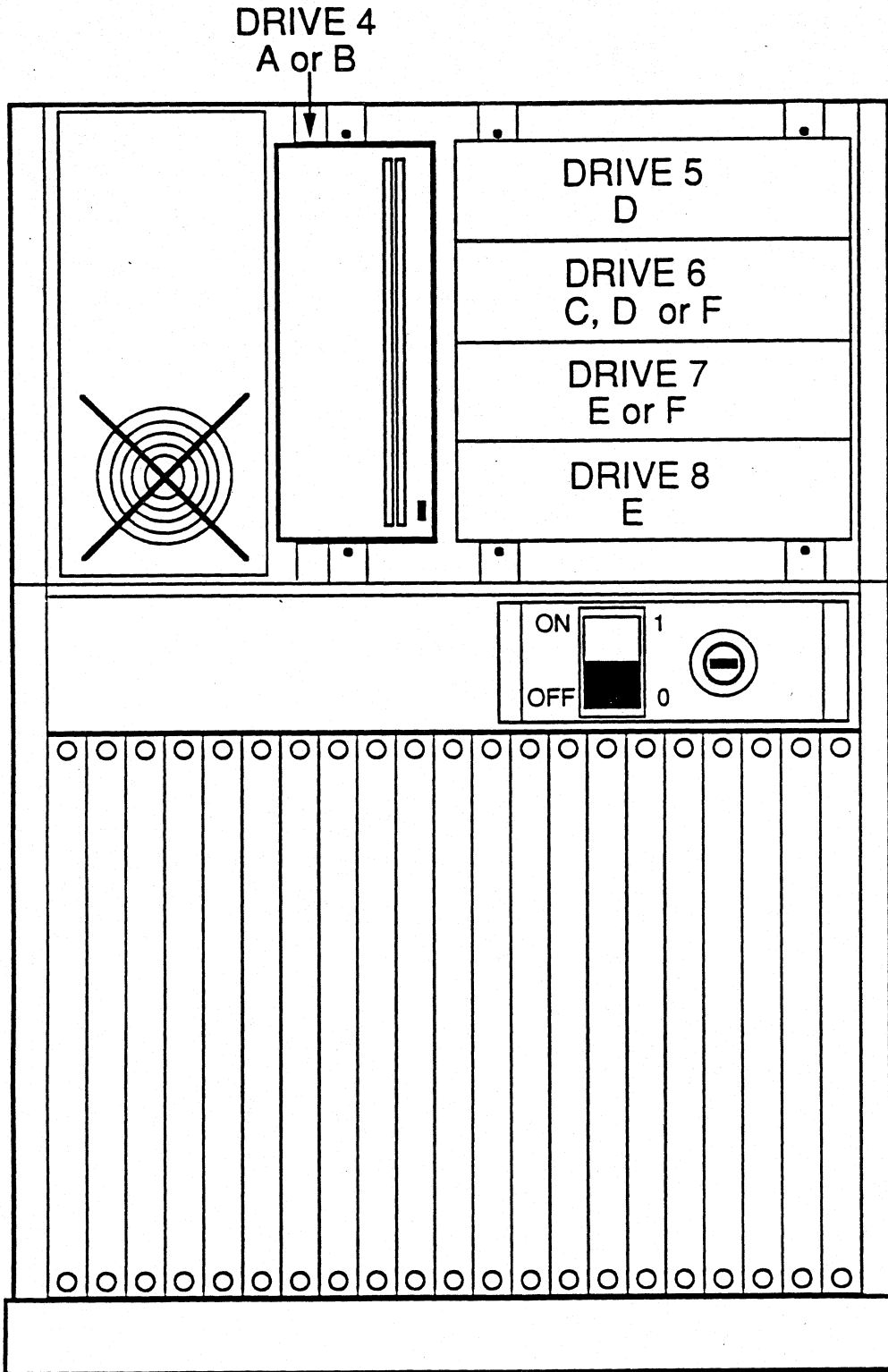
NOTE 2: CONNECT INDICATED DRIVES TO CHANNEL "0" (CABLE AT P4 OF 328P2.)

NOTE 3: CONNECT INDICATED DRIVES TO CHANNEL "1" (CABLE AT P3 OF 328P2.)



PRIORITY	LOCATION										
	DRIVE (ADDRESS LABEL)	1	2	3	4	5	6	7	8	9	10
1	1st 150MB Tape - 853(4)									1st	
2	2nd Tape --										
	150MB - 853 (5) or 2.3Gb - 856 (5)										1st
3	1.2MB Floppy - 881A (6)								2nd		1st
	3rd Tape --										
4	150MB - 853 (4) or							2nd	1st		
	2.3Gb - 856 (5)							1st			
5	4th Tape - 150Mb - 853 (5)							1st			
6	2nd 1.2MB Floppy - 881A (6)							2nd	1st		
7	1st Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)	1st									
8	2nd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)			1st							
9	3rd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)					1st					
10	4th Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)							1st			

07/01/91



06/06/91

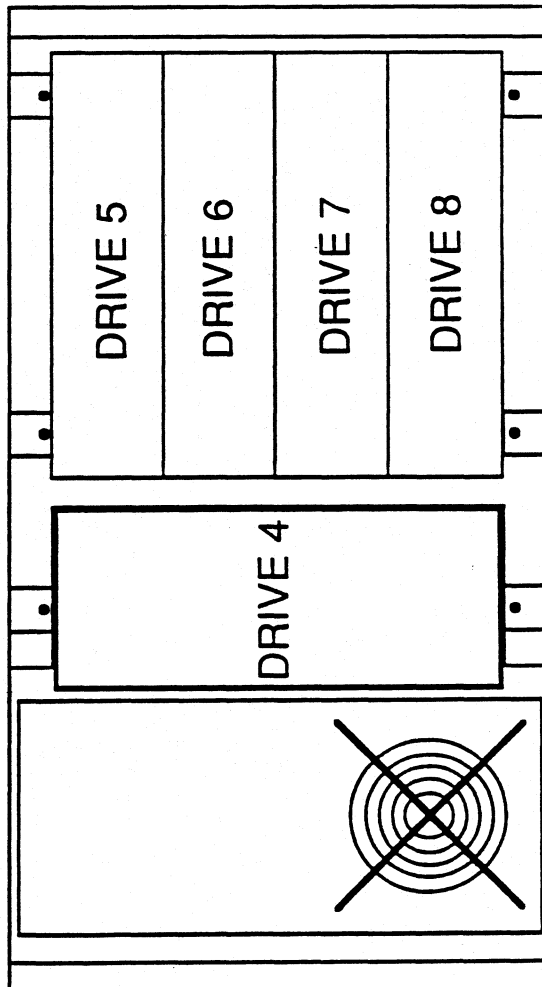
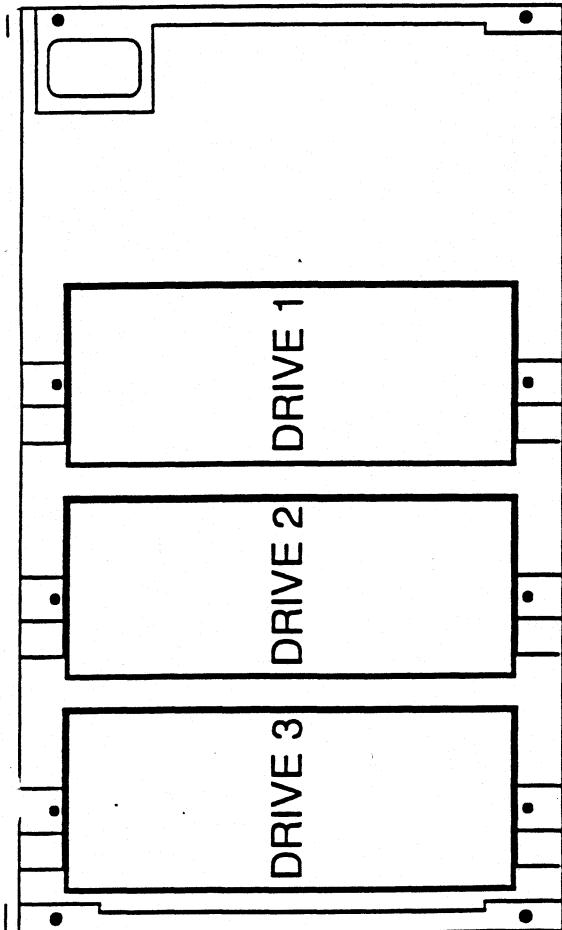
**SYS8840 CHASSIS
FRONT VIEW**

FOR SYS8840's w/ VME328-1

NOTE 1: LOCATE DRIVES AS SHOWN BELOW.

NOTE 2: CONNECT INDICATED DRIVES TO P4 OF 328P2.

PRIORITY	LOCATION DRIVE (ADDRESS LABEL)	1	2	3	4	5	6	7	8
		1	1.2MB Floppy - 881A (6)					1st	
2	1st 150MB Tape - 853(4)					1st	2nd		
3	2nd Tape --								
	150MB - 853(5) or						1st	2nd	
	2.3Gb - 856 (5)								1st
4	1st Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)	1st							
5	2nd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)		1st						
6	3rd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)			1st					
7	4th Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)				1st				



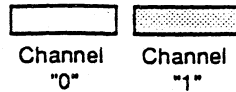
06/06/91

FOR SYS8840's w/ VME328-2

NOTE 1: LOCATE DRIVES AS SHOWN BELOW.

NOTE 2: CONNECT INDICATED DRIVES TO CHANNEL "0" (CABLE AT P4 OF 328P2.)

NOTE 3: CONNECT INDICATED DRIVES TO CHANNEL "1" (CABLE AT P3 OF 328P2.)

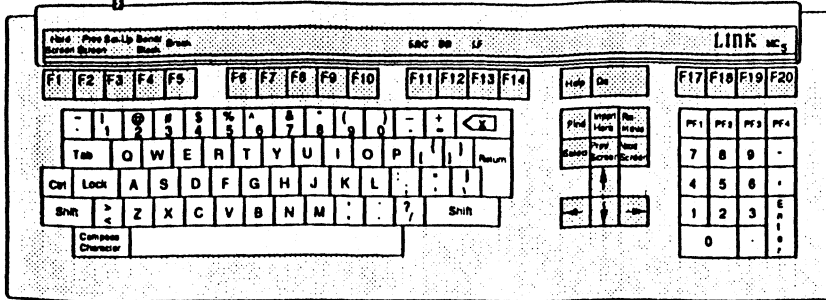
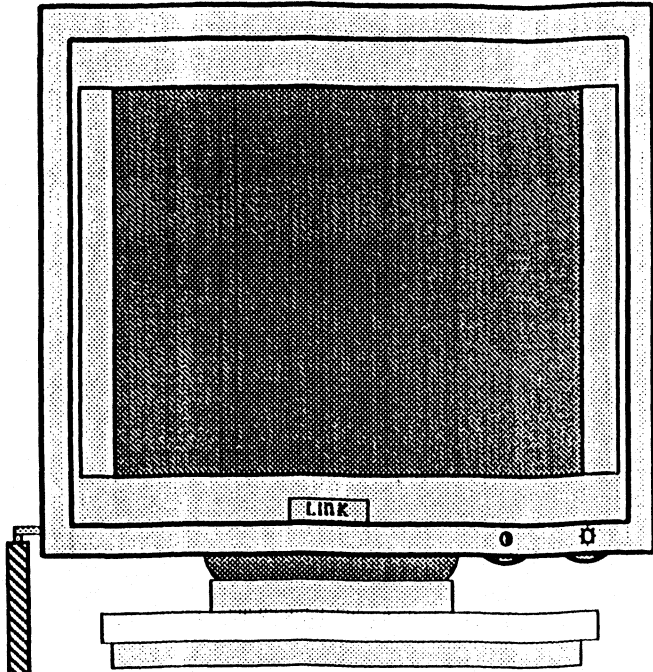


PRIORITY	LOCATION DRIVE (ADDRESS LABEL)	LOCATION							
		1	2	3	4	5	6	7	8
1	1.2MB Floppy - 881A (6)					1st			
2	1st 150MB Tape - 853(4)					1st	2nd		
3	2nd Tape --								
	150MB - 853(5) or						1st	2nd	
	2.3Gb - 856 (5)								1st
4	3rd Tape --								
	150MB - 853(4) or							1st	2nd
	2.3Gb - 856 (5)								1st
5	1st Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)	1st							
6	2nd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)		1st						
7	3rd Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)			1st					
	4th Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)				1st				
	5th Hard Drive -- 1GB - 887 (0) or 600MB - 876 (0) or 300MB - 875 (0)								1st

06/06/91

APPENDIX K

09/17/90



SETUP SCREEN 1 (Press F1)

Emulation	VT-220-8	Auto Page	OFF
Enhancements	ON	Warning Bell	OFF
Virtual Term	OFF	Margin Bell	OFF
Scroll Style	Jump	Bell Sound	1
Auto Wrap	ON	Block Terminator	US/CR
Received CR	CR	Monitor Mode	OFF

SETUP SCREEN 2 (Press F2)

Main Rcv Baud	9600	Aux 1 Baud	9600
Main Xmt Baud	9600	Aux 1 Data/Parity	8/None
Main Data/Parity	8/None	Aux 1 Stop Bits	1
Main Stop Bit	1	Aux 2 Rcv Hndsk	XON/XOFF
Main Rcv Hndsk	XON/XOFF	Aux 1 Xmt Hndsk	None
Main Xmt Hndsk	None	Disconnect	2 Sec
Comm Mode	Full Duplex	Printer	Parallel

SETUP SCREEN 3 (Press F3)

Column	80	Background	Dark
80/132 Clear	OFF	Attributes	Char
Lines	24	Wprt Intensity	Dim
Pages	1 x Lines	Wprt Reverse	OFF
Status Line	Ext	Wprt Underline	OFF
Cursor Style	Blink Block	Refresh Rate	60 Hz
Cursor	ON	Pound Char	US
Screen Saver	15 Min	Auto Font Load	ON

SETUP SCREEN 4 (Press F4)

Key Click	OFF	Compose Key	Funct
Key Repeat	ON	Break	250 ms
Key Lock	CAPS	Xmt Limit	None
Return Key	CR	FKey Xmt Limit	None
Enter Key	CR	Key Code	ASCII
Erase Key	DEL/BS	Language	US

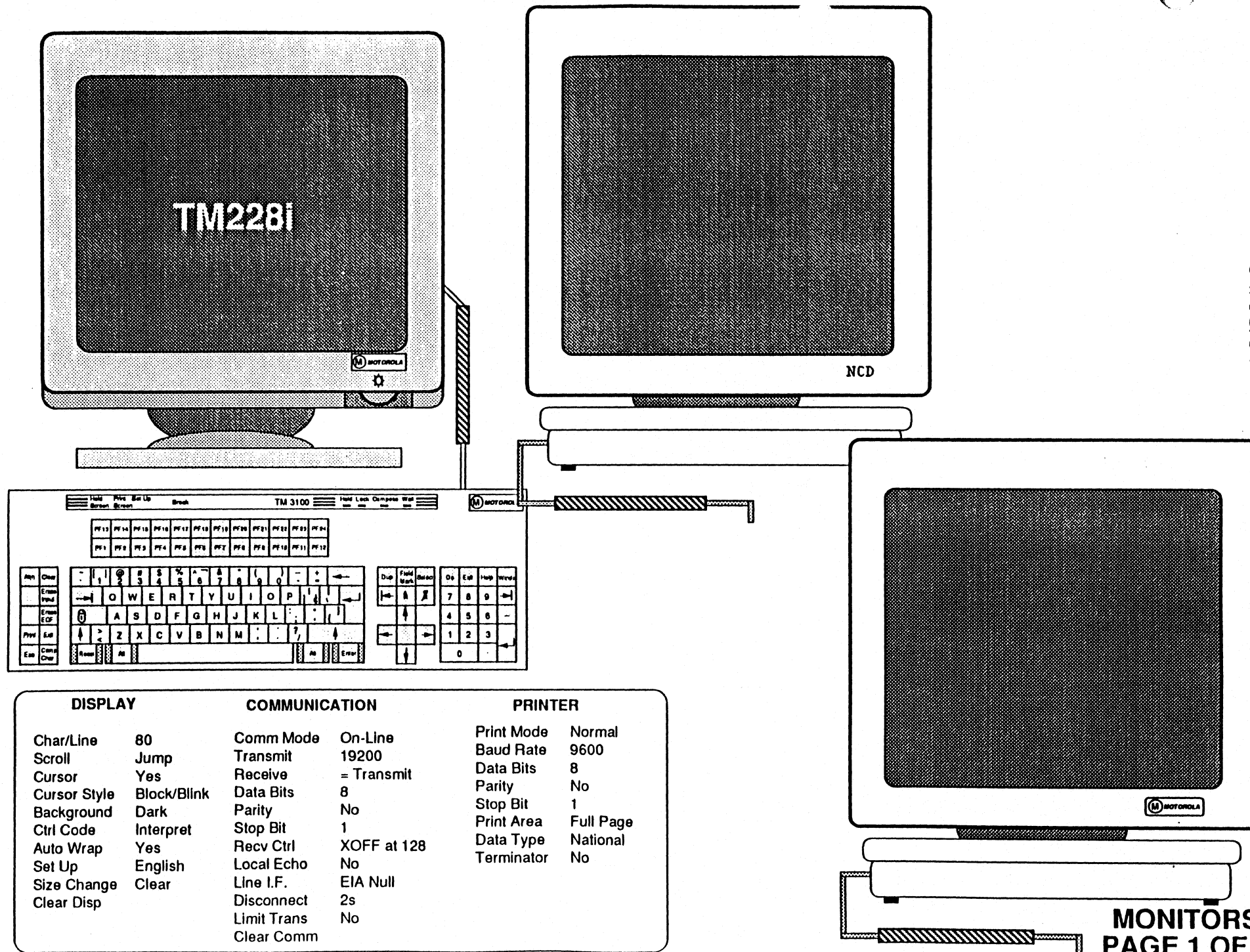
SETUP SCREEN 5 (Press F5)

FKey Lock	OFF	Print	National
Feature Lock	OFF	Port	EIA Data
Keypad	Numeric	Send Area	Screen
Xfer Term	EOS	Print Area	Screen
Cursor Keys	Normal	Send Term	None
Char Mode	Multi	Print Term	None
Keys	Typewriter	Print Mode	Normal
Send	All	VT 100 ID	VT 220
		Auto Answerback	OFF

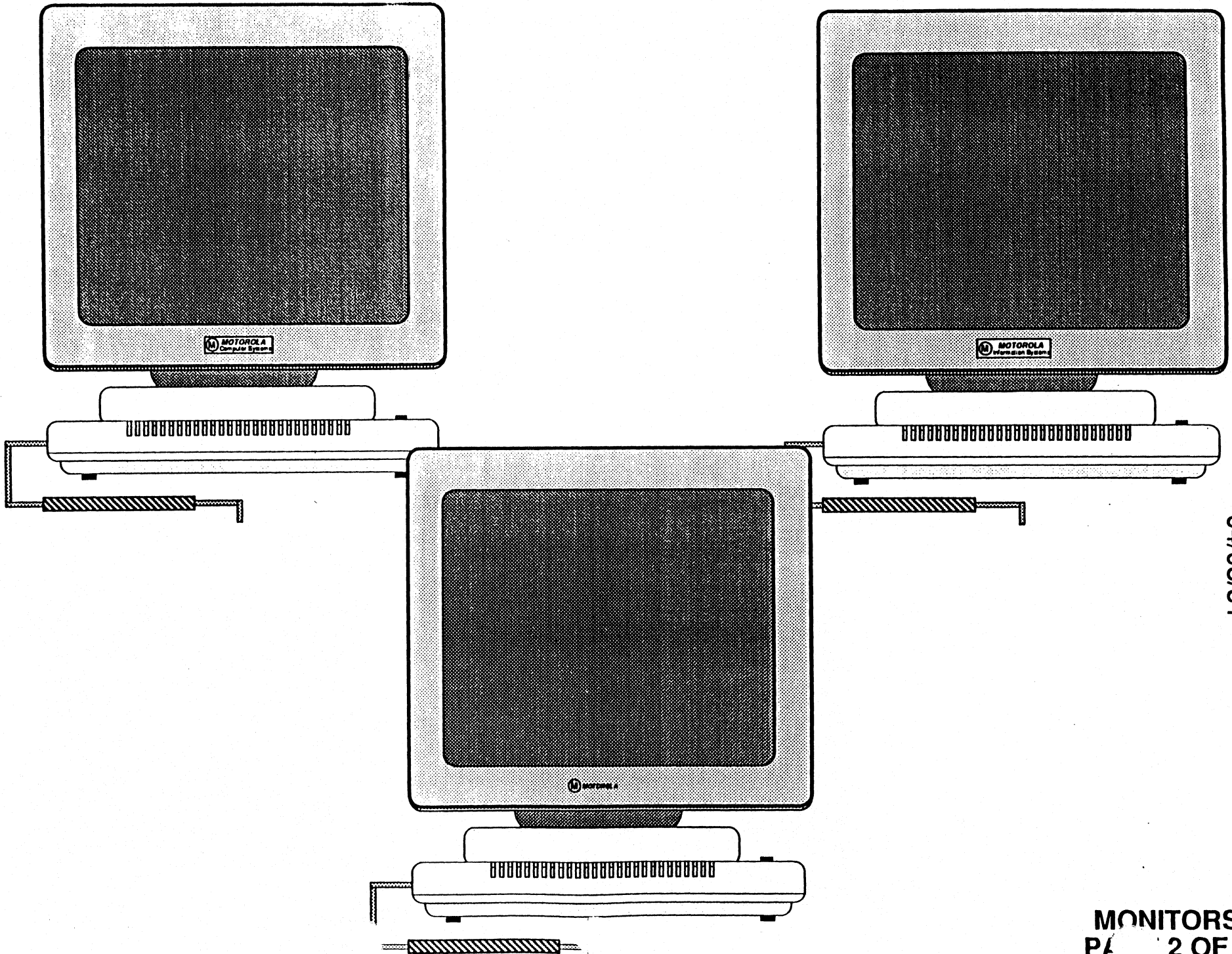
NOTE 1: THIS IS THE STANDARD SETUP FOR THIS TERMINAL.

**LINK
TERMINAL
3E 1**

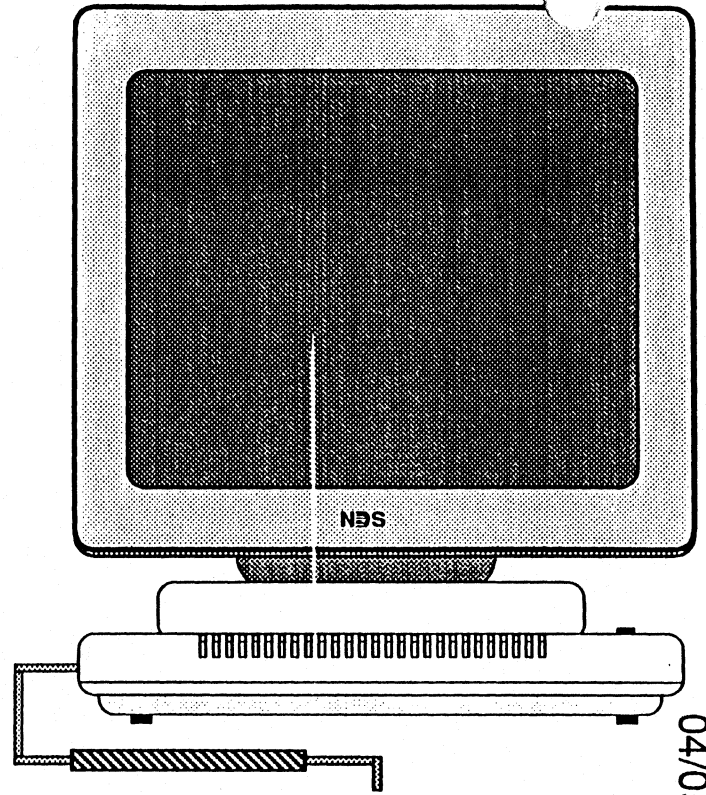
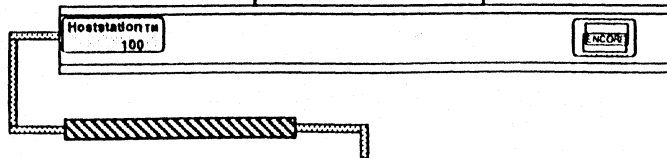
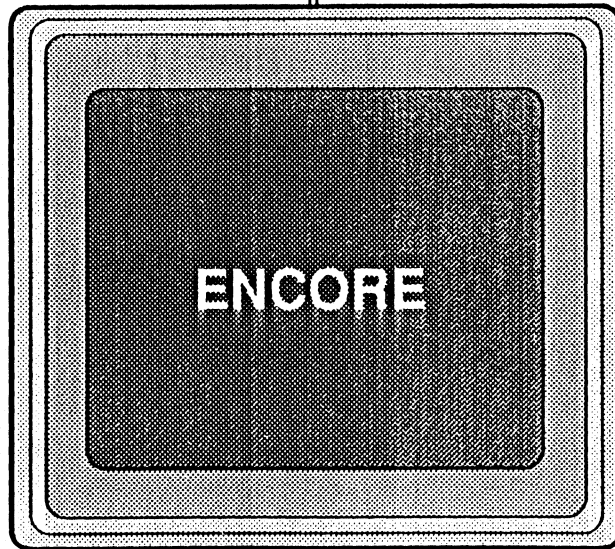
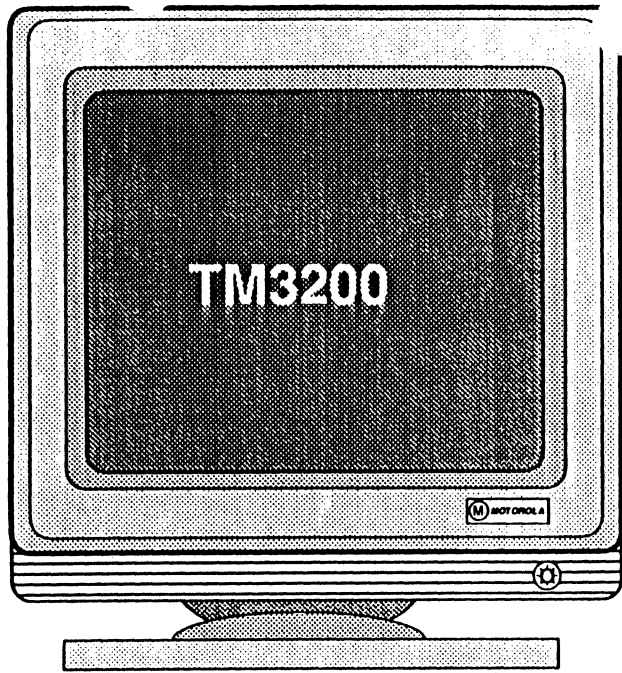
04/05/91



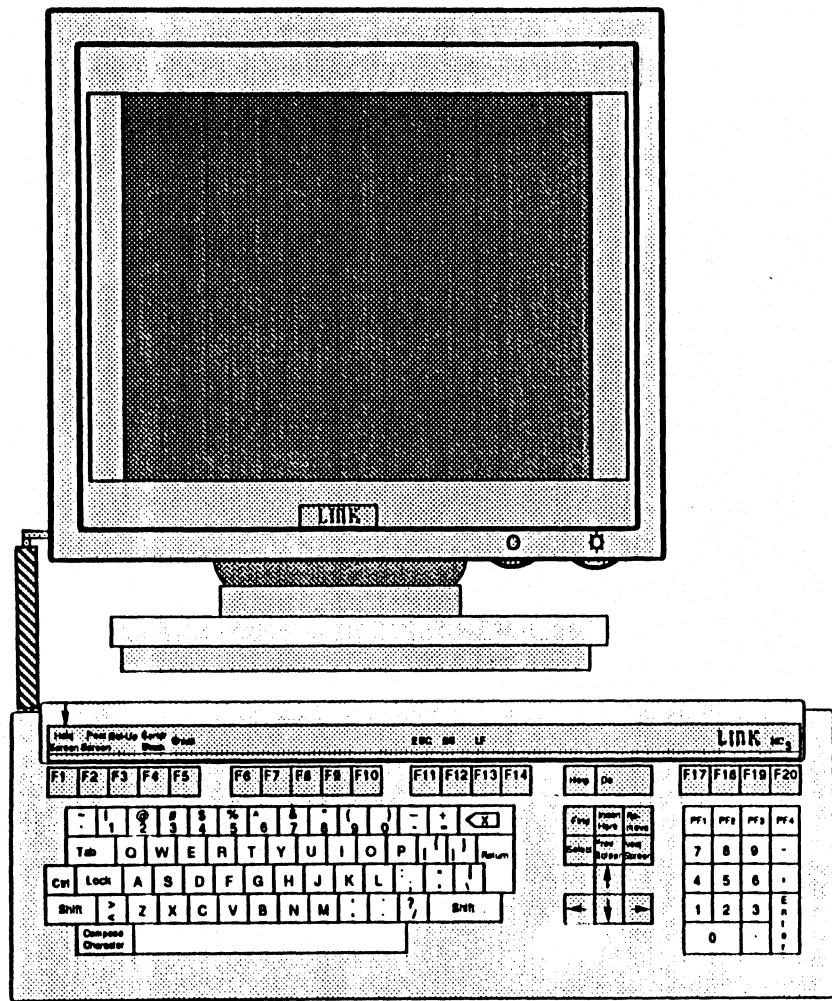
DISPLAY		COMMUNICATION		PRINTER	
Char/Line	80	Comm Mode	On-Line	Print Mode	Normal
Scroll	Jump	Transmit	19200	Baud Rate	9600
Cursor	Yes	Receive	= Transmit	Data Bits	8
Cursor Style	Block/Blink	Data Bits	8	Parity	No
Background	Dark	Parity	No	Stop Bit	1
Ctrl Code	Interpret	Stop Bit	1	Print Area	Full Page
Auto Wrap	Yes	Recv Ctrl	XOFF at 128	Data Type	National
Set Up	English	Local Echo	No	Terminator	No
Size Change	Clear	Line I.F.	EIA Null		
Clear Disp		Disconnect	2s		
		Limit Trans	No		
		Clear Comm			



04/05/91

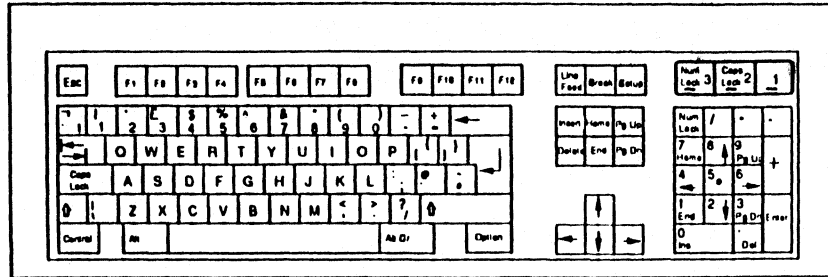


04/05/91

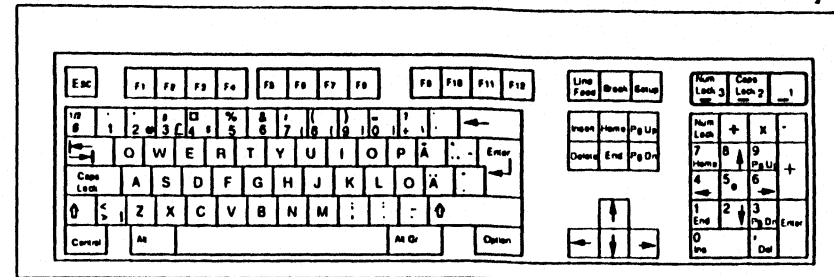


04/05/91

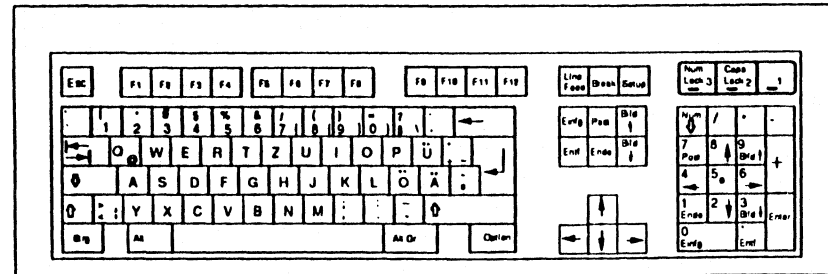
UK ENGLISH KEYBOARD Assy. # 01-W2587C01 FSD # 96800313
 [Cherry Corp. P/N G80-3000f 3j]



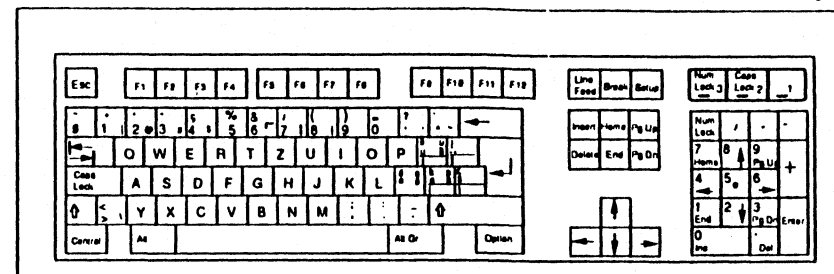
SWEDISH KEYBOARD Assy. # 01-W2587C05 FSD # 6643187
 [Cherry Corp. P/N GLV-1894AC, Key Source KSGLV-1894AC]



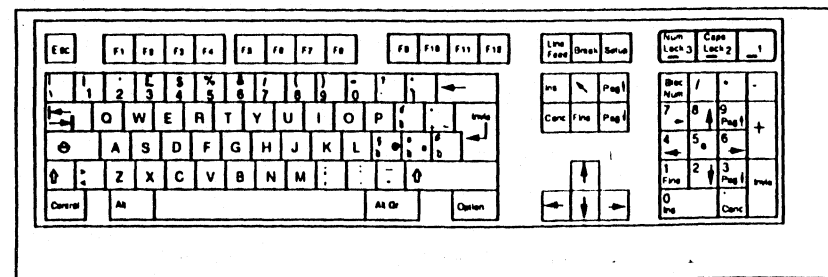
GERMAN KEYBOARD Assy. # 01-W2587C02 FSD # 66431868
 [Cherry Corp. P/N GLV-1807AC, Key Source KSGLV-1807AC]



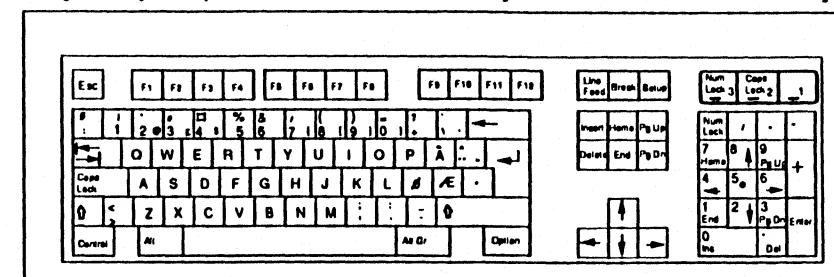
SWISS KEYBOARD Assy. # 01-W2587C06 FSD # 66431872
 [Cherry Corp. P/N GLV-1859AC, Key Source KSGLV-1859AC]



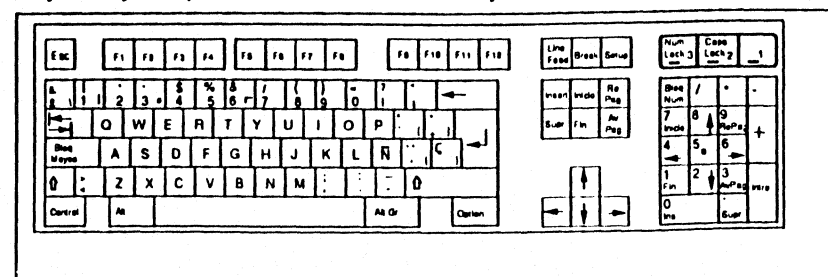
ITALIAN KEYBOARD Assy. # 01-W2587C03 FSD # 66431869
 [Cherry Corp. P/N GLV-1811AC, Key Source KSGLV-1811AC]



NORWEGIAN KEYBOARD Assy. # 01-W2587C07 FSD # 66431873
 [Cherry Corp. P/N GLV-1794AC, Key Source KSGLV-1794AC]



SPANISH KEYBOARD Assy. # 01-W2587C04 FSD # 66431870
 [Cherry Corp. P/N GLV-1810AC, Key Source KSGLV-1810AC]

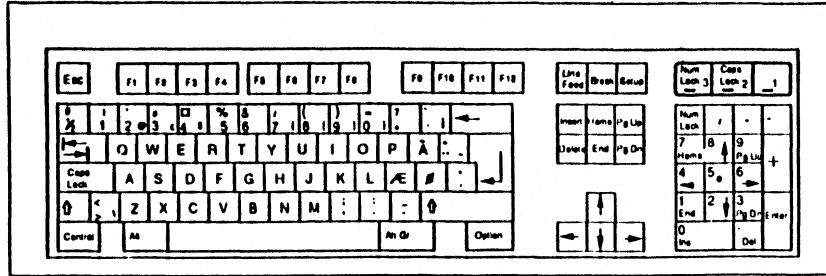


VENDOR = KEYSOURCE

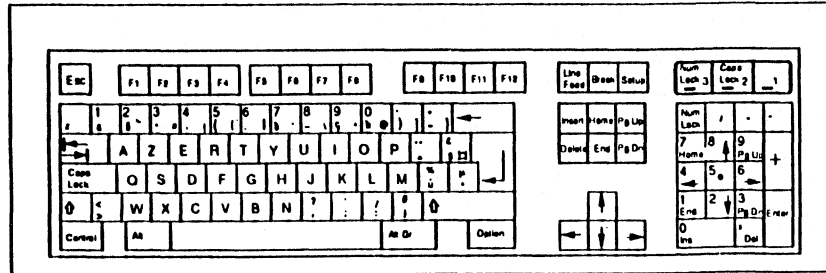
- 01-W2587C01 G80-3055HAG UK KEYBOARD 96800313
- 01-W2587C02 G80-3055HAD GERMAN KEYBOARD 66431868
- 01-W2587C03 G80-3055HAI ITALIAN KEYBOARD 66431869
- 01-W2587C04 G80-3055HAE SPANISH KEYBOARD 66431870
- 01-W2587C05 G80-3055HAO SWEDISH KEYBOARD 66431871
- 01-W2587C06 G80-3055HAC SWISS KEYBOARD 66431872
- 01-W2587C07 G80-3055HAN NORWEGIAN KEYBOARD 66431873
- 01-W2587C08 G80-3055HAM DANISH KEYBOARD 66431874
- 01-W2587C09 G80-3055HAB BELGIAN KEYBOARD 66431875

11/21/91

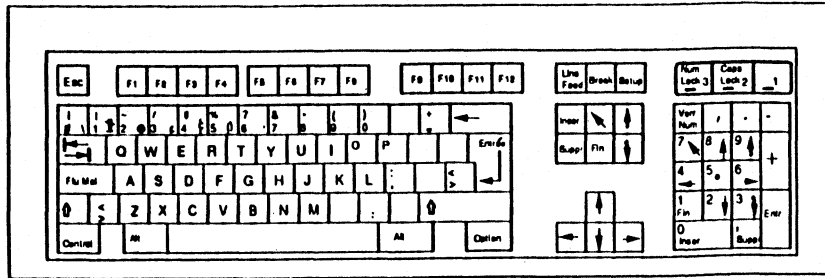
DANISH KEYBOARD Assy. # 01-W2587C08 FSD # 66431874
 [Cherry Corp. P/N GLV-1860AC, Key Source KSGLV-1860AC]



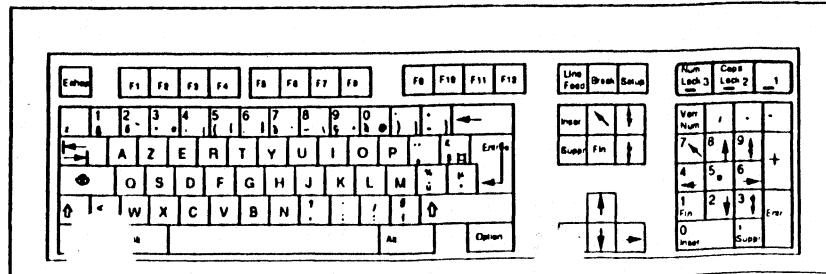
BELGIAN KEYBOARD Assy. # 01-W2587C09 FSD # 66431875
 [Cherry Corp. P/N G99-2241AC, Key Source KSG99-2241AC]



CANAIAN KEYBOARD Assy. # 01-W2587C10 FSD # 66431876
 [Cherry Corp. P/N G99-2266AC, Key Source KSG99-2266AC]

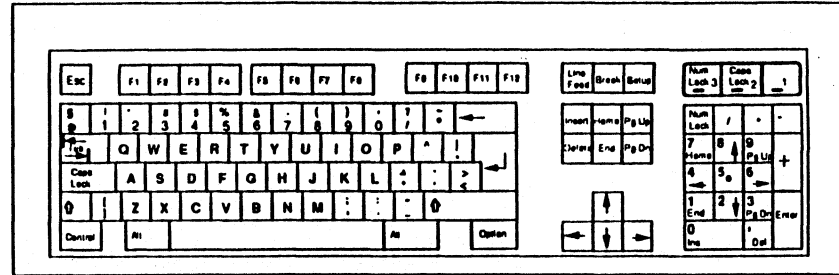


FRENCH KEYBOARD Assy. # 01-W2587C11 FSD # 66431877
 [Cherry Corp. P/N G99-1808AC, Key Source KSG99-1808AC]

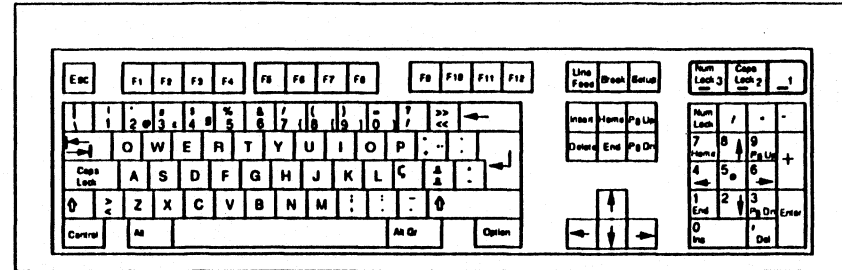


- 01-W2587C10 CANADIAN KEYBOARD 66431876
- 01-W2587C11 G80-3055HAF FRENCH KEYBOARD 66431877
- 01-W2587C12 DUTCH KEYBOARD 66431879
- 01-W2587C13 G80-3055HAP PORTUGUESE KEYBOARD 66431880
- 01-W2587C14 FLEMISH KEYBOARD 66431881
- 01-W2587C15 BLANK KEYBOARD 66431882
- 01-W2587C16 PARTIAL BLANK KIT 66431883

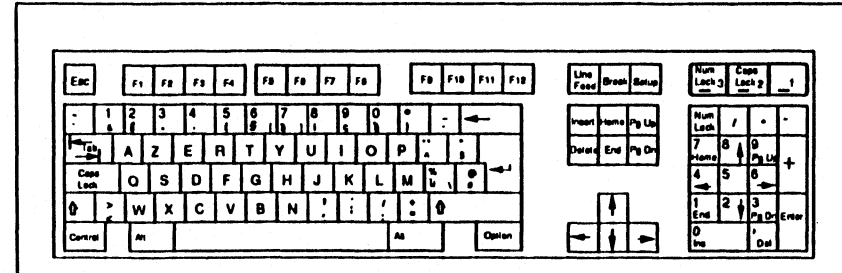
DUTCH KEYBOARD Assy. # 01-W2587C12 FSD # 66431879
 [Cherry Corp. P/N TBD, Key Source TBD]



PORTUGUESE KEYBOARD Assy. # 01-W2587C13 FSD # 66431880
 [Cherry Corp. P/N TBD, Key Source TBD]



FLEMISH KEYBOARD Assy. # 01-W2587C14 FSD # 66431881
 [Cherry Corp. P/N TBD, Key Source TBD]

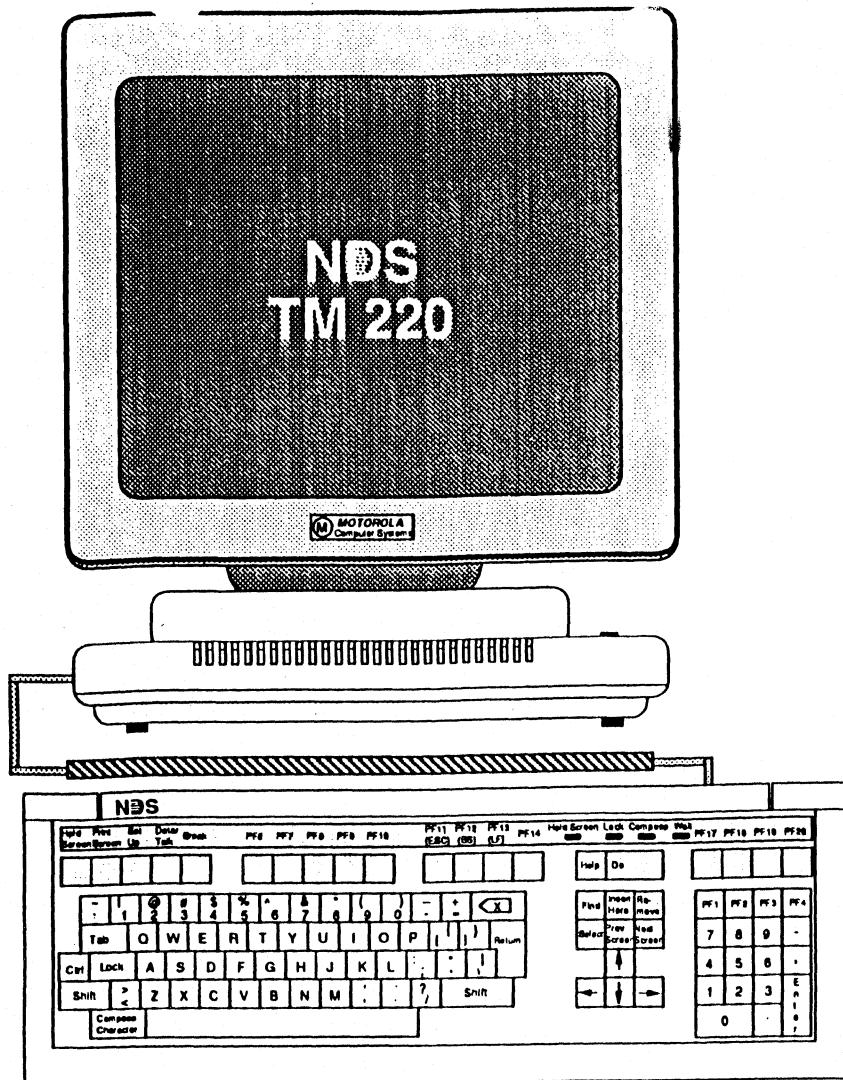


BLANK KEYBOARD Assy. # 01-W2587C15 FSD # 66431882
 [Cherry Corp. P/N TBD, Key Source TBD]

PARTIAL BLANK KEYBOARD Assy. # 01-W2587C16 FSD # 66431883
 [Cherry Corp. P/N TBD, Key Source TBD]

NCD
KEYBOARDS
 PART 2 OF 2

11/21/91



PART NUMBERS:

TM220 01-WXXXXB01

01-W2569C01 FSD P/N 96011319
 NCD KEYBOARD P/N 9100007 PS/2 STYLE
 KEYSOURCE/CHERRY P/N G80-3055HAU PS/E/AT STYLE

NOTE 1: THESE SETUP SCREENS ARE THE STANDARD SCREENS USED BY MOTOROLA AND DO NOT SHOW ALL OPTIONS AVAILABLE.

SETUP MENU	HOME	
Select by Letter:	D: Devices	T: Text
H: Home	P: Pri Aux Port (DCE)	G: Graphics
Q: Quick Setup	S: Sec Aux Port (DCE)	A: Bank A
U: User Preference	M: Main Port DTE	B: Bank B
O: Operating Modes	K: Keyboard	E: Edit Key Programs
Clock Setting		Disabled
Setup Parameters :	Save 1	
	Save 2	
	Save 3	
	Save 4	
	Restore 1	
	Restore 2	
	Restore 3	
	Restore 4	
	Restore Factory Defaults	

Use Shift - Help When Not in Setup for Keyboard Help
 Use Ctrl - Compose - Abort at Any Time for Reset
 Use Do to Return to Home Line or Menu
 Use Enter to Perform Highlighted Action
 Use ↑ & ↓ to Move Highlighted Area (Cursor)
 Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

04/01/91

SETUP MENU

QUICK SETUP

Terminal Emulated	ANSI VT100
Transmit/Receive Speed	9600
Data Format : Number of Data Bits	8
Parity	NONE
Parity Error	
Handshaking : Transmit, Hardware	Disabled
Software	Enabled
Receive, Hardware	Disabled
Software	Enabled
X-ON/X-OFF in Transmitted Data Are	Coordinated
Error Notification	No Alarm
Bells	Enabled
Lock	Caps
Style	Smart
after Reset	Disabled
Cursor Style	Block
	Blinking
	Enabled
	Enabled
Wrap at End of Line	Unshifted
Large Keypad	Normal

Use ↑ & ↓ to Move Highlighted Area (Cursor)
 Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU

USER PREFERENCE

Scrolling Speed	Fast
24 Line Smooth Scroll Rate	20 Lines/Sec
Page Forward or Reverse Motion	Scroll
Action on 80/132 Width Change	Scroll
Port Data to Screen with Test below Screen Action	Scroll to End
Cursor Style	Block
	Blinking
	Enabled
Undefined Key Is Error	Disabled
Error Notification	No Alarm
Bells	Enabled
Auto Margin Bell	Disabled
Key Click	Disabled
Auto Repeat	Enabled
Rate	30 Chars/Sec
Interlaced	When Req'd
Transparent Mode Display Style	Unique Chars

Use ↑ & ↓ to Move Highlighted Area (Cursor)
 Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU OPERATING MODES

Memory Banks (**Changing Clears Memory**) Independent
 On Text/Graphics Change, Bank Used Is Alternate
 Current Bank Is A

Auxiliary Port Configuration
 (**Changing Affects DEVICES Menu**)
 Port Expander Is Not Used
 Primary Output Is Direct

Transmit Raster Code SUB

Use ↑ & ↓ to Move Highlighted Area (Cursor)
 Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU DEVICES

Mouse or Digitizer None

(Setup Menu O before, and P & S after, This Menu)

Printer None

Use ↑ & ↓ to Move Highlighted Area (Cursor)
 Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU PRI AUX PORT

(Setup Menus O & D Before This Menu)

Transmit/Receive Speed	9600
Data Format : Number of Data Bits	8
Parity	Odd
Parity Error	Checked
Transmit Speed Limited to 60 CPS	Disabled
Handshaking : Transmit, Hardware	Enabled
Software	Disabled
Receive, Hardware	Disabled
Software	Disabled
Port Input Goes to: Display	Disabled
Secondary Auxiliary Port	Disabled
Primary Auxiliary Port	Disabled
Main Port	Disabled

Use ↑ & ↓ to Move Highlighted Area (Cursor)
Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU SEC AUX PORT

(Setup Menus O & D Before This Menu)

This Port Is Disabled

Transmit/Receive Speed	9600
Data Format : Number of Data Bits	7
Parity	Space (0)
Parity Error	Ignored
Transmit Speed Limited to 60 CPS	Disabled
Handshaking : Transmit, Hardware	Disabled
Software	Enabled
Receive, Hardware	Disabled
Software	Enabled
X-ON/X-OFF in Trasmitted Data Are	Cordinated
X-ON or Ready at	50
First X-OFF or Not Ready at	100
Repeat X-OFF at	245
Port Input Goes to: Display	Disabled
Secondary Auxiliary Port	Disabled
Primary Auxiliary Port	Disabled
Main Port	Disabled

Use ↑ & ↓ to Move Highlighted Area (Cursor)
Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU MAIN PORT

Transmit/Receive Speed	9600
Data Format : Number of Data Bits	8
Parity	NONE
Parity Error	
Transmit Speed Limited to 60 CPS	Disabled
Handshaking : Transmit, Hardware	Disabled
Software	Enabled
Receive, Hardware	Disabled
Software	Enabled
X-ON/X-OFF in Trasmitted Data Are	Cordinated
X-ON or Ready at	50
First X-OFF or Not Ready at	100
Repeat X-OFF at	245
Port Input Goes to: Display	Enabled
Secondary Auxiliary Port	Disabled
Primary Auxiliary Port	Disabled
Main Port	Disabled

Use ↑ & ↓ to Move Highlighted Area (Cursor)
Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU KEYBOARD

Keyboard Arrangement	North American QWERTY Arrangement
Control Key Layout as per	Disabled
Special Alphabetic Key Control-Shift Codes	Enabled
User Definable Keys	Unshifted
Large Keypad	Normal
Shifted = PF1 (Gold) Key First	Disabled
Small Keypad in Graphics Mode	Unshifted
Function Keys	Unshifted
Cursor Keys ANSI Escape Sequences	Normal
Lock	Shift
Style	Smart
after Reset	Disabled
Hold Screen Key Makes Keyboard Action Local	Disabled
Hold Screen Mode	Unrecognized
More Keyboard Items May Be Found on the User Preference Menu	
Keyboard Input Goes to: Display	Disabled
Secondary Auxiliary Port	Disabled
Primary Auxiliary Port	Disabled
Main Port	Enabled

Use ↑ & ↓ to Move Highlighted Area (Cursor)
Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

SETUP MENU EDIT KEY PROGRAM

Select Operation: Program/Delete Selected Keys
Delete All Programmed Keys

All large keypad keys have special meanings for key programming. These meanings are shown on the help menus available during programming. To enter a keypad key, precede it by keypad 1.

To program or delete keys: **Space Used: 334/2948**

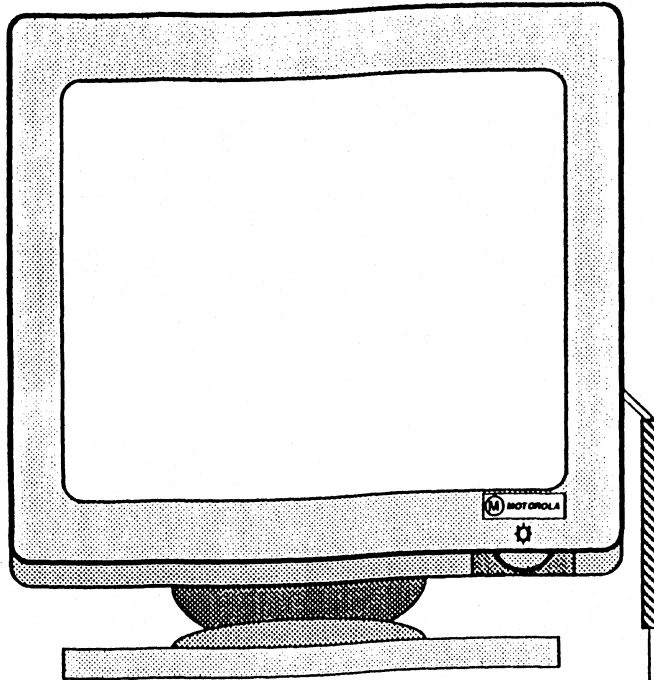
1. Use ↓ to position the cursor to "Program/Delete..."
2. Use **Enter** to select this operation.
A reverse video region will appear at the screen bottom.
3. Push the key to be programmed or deleted.
The key will be displayed as the first character of the reverse video region. The definition, if any, follows.

A list of the keys currently programmed is shown below.

```
K BRF HE F F F F F F F F P P P P P P F W S P S S E F I H R E F F F F LE F F F F F F F F F F F F  
K 17 DO LP 14 13 12 11 10 9 8 7 6 4 3 2 1 W S P S S E W D I H R E 20191817 DO LP 14 13 12 11 10 9 8 7 6 182D
```

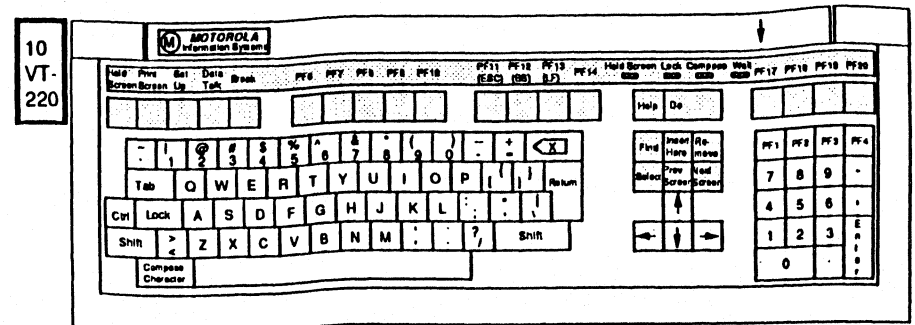
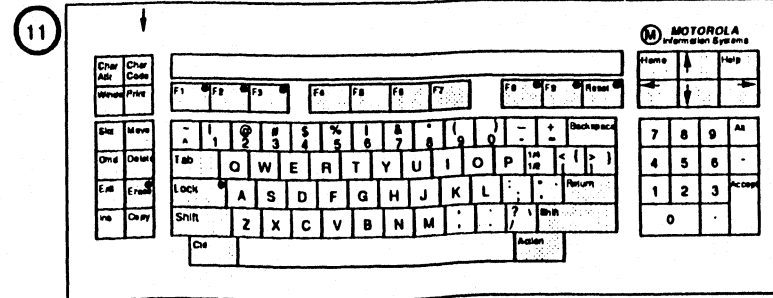
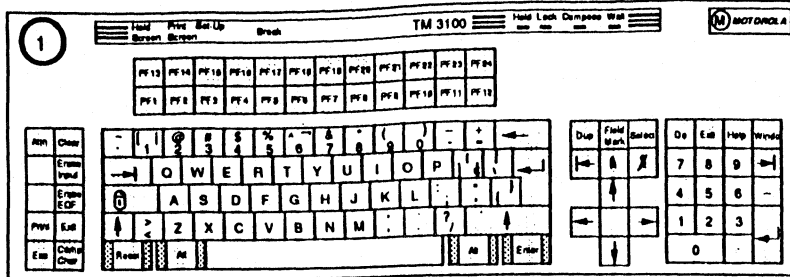
Use ↑ & ↓ to Move Highlighted Area (Cursor)
Use ← & → to Change Highlighted Text

NDS GP-220
Version 1.XX

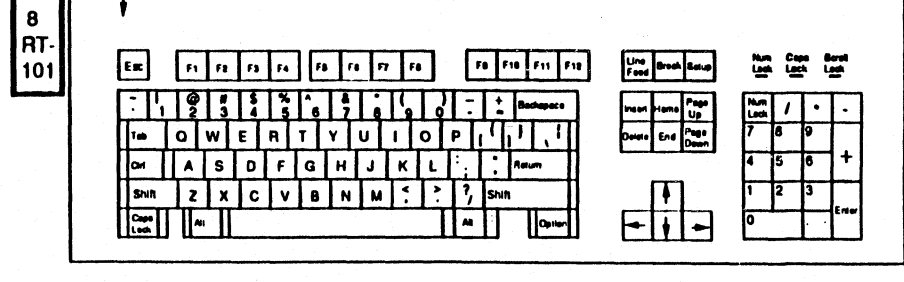
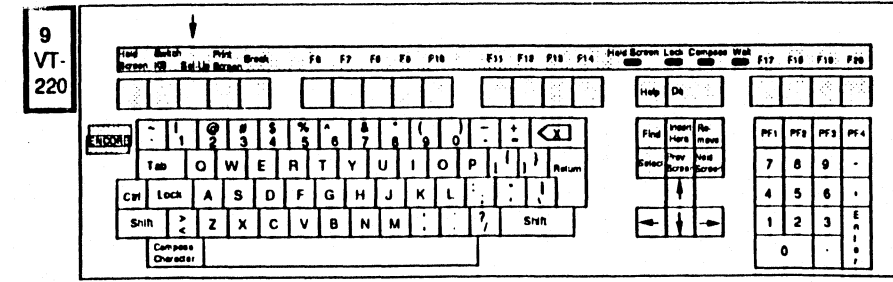
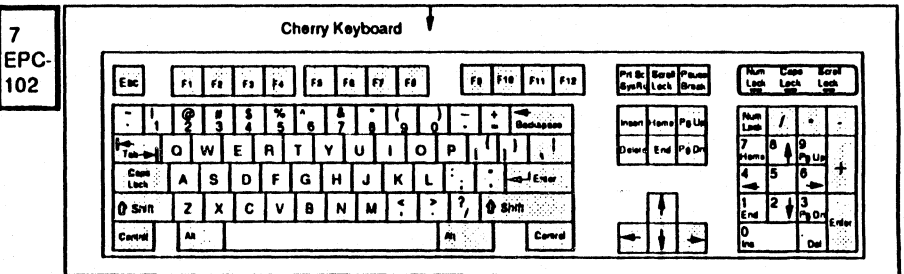
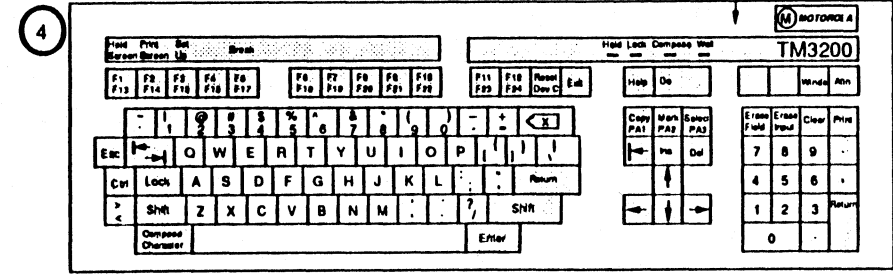
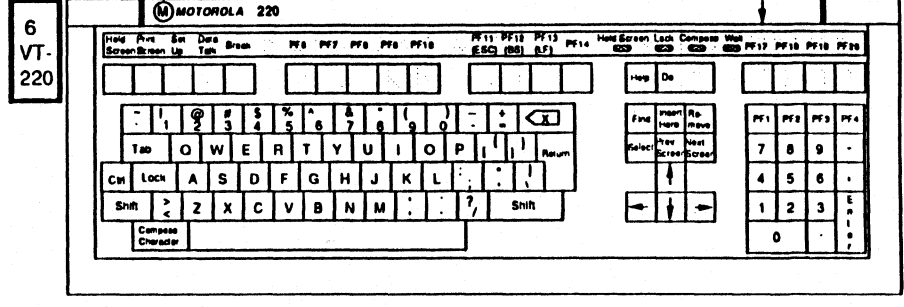
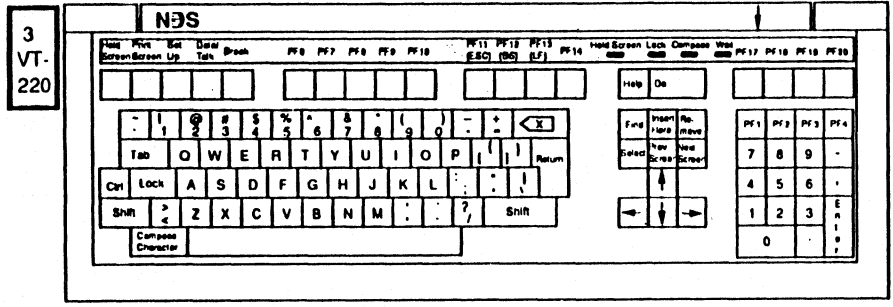
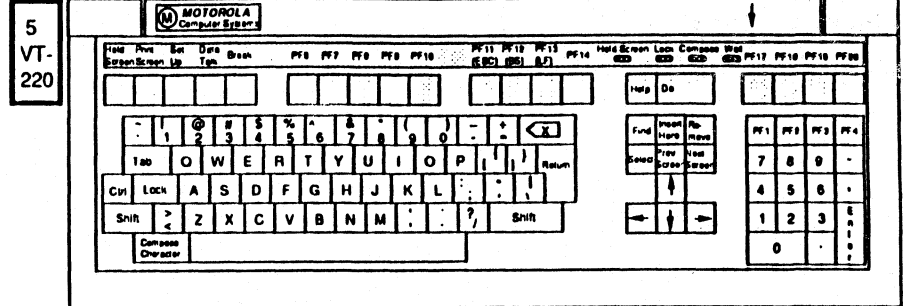
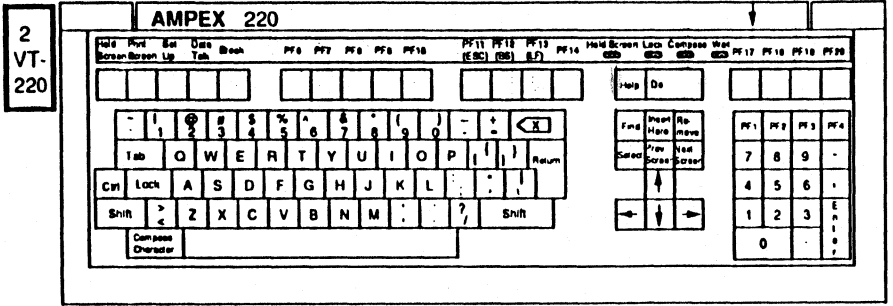


1. TM 3100 U 2834B01
2. AMPEX 220 ASSY # 3515465-0 1-W2448C02
3. NDS # 3515465-11; 01-W2448C02
4. TM 3200 # 44214
5. MOTOROLA COMPUTER SYSTEMS ASSY # 3515465-11; 01-W2448C02
6. MOTOROLA 220 ASSY # 3515850-01; 01-W2871B01 96800292
7. CHERRY ARTICLE # G80-3000NAU/10; EPC-102; 01-W2587C01
8. UNKNOWN MODEL # RT-101; 01-W2448C03
9. ENCORE ?????; 01-W2448C02
10. MOTOROLA INFORMATION SYSTEMS ASSY # 3515465-11; 01-W2448C02
11. CONVERGENT TECHNOLOGIES KM-001 B
12. KB0290; 01-W2448C01; CAN'T FIND ONE TO COPY

11/21/91

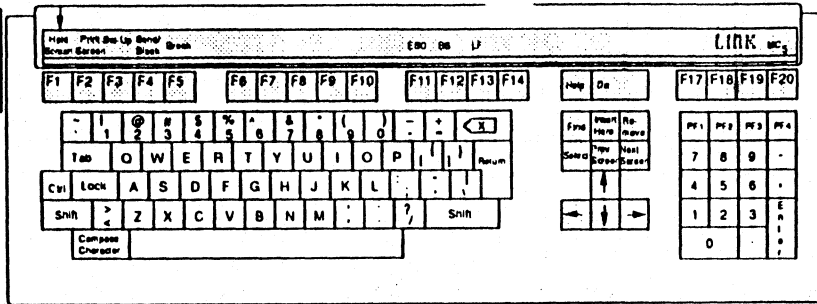


DISPLAY		COMMUNICATION		PRINTER	
Char/Line	80	Comm Mode	On-Line	Print Mode	Normal
Scroll	Jump	Transmit	19200	Baud Rate	9600
Cursor	Yes	Receive	= Transmit	Data Bits	8
Cursor Style	Block/Blink	Data Bits	8	Parity	No
Background	Dark	Parity	No	Stop Bit	1
Ctrl Code	Interpret	Stop Bit	1	Print Area	Full Page
Auto Wrap	Yes	Recv Ctrl	XOFF at 128	Data Type	National
Set Up	English	Local Echo	No	Terminator	No
Size Change	Clear	Line I.F.	EIA Null		
Clear Disp		Disconnect	2s		
		Limit Trans	No		
		Clear Comm			



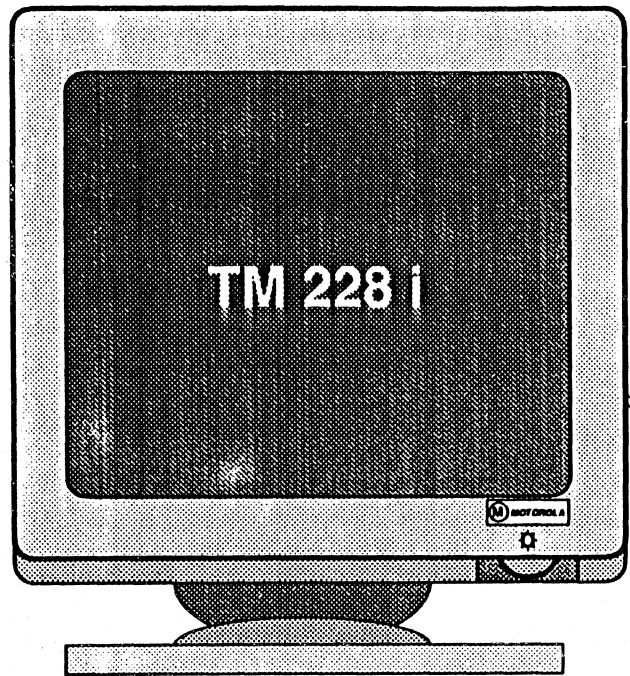
04/05/91

13
VT-
220



- 01-W2587C01 G80-3055HAG UK KEYBOARD
- 01-W2587C02 G80-3055HAD GERMAN KEYBOARD
- 01-W2587C03 G80-3055HAI ITALIAN KEYBOARD
- 01-W2587C04 G80-3055HAE SPANISH KEYBOARD
- 01-W2587C05 G80-3055HAO SWEDISH KEYBOARD
- 01-W2587C06 G80-3055HAC SWISS KEYBOARD
- 01-W2587C07 G80-3055HAN NORWEGIAN KEYBOARD
- 01-W2587C08 G80-3055HAM DANISH KEYBOARD
- 01-W2587C09 G80-3055HAB BELGIAN KEYBOARD
- 01-W2587C10 CANADIAN KEYBOARD
- 01-W2587C11 G80-3055HAF FRENCH KEYBOARD
- 01-W2587C12 DUTCH KEYBOARD
- 01-W2587C13 G80-3055HAP PORTUGUESE KEYBOARD
- 01-W2587C14 FLEMISH KEYBOARD
- 01-W2587C15 BLANK KEYBOARD
- 01-W2587C16 PARTIAL BLANK KIT

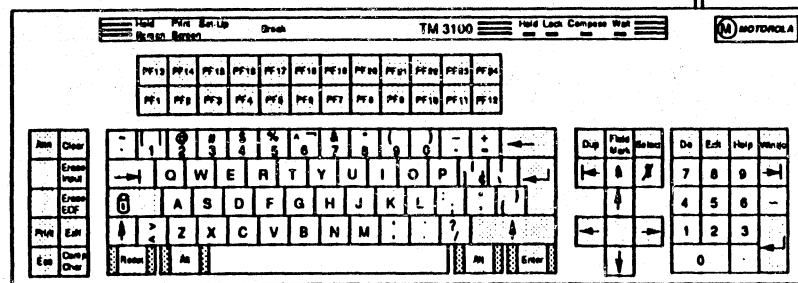
04/05/91



PART NUMBERS:

TM228i 01-WXXXXB01

NOTE 1: THIS IS THE STANDARD SETUP FOR THIS TERMINAL.

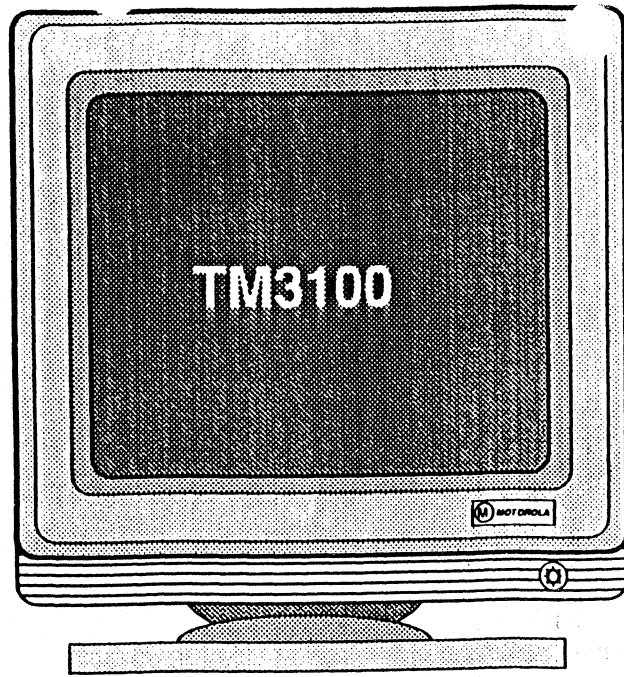


09/17/90

DISPLAY		COMMUNICATION		PRINTER	
Char/Line	80	Comm Mode	On-Line	Print Mode	Normal
Scroll	Jump	Transmit	19200	Baud Rate	9600
Cursor	Yes	Receive	= Transmit	Data Bits	8
Cursor Style	Block/Blink	Data Bits	8	Parity	No
Background	Dark	Parity	No	Stop Bit	1
Ctrl Code	Interpret	Stop Bit	1	Print Area	Full Page
Auto Wrap	Yes	Recv Ctrl	XOFF at 128	Data Type	National
Set Up	English	Local Echo	No	Terminator	No
Size Change	Clear	Line I.F.	EIA Null		
Clear Disp		Disconnect	2s		
		Limit Trans	No		
		Clear Comm			

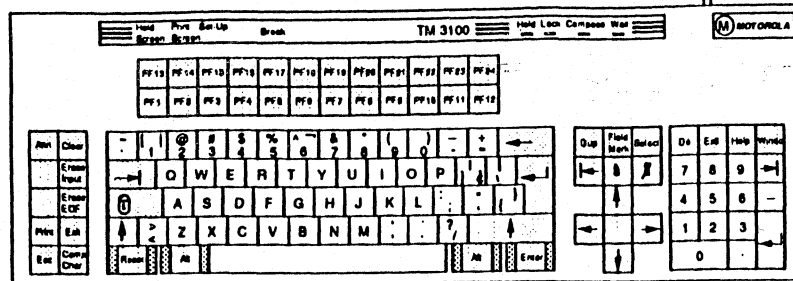
**TM228i
TERMINAL
GE 1**

PART NUMBERS:



- TM3100 - 01-W2834B01 KBD, IBM COMPATIBLE, KOKUSI P/N 36006 FSD P/N 96800298
- TM3179 - 01-W1346B01 TERM, COLOR W/ IBM KBD, FSD P/N 96700160
01-W2833B01 KOKUSI P/N D79620-01 W/ 2.1 F/W MODEL # TM3600
- TM3179G - 01-W1346B02 TERM, COLOR W/ GRAPHICS & IBM KBD. FSD P/N 96700155
01-W2833B02 KOKUSI P/N D79620-02 W/ 2.1 F/W MODEL # TM3699G
- TM3179V - 01-W1346B04 TERM, COLOR W/ BS & IBM KBD.
01-W2833B21 KOKUSI P/N D79620-21 W/ 2.1 F/W MODEL # TM3601
- TM3179VG - 01-W1346B08 TERM, COLOR W/ GRAPHICS, BS & IBM KBD.
01-W2833B22 KOKUSI P/N D79620D-22 W/ 2.1 F/W MODEL # TM3601G
- TM3180 - 01-W1345B02 TERM, GREEN W/ IBM KBD. FSD P/N 96700156 OR 96700157
PRIME - 01-W2832B01 KOKUSI P/N D79653-01 W/ 2.1 F/W MODEL # TM3500
ALT - 01-W2832B02 KOKUSI P/N D79653-02 W/ 2.1 F/W MODEL # TM5000G
- TM3180A - 01-W1345B06 TERM, AMBER W/ IBM KBD FSD P/N 96700158
01-W2832B03 KOKUSI P/N D79653-03 W/ 2.1 F/W MODEL # TM3500A

09/13/90

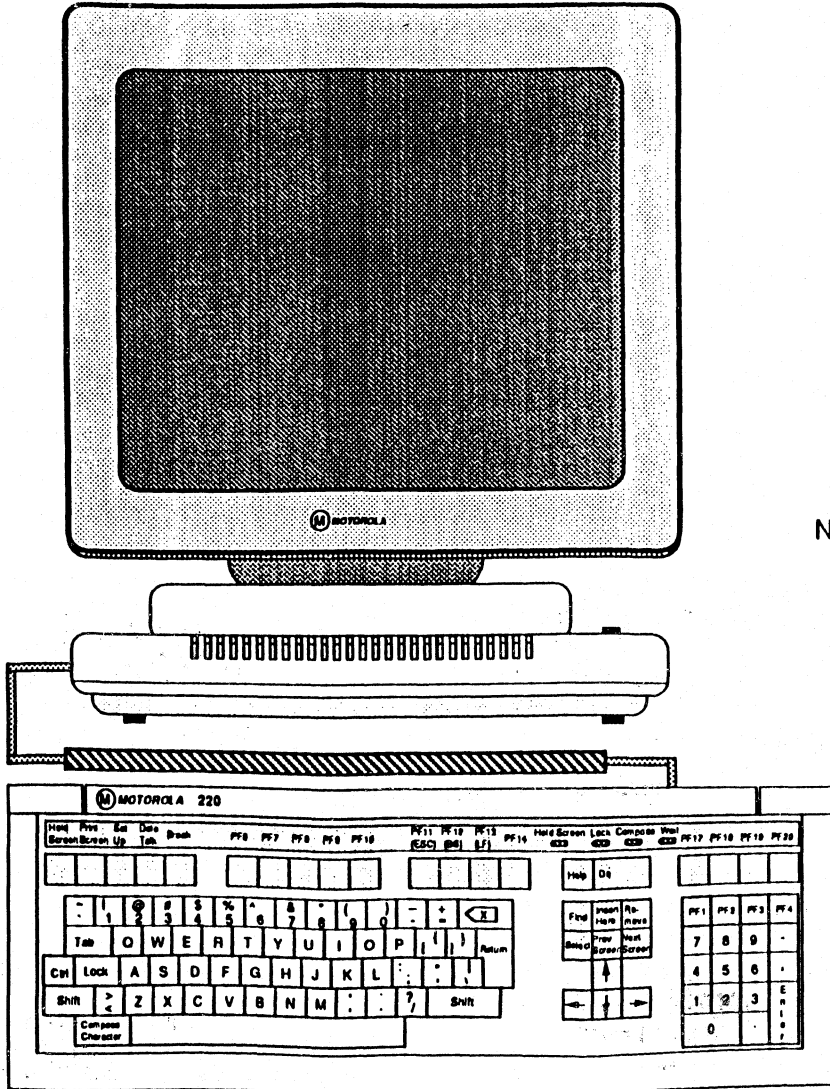


- TM3180AG - 01-W1345B08 TERM, W/ GRAPHICS & IBM KBD FSD P/N 96700159
01-W2832B04 KOKUSI P/N D79653-04 W/ 2.1 F/W MODEL # TM3500AG
- TM3180AV - 01-W1345B14 TERM, AMBER W/ BS & IBM KBD.
01-W2832B23 KOKUSI P/N D79653-23 W/ 2.1 F/W
- TM3180AVG - 01-W1345B16 TERM, AMBER W. GRAPHICS, BS, & IBM KBD.
01-W2832B24 KOKUSI P/N D79653-24 W/ 2.1 F/W

DISPLAY		COMMUNICATION		PRINTER	
Char/Line	80	Comm Mode	On-Line	Print Mode	Normal
Scroll	Jump	Transmit	19200	Baud Rate	9600
Cursor	Yes	Receive	= Transmit	Data Bits	8
Cursor Style	Block/Blink	Data Bits	8	Parity	No
Background	Dark	Parity	No	Stop Bit	1
Ctrl Code	Interpret	Stop Bit	1	Print Area	Full Page
Auto Wrap	Yes	Recv Ctrl	XOFF at 128	Data Type	National
Set Up	English	Local Echo	No	Terminator	No
Size Change	Clear	Line I.F.	EIA Null		
Clear Disp		Disconnect	2s		
		Limit Trans	No		
		Clear Comm			

- TM3180G - 01-W1345B04 TERM, AMBER W/ GRAPHICS & IBM KBD.
01-W2832B02 KOKUSI P/N D79653-02 W/ 2.1 F/W MODEL # TM3500G FSD P/N 96700157
- TM3180V - 01-W1345B10 TERM, AMBER W/ GRAPHICS, BS. & IBM KBD.
01-W2832B21 KOKUSI P/N D79653-21 W/ 2.1 F/W
- TM3180VG-01-W1345B12 TERM, GREEN W/ GRAPHICS, BS, & IBM KBD.
01-W2832B22 KOKUSI P/N D79653-22 S/ 2.1 F/W

NOTE 1: THIS IS THE STANDARD SETUP FOR THIS TERMINAL



PART NUMBERS:

TM220 01-W2856B01 96720012 & 96700154
 AMPEX P/N3513602-02 (44185-001) AMBER DISPLAY

KEYBOARD 01-W2871B01 96800292 VENDOR # 3515850-01

TM220A 01-W2856B02 AMPEX P/N 3513602-01 (44185-002)
 GREEN DISPLAY, WITH SAME KEYBOARD

TM2201 IS THE SAME AS THE TM220

TM2201A IS THE SAME AS THE TM220A

NOTE 1: THIS IS THE STANDARD SETUP FOR THIS TERMINAL.

11/21/91

M220 SET-UP MODE V2.0

General Set-Up

To Next Set-up Screen On Line	VT220 Mode, 7 Bit Controls
Clear Display	User Defined Keys Unlocked
Clear Communications	User Features Unlocked
Reset Terminal	Numeric Keypad
Recall "SAVED" Values	Normal Cursor Keys
Save Current Values	No New Line
Default Values	Set-Up = English
Refresh Rate = 60 Hz	North American Keyboard

MODE: Replace PRINTER: None

M220 SET-UP MODE V2.0

Printer Comm. Set-Up

Host Comm. Set-Up

To Next Set-Up Screen	Transmit = 9600
Speed = 9600	Receive = Transmit
Normal Print Mode	XOFF At 64
7 bits, Even Parity	8 Bits, No Parity
1 Stop Bit	1 Stop Bit
Print Full Page	No Local Echo
Print National Only	EIA Port, Data Leads Only
No Terminator	Disconnect, 2 s Delay
	Limited Transmit

MODE: Replace PRINTER: None

M220 SET-UP MODE V2.0

Display Set-Up

Keyboard Set-Up

To Next Set-Up Screen	Typewriter Keys
80 Column	Caps Lock
Interpret Controls	Auto Repeat
Auto Wrap	No Keyclick
Jump Scroll	No Margin Bell
Light Text, Dark Screen	No Warning Bell
Cursor	Break
Block Cursor Style	Multinational

MODE: Replace PRINTER: None

M220 SET-UP MODE V2.0

Answerback/Tab Set-Up

Block Mode Set-Up

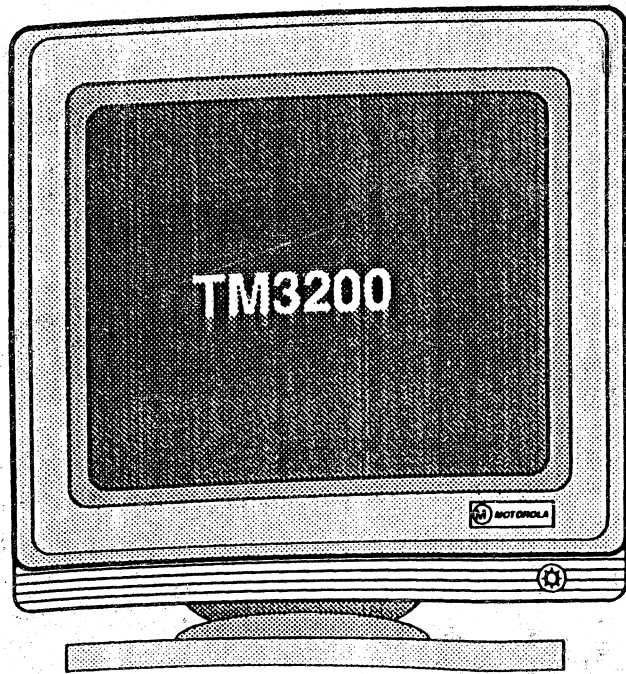
To Next Set-Up Screen	Transmit Line
No Auto Answerback	End Of Line Char = CR/CRLF
Not Concealed	No End Of Block Char
Answerback =	

Clear All Tabs
Set 8 Column Tabs

T T T T T T T T T T
1234567890123456789012345678901234567890123456789012345678901234567890

MODE: Replace PRINTER: None

09/17/90

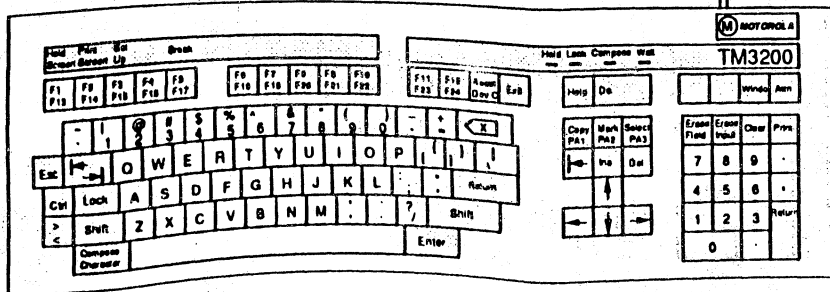


PART NUMBERS:

TM 3200 01-W2841B01 89000375/96800299

DISPLAY		COMMUNICATION		PRINTER	
Char/Line	80	Comm Mode	On-Line	Print Mode	Normal
Scroll	Jump	Transmit	19200	Baud Rate	2400
Cursor	Yes	Receive	= Transmit	Data Bits	7
Cursor Style	Block/Blink	Data Bits	8	Parity	Even
Background	Dark	Parity	No	Stop Bit	1
Ctrl Code	Interpret	Stop Bit	1	Print Area	Full Page
Auto Wrap	Yes	Recv Ctrl	XOFF at 128	Data Type	National
Set Up	English	Local Echo	No	Terminator	No
Size Change	Clear	Line I.F.	EIA Null		
Clear Disp		Disconnect	2s		
		Limit Trans	No		
		Clear Comm			

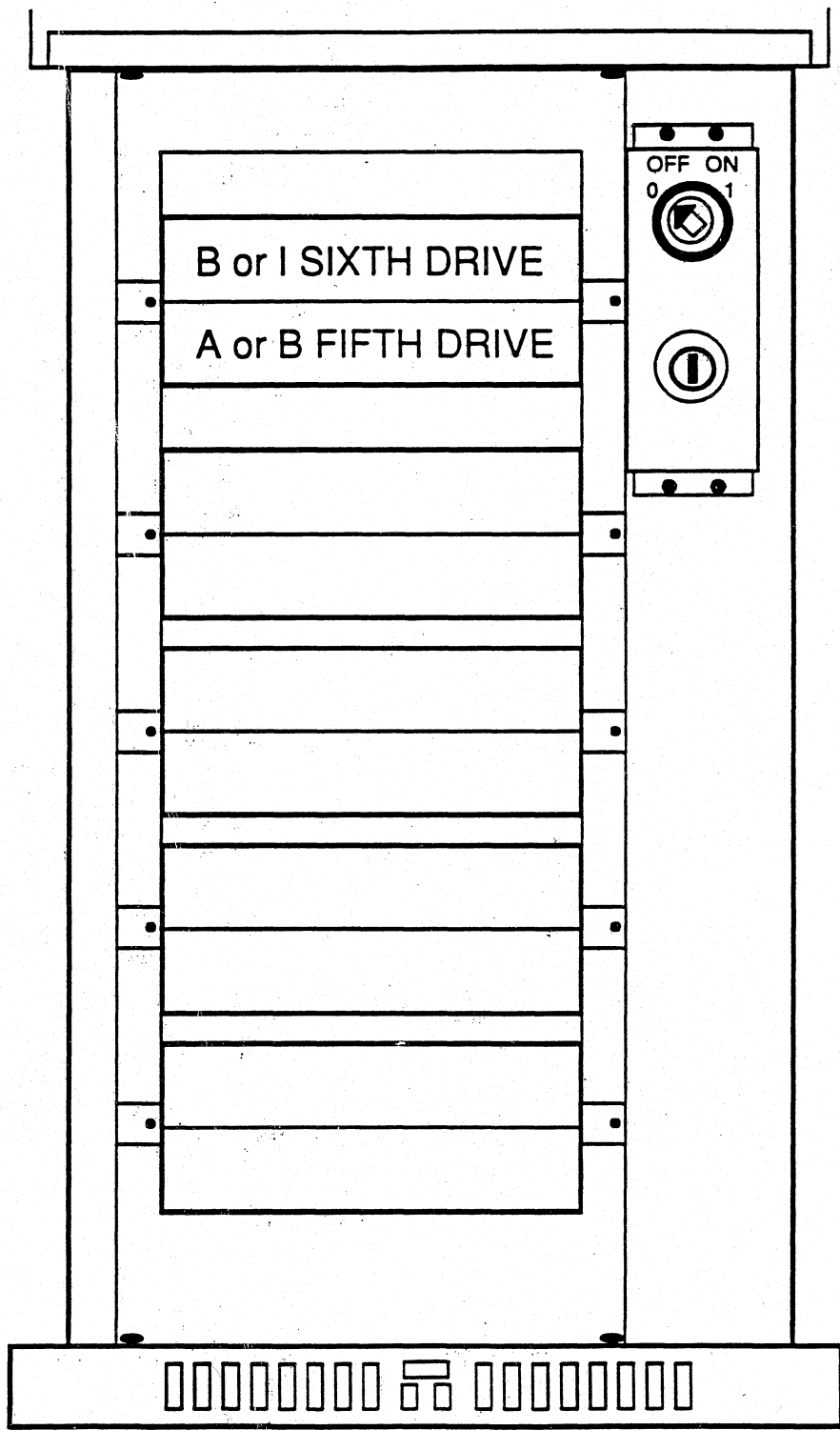
NOTE 1: THIS IS THE STANDARD SETUP FOR THIS TERMINAL



- TM3220 - MONOCHROME MONITOR WITH OFFICE KEYBOARD
- TM3220A - AMBER MONOCHROME MONITOR WITH OFFICE KEYBOARD
- TM3220G - MONOCHROME GRAPHICS MONITOR WITH OFFICE KEYBOARD
- TM3220AG - AMBER MONOCHROME GRAPHICS MONITOR WITH OFF. KEYBOARD
- TM3241G - COLOR GRAPHICS WITH OFFICE KEYBOARD

11/21/91

APPENDIX L



MPC
DRIVE PLACEMENT

04/05/91